

DQ11

OVERLAY FOR ITEP
MD-11-DZDQO-B

EP-DZDQO-B-DL-A

NOV 1976

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digital

FICHE 1 OF 1

MADE IN U.S.A.

The microfiche card contains 30 frames of technical data, organized in a 10x3 grid. The frames contain various tables, charts, and diagrams, including what appears to be a flight deck layout and various performance charts. The text is small and difficult to read, but the layout suggests a comprehensive technical manual or flight deck overlay.

IDENTIFICATION

PRODUCT CODE: MAINDEC-11-DZD00-B-D

PRODUCT NAME: DQ11 OVERLAY FOR INTERPROCESSOR TEST PROGRAM

PROGRAM DATE: OCTOBER 1976

MAINTAINER: DIAGNOSTICS

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1.0 ABSTRACT.

THIS PROGRAM IS DESIGNED AS A MAINTENANCE AID FOR FIELD SERVICE PERSONEL. IT WILL VERIFY THE PROPER OPERATION OF A COMPLETE COMMUNICATION LINK FROM ONE PDP-11 SYSTEM TO ANOTHER OR TO A COMMUNICATION TEST CENTER.

THIS PROGRAM MUST BE USED IN CONJUNCTION WITH THE INTERPROCESSOR TEST PROGRAM(DZITP) ON A PDP-11 SYSTEM WITH A DL-11 INTERFACE.

2.0 REQUIREMENTS.

2.1 EQUIPMENT

- A. PDP-11 SYSTEM WITH 4K OF CORE.
- B. A DQ11 COMMUNICATION INTERFACE.

2.2 STORAGE.

4K OF CORE

3.0 LOADING PROCEDURE

THIS PROGRAM IS IN ABSOLUTE FORMAT.
THE ABS LOADER MUST BE USED TO LOAD THE PROGRAM.

4.0 OPERATING PROCEDURES.

- A. TWO METHODS OF ENTERING PARAMETERS ARE PROVIDED
 - 1. LOAD ADDRESS 200 AND START TO ENTER PARAMS FROM CONSOLE TTY, PROCEED TO SECTION B.
 - 2. LOAD ADDRESS 200 AND SET SWITCH REGISTER BIT 15 BEFORE STARTING TO ENTER PARAMS FROM CONSOLE SWITCHES, PROCEED TO SECTION C.
- *THE PROGRAM MAY BE RESTARTED AT LOC 204 (ONCE PARAMETERS HAVE ALREADY BEEN SELECTED)
- B. CONSOLE DIALOGUE PARAMETER INPUT (CURRENT VALUES FOR PARAMETERS ARE FOUND IN OVERLAY)

- 1. THE PROGRAM WILL TYPEOUT THE NAME OF THE VARIABLE OVERLAY.
 - A. IF YOU WISH TO SETUP JUST THE INDICATED OVERLAY, TYPE A CARAGE RETURN
 - B. IF YOU WISH TO SETUP A DN11, TYPE IN DN.
 - C. IF YOU WISH TO SETUP A DN11B, TYPE IN DNB.

IF DN OR DNB WAS TYPED IN STEP 1 ABOVE THEN THE BUS ADDRESS, VECTOR ETC. REFERED TO IN STEPS 2 THRU 7, PERTAIN TO THE DN11 OR DN11B.

- 2. THE PROGRAM WILL TYPE THE DEFAULT BUS ADDRESS OF THE INTERFACE UNDER TEST.
 - A. TYPE A CAR. RETURN TO USE DEFAULT BUS ADDRESS
 - B. TYPEIN ACTUAL BUS ADDRESS
- 3. THE PROGRAM WILL TYPE OUT THE DEFAULT VECTOR ADDRESS
 - A. TYPE A CAR. RETURN TO USE DEFAULT ADDRESS
 - B. TYPEIN ACTUAL VECTOR ADDRESS
- 4. THE PROGRAM WILL TYPE OUT THE DEFAULT INTERFACE PRIORITY
NOTE: 200=PRIO 4, 240=PRIO 5, 300=PRIO 6, ETC.

- A. TYPE A CAR. RETURN TO USE DEFAULT VALUE
 - B. TYPEIN ACTUAL VALUE
5. THE PROGRAM WILL TYPEOUT THE DEFAULT VALUE OF PARAM#1
IF REQUIRED BY THE ISR. (SEE SECT. 10.0 IN OVERLAY LISTING FOR PARAMETER DESCRIPTION)
- A. TYPE A CAR. RETURN TO USE DEFAULT VALUE
 - B. TYPEIN ACTUAL VALUE
6. THE PROGRAM WILL TYPEOUT THE DEFAULT VALUE OF PARAM#2
IF REQUIRED BY THE ISR.
- A. TYPE A CAR. RETURN TO USE DEFAULT VALUE
 - B. ENTER ACTUAL VALUE
7. THE PROGRAM WILL TYPEOUT THE DEFAULT VALUE OF PARAM#3
IF REQUIRED BY THE OVERLAY.
- A. TYPE A CAR. RETURN TO USE DEFAULT VALUE
THE DN-11 WILL USE PARAM #3 AS THE # TO DIAL.
IF USING A MODEM WITHOUT AUTOMATIC HANDSHAKING,
THE NUMBER MUST TERMINATE WITH A
"END-OF-NUMBER" CHARACTER (:).
 - B. ENTER ACTUAL VALUE.
8. THE PROGRAM WILL RETURN TO STEP B1 IF THIS SETUP
WAS FOR DN11 OR DN11BB.
9. THE PROGRAM WILL REQUEST THAT SWITCH REGISTER BE SET.
- A. SETUP SWITCH REGISTER AS SPECIFIED IN STEP D.
AND TYPE A CAR. RETURN.

NOTE: IF ANY OF THE ABOVE ITEMS 2 THRU 7 WERE CHANGED BY ENTERING
NEW VALUES, THE NEW VALUE BECOMES THE DEFAULT VALUE FOR SUBSEQUENT
RESTARTS OF THE PROGRAM.

- C. MANUAL PARAMETER INPUT FROM SWITCH REGISTER
1. THE PROGRAM HALTS FOR ISR (INTERFACE SERVICE ROUTINE) SPECIFICATION
SWR14=SETUP DN-11B ISR
SWR13=SETUP DN-11 ISR
SWR=00000=SETUP VARIABLE ISR
 2. THE FOLLOWING HALTS ARE REPEATED FOR EACH ISR SPECIFIED.
SETUP SEQUENCE IS: DN11, DN11-BB THEN VARIABLE OVERLAY. (EACH ENTRY SET SWITCHES THEN HIT CONTINUE.)
 - A. HALT FOR BUS ADDRESS OF INTERFACE
 - B. HALT FOR VECTOR ADDRESS OF INTERFACE
 - C. HALT FOR PRIORITY OF INTERFACE
 - D. HALT FOR INTERFACE PARAM #1 (SEE SECT. 10.0 IN OVERLAY LISTING FOR PARAMETER DESCRIPTION)
 - E. HALT FOR INTERFACE PARAM #2 (DN11 AND DNBB PARAMETERS ARE DISCUSSED IN SECT. 10.0 OF THE MONITOR.)
 - F. GO BACK TO STEP A IF THIS SETUP WAS FOR DN OR DNBB.
 3. HALT FOR OPERATIONAL SWITCH SETTINGS. (SEE STEP D.)
 - A. PRESS CONTINUE TO START TESTING

BEFORE ATTEMPTING TO RUN THIS PROGRAM, THE OPERATOR MUST ACCERTAIN THE COMPLETE COMMUNICATION LOOP AND PROCEDURES TO BE USED, INCLUDING THE TYPE OF MODEMS, THE TYPE OF INTERFACE BEING USED AT THE OTHER CPU AND THE MODES OF OPERATION, DATA AND PARAMETERS TO BE USED AT EACH CPU.

THIS WILL REQUIRED VOCAL COMMUNICATION WITH THE OPERATOR AT THE OTHER CPU UNLESS ITS CONFIGURATION AND OPERATION ARE FIXED AS A TEST CENTER.

AFTER DETERMINING THAT THE EQUIPMENTS ARE COMPATIBLE AND AGREEING ON THE MODE AND VARIABLE PARAMETERS TO BE USED, THE SYSTEM WHICH IS TO RECEIVE DATA FIRST SHOULD BE LOADED AND STARTED. IF THE MODEM BEING USED ON THIS SYSTEM HAS AN AUTOMATIC ANSWER FEATURE, IT SHOULD BE ENABLED.

THE SYSTEM WHICH IS TO TRANSMIT FIRST SHOULD THEN BE LOADED AND STARTED AND THE CONNECTION ESTABLISHED EITHER MANUALLY OR AUTOMATICALLY (VIA DN-11).

D. OPERATIONAL SWITCH SETTINGS.

SW15=1 HALT ON ERROR
SW14=1 SINGLE PASS
SW14 HAS NO EFFECT IF SW04=0
SW13=1 INHIBIT ERROR TYPEOUTS
SW12=1 INHIBIT ALL TYPEOUTS EXCEPT ERRORS
IF SW12=0 AND SW04=1 END PASS IS TYPED
AND TRANSMITTED/RECEIVED DATA IS TYPED.
SW11=1 USE PREVIOUSLY SPECIFIED DATA
SW10=1 DATA SELECT (WITH SW09)
SW09=1 DATA SELECT (WITH SW10)
00=1 GET DATA FROM OPERATOR
01=1 TEST MESSAGE 01 (SA QUICK BROWN FOX)
10=1 TEST MESSAGE 02 (99 NUMERICS)
11=1 TEST MESSAGE 03 (SC CONTEST/QUICK BROWN FOX/NUMERICS)
SW08=1 TRANSMIT RECEIVED DATA (INTERNAL LOOPBACK MODE)
SW07=1 DO NOT TEST RECEIVED DATA
SW06=1 MONITOR TRANSMITTED DATA ON CONSOLE TTY.*
SW05=1 MONITOR RECEIVED DATA ON CONSOLE TTY.*
* IN MANY CASES, NOT ALL DATA WILL APPEAR ON THE CONSOLE
TTY. THIS IS ESPECIALLY TRUE WHEN THE COMM INTERFACE IS
RUNNING AT A FASTER BAUD THAN THE CONSOLE, BUT EVEN AT EQUAL
OR SLOWER BAUDS, ALL CHARACTERS MAY NOT APPEAR ON THE CONSOLE.

SW04=1 RETURN TO MONITOR FOR END PASS
WHEN SW04=0 PROGRAM LOOPS IN THE OVERLAY NEVER RETURNING TO THE MONITOR.
SW03=1 INTERNAL LOOPBACK MODE
SW02=1 EXTERNAL LOOPBACK MODE
SW01=1 ONE-WAY-IN MODE
SW00=1 ONE-WAY-OUT MODE

THIS PROGRAM HAS BEEN MODIFIED TO RUN ON A PROCESSOR WITH OR WITHOUT A HARDWARE SWITCH REGISTER. WHEN FIRST EXECUTED THE PROGRAM TESTS THE EXISTENCE OF A HARDWARE SWITCH REGISTER. IF NOT FOUND A SOFTWARE SWITCH REGISTER LOCATION (SWREG=LOC. 176) IS DEFAULTED TO. IF THIS IS THE CASE, UPON EXECUTION THE CONTENTS OF THE SWREG ARE DUMPED IN OCTAL ON THE CONSOLE TTY AND ANY CHANGES ARE REQUESTED

(IE) SWR=XXXXXX NEW=

POSSIBLE RESPONSES ARE:

1. (CR) IF NO CHANGES ARE TO BE MADE
2. 6 DIGITS 0-7 TO REPRESENT IN OCTAL THE NEW SWITCH REGISTER VALUE ;LAST DIGIT FOLLOWED BY (CR).
3. ↑ TO ALLOW REENTERING VALUE IF ERROR IS COMMITTED KEYING IN SWREG VALUE.

BUILT INTO THE PROGRAM IS THE ABILITY TO DYNAMICALLY CHANGE THE CONTENTS OF SWREG DURING PROGRAM EXECUTION. BY STRIKING ↑G (CNTRL G) ON CONSOLE TTY THE OPERATOR SETS A REQUEST FLAG TO CHANGE THE CONTENTS OF SWREG, WHICH IS PROCESSED IN KEY AREAS OF THE PROGRAM CODE (IE) ERROR ROUTINES, AFTER HALTS END OF PASS, AND OTHER APPLICABLE AREAS.

IF OPERATOR SPECIFIED DATA WAS INDICATED, THE PROGRAM WILL TYPE A REQUEST FOR THE DATA. DATA MAY BE ENTERED AS ASCII CHARACTERS OR OCTAL CODE. TYPE IN THE DATA TERMINATED WITH A CR. OCTAL CODE MAY BE ENTERED BY TYPING AN ↑(UP ARROW) FOLLOWED BY THE OCTAL CODE (IN THE RANGE 000 TO 377) SEPERATED BY SPACES AND TERMINATED BY ↑(UP ARROW).
I.E. ABCD↑ 000 123 377↑ EFG (CAR.RETURN)

A TYPICAL SWITCH SETTING FOR HALF-DUPLEX=003150 THIS SETTING USES INTERNAL LOOPBACK MODE, LOOPS IN OVERLAY, MONITORS TRANSMITTED AND RECEIVED DATA ON THE CONSOLE TTY, AND TESTS RECEIVED DATA USING TEST MESSAGE #3.

A TYPICAL SWITCH SETTING FOR FULL-DUPLEX=003144 THIS SETTING IS THE SAME AS ABOVE EXCEPT IT USES THE EXTERNAL LOOPBACK MODE.

ALL STANDARD MESSAGES (TEST MESSAGES 1-3) ARE PRECEDED BY 2 FILL CHARACTERS(177), AND ARE FOLLOWED BY A CR(015), LF(012), RECEIVE TERMINATING CHARACTER(001), 4 FILLS(177), AND A TRANSMIT TERMINATING CHARACTER(000). DURING TRANSMISSION, WHEN A 000 CHARACTER IS SEEN THE TRANSMISSION IS STOPPED. DURING RECEPTION, WHEN A 001 CHARACTER IS RECEIVED, THE RECEIVER IS SHUT OFF. IF THE MESSAGE WAS INPUTED BY THE OPERATER, THE TERMINATING CHARACTERS ARE ADDED.

TEST MODES

INTERNAL LOOPBACK MODE

1. THE OVERLAY WAITS TO RECEIVE A MESSAGE (TERMINATED BY <001>)
2. VERIFIES THE DATA AGAINST THE DATA SELECTED BY SM09 AND SM10 (SM7=0)
3. TRANSMIT THE DATA SELECTED BY SM09 AND SM10 (SM8=0) OR
TRANSMIT THE RECEIVED DATA (SM8=1)
4. RETURNS TO MONITOR FOR "END PASS" (SM4=1) OR
GO TO STEP 1. (SM4=0)

EXTERNAL LOOPBACK MODE

1. THE OVERLAY SETS REQUEST TO SEND
2. WAIT FOR CLEAR TO SEND
3. TRANSMITS THE SELECTED DATA
4. RESETS REQUEST TO SEND
5. WAIT FOR MESSAGE TO BE RECEIVED
6. VERIFIES THE DATA (SM7=0)
7. RETURNS TO MONITOR FOR "END PASS". (SM4=1) OR
GO TO STEP 1 (SM4=0)

ONE-WAY-IN MODE

1. THE OVERLAY WAITS FOR MESSAGE TO BE RECEIVED.
2. VERIFIES THE DATA (SM7=0)
3. RETURNS TO MONITOR FOR "END PASS" (SM4=1) OR
GO TO STEP 1 (SM4=0)

ONE-WAY-OUT MODE

1. THE OVERLAY SETS REQUEST TO SEND
2. WAITS FOR CLEAR TO SEND
3. TRANSMITS SELECTED DATA
4. RETURNS TO MONITOR FOR "END PASS". (SM4=1) OR
GO TO STEP 1 (SM4=0)

E. THE OVERLAY IS THEN ENTERED AND A CONNECTION ESTABLISHED EITHER MANUALLY OR AUTOMATICALLY.

IF ONE-WAY-IN OR INTERNAL LOOPBACK MODES ARE SELECTED.
THE OVERLAY WILL SET DATA TERMINAL READY AND WAIT FOR DATA.

IF ONE-WAY-OUT OR EXTERNAL LOOPBACK MODES WERE SELECTED.
THE OVERLAY WILL SET DATA TERMINAL READY AND REQUEST TO SEND.
THE OVERLAY WILL THEN WAIT FOR CLEAR TO SEND BEFORE ATTEMPTING TO
TRANSMIT DATA.

THE PROGRAM WILL PRINTOUT A "WAITING FOR CLEAR TO SEND"
MESSAGE AND THE CONTENTS OF THE XMIT CSR EVERY 60 SECS.
UNTIL CLEAR TO SEND IS ASSERTED.

F. IF SMD4=0 THE OVERLAY WILL CONTINUE TO TRANSMIT/RECEIVE DATA.

IF SMD4=1 THE OVERLAY WILL RETURN TO THE MONITOR AND TYPE "END PASS".

IF BOTH SMD4=1 AND SW14=1, THE PROGRAM WILL REQUEST NEW INTERFACE PARAMS AFTER ONE PASS OF THE SELECTED TEST MODE.

TEST EXECUTION MAY BE INTERRUPTED BY TYPING THE FOLLOWING CHARACTERS ON THE CONSOLE TTY.

LINE FEED = RESTART PROGRAM AT LOCATION 200.

QUESTION MARK = PRINTOUT FIRST 8 WORDS OF INPUT BUFFER. (ASCII)

THEN TYPE EITHER:

#D00000X

TO PRINTOUT THE 8 WORDS AT LOC X0000X.

#B00000X

TO PRINTOUT THE 16 BYTES AFTER LOC X0000X.

#C

TO CONTINUE

PROGRAM MUST BE RESTARTED AT 200 AFTER PRINTING.

CARRIAGE RETURN = RESTART AT REQUEST FOR NEW OPERATIONAL SWITCHES.

5.0 PROGRAM AND/OR OPERATOR ACTION

IF THE OPERATOR WISHES TO MANUALLY EXAMINE THE TRANSMIT OR RECEIVE BUFFERS, DO THE FOLLOWING: TO FIND THE STARTING ADDRESS OF THE RECEIVE BUFFER, LOAD ADDRESS 11020 AND EXAMINE. TO FIND THE STARTING ADDRESS OF THE TRANSMIT BUFFER, LOAD ADDRESS 11022 AND EXAMINE.

5.1 NORMAL HALTS SEE SECTION 4.

6.0 ERRORS

6.1 ERROR REPORTING

THE ONLY ERROR REPORT FROM THE CONTROL PROGRAM OCCURS IF THE INTERFACE SPECIFIED IS NOT LOADED.

IF DATA IS RECEIVED AND SWITCH 7 (NO DATA COMPARE) IS RESET, THE DATA WILL BE COMPARED AGAINST THE PRESELECTED DATA AFTER A LINE FEED CHARACTER IS RECEIVED. IF THERE IS A MISMATCH, THE FOLLOWING ERROR REPORT IS PRINTED:

RECEIVED DATA=RRRRR
DATA SHOULD BE TTTTTT
DATA COMPARE ERROR; BAD DATA=BBB GOOD DATA=GGG

WHERE RRRRRR IS THE RECEIVE BUFFER (UP TO 512 CHARACTERS)
 TTTTTT IS THE TRANSMIT BUFFER (UP TO 512 CHARACTERS)
 BBB IS THE BAD DATA CHARACTER
 GGG IS THE GOOD DATA CHARACTER

IF THE INTERFACE DETECTS A DATA ERROR, THE FOLLOWING
 WILL BE PRINTED BEFORE THE DATA IS COMPARED:

THERE WAS A RECEIVER ERROR. RECEIVER DATA REGISTER =XXXXXX

WHERE XXXXXX IS THE CONTENTS OF THE RECEIVER DATA REGISTER
 THE LOW BYTE IS THE DATA, AND THE HIGH BYTE IS THE ERROR BITS.

IF A RECEIVE TERMINATING CHARACTER(DD1) IS NOT DETECTED
 WITHIN 512 CHARACTERS A "BUFFER FULL" PRINTOUT WILL OCCUR.

7.0 RESTRICTIONS

THE OPERATION OF THIS PROGRAM REQUIRES COORDINATION BETWEEN
 THE OPERATOR AND THE OPERATOR OF ANOTHER PDP-11 SYSTEM
 UNLESS ONE OF THE SYSTEMS IS ALWAYS OPERATING IN A FIXED
 MODE. THE FOLLOWING TABLE LISTS THE VALID COMBINATIONS:

CPU #1	CPU #2
ONE-WAY-OUT	ONE-WAY-IN
ONE-WAY-IN	ONE-WAY-OUT
EXTERNAL-LOOPBACK	INTERNAL-LOOPBACK
INTERNAL-LOOPBACK	EXTERNAL-LOOPBACK
EXTERNAL-LOOPBACK	EXTERNAL-LOOPBACK (FULL DUPLEX)

WHEN THE COMMUNICATION LINK INVOLVES MODEMS THE FOLLOWING
 RESTRICTION APPLY:

IF RUNNING IN FULL DUPLEX MODE BOTH SYSTEMS
 MUST BE IN EXTERNAL LOOP BACK MODE.

BOTH SYSTEMS SHOULD BE RUNNING IDENTICAL ROUTINES.

EXAMPLE:
 SWITCHES 14,13,7,4 SHOULD BE THE SAME
 ON BOTH CPU'S

IF PROGRAM IS WAITING IN A SCAN ROUTINE AND TYPES OUT
 A "WAITING MESSAGE", IF AN INCOMING MESSAGE STARTS DURING
 THE TYPE OUT, IT WILL BE LOST BECAUSE THE TYPEOUT PRIORITY
 IS AT LEVEL 7. THIS WILL RESULT IN OVERRUN OR SILO OVER-
 RUN ERRORS, DEPENDING ON THE DEVICE. TO AVOID THIS SITUATION
 RUN WITH SWITCH 13 UP. IF OVERRUN DOES OCCURE DURING A
 TYPEOUT THE PROGRAM SHOULD BE RESTARTED.

IF USING AN ASYNCHRONOUS DEVICE, MODEMS AND THE
 MAYNARD TEST STATION AND INITIALIZE DOES NOT CLEAR THE
 CONNECTION (EXAMPLE THE DQ11) IF THE PROGRAM IS RESTARTED
 IN THE MIDDLE OF A MESSAGE AT LOC 204 OR BY HITTING CR
 AN IMMEDIATE ERROR MESSAGE FROM MAYNARD WILL BE RE-

CEIVED. THIS IS BECAUSE THE TEST STATION IS STILL LOOKING FOR THE REST OF THE INTERRUPTED MESSAGE. TO AVOID THIS ERROR, RESTART PROGRAM ONLY AT THE END OF THE MESSAGE CURRENTLY BEING TRANSMITTED.

8.0 MISCELLANEOUS

ITEP WAS CHECKED OUT USING THE FOLLOWING BELL TELEPHONE MODEMS.
201A (HALF-DUPLEX SYNCHRONOUS 2000 BAUD)
202C (HALF-DUPLEX ASYNCHRONOUS 1200 BAUD)
103A (FULL-DUPLEX ASYNCHRONOUS 110 BAUD)

9.0 PROGRAM DESCRIPTION

9.1 THE D011 INTERFACE SERVICE PARAMS ARE SETUP, AS SPECIFIED BY THE OPERATOR, BY THE ITEP CONTROL PROGRAM.

TIME: PROVIDES A MEANS OF MEASURING ELAPSED TIME. IT IS INCREMENTED EVERY SECOND BY A CLOCK INTERRUPT ROUTINE IN ITEP.

9.2 WHEN THE OVERLAY IS FIRST ENTERED BY ITEP AT LOCATION START:, THE CONTENTS OF THE SWITCH REGISTER ARE STORED IN REGISTER 0. THE MODE AND DATA SELECTIONS ARE FIXED AT THIS TIME AND CANNOT BE ALTERED WITHOUT RETURNING TO THE CONTROL PROGRAM. THE INTERRUPT VECTORS AND VARIABLES ARE THEN SETUP. THE SELECTED ROUTINE DETERMINED BY THE MODE IS THEN ENTERED

9.3 THE OVERLAY THEN LOOPS IN ROUTINES: SOWI, IF "ONE WAY IN" MODE WAS SELECTED. SOWO, IF "ONE WAY OUT" MODE WAS SELECTED. SILB, IF "INTERNAL LOOP BACK" MODE WAS SELECTED. SXLB, IF "EXTERNAL LOOP BACK" WAS SELECTED.

9.31 SOWI: IN THIS ROUTINE THE RECEIVER IS INITIALIZED AND PROGRAM LOOPS WAITING FOR THE RECEIVER TO FINISH. IF NOTHING IS RECEIVED FOR 60 SECS A "WAITING" MESSAGE IS TYPED. WHEN THE RECEIVER IS DONE, THE PROGRAM CHECKS DATA IF SWITCHES PERMIT, AND TYPES END PASS DEPENDING ON SWITCH SETTINGS.

9.32 SOWO: THE TRANSMITTER IS INITIALIZED AND PROGRAM LOOPS WAITING FOR TRANSMITTER TO FINISH. A "WAITING" MESSAGE IS TYPED EVERY 60 SECS IF THERE IS NO ACTION. WHEN THE TRANSMITTER IS DONE, THE PROGRAM EITHER LOOPS BACK TO SOWO OR TYPES END PASS DEPENDING ON SWITCH SETTINGS.

9.33 SILB: THE RECEIVER IS INITIALIZED AND PROGRAM LOOPS WAITING FOR RECEIVER TO FINISH. A "WAITING" MESSAGE IS TYPED EVERY 60 SEC IF NO ACTION. WHEN RECEIVER IS DONE PROGRAM CHECKS DATA IF SWITCH SETTINGS PERMIT, AND END PASS IS TYPED IF SWITCH SETTINGS PERMIT. THEN THE TRANSMITTER IS INITIALIZED, A "WAITING" MESSAGE IS TYPED EVERY 60 SEC IF NO ACTION. WHEN TRANSMITTER IS DONE PROGRAM RETURNS TO START OF ROUTINE. (SILB)

9.34 SXLB: IF IN HALF DUPLEX THE TRANSMITTER IS INITIALIZED, A "WAITING MESSAGE IS TYPED EVERY 60 SEC IF THERE IS NO ACTION

WHEN THE TRANSMITTER IS DONE THE RECEIVER IS INITIALIZED
A "WAITING" MESSAGE IS TYPED EVERY 60 SEC IF THERE IS NO ACTION.
WHEN THE RECEIVER IS DONE, DATA IS CHECKED IF SWITCH SETTINGS
PERMIT AND END PASS IS TYPED IF SWITCHES ALLOW. THE PROGRAM NOW
REPEATS CYCLE STARTING AT \$XLB.
IF IN FULL DUPLEX THE RECEIVER AND TRANSMITTER ARE INITIALIZED
A "WAITING" MESSAGE IS TYPED EVERY 60 SEC IF THERE IS NO
ACTION. WHEN BOTH THE RECEIVER AND TRANSMITTER ARE DONE, DATA IS
CHECKED, END PASS IS TYPED AND PROGRAM LOOPS TO \$XLB DEPENDING
ON THE SWITCH SETTINGS.

- 9.4 THE RETURN TO MONITOR ROUTINE FOR END PASS AT EOP:
LOCKS OUT INTERRUPTS AND SAVES THE TRANSMITTER INTERRUPT ENABLE
BIT AND ALL GENERAL REGISTERS. IT THEN RETURNS TO THE MONITOR
TO TYPE "END PASS". THE MONITOR CHECKS SW14 IF UP IT RETURNS
TO ENTER:, OTHERWISE IT RESTARTS THE PROGRAM.
- 9.5 ENTER: IS ENTERED FROM THE MONITOR AFTER TYPEING "END PASS",
IT RESTORES THE GENERAL REGISTERS AND THE TRANSMITTER CSR
AS SAVED IN EOP. THE DELAY FLAG IS SET AND PROGRAM RETURNS TO
THE SCAN ROUTINE(OWO,OWI,ILB,XLB) WHERE IT CAME FROM.
- 9.6 THE INITIALIZE TRANSMIT SUBROUTINE AT STARTX:
SETS UP THE INTERFACE AND POINTERS NECESSARY TO
INITIATE A TRANSMIT OPERATION.
AFTER SETTING "DATA TERMINAL READY" AND "REQUEST TO SEND" A CHECK
IS MADE ON PARAM2 TO DETERMINE IF HALF DUPLEX OPERATION
WAS SELECTED BY THE OPERATOR. IF IT WAS, THE
SUBROUTINE WAITS FOR CLEAR TO SEND.
A 'WAITING FOR CLEAR TO SEND' PRINTOUT OCCURS
EVERY 30 SECONDS UNTIL CLEAR TO SEND IS ASSERTED.
- 9.7 THE INITIALIZE RECEIVED SUBROUTINE AT STARTR:
SETS UP THE INTERFACE AND POINTERS NECESSARY TO
RECEIVE A MESSAGE.
- 9.8 THE TRANSMIT INTERRUPT SERVICE ROUTINE,
AT XISR:, IS ENTERED VIA TRANSMIT INTERRUPTS
FROM THE INTERFACE.
A TEST IS MADE TO SEE IF THE LAST CHARACTER
TRANSMITTED WAS A NULL (ALL ZEROS) CHARACTER.
IF IT WAS: THE TRANSMIT LOGIC IN THE INTERFACE
IS RESET AND THE TRANSMIT COMPLETE FLAG IS SET.
AT XISR1: THE NEXT CHARACTER IS TRANSMITTED
AND PRINTED ON THE TTY IF THE MONITOR TRANSMIT
SWITCH IS SET.
- 9.9 THE RECEIVE INTERRUPT SERVICE ROUTINE
AT RISR:, IS ENTERED VIA RECEIVER INTERRUPTS
FROM THE INTERFACE.
THE RECEIVED CHARACTER IS STORED IN
THE INPUT BUFFER AND PRINTED ON THE TTY IF
THE MONITOR RECEIVER SWITCH IS SET.
IF THE INPUT BUFFER IS FULL, A 'BUFFER FULL'
PRINTOUT WILL OCCUR. THIS INDICATES THAT A
LINE FEED CHARACTER WAS NOT RECOGNIZED

IN THE RECEIVED DATA (WITHIN 1000 CHARACTERS).
IF THE RECEIVED CHARACTER IS A LINE FEED,
THE RECEIVED LOGIC IS RESET AND THE
RECEIVE COMPLETE FLAG IS SET.
IF A 'RECEIVE ERROR' IS DETECTED AT RISR:, THE
CSR AND DBR WILL BE SAVED AND PRINTED OUT
AFTER THE COMPLETE MESSAGE HAS BEEN RECEIVED.

- 9.10 THE DATA TEST SUBROUTINE AT TESTD: IS
ENTERED AFTER A COMPLETE MESSAGE HAS BEEN
RECEIVED.
IF A 'RECEIVE ERROR' HAD BEEN DETECTED,
THE CONTENTS OF THE 'RECEIVE BUFFER' AT THE
TIME THE ERROR OCCURRED WILL BE PRINTED.
THE DATA IS COMPARED UNTIL A 'ALL ZEROS'
CHARACTER IS RECOGNIZED. 'FILL' (ALL ONES)
CHARACTERS ARE IGNORED. IF A MISMATCH
IS DETECTED, THE COMPLETE CONTENTS OF THE
INPUT BUFFER AND GOOD DATA IS PRINTED.

DQ11 RESTRICTIONS

THE DQ11 HAS TWO MODES OF OPERATION IN ITEP, NORMAL (WHICH
INTERUPTS EVERY TWO CHARACTERS), AND HI-BAUD (ONE INTERUPT PER
MESSAGE, THE WHOLE MESSAGE IS TRANSMITTED OR RECEIVED ON A
COMPLETE WORD COUNT.). WHEN IN HI-BAUD MODE, DATA CANNOT BE
MONITORED ON THE CONSOLE TTY. IF SW 5=1 (MONITOR RECEIVED DATA)
AN "R" WILL BE TYPED AFTER THE WHOLE MESSAGE IS RECEIVED.
IF SW 6=1 (MONITOR TRANSMIT DATA) A "T" WILL BE TYPED AFTER THE
WHOLE MESSAGE IS TRANSMITTED. IN NORMAL MODE, EVEN IF THE CONSOLE
TTY IS FASTER OR THE SAME BAUD AS THE DQ11, NOT EVERY CHARACTER
CAN BE TYPED BECAUSE OF THE TWO CHARACTER PER INTERUPT
OPERATION. IN THIS CASE EVERY OTHER CHARACTER WILL BE TYPED
IF DATA MONITORING IS SELECTED. HI-BAUD MODE (SELECTABLE IN PARAM#2)
SHOULD BE USED FOR BAUDS OF 40,000 OR HIGHER. NORMAL MODE IS
SUFFICIENT FOR BAUDS LOWER THAN 40,000 .

10.0 PARAMETERS FOR THE DQ11

PARAM#1 IS NOT USED (0)

PARAM#2 (LOW BYTE)

BIT 0
BIT 1

FULL DUPLEX (1), DEFAULT= HALF DUPLEX (0)

HI-BAUD (1), DEFAULT= NORMAL (0)

(BITS 0,1 ARE NOT LOADED INTO ANY DQ11 REGISTERS, THEY ARE ONLY SOFTWARE FLAGS)

PARAM#2 (HIGH BYTE) IS LOADED INTO THE SYNC REGISTER.

BITS 8-15 SYNC CHARACTER, DEFAULT= 26 (26)

PARAM#3 IS NOT USED (177777)

NO1

DQ11 ITEP OVERLAY MACY11 27(1006) 29-OCT-76 14:38 PAGE 14
DZDQ08.P11 05-AUG-76 09:23

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599
600
601
602
603
604 011000 011000
605 011004 160010 000040
606 011006 000300
607 011010 000240
608 011012 000000
609 011014 013000
610 011016 177777
611 011020 000000
612 011022 000000
613 011024 000000
614 011026 000000
615 011030 000000
616 011032 000000
617 011034 000000
618 011036 011106
619 011040
620 011040 000
621 011041
622 011041 001
623 011042 000000
624 011044 177570
625 011046 177570
626
627
628
629
630
631 000000
632 100000
633 040000
634 020000
635 020000
636
637 011050 000000
638 011052 000000
639 011054 000000
640 011056 000000
641 011060 000000
642 011062 000000
643 011064 000000
644 011066 000000
645 011070 000000
646 011072 000000
647 011074 000000
648
649 011076 177560
650 011100 177562
651 011102 177564
652 011104 177566
653
654 000001

```

```

*****
DQ11 INTERFACE SERVICE PARAMS
*****

```

```

DQ11: .=11000
BA: .ASCIZ /DQ /
RIV: 300
PRIOR: 240
PARAM1: 0
PARAM2: 013000
PARAM3: 177777
IRDA: .WORD 0
IXDP: .WORD 0
SET/LE: .WORD 0
B2016: .WORD 0
TIME: .WORD 0
TX.TERM: .WORD START
RX.TERM: .BYTE 000
FLAG: .BYTE 001
SMR: .WORD 0
DISPLAY: 177570

```

```

:ISR NAME
:BUS ADDRESS
:VECTOR ADDRESS
:PRIORITY
:PARAM #1
:PARAM #2
:PARAM #3
:INITIAL READ DATA ADDRESS
:INITIAL XMIT DATA ADDRESS
:LINE SETTLE DELAY FLAG
:ADDR OF BIN TO OCT TYPE ROUTINE
:TIMER
:ADDR OF START OF PROGRAM
:TRANSMITTER TERMINATING CHAR.
:RECEIVER TERMINATING CHAR.

```

```

*****
CONSTANTS + WORKING STORAGE
*****

```

```

STAT=00
XFLG=100000
RFLG=40000
DSFLG=20000
BIT13=20000

```

```

: XMIT COMPLETE FLAG
: RCV COMPLETE FLAG
: DATA SET STATUS CHANGE FLAG
: INHIBIT PRINTOUTS

```

```

SXCSR: 0
SRCSR: 0
ERCSR: 0
ERDR: 0
DSSTAT: 0

```

```

: SAVED XMIT CSR
: SAVED RCV CSR
: RCV CSR SAVED ON ERROR
: RCV DATA REG SAVED ON ERROR
: RCV CSR SAVED ON DS CHANGE

```

```

TXMC: 0
RXMC: 0
XCC: 0
RCC: 0
RDA: 0
XDA: 0

```

```

: XMIT CHAR COUNT
: RCV CHAR COUNT
: RCV DATA ADDR.
: XMIT DATA ADDR.

```

```

TKS: 177560
TKB: 177562
TPS: 177564
TPB: 177566
FULL.DUPLEX=000001

```

```

655
656
657
658 011106 000240
659 011110 017700 177730
660 011114 042700 177400
661 011120 013702 011006
662 011124 012722 015012
663 011130 013722 011010
664 011134 012722 014170
665 011140 013722 011010
666 011144 013704 011004
667 011150 013714 011012
668 011154 013702 011014
669 011160 042702 000001
670 011164 010264 000002
671 011170 005014
672 011172 005064 000002
673 011176 005064 000004
674 011202 012703 000020
675 011206 052764 010000 000004 1S:
676 011214 042764 060000 000004
677 011222 005064 000006
678 011226 105264 000005
679 011232 005303
680 011234 001364
681 011236 112764 000012 000005
682 011244 012764 000040 000006
683 011252 005037 014160
684 011256 052764 000010 000002
685 011264 005037 011032
686 011270 005037 013246
687
688
689
690
691
692
693
694
695 011274
696 011274 013702 011022
697 011300 005003
698 011302 005203
699 011304 123722 011040
700 011310 001374
701 011312 005403
702 011314 010337 011062
703 011320 013702 011022
704 011324 005003
705 011326 005203
706 011330 123722 011041
707 011334 001374
708 011336 010302
709 011340 005403
710 011342 010337 011064

```

```

*****
D011-X INTERFACE SERVICE ROUTINE
*****
START:  NOP
        MOV     2SWR, R0      ;SETUP MODE IN R0
        BIC     2177400, R0   ;STRIP JUNK
        MOV     RIV, R2      ;SETUP
        MOV     2RISR, (R2)+  ;INTERRUPT
        MOV     PRIOR, (R2)+ ;VECTORS
        MOV     2XISR, (R2)+
        MOV     PRIOR, (R2)+
        MOV     BA, R4       ;SETUP BUS ADDR INDEX
        MOV     PARAM1, 2RCSR ;SETUP VARIABLES
        MOV     PARAM2, R2
        BIC     20001, R2
        MOV     R2, XCSR(R4) ;IN CSR'S
        CLR     2RCSR        ;CLEAR THE RX CSR
        CLR     XCSR(R4)    ;CLEAR TX CSR
        CLR     ERR(R4)     ;CLEAR THE ERROR REGISTER
        MOV     216, R3     ;SET COUNTER
        BIS     2BIT12, ERR(R4) ;SET WRITE ENABLE
        BIC     260000, ERR(R4) ;CLEAR EXT MEM BITS
        CLR     SEC(R4)     ;CLEAR THE SECONDARY REGISTER
        INCB   REG(R4)     ;GET NEXT REGISTER
        DEC    R3          ;DONE YET??
        BNE    1S         ;KEEP CLEARING
        MOVB   212, REG(R4) ;SELECT THE MISC REGISTER
        MOV    2BIT5, SEC(R4) ;ISSUE A MASTER CLEAR.
        CLR    ERRORS
        BIS    2BIT3, XCSR(R4) ;ENABLE ERROR INTERUPTS
        CLR    TIME       ;RESET TIMER
        CLR    DELAY      ;RESET DELAY INDICATOR.

```

```

;ROUTINE TO FIGURE RX AND TX WORD COUNTS.
;FOR HIGH BAUD THE TX AND RX MESSAGES MUST BE IDENTICAL
;THE DATA WILL BE TRANSFERED AT A BURST MODE.
;THE TXMC AND RXMC SET FOR HOW MANY CHARS TO DEAL WITH.

```

```

X.X:
      MOV     IXDA, R2
      CLR    R3
1S:   INC    R3
      CMPB   TX.TERM, (R2)+
      BNE    1S
      NEG   R3
      MOV   R3, TXWC
      MOV   IXDA, R2
      CLR  R3
2S:   INC   R3
      CMPB RX.TERM, (R2)+
      BNE  2S
      MOV  R3, R2
      NEG  R3
      MOV  R3, RXWC

```

```

711
712
713
714
715
716
717
718
719
720 011346 005037 011032
721 011352 005037 013246
722 011356 005037 013252
723 011362 032700 000001
724 011366 001402
725 011370 000137 011544
726 011374 032700 000002
727 011400 001402
728 011402 000137 011436
729 011406 032700 000010
730 011412 001402
731 011414 000137 011642
732 011420 032700 000004
733 011424 001402
734 011426 000137 012072
735 011432 000000
736 011434 000776

```

```

*****
ROUTINE USED TO GOTO
SUBROUTINE DEPENDENT
ON MODE SELECTED.
*****

```

```

GO:   CLR   TIME
      CLR   DELAY
      CLR   STOP
      BIT   @OWO,MODE
      BEQ   15
      JMP   SOWO
15:   BIT   @OWI,MODE
      BEQ   25
      JMP   SOWI
25:   BIT   @ILB,MODE
      BEQ   35
      JMP   $ILB
35:   BIT   @XLB,MODE
      BEQ   45
      JMP   $XLB
45:   HALT
      BR    -2

```

```

737
738
739
740
741
742
743
744
745
746
747
748
749
750
751 011436 104416
752 011440 004737 014516
753 011444 032700 040000
754 011450 001013
755 011452 023727 011032 000100
756 011460 103771
757 011462 011402
758 011464 016403 000002
759 011470 104001
760 011472 005037 011032
761 011476 000762
762
763 011500 032777 000200 177336
764 011506 001002
765 011510 004737 012462
766 011514 042700 040000

```

```

*****
ROUTINE USED IF "ONE WAY IN" MODE WAS SELECTED.
NOTE THAT WHEN IN THIS MODE HALF DUPLEX IS THE
ONLY MODE AVAILABLE.
"ONE WAY IN" MEANS THAT ONLY THE RECEIVER IS
ENABLED. THE TRANSMITTER IS NEVER "TURNED ON".
*****

```

```

SOWI: KBDIN
15:   JSR   PC,STARTR
      BIT   @RFLG,STAT
      BNE   25
      CMP   TIME,@100
      BLO   15
      MOV   @RCSR,R2
      MOV   @XCSR(R4),R3
      HLT   1
      CLR   TIME
      BR    15
25:   BIT   @NODAT,@SWR
      BNE   35
      JSR   PC,TESTD
35:   BIC   @RFLG,STAT

```

767	011520	032777	000020	177316		BIT	#LOOP,JSWR
768	011526	001405				BEQ	45
769	011530	012737	011542	013250		MOV	#45,BACK
770	011536	000137	012322			JMP	EOP
771	011542	000735			45:	BR	SOWI

```

*****
ROUTINE USED IF "ONE WAY OUT" WAS SELECTED.
NOTE THAT WHEN IN THIS MODE HALF DUPLEX IS THE ONLY
MODE AVAILABLE.
"ONE WAY OUT" MEANS THAT ONLY THE TRANSMITTER IS
ENABLED. THE RECEIVER IS NEVER "TURNED ON."
*****

```

781							
782	011544	104416			SOWO:	KBDIN	
783	011546	004737	013254			JSR	PC,STARTX
784	011552	005037	011032			CLR	TIME
785	011556	032700	100000		15:	BIT	#XFLG,STAT
786	011562	001013				BNE	25
787	011564	023727	011032	000100		CMF	TIME,#100
788	011572	103771				BLO	15
789	011574	011402				MOV	2RCSR,R2
790	011576	016403	000002			MOV	XCSR(R4),R3
791	011602	104001				HLT	1
792	011604	005037	011032			CLR	TIME
793	011610	000762				BR	15
794	011612	042700	100000		25:	BIC	#XFLG,STAT
795	011616	032777	000020	177220		BIT	#LOOP,JSWR
796	011624	001405				BEQ	35
797	011626	012737	011640	013250		MOV	#35,BACK
798	011634	000137	012322			JMP	EOP
799	011640	000741			35:	BR	SOWO
800							
801							
802							

```

803
804
805
806
807
808
809
810
811
812
813
814 011642 104416
815 011644 004737 014516
816 011650 005037 011032
817 011654 032700 040000
818 011660 001013
819 011662 023727 011032 000100
820 011670 103771
821 011672 011402
822 011674 016403 000002
823 011700 104001
824 011702 005037 011032
825 011706 000762
826 011710 032777 000200 177126
827 011716 001002
828 011720 004737 012462
829 011724 042700 040000
830 011730 032777 000020 177106
831 011736 001405
832 011740 012737 011752 013250
833 011746 000137 012322
834 011752 032777 000400 177064
835 011760 001416
836 011762 013702 011020
837 011766 013703 011022
838 011772 010337 011074
839 011776 112223
840 012000 001376
841 012002 112743 000177
842 012006 005203
843 012010 112723 000177
844 012014 105623
845 012016 005037 011032
846 012022 004737 013254
847 012026 032700 100000
848 012032 001013
849 012034 023727 011032 000100
850 012042 103771
851 012044 011402
852 012046 016403 000002
853 012052 104001
854 012054 005037 011032
855 012060 000762
856 012062 042700 100000
857 012066 000137 011642

```

```

*****
ROUTINE USED IF INTERNAL LOOP BACK" WAS SELECTED.
NOTE THAT WHEN IN THIS MODE; HALF DUPLEX IS THE
ONLY MODE AVAILABLE.
"INTERNAL LOOP BACK" MEANS THAT THE RECEIVER IS "TURNED ON"
AND A COMPLETE MESSAGE IS RECEIVED. IF DATA IS TO BE CHECKED
IT IS; IF "END PASS" IS DESIRED; IT IS GIVEN.
THEN THE TRANSMITTER IS ENABLED. AFTER THE WHOLE MESSAGE
IS TRANSMITTED; THE CYCLE IS REPETED AS ABOVE.
*****

```

```

SILB:  KBDIN
        JSR  PC,STARTR
        CLR  TIME
1S:    BIT  @RFLG,STAT
        BNE  2S
        CMP  TIME,#100
        BLO  1S
        MOV  @RCSR,R2
        MOV  XCSR(R4),R3
        HLT  1
        CLR  TIME
        BR   1S
2S:    BIT  @NODAT,@SWR
        BNE  3S
        JSR  PC,TESTD
        BIC  @RFLG,STAT
        BIT  @LOOP,@SWR
        BEQ  4S
        MOV  @4S,BACK
        JMP  EOP
4S:    BIT  @400,@SWR
        BEQ  7S
        MOV  IRDA,R2
        MOV  IXDA,R3
        MOV  R3,XDA
        MOVB (R2)+,(R3)+
        BNE  5S
        MOVB @177,-(R3)
        INC  R3
        MOVB @177,(R3)+
        CLRB (R3)+
7S:    CLR  TIME
        JSR  PC,STARTX
5S:    BIT  @XFLG,STAT
        BNE  6S
        CMP  TIME,#100
        BLO  5S
        MOV  @RCSR,R2
        MOV  XCSR(R4),R3
        HLT  1
        CLR  TIME
        BR   5S
6S:    BIC  @XFLG,STAT
        JMP  SILB

```

```

:USE EXTERNAL DATA?
:BR IF NO
:SET POINTER
:SET POINTER
:SETUP XMIT DATA ADDR
:MOVE INPUT TO OUTPUT
:LOOP IF NOT ZERO CHAR
:INSERT A FILL CHAR
:BUMP ADDRESS
:INSERT ANOTHER FILL
:INSERT ZERO CHAR

```

```

858
859
860
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862
863
864
865
866
867
868
869
870
871 012072 104416
872 012074 032737 000001 011014
873 012102 001402
874 012104 004737 014516
875 012110 004737 013254
876 012114 005037 011032
877 012120 032700 100000
878 012124 001016
879 012126 032700 040000
880 012132 001024
881 012134 023727 011032 000100
882 012142 103766
883 012144 011402
884 012146 016403 000002
885 012152 104001
886 012154 005037 011032
887 012160 000757
888 012162 032737 000001 011014
889 012170 001356
890 012172 042700 100000
891 012176 004737 014516
892 012202 000746
893 012204 032737 000001 011014
894 012212 001420
895 012214 032700 100000
896 012220 001013
897 012222 023727 011032 000100
898 012230 103765
899 012232 011402
900 012234 016403 000002
901 012240 104001
902 012242 005037 011032
903 012246 000756
904 012250 042700 100000
905 012254 042700 040000
906 012260 005037 011032
907 012264 032777 000200 176552
908 012272 001002
909 012274 004737 012462
910 012300 032777 000020 176536
911 012306 001671
912 012310 012737 012072 013250
913 012316 000137 012322

```

```

*****
ROUTINE USED IF "EXTERNAL LOOP BACK" WAS SELECTED.
EITHER HALF OR FULL DUPLEX MAY BE SELECTED IN THIS MODE.
"EXTERNAL LOOP BACK" MEANS THAT THE TRANSMITTER IS FIRST
TURNED ON (IF HALF DUPLEX) AND THE WHOLE MESSAGE IS TRANSMITTED;
THEN THE RECEIVER IS ENABLED. AFTER THE WHOLE MESSAGE IS RECEIVED
DATA WILL THEN BE CHECKED IF DESIRED AND END PASS WILL
BE GIVEN IF DESIRED. THEN THE CYCLE IS REPEATED
AS ABOVE. IF RUNNING IN FULL DUPLEX THE PROGRAM
WAITS FOR BOTH THE RECEIVER AND TRANSMITTER TO
FINISH THEN RESTARTS THE RECEIVER AND TRANSMITTER.
*****

```

```

$XLB: KBDIN
      BIT      #FULL.DUPLEX,PARAM2
      BEQ      1$
      JSR      PC,STARTR
      JSR      PC,STARTX
      CLR      TIME
      1$:     BIT      #XFLG,STAT
      2$:     BNE      3$
      7$:     BIT      #RFLG,STAT
      BNE      4$
      CMP      TIME,#100
      BLO      2$
      MOV      @RCSR,R2
      MOV      XCSR(R4),R3
      HLT      1
      CLR      TIME
      BR       2$
      3$:     BIT      #FULL.DUPLEX,PARAM2
      BNE      7$
      BIC      #XFLG,STAT
      JSR      PC,STARTR
      BR       2$
      4$:     BIT      #FULL.DUPLEX,PARAM2
      BEQ      8$
      BIT      #XFLG,STAT
      BNE      6$
      CMP      TIME,#100
      BLO      4$
      MOV      @RCSR,R2
      MOV      XCSR(R4),R3
      HLT      1
      CLR      TIME
      BR       4$
      6$:     BIC      #XFLG,STAT
      8$:     BIC      #RFLG,STAT
      CLR      TIME
      BIT      @NO DAT,@SMR
      BNE      5$
      JSR      PC,TESTD
      5$:     BIT      @LOOP,@SMR
      BEQ      $XLB
      MOV      #XLB,BACK
      JMP      EOP

```

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1000

```
012322 104414 000340  
012322 016437 000002 012460  
012326 042737 177737 012460  
012334 042764 000040 000002  
012342 012766 012410 000002  
012350 010037 013232  
012356 010137 013234  
012362 010237 013236  
012368 010337 013240  
012374 010437 013242  
012380 010537 013244  
012406 000207  
  
012410 013700 013232  
012410 013701 013234  
012414 013702 013236  
012418 013703 013240  
012422 013704 013242  
012426 013705 013244  
012430 012737 177777 013246  
012434 053764 012460 000002  
012438 000177 000570  
012460 000000
```

```
*****  
ROUTINE TO RETURN  
TO MONITOR FOR  
END PASS.  
*****
```

```
EOP: STPS, PRTY7 ; SET PS PRIORITY TO 7  
MOV XCSR(R4), 0TPIE ; SAVE TX CSR  
BIC 81C<TIE>, 0TPIE ; CLEAR ALL BUT TX IE  
BIC 0TIE, XCSR(R4) ; CLEAR TX IE (EVEN IF IT WASN'T SET)  
MOV BENTER, 2(SP) ; SET FOR RETURN IF SW 14=1  
MOV R0, SAVR0 ; SAVE REGISTER 0  
MOV R1, SAVR1 ; SAVE REGISTER 1  
MOV R2, SAVR2 ; SAVE REGISTER 2  
MOV R3, SAVR3 ; SAVE REGISTER 3  
MOV R4, SAVR4 ; SAVE REGISTER 4  
MOV R5, SAVR5 ; SAVE REGISTER 5  
RTS PC ; RETURN TO CONTROL PROGRAM
```

```
ENTER: MOV SAVR0, R0 ; RESTORE R0  
MOV SAVR1, R1 ; RESTORE R1  
MOV SAVR2, R2 ; RESTORE R2  
MOV SAVR3, R3 ; RESTORE R3  
MOV SAVR4, R4 ; RESTORE R4  
MOV SAVR5, R5 ; RESTORE R5  
MOV 8-1, DELAY  
BIS 0TPIE, XCSR(R4) ; IF ORIGINALLY SET; SET TX IE  
JMP @BACK  
0TPIE: 000000
```

```
*****  
SUBROUTINE TO CHECK  
RECEIVER DATA.  
*****
```

```
TESTD: MOV ERDR, -(SP) ; WAS THERE A RECEIVE ERROR?  
BEQ TSTDAT ; BR IF NO  
BIT #BIT13, @SWR ; INHIBIT PRINTOUTS?  
BNE TSTDAT ; BR IF YES  
TYPE MSG0 ; <15><12> THERE WAS A RECEIVE ERROR. RBUF=  
JSR @R2016 ; PRINT CONTENTS OF RBUF  
TST -(SP)  
TYPE MSG1 ; <15><12>  
TSTDAT: MOV IXDA, R1 ; SETUP XMIT DATA ADDR  
MOV IRDA, R2 ; SETUP RCY DATA ADDR  
SCAN: CHPB (R1)+, (R2)+ ; DATA OK ?  
BEQ SCAN ; BR IF OK  
CHPB TX_TERM, -(R1) ; IS IT END OF DATA  
BEQ TESTD ; BR IF YES  
CHPB @002, -(R2)  
BNE @25  
MOV R2, 18  
TYPE
```

```

970 012554 000000 15: WORD 0
971 012555 000443 BR TESTDX
972 012556 000000 25:
973 012556 105712 TSTB (R2)
974 012556 001441 BEQ TESTDX ;BR IF YES
975 012556 122721 000177 CMPB #177, (R1)+ ;IS IT FILL CHAR?
976 012570 001756 BEQ SCAN4 ;BR IF YES
977 012572 005301 DEC R1 ;BACKUP
978 012574 122722 000177 CMPB #177, (R2)+ ;IS IT FILL?
979 012600 001756 BEQ SCAN4 ;BR IF YES
980 012602 105742 TSTB -(R2) ;BACK UP POINTER
981 012604 123722 011015 CMPB PARAM2+1, (R2)+
982 012610 001746 BEQ SCAN4 ;BR IF CHAR WAS SYNC
983 012612 000240 SCANS: NOP ;DATA ERROR
984 012614 032777 020000 176222 BIT #BIT13, 2SWR ;INHIBIT PRINTOUTS
985 012622 001016 BNE DERR ;BR IF YES
986 012624 104400 012756 104400 TYPE MSG2 ;<15><12>RECEIVED DATA = <15><12>
987 012630 013737 011020 012640 MOV IRDA, RDX ;SETUP DATA ADDRESS
988 012636 104400 TYPE ;PRINT RECEIVED DATA
989 012640 000000 RDX: 0 ;RECEIVED DATA ADDR.
990 012642 104400 013003 012656 TYPE MSG3 ;<15><12>DATA SHOULD BE<15><12>
991 012646 013737 011022 012656 MOV IXDA, .+10 ;SETUP ADDR.
992 012654 104400 TYPE ;PRINT GOOD DATA
993 012656 011022 IXDA
994 012660 111103 DERR: MOVB (R1), R3 ;SETUP XMIT DATA
995 012662 114202 MOVB -(R2), R2 ;SETUP RCV DATA
996 012664 104007 HLT+7 ;DATA ERROR HALT
997 012666 005726 TESTDX: TST (SP)+ ;POP STACK
998 012670 000207 RTS ;RETURN FROM SUB/ROUT
999
1000 012672 005015 044124 051105 MSG0: .ASCIZ <15><12>/THERE WAS A RECEIVER ERROR. REGISTER (SEL 2) =/
(1) 012753 015 000012 MSG1: .ASCIZ <15><12>
(1) 012756 005015 042522 042503 MSG2: .ASCIZ <15><12>/RECEIVED DATA = /<15><12>
(1) 013003 015 042012 052101 MSG3: .ASCIZ <15><12>/DATA SHOULD BE/<15><12>
(1) 013026 005015 046120 040505 MSG4: .ASCII <15><12>/PLEASE MAKE CONNECTION (DIAL NUMBER)./
(1) 013075 015 053412 042510 .ASCIZ <15><12>/WHEN CONNECTION COMPLETE: HIT CONTINUE SWITCH./<15><12>
(1) 013160 005015 046120 040505 MSG5: .ASCIZ <15><12>/PLEASE MAKE CONNECTION (DIAL NUMBER)./<15><12>
(1)
(1) 013232 000000 .EVEN
1001 013234 000000 SAVR0: 0
1002 013236 000000 SAVR1: 0
1003 013240 000000 SAVR2: 0
1004 013242 000000 SAVR3: 0
1005 013244 000000 SAVR4: 0
1006 013246 000000 SAVR5: 0
1007 013250 000000 DELAY: 0
1008 013252 000000 BACK: 0
1009 013254 000000 STOP: 0

```

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1020 013254 005737 013246
1021 013260 001416
1022 013262 005037 014154
1023 013266 012737 000007 014156
1024 013274 062737 000001 014154
1025 013302 001374
1026 013304 005337 014156
1027 013310 001371
1028 013312 005037 013246
1029 013316 005037 011032
1030 013322 042764 000300 000002
1031 013330 052764 000010 000002
1032 013336 032764 000004 000002
1033 013344 001403
1034 013346 004737 015366
1035 013352 000740
1036 013354 012701 000005
1037 013360 012705 014162
1038 013364 113725 011015
1039 013370 005301
1040 013372 001374
1041 013374 105015
1042
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1046 013376 112764 000012 000005
1047 013404 012764 004000 000006
1048 013412 112764 000002 000005
1049 013420 012764 014162 000006
1050 013426 105264 000005
1051 013432 012764 177773 000006
1052 013440 042700 100000
1053 013444 052764 001000 000002
1054 013452 005737 013252
1055 013456 001004
1056 013460 104400 013160
1057 013464 005137 013252
1058 013470 032764 002000 000002 16S:
1059 013476 001037
1060 013500 032764 002000 000002 8S:
1061 013506 001017
1062 013510 023727 011032 000036
1063 013516 103770
1064 013520 011402
1065 013522 016403 000002

```

```

:*****
:INITIALIZE TRANSMITTER.
:PURPOSE OF THIS ROUTINE IS TO SEND OUT FIVE
:SYNC CHARS. THE SYNC CHAR TO BE SENT IS FOUND
:IN LOCATION PARAM2+1 (HIGH BYTE OF PARAM2).
:WHEN THE SYNC CHARS. HAVE BEEN TRANSMITTED
:THE TRANSMITTER WILL BE ENABLED FOR REAL DATA
:TRANSFER. CHARACTERS WILL BE TRANSMITTED ONE AT A TIME.
:*****
STARTX: TST DELAY ;IF SW04=1 & SW14=0 WAIT BEFORE TURNING TX ON
        BEQ NDLY ;NO GO AHEAD AND TURN ON TX
        CLR TEMP1
        MOV #7,TEMP2
        ADD #1,TEMP1
        BNE -6
        DEC TEMP2
        BNE -14
        CLR DELAY ;ZERO DELAY
NDLY:   CLR TIME
        BIC #300,XCSR(R4) ;CLEAR BOTH DONE BITS FROM TX
        BIS #BIT3,XCSR(R4) ;ENABLE ERRORS
        BIT #BIT2,XCSR(R4) ;WHERE IS THE POINTER POINTING
        BEQ 1S ;BR IF PRI IS NEXT.
        JSR PC,GETPRI ;GO TOGGLE TO GET PRIMARY.
        BR STARTX ;GOTO BEGGINING
1S:     MOV #5,R1 ;SET FOR FIVE SYNC CHARS.
        MOV #SYNC,R5 ;SET POINTER LOCATION
        MOVB PARAM2+1,(R5)+ ;LOAD IN SYNC CHARS.
        DEC R1 ;ALL DONE??
        BNE -6 ;BRANCH IF NOT DONE
        CLRB (R5) ;SET LAST BYTE TO ZERO

;NOW THE SYNC CHARS ARE LOADED IN CORE FOR THE D011 TO
;PICK UP AND TRANSMIT OUT.

        MOVB #12,REG(R4) ;SELECT MISC REGISTER
        MOV #4000,SEC(R4) ;SET FOR EIGHT BITS PER CHAR.
        MOVB #2,REG(R4) ;SELECT THE TX BA PRI.
        MOV #SYNC,SEC(R4) ;LOAD TX BA PRI. WITH SYNC ADDR.
        INCB REG(R4) ;SELECT THE TX MC PRI.
        MOV #5,SEC(R4) ;SET FOR FIVE CHARS.
        BIC #XFLG,STAT ;RESET TX COMPLETE FLAG
        BIS #DTR,XCSR(R4) ;SET DATA TERMINAL READY
        TST STOP ;FIRST TIME HERE?
        BNE 16S ;BR IF NO
        TYPE ,MSG5 ;MAKE CONNECTION
        COM STOP
16S:   BIT #MRDY,XCSR(R4)
        BNE 2S
2S:    BIT #MRDY,XCSR(R4)
        BNE 3S
3S:    CMP TIME,#36 ;HAVE 30 SEC ELAPSED YET
        BLO 8S ;NO NOT YET
        MOV #ACSR,R2 ;LOAD FOR TYPEOUT
        MOV XCSR(R4),R3 ;LOAD FOR TYPEOUT

```

K02

```

1066 013526 032777 010000 175310 BIT #SM12,ASMR ;INHIBIT PRINTOUTS?
1067 013534 001001 BNE 12S ;BR IF YES
1068 013536 104002 HLT+2 ;TYPE "WAITING TO TRANSMIT" MESSAGE.
1069 013540 005037 011032 12S: CLR TIME
1070 013544 000755 BR 8S
1071 013546 005037 014154 3S: CLR TEMP1
1072 013552 012737 000005 014156 MOV #5,TEMP2
1073 013560 062737 000001 014154 ADD #1,TEMP1
1074 013566 001374 BNE -6
1075 013570 005337 014156 DEC TEMP2
1076 013574 001371 BNE -14
1077 013576 032737 000001 011014 2S: BIT #FULL.DUPLEX,PARAM2
1078 013604 001023 BNE 9S
1079 013606 032764 010000 000002 10S: BIT #10000,XCSR(R4) ;IS CARRIER UP?
1080 013614 001417 BEQ 9S
1081 013616 023727 011032 000036 CMP TIME,#36 ;30 SECONDS UP ?
1082 013624 103770 BLO 10S ;NOT YET
1083 013626 011402 MOV #ACSR,R2 ;PREPARE TYPE OUT
1084 013630 016403 000002 MOV XCSR(R4),R3 ;AS ABOVE
1085 013634 032777 010000 175202 BIT #SM12,ASMR ;INHIBIT PRINTOUTS?
1086 013642 001001 BNE 13S ;BR IF YES
1087 013644 104001 HLT ;TYPE "WAITING "
1088 013646 005037 011032 13S: CLR TIME ;ZERO TIMER
1089 013652 000755 BR 10S
1090 013654 005037 011032 9S: CLR TIME ;SET TIME=0
1091 013660 052764 000400 000002 BIS #RQTS,XCSR(R4)
1092 013666 032764 020000 000002 11S: BIT #CTS,XCSR(R4)
1093 013674 001017 BNE 6S
1094 013676 023727 011032 000036 CMP TIME,#36 ;30 SECONDS UP ??
1095 013704 103770 BLO 11S ;NOT YET
1096 013706 011402 MOV #ACSR,R2 ;PREPARE TYPE OUT
1097 013710 016403 000002 MOV XCSR(R4),R3 ;AS ABOVE
1098 013714 032777 010000 175122 BIT #SM12,ASMR ;INHIBIT PRINTOUTS?
1099 013722 001001 BNE 14S ;BR IF YES
1100 013724 104002 HLT ;TYPE "WAITING "
1101 013726 005037 011032 14S: CLR TIME ;ZERO TIMER
1102 013732 000755 BR 11S
1103
1104
1105 ;HERE NOW BECAUSE CLEAR TO SEND HAS BEEN SET
1106 ;NOW SEND THE SYNC CHARS.
1107
1108 013734 052764 000001 000002 6S: BIS #GO,XCSR(R4) ;SET THE GO BIT
1109 013742 005037 011032 CLR TIME ;SET TIME TO ZERO
1110
1111 013746 105764 000002 4S: TSTB XCSR(R4) ;IS TX DONE WITH SYNCs
1112 013752 100417 BMI 5S ;BR IF YES
1113 013754 023727 011032 000036 CMP TIME,#36 ;HAVE 30 SECOND GONE BY??
1114 013762 103771 BLO 4S ;BR IF NOT YET
1115 013764 011402 MOV #ACSR,R2 ;LOAD FOR TYPE OUT
1116 013766 016403 000002 MOV XCSR(R4),R3 ;LOAD FOR TYPE OUT
1117 013772 032777 010000 175044 BIT #SM12,ASMR ;INHIBIT PRINTOUTS?
1118 014000 001001 BNE 15S ;BR IF YES
1119 014002 104002 HLT ;TYPE "WAITING..."
1120 014004 005037 011032 15S: CLR TIME ;ZERO TIMER
1121 014010 000755 BR 4S ;GO WAIT FOR DONE

```

```

1122
1123
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1125
1126
1127 014012 112764 000002 000005
1128 014020 013764 011022 000006
1129 014026 062764 000002 000006
1130 014034 112764 000003 000005
1131 014042 012764 177776 000006
1132 014050 112764 000006 000005
1133 014056 013764 011022 000006
1134 014064 112764 000007 000005
1135 014072 012764 177776 000006
1136
1137
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1140 014100 032737 000002 011014
1141 014106 001413
1142 014110 112764 000003 000005
1143 014116 005064 000006
1144 014122 112764 000007 000005
1145 014130 013764 011062 000006
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1154 014136 042764 000300 000002
1155 014144 052764 000041 000002
1156
1157 014152 000207
1158
1159
1160
1161 014154 000000
1162 014156 000000
1163 014160 000000
1164 014162 026 026
1165 014164 026 026
1166 014166 026 026

```

```

;NOW TRANSMITTER DONE HAS SET AND THE SYNC CHARS
;HAVE BEEN SENT OUT. THE TRANSMITTER IS TO BE
;ENABLED AND THE DATA WILL BE TRANSFERED.

```

```

55:  MOVB  #2,REG(R4)      ;SELECT TX BA PRI
      MOV  IXDA,SEC(R4)   ;LOAD BA
      ADD  #2,SEC(R4)     ;POINT BA TWO HIGHER
      MOVB #3,REG(R4)     ;SELECT TX MC PRI
      MOV  #2,SEC(R4)     ;SET TWO CHARS AT A TIME
      MOVB #6,REG(R4)     ;SELECT TX BA SEC
      MOV  IXDA,SEC(R4)   ;LOAD TX BA SEC WITH DATA POINTER
      MOVB #7,REG(R4)     ;SELECT TX MC SEC
      MOV  #2,SEC(R4)     ;SET MC WITH -2

```

```

;CHECK FOR HIGH BAUD RATE.
;NOTE WELL THAT WHEN IN HIGH BAUD MESSAGES MUST BE THE SAME.

```

```

      BIT  #HI.BAUD,PARAM2 ;HAS HI BAUD BEEN SELECTED?
      BEQ  75              ;BR IF NO
      MOVB #3,REG(R4)     ;SELECT THE TX MC PRI
      CLR  SEC(R4)        ;ZERO THE PRI MC
      MOVB #7,REG(R4)     ;SELECT THE TX MC SEC
      MOV  TXMC,SEC(R4)   ;SET FOR MAX CHAR TRANSFER.

```

```

;THE D011 IS TOTALLY ENABLE TO GO. THE TRANSMISSION
;WILL BEGIN ON THE SECONDARY (THE SYNC CHARS WENT ON
;THE PRIMARY). THE D011 WILL INTERRUPT AFTER EACH CHAR
;AND CONTINUE TRANSMITTING WHILE THE END CHAR IS
;SEARCHED FOR.

```

```

75:  BIC  #300,XCSR(R4)   ;CLEAR ALL TX DONES
      BIS  #IE+.GO,XCSR(R4) ;SET INTERRUPT ENABLE AND GO
      RTS  PC             ;GO BACK TO MAIN PROGRAM

```

```

;SYNC CHARACTER BUFFER AREA.

```

```

TEMP1:  0
TEMP2:  0
ERRORS: 0
SYNC:   .BYTE 26,26
        .BYTE 26,26
        .BYTE 26,26

```

M02

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1167
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1173
1174
1175 014170 000240 1.5 XISR: NOP ;LOCATION SET FOR HALT INSTRUCTION IN DEBUGGING.
1176 014172 005764 4.4 TST ERR(R4) ;IS THE DQ11 ERROR FLAG SET??
1177 014176 100012 2.6 BPL 25 ;NOT SET GOOD SO FAR.
1178 014200 016403 5.0 MOV XCSR(R4),R3
1179 014204 011402 3.8 MOV JRCR,R2
1180 014206 104000 9.3 HLT 0 ;DQ11 ERROR FLAG SET. EXAMINE REGISTERS FOR ERRO
1181 014210 016437 6.4 MOV ERR(R4),ERRORS ;SAVE DQ ERROR REG
1182 014216 042764 7.0 BIC #377,ERR(R4)
1183 014224 011402 3.8 2S: MOV JRCR,R2 ;PREPARE FOR ERROR TYPEOUT
1184 014226 016403 5.0 MOV XCSR(R4),R3 ;PREPARE FOR ERROR TYPEOUT
1185 014232 032764 6.5 BIT #300,XCSR(R4) ;IS EITHER TX DONE SET??
1186 014240 001001 2.6 BNE .+4 ;GOOD EITHER PRI OR SEC DONE IS SET
1187 014242 104000 9.3 HLT 0 ;REPORT ERROR. INTERRUPT WAS TO
1188 ;BE CAUSED BY TX DONE; AND TX IS NOT DONE.
1189 014244 032737 5.3 BIT #HI.BAUD,PARAM2 ;IS HIGH BAUD RATE SELECTED?
1190 014252 001402 2.6 BEQ IS ;BR IF NO
1191 014254 005746 4.4 TST -(SP) ;FAKE STACK
1192 014256 000474 2.6 BR HBCHK ;XFER ALL DONE
1193 014260 032764 6.5 1S: BIT #BIT2,XCSR(R4) ;WHERE IS POINTER??
1194 014266 001424 2.6 BEQ TX.SEC ;SECONDARY IS NEXT.
1195 014270 112764 6.4 TX.PRI: MOV #2,REG(R4) ;SELECT TX BA PRIMARY
1196 014276 016401 5.0 MOV SEC(R4),R1 ;GET NEXT ADDRESS TO TRANSMIT FROM
1197 014302 062764 6.4 ADD #2,SEC(R4) ;UPDATE CURRENT ADDRESS.
1198 014310 004737 5.8 JSR PC,TX.CK ;GO CHECK FOR END CHAR.
1199
1200 ;IF I COME BACK FROM THE SUBROUTINE THAT MEANS THAT THE
1201 ;END CHAR WAS NOT FOUND AND THE TRANSMISSON GOES ON.
1202
1203 014314 112764 6.4 MOV #3,REG(R4) ;SELECT TX MC SEC
1204 014322 012764 6.4 MOV #2,SEC(R4) ;LOAD WITH A -2
1205 014330 042764 7.0 BIC #BIT7,XCSR(R4) ;CLEAR PRI DONE FROM TX
1206 014336 000002 4.8 RTI ;LEAVE TX ISR
1207
1208 ;THE ABOVE ROUTINE SERVICED THE INTERRUPT IF THE
1209 ;PRIMARY REGISTERS CAUSED THE INTERRUPT.
1210
1211 014340 112764 6.4 TX.SEC: MOV #6,REG(R4) ;SELECT THE TX BA SECONDARY.
1212 014346 016401 5.0 MOV SEC(R4),R1 ;GET ADDRESS POINTER.
1213 014352 062764 6.4 ADD #2,SEC(R4) ;UPDATE CURRENT ADDRESS.
1214 014360 004737 5.8 JSR PC,TX.CK ;GO CHECK THE LAST CHARACTER.
1215
1216 ;JUST LIKE ABOVE; IF I COME BACK FROM THE ABOVE SUBROUTIN
1217 ;THAT MEANS THE THE LAST CHAR HASN'T BEEN TRANSMITTED
1218 ;AND THAT TRANSMISSON SHOULD CONTINUE.
1219
1220 014364 112764 6.4 MOV #7,REG(R4) ;SELECT THE TX MC SEC
1221 014372 012764 6.4 MOV #2,SEC(R4) ;LOAD WITH A -2
1222 014400 042764 7.0 BIC #BIT6,XCSR(R4) ;CLEAR SEC DONE
  
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014516 000240
014520 042714 000300
014524 032714 000004
014530 001403
014532 004737 015366
014536 000767
014540 113737 011015 014162
014546 113737 011015 014163
014554 112764 000011 000005
014562 013764 014162 000006
014570 112764 000000 000005
014576 013764 011020 000006
014604 105264 000005
014610 012764 177776 000006
014616 112764 000004 000005
014624 013764 011020 000006
014632 062764 000002 000006
014640 105264 000005
014644 012764 177776 000006
014652 112764 000012 000005
014660 012764 004000 000006
014666 012737 000750 011070
014674 042700 040000
014700 005037 011054
014704 005037 011056
014710 032737 000002 011014
014716 001413
014720 112764 000001 000005
014726 013764 011064 000006
014734 112764 000005 000005
014742 005064 000006
014746 052764 001000 000002 25:
014754 005737 013252
014760 001004
014762 104400 013160
014766 005137 013252
014772
014772 000240 35:
014774 000240
014776 000240
015000 000240
015002 000240
015004 052714 000041
015010 000207

: INITIALIZE RECEIVER ROUTINE

STARTR: NOP ; LEAVE OPEN FOR TEST PURPOSES.
BIC #300, JRCSR ; CLEAR ALL DONES
BIT #BIT2, JRCSR ; CHECK FOR POINTER TO SEC.
BEQ 15 ; BR IF POINTING TO PRI
JSR PC, GETPRI ; POINT REGISTERS TO PRI
BR STARTR ; BEGIN AGAIN
15: MOV #PARAM2+1, SYNC ; GET SYNC CHAR
MOV #PARAM2+1, SYNC+1 ; GET SECOND SYNC CHAR
MOV #11, REG(R4) ; SELECT SYNC REGISTER
MOV #SYNC, SEC(R4) ; LOAD SYNC REGISTER
MOV #80, REG(R4) ; SELECT RX BA PRI.
MOV #IRDA, SEC(R4) ; LOAD RX BA FOR DATA
INCB REG(R4) ; GET MC REGISTER
MOV #8-2, SEC(R4) ; SET FOR ONE CHAR.
MOV #84, REG(R4) ; SELECT RX BA SEC.
MOV #IRDA, SEC(R4) ; LOAD RX BA SEC.
ADD #82, SEC(R4) ; UPDATE DATA POINTER BY ONE
INCB REG(R4) ; GET MC REGISTER
MOV #8-2, SEC(R4) ; SET FOR TWO CHAR.
MOV #812, REG(R4) ; SELECT MISC REGISTER
MOV #84000, SEC(R4) ; SELECT EIGHT BITS PER CHAR.
MOV #8750, ACC ; SET FOR MAX 750 CHARS TO BE RXED
BIC #RFLG, STAT ; RESET RX COMPLETE FLAG
CLR ERCSR
CLR ERDBR
BIT #HI.BAUD, PARAM2 ; IS HIGH BAUD RATE SELECTED?
BEQ 25 ; BR IF NO
MOV #81, REG(R4) ; SELECT THE RX MC PRI
MOV #RXMC, SEC(R4) ; RECEIVE CHARS MAX.
MOV #85, REG(R4) ; SELECT THE RX MC SEC.
CLR SEC(R4) ; ZERO THE RX MC SECONDARY.
25: BIS #DTR, XCSR(R4) ; SET DATA TERMINAL READY.
TST STOP ; FIRST TIME HERE?
BNE 35
TYPE #MSG5
COM STOP
35: NOP
NOP
NOP
NOP
NOP
BIS #IE+.GO, JRCSR ; SET INTERRUPT ENABLE AND GO
RTS PC ; GO TO MAINLINE.....

: THE ABOVE SHOULD HAVE PREPARED THE RECEIVER TO
: RECEIVE ALL THE CHARS NEEDED FOR TESTING.
: THE RECEIVER WILL INTERRUPT AFTER EACH CHAR IS RECEIVED.
: CHECKING FOR THE END CHARACTER.


```

1356                                     ;IF I COME BACK THAT MEANS THAT THE END CHARACTER WASN'T
1357                                     ;FOUND AND I SHOULD CONTINUE RECEIVING.
1358
1359 015222 112764 000005 000005 6.4      MOVB    #5,REG(R4)      ;SELECT THE RX MC SEC
1360 015230 012764 177776 000006 6.4      MOV     #2,SEC(R4)    ;LOAD WITH A -2
1361 015236 042714 000100 5.8      BIC     #BIT6,RCCSR   ;CLEAR RX DONE
1362 015242 000002 4.8      RTI                                     ;GO BACK TO MAINLINE ....

```

;THE BELOW ROUTINE CHECKS THE DATA FOR END CHAR.

```

1363
1364
1365
1366 015244 142741 000200 7.0      RX.CK: BICB    #BIT7,-(R1)  ;CLEAR PARITY BIT.
1367 015250 142741 000200 7.0      BICB    #BIT7,-(R1)  ;CLEAR PARITY BIT.
1368 015254 123721 011041 4.7      CMPB    RX_TERM,(R1)+ ;WAS THE CHAR =0011
1369 015260 001403 2.6      BEQ     RXISRDN      ;BR IF 001
1370 015262 123721 011041 4.7      CMPB    RX_TERM,(R1)+ ;001?
1371 015266 001012 2.6      BNE     NO.12        ;NO 001
1372 015270
1373 015270 042714 000041 5.8      RXISRDN: BIC     #IE+.GO,RCCSR ;CLEAR GO AND INTERRUPT ENABLE
1374 015274 042714 000300 5.8      BIC     #300,RCCSR   ;CLEAR ALL RX DONES
1375 015300 052700 040000 3.8      BIS     #RFLG,STAT  ;SET RX COMPLETE FLAG
1376 015304 005037 011032 3.7      CLR     TIME        ;ZERO TIMER
1377 015310 005726 3.2      TST     (SP)+       ;POP SUBROUTINE POINTER.
1378 015312 000002 4.8      RTI                                     ;ALL DONE GO HOME.....
1379 015314 032777 000040 173522 7.7      NO.12: BIT     #BITS,RSR  ;MONITOR RX DATA?!
1380 015322 001405 2.6      BEQ     IS          ;DON'T MONITOR
1381 015324 105777 173552 5.6      TSTB   #TPS        ;TTY READY??
1382 015330 100002 2.6      BPL     IS          ;NOT READY GO ON WITH TEST
1383 015332 114177 173546 8.8      MOVB   -(R1),#TPB  ;PRINT CHARACTER
1384 015336 000207 3.5      IS:    RTS     PC    ;GO TO RX ISR
1385 015340 032777 000040 173476 7.7      HERCHK: BIT     #BITS,RSR  ;MONITOR RX DATA?
1386 015346 001750 2.6      BEQ     RXISRDN    ;BR IF NO
1387 015350 105777 173526 5.6      TSTB   #TPS        ;TTY READY?
1388 015354 100345 2.6      BPL     RXISRDN    ;NO GO ON WITH TEST
1389 015356 112777 000122 173520 7.6      MOVB   #R,#TPB    ;TYPE "R"
1390 015364 000741 2.6      BR     RXISRDN

```

E03

D011 ITEP OVERLAY MACY11 27(1006) 29-OCT-76 14:38 PAGE 31
 DZD008.P11 05-AUG-76 09:23 RECEIVER INTERRUPT SERVICE ROUTINE.

```

1391
1392
1393
1394 015366 032764 000004 000002 GETPRI: BIT      #BIT2,XCSR(R4)  :IS THE TX ON THE SEC??
1395 015374 001424          BEQ      15          :BR IF NO
1396 015376 112764 000012 000005      MOVB     #12,REG(R4)  :SELECT THE MISC REG
1397 015404 012764 004010 000006      MOV      #4010,SEC(R4) :SET EIGHT BITS AND TEST LOOP FOR CLK
1398 015412 112764 000007 000005      MOVB     #7,REG(R4)   :SELECT TX MC SEC
1399 015420 012764 177777 000006      MOV      #-1,SEC(R4)  :TX ONE CHAR
1400 015426 052764 000001 000002      BIS      #.GO,XCSR(R4) :SET GO
1401 015434 032764 000100 000002      BIT      #BIT6,XCSR(R4) :HANG HERE FOR SEC DONE
1402 015442 001774          BEQ      -6          :KEEP WAITING FOR TX DONE SEC.
1403 015444 000750          BR       GETPRI      :GO RECHECK AND CKECK RX
1404
1405 015446 032714 000004          15:      BIT      #BIT2,IRCSR  :IS RX ON SECONDARY REG
1406 015452 001430          BEQ      25          :BR IF ON PRI.
1407 015454 112764 000012 000005      MOVB     #12,REG(R4)  :SELECT MISC REGISTERS
1408 015462 012764 004010 000006      MOV      #4010,SEC(R4) :EIGHT BITS AND TEST LOOP (CLK)
1409 015470 112764 000005 000005      MOVB     #5,REG(R4)   :SELECT RX MC SEC
1410 015476 012764 177777 000006      MOV      #-1,SEC(R4)  :RX ONE CHAR
1411 015504 112764 000004 000005      MOVB     #4,REG(R4)   :SEL RX BA SEC.
1412 015512 012764 015564 000006      MOV      #NO.DAT,SEC(R4) :LOAD RX BA SEC.
1413 015520 052714 010001          BIS      #BIT12+.GO,IRCSR :SET GO!!!!!!+ACTIVE
1414 015524 032714 000100          BIT      #BIT6,IRCSR  :HANG HERE FOR RX SEC DONE.
1415 015530 001775          BEQ      -4          :KEEP WAITING
1416 015532 000715          BR       GETPRI      :GO CHECK EVERY ONE
1417
1418 015534 112764 000012 000005      25:      MOVB     #12,REG(R4)  :SELECT MISC REGISTER
1419 015542 042764 004010 000006      BIC      #4010,SEC(R4) :CLEAR EIGHT BITS AND TEST LOOP
1420 015550 042714 000300          BIC      #300,IRCSR   :CLEAR RX DONES
1421 015554 042764 000300 000002      BIC      #300,XCSR(R4) :CLEAR TX DONES
1422 015562 000207          RTS      PC          :GO HOME
1423 015564 000000          NO.DAT: 0
1424
1425 015566 005015 042522 042503 MFULL:  .ASCIZ  <15><12>/RECEIVER BUFFER FILLED. ERROR!!/
1426 015630 005015 042522 042503 MRxDA:  .ASCIZ  <15><12>/RECEIVED DATA = /<15><12>
1427 015655 015 051012 041505 MRXSB:  .ASCIZ  <15><12>/RECEIVED DATA SHOULD BE /<15><12>

```

F03

D011 ITP OVERLAY MACY11 27(1006) 29-OCT-76 14:38 PAGE 32
DZD008.P11 05-AUG-76 09:23 RECEIVER INTERRUPT SERVICE ROUTINE.

1425

000001

.EVEN
.END

BOX	18	600	627	655															
DCPARM	18																		
DHDOC1	18																		
DHPARM	18																		
DJPARM	18																		
DLPARM	18																		
DPPARM	18																		
DQDOC1	18	560																	
DOPARM	18	581																	
DUPARM	18																		
DUPPAR	18																		
DVDOC1	18																		
DVPARM	18																		
DZPARM	18																		
HELLO	18																		
HLT	5998 1320	759 1327	791	823	853	885	901	996	1068	1087	1100	1119	1180	1187	1314				
SEQUAT	18	599																	
SINTF	18	599																	
SITEP	18	713																	
SSERV	18	644																	

. ABS. 015712 000

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

DZDQ08.SEG/SOL/CRF/NL:TOC=ITEP1.MAC,DZDQ08.P11
RUN-TIME: 11 16 1 SECONDS
RUN-TIME RATIO: 65/29=2.2
CORE USED: 16K (31 PAGES)

