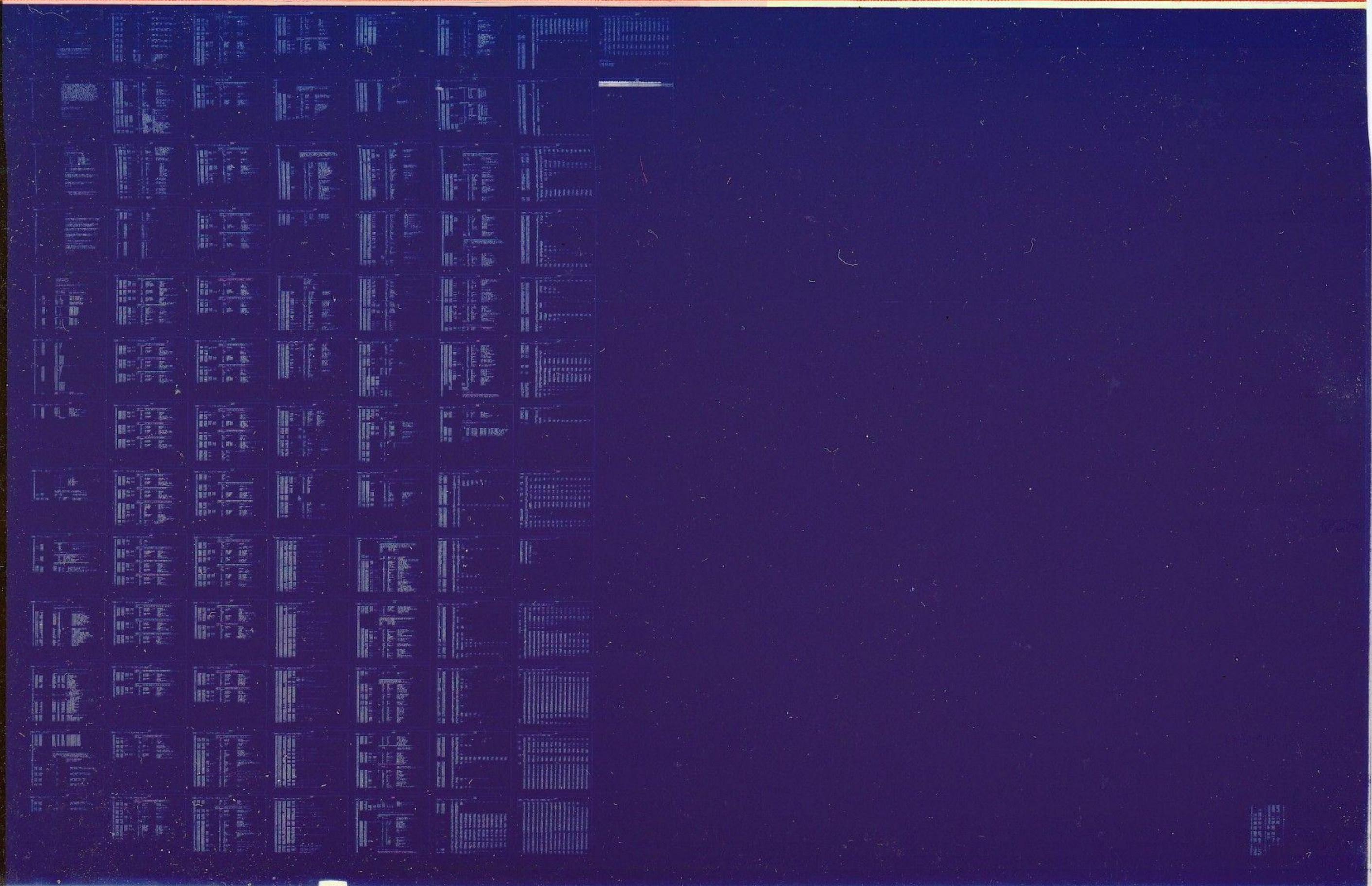


AR11

WRAPAROUND TEST 2
MD-11-DZARC-B

EP-DZARC-B-DL-A
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FICHE 1 OF 1

NOV 1976
digital
MADE IN USA



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8. MISCELLANEOUS

8.1 EXECUTION TIME

THE FIRST PASS WILL TAKE APPROXIMATELY 45 SECONDS FOR COMPLETION.
ADDITIONAL PASSES WILL TAKE APPROXIMATELY 4 MIN. FOR COMPLETION.
THE END OF A PASS IS INDICATED BY THE 'END PASS *X' MESSAGE.

8.2 DEVICE ADDRESS PROGRAM LOCATIONS

LOCATION \$BASE CONTAINS THE AR11 BASE DEVICE ADDRESS (170400)
LOCATION \$VECT1 CONTAINS THE AR11 BASE INTERRUPT VECTOR (340)
LOCATION \$FILLS CONTAINS THE TTY FILLER CHARACTER COUNT
LOCATION \$NULL CONTAINS THE TTY FILLER CHARACTER

*NOTE: IF THESE LOCATIONS ARE CHANGED, THE OPERATOR MUST START
THE TEST AGAIN AT LOC. 200. THE PROGRAM WILL USE THE \$BASE
ADDRESS AND VECTOR AND UPDATE THE ACTUAL PROGRAM VALUES.

8.3 MULTIPLE AR-11 TESTING

A PROVISION IS MADE FOR TESTING SEQUENTIAL AR-11, STARTING
AT BUS ADDRESS/VECTOR DEFINED BY \$BASE AND \$VECT1.
THE HEADER TYPEOUT WILL INFORM THE OPERATOR OF THE NUMBER
OF AR-11'S FOUND.

8.4 XXDP/ACT/APT NOTES

THIS PROGRAM IS A CHAINABLE PROGRAM UNDER XXDP ACT.
THE APT HOOKS HAVE BEEN INSTALLED BUT NOT TESTED.

9. SOFTWARE SWITCH REGISTER OPERATION

THE PROGRAM SUPPORTS NON-SWITCH REGISTER CPU TYPES.
A CHANGE IN SWR VALUE IS ACCOMPLISHED BY TYPING A "CTRL G".
THE RESPONSE WILL BE "SWR = " AND WAIT FOR A NEW VALUE.
THE OPERATOR NOW INPUTS THE NEW VALUE AND TERMINATES WITH A "CR".

10. TABLE OF CONTENTS

ATTACHED

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```

%
.TITLE MAINDEC-11-DZARC-B
.*COPYRIGHT (C) 1976
.*DIGITAL EQUIPMENT CORP.
.*MAYNARD, MASS. 01754
.*
.*PROGRAM BY RAYMOND SHOOP
.*
.*THIS PROGRAM WAS ASSEMBLED USING THE PCP-11 MAINDEC SYSMAC
.*PACKAGE (MAINDEC-11-DZGAC-B2), NOV 21, 1975.
.*

```

.SBTTL BASIC DEFINITIONS

```

.*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
CO1100 STACK= 1100
.EQUIV EMT.ERROR ::BASIC DEFINITION OF ERROR CALL
.EQUIV IOT.SCOPE ::BASIC DEFINITION OF SCOPE CALL

```

.*MISCELLANEOUS DEFINITIONS

```

000011 HT= 11 ::CODE FOR HORIZONTAL TAB
000012 LF= 12 ::CODE FOR LINE FEED
000015 CR= 15 ::CODE FOR CARRIAGE RETURN
000200 CRLF= 200 ::CODE FOR CARRIAGE RETURN-LINE FEED
177776 PS= 177776 ::PROCESSOR STATUS WORD
.EQUIV PS,PSW
177774 STKLMT= 177774 ::STACK LIMIT REGISTER
177772 PIRQ= 177772 ::PROGRAM INTERRUPT REQUEST REGISTER
177570 DSWR= 177570 ::HARDWARE SWITCH REGISTER
177570 DDISP= 177570 ::HARDWARE DISPLAY REGISTER

```

.*GENERAL PURPOSE REGISTER DEFINITIONS

```

000000 R0= %0 ::GENERAL REGISTER
000001 R1= %1 ::GENERAL REGISTER
000002 R2= %2 ::GENERAL REGISTER
000003 R3= %3 ::GENERAL REGISTER
000004 R4= %4 ::GENERAL REGISTER
000005 R5= %5 ::GENERAL REGISTER
000006 R6= %6 ::GENERAL REGISTER
000007 R7= %7 ::GENERAL REGISTER
.EQUIV R6,SP ::STACK POINTER
.EQUIV R7,PC ::PROGRAM COUNTER

```

.*PRIORITY LEVEL DEFINITIONS

```

000000 PR0= 0 ::PRIORITY LEVEL 0
000040 PR1= 40 ::PRIORITY LEVEL 1
000100 PR2= 100 ::PRIORITY LEVEL 2
000140 PR3= 140 ::PRIORITY LEVEL 3
000200 PR4= 200 ::PRIORITY LEVEL 4
000240 PR5= 240 ::PRIORITY LEVEL 5
000300 PR6= 300 ::PRIORITY LEVEL 6
000340 PR7= 340 ::PRIORITY LEVEL 7

```

.*"SWITCH REGISTER" SWITCH DEFINITIONS

```

100000 SW15= 100000
040000 SW14= 40000

```

000001
000002
000004
000010
000020
000040
000100
000200
000400
001000
002000
004000
010000
020000
040000
100000

000001
000002
000004
000010
000020
000040
000100
000200
000400
001000
002000
004000
010000
020000
040000
100000

000001
000002
000004
000010
000020
000040
000100
000200
000400
001000
002000
004000
010000
020000
040000
100000

000010
000004

SW13= 20000
SW12= 10000
SW11= 4000
SW10= 2000
SW09= 1000
SW08= 400
SW07= 200
SW06= 100
SW05= 40
SW04= 20
SW03= 10
SW02= 4
SW01= 2
SW00= 1
.EQUIV SW09,SW9
.EQUIV SW08,SW8
.EQUIV SW07,SW7
.EQUIV SW06,SW6
.EQUIV SW05,SW5
.EQUIV SW04,SW4
.EQUIV SW03,SW3
.EQUIV SW02,SW2
.EQUIV SW01,SW1
.EQUIV SW00,SW0

.*DATA BIT DEFINITIONS (BIT00 TO BIT15)

BIT15= 100000
BIT14= 40000
BIT13= 20000
BIT12= 10000
BIT11= 4000
BIT10= 2000
BIT09= 1000
BIT08= 400
BIT07= 200
BIT06= 100
BIT05= 40
BIT04= 20
BIT03= 10
BIT02= 4
BIT01= 2
BIT00= 1
.EQUIV BIT09,BIT9
.EQUIV BIT08,BIT8
.EQUIV BIT07,BIT7
.EQUIV BIT06,BIT6
.EQUIV BIT05,BIT5
.EQUIV BIT04,BIT4
.EQUIV BIT03,BIT3
.EQUIV BIT02,BIT2
.EQUIV BIT01,BIT1
.EQUIV BIT00,BIT0

.*BASIC "CPU" TRAP VECTOR ADDRESSES

ERRVEC= 4 ;:TIME OUT AND OTHER ERRORS
RESVEC= 10 ;:RESERVED AND ILLEGAL INSTRUCTIONS

001 000014
002 000014
003 000014
004 000020
005 000024
006 000030
007 000034
008 000060
009 000064
010 000240
011 170400
012 000340
013 000200

TBITVEC=14
TRTVEC= 14
BPTVEC= 14
IOTVEC= 20
PWRVEC= 24
EMTVEC= 30
TRAPVEC=34
TKVEC= 60
TPVEC= 64
PIRQVEC=240
ABASE=170400
AVECT1=340
APRIOR=200

:: "T" BIT
:: TRACE TRAP
:: BREAKPOINT TRAP (BPT)
:: INPUT/OUTPUT TRAP (IOT) **SCOPE**
:: POWER FAIL
:: EMULATOR TRAP (EMT) **ERROR**
:: "TRAP" TRAP
:: TTY KEYBOARD VECTOR
:: TTY PRINTER VECTOR
:: PROGRAM INTERRUPT REQUEST VECTOR

000000
000001
000002
000003
000004
000005
000006
000007
000008
000009
000010
000011
000012
000013
000014
000015
000016
000017
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000042

.SBTTL OPERATIONAL SWITCH SETTINGS

```

:*
:* SWITCH USE
:* -----
:* 15 HALT ON ERROR
:* 14 LOOP ON TEST
:* 13 INHIBIT ERROR TYPEOUTS
:* 12 FORCED PRINTOUT
:* 11 INHIBIT ITERATIONS
:* 10 BELL ON ERROR
:* 9 LOOP ON ERROR
:* 8 LOOP ON TEST IN SWR<7:0>

```

.SBTTL TRAP CATCHER

000000

```

.=0
;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS

```

```

000174 000174
000176 000176

```

```

.=174
DISPREG: .WORD 0 ;;SOFTWARE DISPLAY REGISTER
SWREG: .WORD 0 ;;SOFTWARE SWITCH REGISTER

```

.SBTTL STARTING ADDRESS(ES)

```

000200 000137 001516
000204 000137 001536
000210 000137 001530

```

```

JMP @*BEGIN ;;JUMP TO STARTING ADDRESS OF PROGRAM
JMP BEGIN1 ;RESTART ADDRESS
JMP BEGIN2 ;STARTING ADDRESS OF OPTION TEST AREA

```

343
344
345
346
347
348
349
350
351 000046
352 000052
353 000052
354 000214
355 001000
356
357
358
359
360
361
362 001000
363 000024
364 000024
365 000044
366 001000
367 001000
368
369
370
371
372 001000
373 001000 000000
374 001002 001200
375 001004 000020
376 001006 000030
377 001010 000120
378 001012 000052

.SBTTL ACT11 HOOKS

```

:*****
:HOOKS REQUIRED BY ACT11
      $SVPC=          ;SAVE PC
      .=46
      $ENDAD          ;;1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SECP
      .=52
      .WORD 0          ;;2)SET LOC.52 TO ZERO
      .=$SVPC         ;; RESTORE PC
      .=1000

```

.SBTTL APT PARAMETER BLOCK

```

:*****
:SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
:*****
      .SX=          ;;SAVE CURRENT LOCATION
      .=24          ;;SET POWER FAIL TO POINT TO START OF PROGRAM
      200           ;;FOR APT START UP
      .=44          ;;POINT TO APT INDIRECT ADDRESS PNTR.
      $APTHDR       ;;POINT TO APT HEADER BLOCK
      .=.SX         ;;RESET LOCATION COUNTER
:*****
:SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
:INTERFACE SPEC.

```

```

$APTHD:
$HIBTS: .WORD 0          ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
$MADR: .WORD $MAIL      ;;ADDRESS OF APT MAILBOX (BITS 0-15)
$STMT: .WORD 20         ;;RUN TIM OF LONGEST TEST
$PASTM: .WORD 30        ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
$UNITM: .WORD 120       ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
          .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)

```

379
380
381
382
383
384
385
386 001100
387 001100 000000
388 001100 000
389 001102 000
390 001103 000
391 001104 000000
392 001105 000000
393 001110 000000
394 001112 000000
395 001114 000
396 001115 001
397 001116 000000
398 001120 000000
399 001122 000000
400 001124 000000
401 001126 000000
402 001130 000000
403 001132 000000
404 001134 000000
405 001136 177570
406 001140 177570
407 001142 177560
408 001144 177562
409 001146 177564
410 001150 177566
411 001152 000
412 001153 002
413 001154 012
414 001155 000
415 001156 000000
416
417 001160 000000
418 001162 000000
419 001164 000000
420 001166 000000
421 001170 177607 000377
422 001174 077
423 001175 015
424 001176 000012

.SBTTL COMMON TAGS

::*****
::*THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
::*USED IN THE PROGRAM.

SCMTAG: . =1100 ;: START OF COMMON TAGS
\$.STNM: .WORD 0 ;: CONTAINS THE TEST NUMBER
\$.SERFLG: .BYTE 000 ;: CONTAINS ERROR FLAG
\$.SICNT: .WORD 000 ;: CONTAINS SUBTEST ITERATION COUNT
\$.SLPADR: .WORD 000 ;: CONTAINS SCOPE LOOP ADDRESS
\$.SLPERR: .WORD 000 ;: CONTAINS SCOPE RETURN FOR ERRORS
\$.SERTTL: .WORD 000 ;: CONTAINS TOTAL ERRORS DETECTED
\$.SITEMB: .BYTE 000 ;: CONTAINS ITEM CONTROL BYTE
\$.SERMAX: .BYTE 001 ;: CONTAINS MAX. ERRORS PER TEST
\$.SERRPC: .WORD 000 ;: CONTAINS PC OF LAST ERROR INSTRUCTION
\$.SGDADR: .WORD 000 ;: CONTAINS ADDRESS OF 'GOOD' DATA
\$.SBDADR: .WORD 000 ;: CONTAINS ADDRESS OF 'BAD' DATA
\$.SGDDAT: .WORD 000 ;: CONTAINS 'GOOD' DATA
\$.SBDAT: .WORD 000 ;: CONTAINS 'BAD' DATA
. ;: RESERVED--NOT TO BE USED
\$.SWR: .WORD DSWR ;: ADDRESS OF SWITCH REGISTER
\$.DISPLAY: .WORD DDISP ;: ADDRESS OF DISPLAY REGISTER
\$.STKS: 177560 ;: TTY KBD STATUS
\$.STKB: 177562 ;: TTY KBD BUFFER
\$.STPS: 177564 ;: TTY PRINTER STATUS REG. ADDRESS
\$.STPB: 177566 ;: TTY PRINTER BUFFER REG. ADDRESS
\$.SNULL: .BYTE 0 ;: CONTAINS NULL CHARACTER FOR FILLS
\$.SFILLS: .BYTE 2 ;: CONTAINS * OF FILLER CHARACTERS REQUIRED
\$.SFILLC: .BYTE 12 ;: INSERT FILL CHARS. AFTER A "LINE FEED"
\$.STPFLG: .BYTE 0 ;: "TERMINAL AVAILABLE" FLAG (BIT<07>=0=YES)
\$.SREGAD: .WORD 0 ;: CONTAINS THE ADDRESS FROM WHICH (\$REGD) WAS OBTAINED
\$.SREGD: .WORD 0 ;: CONTAINS ((\$REGAD)+0)
\$.SREGI: .WORD 0 ;: CONTAINS ((\$REGAD)+2)
\$.STIMES: 0 ;: MAX. NUMBER OF ITERATIONS
\$.SESCAPE: 0 ;: ESCAPE ON ERROR ADDRESS
\$.SBELL: .ASCIZ <207><377><377> ;: CODE FOR BELL
\$.SQUES: .ASCII /?/ ;: QUESTION MARK
\$.SCRLF: .ASCII <15> ;: CARRIAGE RETURN
\$.SLF: .ASCIZ <12> ;: LINE FEED

425
426
427
428
429
430
431 001200
432 001200 000000
433 001202 000000
434 001204 000000
435 001206 000000
436 001210 000000
437 001212 000000
438 001214 000000
439 001216 000000
440 001220
441 001220 000
442 001221 000
443 001222 000000
444 001224 000000
445 001226 000000
446
447
448
449
450
451
452 001230 000
453 001231 000
454
455
456
457
458 001232 000000
459
460 001234 000
461 001235 000
462 001236 000000
463 001240 000
464 001241 000
465 001242 000000
466 001244 000
467 001245 000
468 001246 000000
469 001250 340
470 001251 000
471 001252 200
472 001253 000
473
474 001254 170400
475 001256 000000
476 001260 000000
477 001262 000000
478 001264 000000
479 001266 000000
480 001270 000000

```

:;*****
.SBTTL APT MAILBOX-ETABLE
:;*****
.EVEN
$MAIL:
$MSGTY: .WORD AMSGTY ;; APT MAILBOX
$FATAL: .WORD AFATAL ;; MESSAGE TYPE CODE
$TESTN: .WORD ATESTN ;; FATAL ERROR NUMBER
$PASS: .WORD APASS ;; TEST NUMBER
$DEVCT: .WORD ADEVCT ;; PASS COUNT
$UNIT: .WORD AUNIT ;; DEVICE COUNT
$MSGAD: .WORD AMSGAD ;; I/O UNIT NUMBER
$MSGLG: .WORD AMSGLG ;; MESSAGE ADDRESS
$ETABLE: ;; MESSAGE LENGTH
$ENV: .BYTE AENV ;; APT ENVIRONMENT TABLE
$ENVM: .BYTE AENVM ;; ENVIRONMENT BYTE
$SWREG: .WORD ASWREG ;; ENVIRONMENT MODE BITS
$USWR: .WORD AUSWR ;; APT SWITCH REGISTER
$CPUOP: .WORD ACPUOP ;; USER SWITCHES
:; CPU TYPE, OPTIONS
:; BITS 15-11=CPU TYPE
:; 11/04=01, 11/05=02, 11/20=03, 11/40=04, 11/45=05
:; 11/70=06, PDQ=07, Q=10
:; BIT 10=REAL TIME CLOCK
:; BIT 9=FLOATING POINT PROCESSOR
:; BIT 8=MEMORY MANAGEMENT
$MAMS1: .BYTE AMAMS1 ;; HIGH ADDRESS, M.S. BYTE
$MTYP1: .BYTE AMTYP1 ;; MEM. TYPE, BLK#1
:; MEM. TYPE BYTE -- (HIGH BYTE)
:; 900 NSEC CORE=001
:; 300 NSEC BIPOLAR=002
:; 500 NSEC MOS=003
$MADR1: .WORD AMADR1 ;; HIGH ADDRESS, BLK#1
:; MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
$MAMS2: .BYTE AMAMS2 ;; HIGH ADDRESS, M.S. BYTE
$MTYP2: .BYTE AMTYP2 ;; MEM. TYPE, BLK#2
$MADR2: .WORD AMADR2 ;; MEM. LAST ADDRESS, BLK#2
$MAMS3: .BYTE AMAMS3 ;; HIGH ADDRESS, M.S. BYTE
$MTYP3: .BYTE AMTYP3 ;; MEM. TYPE, BLK#3
$MADR3: .WORD AMADR3 ;; MEM. LAST ADDRESS, BLK#3
$MAMS4: .BYTE AMAMS4 ;; HIGH ADDRESS, M.S. BYTE
$MTYP4: .BYTE AMTYP4 ;; MEM. TYPE, BLK#4
$MADR4: .WORD AMADR4 ;; MEM. LAST ADDRESS, BLK#4
$VECT1: .BYTE AVECT1 ;; INTERRUPT VECTOR#1
$VECT2: .BYTE AVECT2 ;; INTERRUPT VECTOR#2
$PRIOR: .BYTE APRIOR ;; BUS PRIORITY #1, #2
:; .BYTE 0 ;; SPARE, NOT USED
:; .EVEN
$BASE: .WORD ABASE ;; BASE ADDRESS OF EQUIPMENT UNDER TEST
$DEVM: .WORD ADF ;; DEVICE MAP
$CDW1: .WORD ACW1 ;; CONTROLLER DESCRIPTION WORD#1
$CDW2: .WORD ACDW2 ;; CONTROLLER DESCRIPTION WORD#2
$DDW0: .WORD ADDW0 ;; DEVICE DESCRIPTOR WORD#0
$DDW1: .WORD ADDW1 ;; DEVICE DESCRIPTOR WORD#1
$DDW2: .WORD ADDW2 ;; DEVICE DESCRIPTOR WORD#2

```

481 001272 000000
 482 001274 000000
 483 001276 000000
 484 001300 000000
 485 001302 000000
 486 001304 000000
 487 001306 000000
 488 001310 000000
 489 001312 000000
 490 001314 000000
 491 001316 000000
 492 001320 000000
 493 001322 000000

\$DDW3: .WORD ADDW3 ;;DEVICE DESCRIPTOR WORD#3
 \$DDW4: .WORD ADDW4 ;;DEVICE DESCRIPTOR WORD#4
 \$DDW5: .WORD ADDW5 ;;DEVICE DESCRIPTOR WORD#5
 \$DDW6: .WORD ADDW6 ;;DEVICE DESCRIPTOR WORD#6
 \$DDW7: .WORD ADDW7 ;;DEVICE DESCRIPTOR WORD#7
 \$DDW8: .WORD ADDW8 ;;DEVICE DESCRIPTOR WORD#8
 \$DDW9: .WORD ADDW9 ;;DEVICE DESCRIPTOR WORD#9
 \$DDW10: .WORD ADDW10 ;;DEVICE DESCRIPTOR WORD#10
 \$DDW11: .WORD ADDW11 ;;DEVICE DESCRIPTOR WORD#11
 \$DDW12: .WORD ADDW12 ;;DEVICE DESCRIPTOR WORD#12
 \$DDW13: .WORD ADDW13 ;;DEVICE DESCRIPTOR WORD#13
 \$DDW14: .WORD ADDW14 ;;DEVICE DESCRIPTOR WORD#14
 \$DDW15: .WORD ADDW15 ;;DEVICE DESCRIPTOR WORD#15

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SETEND:

.SBTTL ERROR POINTER TABLE

;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
 ;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
 ;*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
 ;*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
 ;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ;;POINTS TO THE ERROR MESSAGE
 ;* DH ;;POINTS TO THE DATA HEADER
 ;* DT ;;POINTS TO THE DATA
 ;* DF ;;POINTS TO THE DATA FORMAT

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\$ERRTB:

001324 011736
 001326 012451
 001330 014572
 001332 000000

; ITEM 1
 EM1 ;ERROR ON A/D CHANNEL
 DH1 ;ERRPC ARADD CHANNEL NOMINAL TOLERANCE ACTUAL
 DT1 ;\$ERRPC ARBADD CHANL \$GDDAT SPREAD \$BDDAT
 0

001334 011763
 001336 012530
 001340 014610
 001342 000000

; ITEM 2
 EM2 ;VC LOGIC SIGNAL HIGH OUTPUT TOO LOW
 DH2 ;ERRPC ARADD CHANNEL 3V LEV. OUTPUT
 DT2 ;\$ERRPC ARBADD CHANL \$GDDAT \$BDDAT
 0

001344 012027
 001346 012577
 001350 014624
 001352 000000

; ITEM 3
 EM3 ;VC STATUS REGISTER IN ERROR
 DH3 ;ERRPC ARADD GOOD BAD
 DT3 ;\$ERRPC ARBADD \$GDDAT \$BDDAT
 0

001354 012063
 001356 012577
 001360 014624

; ITEM 4
 EM4 ;EXTERNAL AD START FAILED
 DH3 ;ERRPC ARADD GOOD BAD
 DT3 ;\$ERRPC ARBADD \$GDDAT \$BDDAT

537 001362 000000
001364 012120
001366 012534
001370 014636
001372 000000

001374 012161
001376 012574
001400 014624
001402 000000

0
:ITEM 5
DMS
DHS
DTS
0
:ITEM 6
DMS
DHS
DTS
0

:AND DIFFERENTIAL LINEARITY ERROR
:ERRPC ARADD # OF STATES ALLOWED
:SERRPC ARBADD \$BDDAT \$GDDAT

:NOISE LEVEL EXCEEDED LIMIT
:ERRPC ARADD LIMIT MEASURED
:SERRPC ARBADD \$GDDAT \$BDDAT

| | | | | |
|---------|--------|---------|--------|--|
| 0001404 | 012214 | : ITEM | 7 | : VC LOGIC SIGNAL LOW OUTPUT TOO HIGH |
| 0001406 | 012236 | | FM7 | :ERRPC ARADD A/D CHAN +.4V LEV. OUTP.T |
| 0001410 | 014610 | | DT2 | :SERRPC ARBADD CHANL \$GDDAT \$BCDAT |
| 0001412 | 000000 | | 0 | |
| 0001414 | 012260 | : ITEM | 10 | : D/A DIFFERENTIAL LINEARITY ERROR |
| 0001416 | 013304 | | FM10 | :ERRPC ARADD STEP NOMINAL SPREAD ACTUAL |
| 0001418 | 014650 | | DT10 | :SERRPC ARBADD EDGE \$GDDAT SPREAD \$BCDAT |
| 0001420 | 000000 | | 0 | |
| 0001424 | 012321 | : ITEM | 11 | : A/D INTER-CHANNEL SETTILING ERROR |
| 0001426 | 013363 | | FM11 | :ERRPC ARADD NOMINAL SPREAD ACTUAL |
| 0001428 | 014666 | | DT11 | :SERRPC ARBADD \$GDDAT SPREAD \$BCDAT |
| 0001430 | 000000 | | 0 | |
| 0001434 | 012369 | : ITEM | 12 | : OFFSET ERROR |
| 0001436 | 013383 | | FM12 | :ERRPC ARADD NOMINAL SPREAD ACTUAL |
| 0001438 | 014686 | | DT12 | :SERRPC ARBADD \$GDDAT SPREAD \$BCDAT |
| 0001440 | 000000 | | 0 | |
| 0001444 | 012377 | : ITEM | 13 | : CALIBRATION ERROR |
| 0001446 | 013393 | | FM13 | :ERRPC ARADD NOMINAL SPREAD ACTUAL |
| 0001448 | 014696 | | DT13 | :SERRPC ARBADD \$GDDAT SPREAD \$BCDAT |
| 0001450 | 000000 | | 0 | |
| 0001454 | 012382 | : ITEM | 14 | : LINEARITY ERROR AT XX00 |
| 0001456 | 013396 | | FM14 | :ERRPC ARADD NOMINAL SPREAD ACTUAL |
| 0001458 | 014711 | | DT14 | :SERRPC ARBADD \$GDDAT SPREAD \$BCDAT |
| 0001460 | 000000 | | 0 | |
| 0001464 | 170400 | ARBADD: | 170400 | |
| 0001466 | 000000 | ARBVCT: | 0 | |
| 0001468 | 000000 | ARBEXT: | 0 | |
| 0001470 | 000000 | ARBEXT: | 0 | |
| 0001474 | 170400 | ADCS: | 170400 | : A TO D STATUS/CONTROL REGISTER |
| 0001476 | 170401 | ADCS1: | 170401 | : A TO D STATUS REGISTER <HIGH BYTE |
| 0001478 | 170402 | ADDBP: | 170402 | : A TO D CONVERTED VALUE <READ |
| 0001480 | 170404 | CSR: | 170404 | : CLOCK STATUS REGISTER |
| 0001482 | 170406 | CSB: | 170406 | : CLOCK PRESET BUFFER |
| 0001484 | 170410 | VCSTAT: | 170410 | : DAC STATUS REGISTER |
| 0001486 | 170412 | VCXREG: | 170412 | : X BUFFER |
| 0001488 | 170414 | VCYREG: | 170414 | : Y BUFFER |
| 0001490 | 170416 | CSC: | 170416 | : CLOCK COUNTER |
| 0001494 | | .SBTTL | | PROGRAM START-JP |

```

001516 005000 BEGIN: CLR R0 :CLEAR R0
001520 005037 016762 CLR WFTST :CLEAR TOLERANCE FLAG
001524 000406 BR RBEG
001528 000406 BR RBEG
001530 012737 000001 016762 BEGIN2: MOV #1,WFTST :SET TOLERANCE FLAG
001534 012700 177777 BEGIN1: MOV #-1,R0 :LOAD R0
001538 000005 RBEG: RESET
001542 005037 177776 CLR PS
001546 012706 001100 MOV #STACK,SP :LOAD STACK
001550 012737 001602 000004 MOV #15,2#4 :LOAD BUS ERROR
001554 012702 001254 MOV $BASE,R2 :LOAD STARTING ADDRESS
001558 005003 CLR R3 :CLEAR COUNT
001562 005712 25: TST (R2) :TEST IF EXISTENT
001566 002702 000020 ADD #20,R2 :EXIST, UPDATE TEST ADDRESS
001570 005203 INC R3 :UPDATE # OF ARI1'S
001574 000773 BR 25
001602 022626 15: CMP (SP)+,(SP)+ :POP STACK
001606 005703 TST R3 :TEST IF FIRST DOES EXIST
001610 000000 BNE HALT :BR
001614 000741 BR BEGIN :FIRST ARI1 DOES NOT EXIST
001618 005303 35: DEC R3 :CHECK THE PROGRAM DEVICE ADDRESS
001622 010337 001470 MOV R3,NMBEXT :ADJUST R3
001626 012737 000006 000004 MOV #6,2#4 :SAVE THE NUMBER OF ADDITIONAL ARI1'S
001630 005037 000006 CLR 2#6 :RESET BUS ERROR
001634 013737 001254 001464 MOV $BASE,ARBADD :LOAD FIRST ADDRESS
001638 013737 001250 001466 MOV $VECT1,ARBVCT :LOAD FIRST VECTOR
001642 013737 001470 001472 MOV NMBEXT,NBEXT :LOAD NUMBER OF ADDITIONAL ARI1'S
001646 000005 RESET
:: CLEAR THE COMMON TAGS ($CMTAG) AREA
001660 012706 001100 MOV #CMTAG,R6 :FIRST LOCATION TO BE CLEARED
001664 005026 CLR (R6)+ :CLEAR MEMORY LOCATION
001668 022706 001126 CMP #SEDDAT,R6 :DONE?
001672 001374 BNE -6 :LOOP BACK IF NO
001676 012706 001100 MOV #STACK,SP :SETUP THE STACK POINTER
:: INITIALIZE A FEW VECTORS
001700 012737 017172 000020 MOV #SCOPE,2#IOTVEC :IOT VECTOR FOR SCOPE ROUTINE
001704 012737 000340 000022 MOV #340,2#IOTVEC+2 :LEVEL 7
001708 012737 017452 000030 MOV #ERROR,2#EMTVEC :EMT VECTOR FOR ERROR ROUTINE
001712 012737 000340 000032 MOV #340,2#EMTVEC+2 :LEVEL 7
001716 012737 021774 000034 MOV #TRAP,2#TRAPVEC :TRAP VECTOR FOR TRAP CALLS
001720 012737 000340 000036 MOV #340,2#TRAPVEC+2 :LEVEL 7
001724 012737 020576 000024 MOV #SPWRDN,2#PWAVEC :POWER FAILLRE VECTOR
001728 012737 000340 000026 MOV #340,2#PWAVEC+2 :LEVEL 7
001732 013737 010404 010376 MOV SENDCT,SEOPCT :SETUP END-OF-PROGRAM COUNTER
001736 005037 001164 CLR $TIMES :INITIALIZE NUMBER OF ITERATIONS
001740 005037 001166 SE$CAPE :CLEAR THE ESCAPE ON ERROR ADDRESS
001744 112737 000001 001115 MOV #1,$ERMAX :ALLOW ONE ERROR PER TEST
001748 012737 002004 001106 MOV #,$SLPADR :INITIALIZE THE LOOP ADDRESS FOR SCOPE
001752 012737 002012 001110 MOV #,$SLPERR :SETUP THE ERROR LOOP ADDRESS
:: SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
:: EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
002020 012746 000004 MOV 2#ERRVEC-(SP) :SAVE ERROR VECTOR
002024 012737 002062 000004 MOV #64,$2#ERRVEC :SET UP ERROR VECTOR
002028 012737 177570 001136 MOV #DSWR,SWR :SETUP FOR A HARDWARE SWICH REGISTER
002032 012737 177570 001140 MOV #DOISP,DISPLAY :AND A HARDWARE DISPLAY REGISTER

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659 002046 022777 177777 177062      CMP      #-1,DSWR      ;; TRY TO REFERENCE HARDWARE SWR
660 002054 001013                    BNE      659          ;; BRANCH IF NO TIMEOUT TRAP OCCURRED
661                                ;; AND THE HARDWARE SWR IS NOT = -1
662 002056 005737 000001      TST      0#1          ;; FORCE A TRAP THROUGH ERRVEC
663 002062 012737 000176 00:136 64$:  MOV      #SWREG,SWR    ;; POINT TO SOFTWARE SWR
664 002070 012737 000174 00:140      MOV      #DISPREG,DISPLAY ;; POINT TO SOFTWARE DISPLAY REG
665 002076 012716 002104      MOV      #655,(SP)    ;; REPLACE OLD PC WITH NEW
666 002102 000002                    RTI                      ;; RESTORE PC AND PSW
667 002104 012637 000004 65$:  MOV      (SP)+,3#ERRVEC ;; RESTORE ERROR VECTOR
668
669 002110                    $ARG1:
670 002110 005037 001206      CLR      $PASS        ;; CLEAR PASS COUNT
671 002114 132737 000200 001221      BITB     #APTSIZE,$ENVM ;; TEST USER SIZE UNDER APT
672 002122 001403                    BEQ      64$          ;; YES,USE NON-APT SWITCH
673 002124 012737 001222 001136      MOV      #SSWREG,SWR  ;; NO,USE APT SWITCH REGISTER
674 002132
675 002132 000005      RBEG2:  RESET
676 002134 012732 000232      MOV      #232,R2      ;LOAD R2
677 002140 012701 000230      MOV      #230,R1      ;LOAD R1
678 002144 010221 65$:  MOV      R2,(R1)+      ;LOAD .+2
679 002146 005021      CLR      (R1)+        ;LOAD HALT
680 002150 010102      MOV      R1,R2        ;LOAD R2
681 002152 005722      TST      (R2)+        ;BUMP R2
682 002154 020227 001002      CMP      R2,#1002     ;TEST FOR LAST
683 002160 001371      BNE      5$           ;BR UNTIL DONE
684 002162 004737 016646      JSR      PC,WADJ      ;ADJUST SOME TOLERANCES
685 002166 005700      TST      R0           ;TEST R0
686 002170 001402      BEQ      2$           ;BR IF CLEARED
687 002172 000137 002476      JMP      4$           ;INHIBIT TYPOUT
688 002176 005737 000042 2$:  TST      0#42         ;TEST ACT-11 OR CCP
689 002202 001402      BEQ      3$           ;BR IF CLEARED
690 002204 000137 002476      JMP      4$           ;INHIBIT TYPOUT
691 002210
692 002210 104400 002216 3$:  TYPE     ,65$        ;; TYPE ASCIZ STRING
693 002214 000422      BR       64$         ;; GET OVER THE ASCIZ
694 002262 64$:  .ASCIZ  <15><12>/AR-11 DIAGNOSTIC WRAPAROUND TEST.
695 002262 104400 002270
696 002266 000414      TYPE     ,67$        ;; TYPE ASCIZ STRING
697 67$:  .ASCIZ  <15><12>/MAINDEC-11-DZARC-B.<15><12>
698 002320 66$:  .ASCIZ  <15><12>/MAINDEC-11-DZARC-B.<15><12>
699 002320 013746 001470      MOV      NMBEXT,-(SP) ;PUSH ON STACK
700 002324 104402      TYPOS    ;TYPE OCTAL
701 002326 000002      .WORD    2
702 002330 104400 002336      .TYPE    ,69$        ;; TYPE ASCIZ STRING
703 002334 000420      BR       68$         ;; GET OVER THE ASCIZ
704 69$:  .ASCIZ  /18) ADDITIONAL ARII'S CONNECTED/
705 002376 68$:  .ASCIZ  <15><12>/ALL AR-11'S MUST HAVE G5036 WRAPAROUND MODULE INSTALLED
706 002376 104400 002404
707 002402 000435      .TYPE    ,71$        ;; TYPE ASCIZ STRING
708 71$:  .ASCIZ  <15><12>/ALL AR-11'S MUST HAVE G5036 WRAPAROUND MODULE INSTALLED
709 002476 70$:  .ASCIZ  <15><12>/ALL AR-11'S MUST HAVE G5036 WRAPAROUND MODULE INSTALLED
710
711 002476 012700 001474 4$:  MOV      #ADCS,R0     ;LOAD POINTER
712 002502 013720 001464 10$:  MOV      ARBADD,R0+   ;LOAD POINTER
713 002506 022700 001516      JMP      #BEGIN,R0   ;TEST FOR END

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002512 001373
002514 005237 001476
002520 012700 001500
002524 012701 000002
002530 060130
002532 005721
002534 022701 000020
002540 001373

BNE 10\$
INC ADCS1
MOV *ADDBR,R0
MOV #2,R1
12\$: ADD R1,(R0)+
TST (R1)+
CMP #20,R1
BNE 12\$
.SBTTL A TO D CHANNEL NUMBERS

:BIPOLAR CHANNEL NUMBERS

000000 CH0 = 0
000001 CH1 = 1
000002 CH2 = 2
000003 CH3 = 3
000004 CH4 = 4
000005 CH5 = 5
000006 CH6 = 6
000007 CH7 = 7
000010 CH10 = 10
000011 CH11 = 11
000012 CH12 = 12
000013 CH13 = 13
000014 CH14 = 14
000015 CH15 = 15
000016 CH16 = 16
000017 CH17 = 17

:UNIPOlar CHANNEL NUMBERS

000040 CH40 = 40
000041 CH41 = 41
000042 CH42 = 42
000043 CH43 = 43
000044 CH44 = 44
000045 CH45 = 45
000046 CH46 = 46
000047 CH47 = 47
000050 CH50 = 50
000051 CH51 = 51
000052 CH52 = 52
000053 CH53 = 53
000054 CH54 = 54
000055 CH55 = 55
000056 CH56 = 56
000057 CH57 = 57

.SBTTL
.SBTTL TEST NUMBER
.SBTTL

ABSTRACT

```

765      ::*****
766      :*TEST 1      TEST THAT "ERASE" AND DONE CAN BE SET AND CLEARED
767      :*****
768      TST1:  SCOPE
769      002542 000004      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
770      002544 012737 000010 001164      CLR      @VCSTAT      ;CLEAR
771      002552 005077 176730      MOV      @BIT12,$GDDAT      ;LOAD EXPECTED
772      002556 012737 010000 001124      MOV      $GDDAT,@VCSTAT      ;LOAD REG
773      002564 013777 001124 176714      MOV      @VCSTAT,@BDDAT      ;READ REG
774      002572 017737 176710 001126      MOV      @VCSTAT,@BDDAT      ;READ REG
775      002600 023737 001124 001126      CMP      $GDDAT,@BDDAT      ;COMPARE
776      002606 001401      BEQ      IS      ;;BR IF SET
777      002610 104003      ERROR    3      ;ERASE FAILED TO SET, CHECK G5036-
778      002612 012737 001000 001124 1S:      MOV      #BIT9,$GDDAT      ;LOAD EXPECTED
779      002620 052777 001000 176660      BIS      #BIT9,@VCSTAT      ;BCOBR CONNECTION: A-VV, VV-A
780      002626 017737 176654 001126      MOV      @VCSTAT,@BDDAT      ;SET CH 2 BIT
781      002634 023737 001124 001126      CMP      $GDDAT,@BDDAT      ;READ REG
782      002642 001401      BEQ      2S      ;COMPARE
783      002644 104003      ERROR    3      ;;BR IF CLEARED
784      ;ERASE BIT FAILED TO CLEAR BY ERASE
785      ;RETURN (SETTING OF THE CH 2 BIT)
786      002646 012737 000200 001124 2S:      MOV      #BIT7,$GDDAT      ;LOAD EXPECTED
787      002654 042777 001000 176624      BIC      #BIT9,@VCSTAT      ;CLEAR CH 02
788      002662 017737 176620 001126      MOV      @VCSTAT,@BDDAT      ;READ REG
789      002670 023737 001124 001126      CMP      $GDDAT,@BDDAT      ;COMPARE
790      002676 001401      BEQ      TST2      ;;BR IF EQUAL
791      002700 104003      ERROR    3      ;READY FAILED TO SET ON THE END
792      ; OF ERASE RETURN (CLEARING CH 2)
793      ::*****
794      :*TEST 2      EXTERNAL A TO D START FROM INTENSIFY
795      :*****
796      TST2:  SCOPE
797      002702 000004      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
798      002704 012737 000010 001164      CLR      @VCSTAT      ;CLEAR DONE
799      002712 005077 176570      MOV      @ADDBR,@ADDBR
800      002716 017777 176556 176554      CLR      @ADCS
801      002724 005077 176544      MOV      #BIT7!BIT4,$GDDAT      ;LOAD EXPECTED
802      002730 012737 000220 001124      MOV      $GDDAT,@ADCS      ;LOAD EXTERNAL A/D START ENABLE
803      002736 013777 001124 176530      INC      @VCSTAT      ;INTENSIFY
804      002744 005277 176536      TCTB    @VCSTAT      ;WAIT FOR READY
805      002750 105777 176532 1S:      BPL      IS
806      002754 100375      MOV      #BIT8,TEMP      ;SET UP A COUNTER
807      002756 012737 000400 015216 2S:      DEC      TEMP      ;DELAY
808      002764 005337 015216      BPL      2S
809      002770 100375      MOV      @ADCS,@BDDAT      ;READ REG.
810      002772 017737 176476 001126      CMP      $GDDAT,@BDDAT      ;COMPARE RESULTS
811      003000 023737 001124 001126      BEQ      3S      ;;BR IF EQUAL
812      003006 001401      ERROR    4      ;INTENSIFY PULSE FAILED TO START
813      003010 104004      MOV      @ADDBR,@ADDBR      ; AN A TO D CONVERSION
814      003012 017777 176462 176460 3S:      CLR      @VCSTAT
815      003020 005077 176462      CLR      @ADCS
816      003024 005077 176444

```

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018
019 003030 000004
020 003032 012737 000010 001164
021 003040 012737 001000 001124
022 003046 004537 011534
023 003052 000000
024 003054 013737 011656 001126
025 003062 012737 000001 011664
026 003070 004737 011666
027 003074 000401
028 003076 104001
029
030
031
032
033
034
035 003100 000004
036 003102 012737 000010 001164
037 003110 012737 000000 001124
038 003116 004537 011534
039 003122 000040
040 003124 013737 011656 001126
041 003132 012737 000001 011664
042 003140 004737 011666
043 003144 000401
044 003146 104001
045
046
047
048
049 003150 000004
050 003152 012737 000010 001164
051 003160 012737 001315 001124
052 003166 004537 011534
053 003172 000002
054 003174 013737 011656 001126
055 003202 012737 000024 011664
056 003210 004737 011666
057 003214 000401
058 003216 104001
059
060

```

```

*****
*TEST 3 TEST THAT CH 0 IS A BIPOLAR GROUND
*****
TST3: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1000,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH0 ;CH 0
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #1,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST4 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 0 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 4 TEST THAT CH 40 IS A UNIPOLAR GROUND
*****
TST4: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #0,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH40 ;CH 40
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #1,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST5 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 40 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 5 TEST THAT CH 2 IS AT +1 VOLT BIPOLAR
*****
TST5: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1315,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH2 ;CH 2
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #24,SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST6 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 2 FAILED TO EQUAL EXPECTED
; VALUE

```

H02

MAINDEC-11-DZARC-B
DZARCB.P11 T6

MACY11 27.732) 21-SEP-76 16:44 PAGE 20
TEST THAT CH 42 IS AT +1 VOLT UNIPOLAR

```
861      ::*****
862      ;*TEST 6      TEST THAT CH 42 IS AT +1 VOLT UNIPOLAR
863      ;******
864      TST6:  SCOPE
865      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
866      MOV      #315,$GDDAT    ;LOAD EXPECTED VALLE
867      JSR      R5,CONVRT      ;CONVERT
868      CH42     ;CH 42
869      MOV      ADEND,$BDDAT   ;LOAD VALUE READ
870      MOV      #24,$SPREAD    ;LOAD + OR - COUNT SPREAD
871      JSR      PC,COMPAR      ;COMPARE $GDDAT AND $BDDAT
872      BR       TST7          ;;BR IF WITHIN TOLERANCE
873      ERROR   1              ;CH 42 FAILED TO EQUAL EXPECTED
874      ; VALUE
875
876      ::*****
877      ;*TEST 7      TEST THAT CH 3 IS AT +2.5 BIPOLAR
878      ;******
879      TST7:  SCOPE
880      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
881      MOV      V1754,$GDDAT   ;LOAD EXPECTED VALLE
882      JSR      R5,CONVRT      ;CONVERT
883      CH3      ;CH 3
884      MOV      ADEND,$BDDAT   ;LOAD VALUE READ
885      MOV      V24,$SPREAD    ;LOAD + OR - COUNT SPREAD
886      JSR      PC,COMPAR      ;COMPARE $GDDAT AND $BDDAT
887      BR       TST10         ;;BR IF WITHIN TOLERANCE
888      ERROR   1              ;CH 3 FAILED TO EQUAL EXPECTED
889      ; VALUE
890
891      ::*****
892      ;*TEST 10     TEST THAT CH 43 IS AT +2.5 UNIPOLAR
893      ;******
894      TST10: SCOPE
895      MOV      #10,$TIMES      ;;DO 10 ITERATIONS
896      MOV      #1000,$GDDAT   ;LOAD EXPECTED VALUE
897      JSR      R5,CONVRT      ;CONVERT
898      CH43     ;CH 43
899      MOV      ADEND,$BDDAT   ;LOAD VALUE READ
900      MOV      V50,$SPREAD    ;LOAD + OR - COUNT SPREAD
901      JSR      PC,COMPAR      ;COMPARE $GDDAT AND $BDDAT
902      BR       TST11         ;;BR IF WITHIN TOLERANCE
903      ERROR   1              ;CH 43 FAILED TO EQUAL EXPECTED
904      ; VALUE
905
```

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908
909 003410 000004
910 003412 012737 000010 001164
911 003420 013737 016766 001124
912 003426 004537 011534
913 003432 000004
914 003434 013737 011656 001126
915 003442 013737 016766 011664
916 003450 004737 011666
917 003454 000401
918 003456 104001

```

```

*****
*TEST 11 TEST THAT CH 4 IS AT -2.5 BIPOLAR
*****
TST11: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV V24,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH4 ;CH 4
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV V24,$SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST12 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 4 FAILED TO EQUAL EXPECTED VALUE

```

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919
920
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923 003460 000004
924 003462 012737 000010 001164
925 003470 012737 001463 001124
926 003476 004537 011534
927 003502 000057
928 003504 013737 011656 001126
929 003512 012737 000120 011664
930 003520 004737 011666
931 003524 000401
932 003526 104001

```

```

*****
*TEST 12 TEST THAT CH 57 IS AT +4 VOLTS UNIPOLAR
*****
TST12: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #1463,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH57 ;CH 57
MOV ADEND,$BDDAT ;LOAD VALUE READ
MOV #120,$SPREAD ;LOAD + OR - COUNT SPREAD
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR TST13 ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 57 FAILED TO EQUAL EXPECTED VALUE

```

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933
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937 003530 000004
938 003532 012737 000004 001164
939 003540 012737 000041 011662
940 003546 112777 000041 175722
941 003554 013737 016772 001124
942 003562 013737 016772 011664
943 003570 012737 000200 015216
944 003576 005337 015216
945 003602 001375
946 003604 005277 175664
947 003610 105777 175660
948 003614 100375
949 003616 017737 175656 001126
950 003624 013737 001126 015164
951 003632 004737 011666
952 003636 000401
953 003640 104001
954
955 003642 032777 010000 175266
956 003650 001432
957 003652 104400 003660
958 003656 000412
959
960 003704
961 003704 013746 001464

```

```

*****
*TEST 13 TEST THAT CH 41 IS LESS THAN +200 MVOLTS UNIPOLAR
*****
TST13: SCOPE
MOV #4,$TIMES ;;DO 4 ITERATIONS
MOV #CH41,CHANL ;LOAD CHANNEL # FOR ERROR "YPCU"
MOV #CH41,$ADCS1 ;LOAD MUX
MOVB VA24,$GDDAT ;LOAD EXPECTED VALUE
MOV VA24,$SPREAD ;LOAD + OR - COUNT SPREAD
MOV #BIT7,TEMP ;LOAD DELAY
DEC TEMP ;DELAY
BNE 2$
INC $ADCS ;CONVERT CH 41
TSTB $ADCS ;DONE
BPL 3$
MOV $ADDBR,$BDDAT ;READ RESULTS
MOV $BDDAT,$BIASCI ;SAVE RESULTS
JSR PC,COMPAR ;COMPARE $GDDAT AND $BDDAT
BR 1$ ;;BR IF WITHIN TOLERANCE
ERROR 1 ;CH 41 FAILED TO EQUAL EXPECTED
; VALUE, BIAS CURRENT TOO HIGH
; TEST FORCED SW
1$: BIT #BIT12,$SWR
BEQ TST14 ;;BR IF NOT FORCED PRINTOUT
TYPE 65$ ;;TYPE ASCII STRING
BR 64$ ;;GET OVER THE ASCII
;;65$: .ASCIIZ <15><12><12>/AR-11 ADDRESS =
64$: MOV ARBADD,-(SP) ;SAVE ADDRESS

```

| | | | | |
|-----|--------|--------|--------|--|
| 962 | 003710 | 104401 | | |
| 963 | 003712 | 104400 | 016536 | |
| 964 | 003716 | 104400 | 013132 | |
| 965 | 003722 | 013746 | 015164 | |
| 966 | 003726 | 104402 | | |
| 967 | 003730 | 003 | 000 | |
| 968 | 003732 | 104400 | 013146 | |

```

TYP0C
TYPE ,ACRLF
TYPE ,BIASST
MOV ,BIASCI,-(SP)
TYPOS
.BYTE 3,0
TYPE ,BIASDN

```

```

*****
*TEST 14 TEST THAT ERASE L (CH 53) IS GREATER THAN +3 VOLTS WHEN HIGH
*****

```

| | | | | |
|-----|--------|--------|--------|--------|
| 971 | | | | |
| 972 | | | | |
| 973 | 003736 | 000004 | | |
| 974 | 003740 | 012737 | 000010 | 001164 |
| 975 | 003746 | 012777 | 005000 | 175532 |
| 976 | 003754 | 012737 | 001146 | 001124 |
| 977 | 003762 | 004537 | 011534 | |
| 978 | 003766 | 000053 | | |
| 979 | 003770 | 013737 | 011656 | 001126 |
| 980 | 003776 | 023737 | 001124 | 001126 |
| 981 | 004004 | 003401 | | |
| 982 | 004006 | 104002 | | |

```

†ST14: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT11!BIT9,@VCSTAT ;LOAD VC STATUS
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH53 ;CH 53
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST15 ;;BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 53 FAILED TO EQUAL EXPECTED VALUE

```

```

*****
*TEST 15 TEST THAT ERASE L (CH 53) IS LESS THAN +400 MVOLTS WHEN LOW
*****

```

| | | | | |
|-----|--------|--------|--------|--------|
| 983 | | | | |
| 984 | | | | |
| 985 | | | | |
| 986 | | | | |
| 987 | 004010 | 000004 | | |
| 988 | 004012 | 012737 | 000010 | 001164 |
| 989 | 004020 | 005077 | 175462 | |
| 990 | 004024 | 012777 | 012000 | 175454 |
| 991 | 004032 | 012737 | 000120 | 001124 |
| 992 | 004040 | 004537 | 011534 | |
| 993 | 004044 | 000053 | | |
| 994 | 004046 | 013737 | 011656 | 001126 |
| 995 | 004054 | 023737 | 001124 | 001126 |
| 996 | 004062 | 002001 | | |
| 997 | 004064 | 104007 | | |

```

†ST15: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
CLR @VCSTAT ;CLEAR VC STATUS
MOV #BIT12!BIT10,@VCSTAT ;SET ERASE
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH53 ;CH 53
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE TST16 ;;BR IF GREATER THAN OR EQUAL
ERROR 7 ;CH 53 FAILED TO EQUAL EXPECTED VALUE

```

```

*****
*TEST 16 TEST THAT WRITE-THRU (CH 54) IS GREATER THAN +3 VOLTS WHEN HIGH
*****

```

| | | | | |
|------|--------|--------|--------|--------|
| 998 | | | | |
| 999 | | | | |
| 1000 | | | | |
| 1001 | | | | |
| 1002 | 004066 | 000004 | | |
| 1003 | 004070 | 012737 | 000010 | 001164 |
| 1004 | 004076 | 012777 | 011000 | 175402 |
| 1005 | 004104 | 012737 | 001146 | 001124 |
| 1006 | 004112 | 004537 | 011534 | |
| 1007 | 004116 | 000054 | | |
| 1008 | 004120 | 013737 | 011656 | 001126 |
| 1009 | 004126 | 023737 | 001124 | 001126 |
| 1010 | 004134 | 003401 | | |
| 1011 | 004136 | 104002 | | |
| 1012 | | | | |

```

†ST16: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT12!BIT9,@VCSTAT ;LOAD VC STATUS
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH54 ;CH 54
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST17 ;;BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 54 FAILED TO EQUAL EXPECTED VALUE

```

K02

MAINDEC-11-DZARC-B
DZARC8.P11 T17

MACY11 27(732) 21-SEP-76 16:44 PAGE 23
TEST THAT WRITE-THRU (CH 54) IS LESS THAN +400 MVOLTS WHEN LOW

```

:013
1014
1015
1016 004140 000004
1017 004142 012737 000010 001164
1018 004150 012777 006000 175330
1019 004156 012737 000120 001124
1020 004164 004537 011534
1021 004170 000054
1022 004172 013737 011656 001126
1023 004200 023737 001124 001126
1024 004206 002001
1025 004210 104007
1026
1027
1028
1029
1030
1031 004212 000004
1032 004214 012737 000010 001164
1033 004222 005077 175260
1034 004226 012737 000120 001124
1035 004234 004537 011534
1036 004240 000055
1037 004242 013737 011656 001126
1038 004250 023737 001124 001126
1039 004256 002001
1040 004260 104007
1041
1042
1043
1044
1045
1046 004262 000004
1047 004264 012737 000010 001164
1048 004272 012777 017000 175206
1049 004300 012737 001146 001124
1050 004306 004537 011534
1051 004312 000055
1052 004314 013737 011656 001126
1053 004322 023737 001124 001126
1054 004330 003401
1055 004332 104002
1056
1057

```

```

:*****
:*TEST 17 TEST THAT WRITE-THRU (CH 54) IS LESS THAN +400 MVOLTS WHEN LOW
:*****

```

```

†ST17: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT11!BIT10,$VCSTAT ;SET WRITE-THRU
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH54 ;CH 54
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE TST20 ;;BR IF GREATER THAN OR EQUAL
ERROR 7 ;CH 54 FAILED TO EQUAL EXPECTED
; VALUE

```

```

:*****
:*TEST 20 TEST THAT NON-STORE (CH 55) IS LESS THAN +400 MVOLTS WHEN LOW
:*****

```

```

†ST20: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
CLR $VCSTAT ;CLEAR STORE MODE
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH55 ;CH 55
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE TST21 ;;BR IF GREATER THAN OR EQUAL
ERROR 7 ;CH 54 FAILED TO EQUAL EXPECTED
; VALUE

```

```

:*****
:*TEST 21 TEST THAT NON-STORE L (CH 55) IS GREATER THAN +3 VOLTS WHEN HIGH
:*****

```

```

†ST21: SCOPE
MOV #10,$TIMES ;;DO 10 ITERATIONS
MOV #BIT12!BIT11!BIT10!BIT9,$VCSTAT ;SET STORE MODE
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH55 ;CH 55
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST22 ;;BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 55 FAILED TO EQUAL EXPECTED
; VALUE

```

L02

MAINDEC-11-DZARC-B
DZARCB.P11 T22

MACY11 27(732) 21-SEP-76 16:44 PAGE 24
TEST THAT CHANNEL 02 L (CH 56) IS GREATER THAN +3 VOLTS WHEN HIGH

```

1058
1059
1060
1061 004334 000004
1062 004336 012737 000010 001164
1063 004344 012777 014000 175134
1064 004352 012737 001146 001124
1065 004360 004537 011534
1066 004364 000056
1067 004366 013737 011656 001126
1068 004374 023737 001124 001126
1069 004402 003401
1070 004404 104002
1071
1072
1073
1074
1075
1076 004406 000004
1077 004410 012737 000010 001164
1078 004416 012777 003000 175062
1079 004424 012737 000120 001124
1080 004432 004537 011534
1081 004436 000056
1082 004440 013737 011656 001126
1083 004446 023737 001124 001126
1084 004454 002001
1085 004456 104007
1086
1087 004460 005077 175022
1088

```

```

*****
*TEST 22 TEST THAT CHANNEL 02 L (CH 56) IS GREATER THAN +3 VOLTS WHEN HIGH
*****
TST22: SCOPE
MOV #10,$TIMES ;:DO 10 ITERATIONS
MOV #BIT12!BIT11,@VCSTAT ;LOAD VC STATUS
MOV #1146,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH56 ;CH 56
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BLE TST23 ;:BR IF LESS THAN OR EQUAL
ERROR 2 ;CH 56 FAILED TO EQUAL EXPECTED
; VALUE

```

```

*****
*TEST 23 TEST THAT CHANNEL 02 L (CH 56) IS LESS THAN +400 M.VOLTS WHEN LOW
*****
TST23: SCOPE
MOV #10,$TIMES ;:DO 10 ITERATIONS
MOV #BIT9!BIT10,@VCSTAT ;SET CHANNEL 2
MOV #120,$GDDAT ;LOAD EXPECTED VALUE
JSR R5,CONVRT ;CONVERT
CH56 ;CH 56
MOV ADEND,$BDDAT ;LOAD VALUE READ
CMP $GDDAT,$BDDAT ;COMPARE
BGE 1$ ;:BR IF GREATER THAN
ERROR 7 ;CH 56 FAILED TO EQUAL EXPECTED
; VALUE
1$: CLR @VCSTAT ;CLEAR BIT 9

```

```

1099          ;:*****
1090          ;*TEST 24          A TO D DIFFERENTIAL LINEARITY TEST
1091          ;:*****
1092 004464 000004          TST24: SCOPE
1093 004466 012737 000001 001164      MOV      #1,$TIMES          ;;DO 1 ITERATION
1094          JSR      R5,DIFLIN          ;START TEST
1095 004474 004537 015220          VCXREG          ;X COARSE
1096 004500 001510          VCYREG          ;Y FINE
1097 004502 001512          .BYTE      1,5          ;GO AND CHANNEL 5
1098 004504      001      005
1099          CLR      $GDDAT          ;CLEAR EXPECTED
1100 004506 005037 001124          MOV      SKIPST,$BDDAT      ;LOAD # OF SKIPPED STATES
1101 004512 013737 015740 001126      BEQ      1$          ;;BR IF NO SKIPPED STATES
1102 004520 001401          ERROR      5          ;SKIPPED STATE(S) DETECTED
1103 004522 104005
1104          MOV      EXCESS,$BDDAT      ;LOAD # OF >2LSB STATES
1105 004524 013737 015744 001126 1$:      BEQ      2$          ;;BR IF NO GREATER THAN 2LSB WIDE STATES
1106 004532 001401          ERROR      5          ;> 2LSB WIDE STATE(S) DETECTED
1107 004534 104005
1108          MOV      DIFERR,$BDDAT      ;LOAD # OUTSIDE +- 5/2 LSB
1109 004536 013737 015742 001126 2$:      MOV      #52,$GDDAT      ;LOAD MAX # OF ALLOWABLE STATES OUTSIDE +- 1/2 L
1110 004544 012737 000064 001124      CMP      $GDDAT,$BDDAT      ;ANY ERRORS
1111 004552 023737 001124 001126      BGE      TST25          ;;BR IF NO ERROR
1112 004560 002001          ERROR      5          ;DIFFERENTIAL LINEARITY ERROR, >5% OF STATE
1113 004562 104005          ;WIDTHS OUTSIDE +- 1/2 LSB
1114
1115

```

```

1116 ::*****
1117 :*TEST 25 X D/A DIFFERENTIAL LINEARITY USING CH 5
1118 :*****
1119 004564 000004 TST25: SCOPE
1120 004566 012737 000010 00:164 MOV #10,$TIMES ;;DO 10 ITERATIONS
1121 004574 012701 000001 MOV #1,R1 ;LOAD EDGE VALUE
1122 004600 012702 015022 MOV #RSLT2,R2 ;LOAD RESULT POINTER
1123
1124 004604 010177 174700 18: MOV R1,$VXREG ;LOAD X DAC WITH PRESET
1125 004610 010137 004624 MOV R1,10$ ;LOAD EDGE VALUE
1126
1127 004614 004437 010716 JSR R4,SAR ;SOFTWARE SUCCESSIVE APPROX. ROUTINE
1128 004620 001512 VCYREG
1129 004622 000 005 .BYTE 0,$H5
1130 004624 000000 10$: 0 ;EDGE VALUE
1131 004626 000200 BIT7 ;50-50
1132
1133 004630 013737 015122 004746 MOV DACSAV,12$ ;SAVE VALUE
1134 004636 005377 174646 DEC $VXREG ;LOAD X DAC
1135 004642 010137 004656 MOV R1,11$ ;LOAD SAME EDGE
1136
1137 004646 004437 010716 JSR R4,SAR ;SOFTWARE S.A.R.
1138 004652 001512 VCYREG
1139 004654 000 005 .BYTE 0,$H5
1140 004656 000000 11$: 0
1141 004660 000200 BIT7
1142
1143 004662 013737 015122 004750 MOV DACSAV,13$ ;SAVE VALUE
1144 004670 013737 015122 001126 MOV DACSAV,$BDDAT ;LOAD VALUE INTO $BDDAT
1145 004676 163737 004746 001126 SUB 12$, $BDDAT ;STEP HEIGHT IN $BDDAT
1146 004704 013722 001126 MOV $BDDAT, R21+ ;SAVE RESULT
1147 004710 012737 000052 001124 MOV #62,$GDDAT ;LOAD EXPECTED VALUE FOR 1 LSB STEP
1148 004716 013737 016774 011664 MOV V62,$SPREAD ;LOAD TOLERANCE
1149 004724 004737 011666 JSR PC,COMPAR ;TEST LIMITS
1150 004730 000401 BR 2$ ;;BR IF WITHIN LIMITS
1151 004732 104010 ERROR 10 ;D/A DIFFERENTIAL LINEARITY ERROR
1152 004734 006301 2$: ASL R1 ;MOVE LEFT
1153 004736 020127 002000 CMP R1,#2000 ;DONE
1154 004742 001329 BNE 1$ ;;BR IF NOT DONE
1155 004744 000402 BR TST26 ;;
1156
1157 004746 000000 12$: 0
1158 004750 000000 13$: 0

```

```

*****
>TEST 26      Y D A DIFFERENTIAL LINEARITY USING CH 6
*****
TST26:  SCOPE
MOV      #10,STIMES      ;;DO 10 ITERATIONS
MOV      #1,R1           ;LOAD EDGE VALUE
MOV      @RSLT3,R2      ;LOAD RESULT POINTER

10:      MOV      R1,@VCYREG ;LOAD Y DAC WITH PRESET
MOV      R1,105         ;LOAD EDGE VALUE

        JSR      R4,SAR   ;SOFTWARE S.A.R.
        VCYREG
        .BYTE   0,CH6
105:     0         ;EDGE VALUE
        BIT7

        MOV      DACSAV,125 ;SAVE VALUE
        DEC     @VCYREG     ;LOAD Y DAC
        MOV      R1,115    ;LOAD EDGE

        JSR      R4,SAR   ;SOFTWARE S.A.R.
        VCYREG
        .BYTE   0,CH6
115:     0         ;EDGE VALUE
        BIT7

        MOV      DACSAV,135 ;SAVE RESULTS
        MOV      DACSAV,@SBCDAT ;LOAD SBCDAT
        SUB     125,@SBCDAT ;STEP HEIGHT IN SBCDAT
        MOV      @SBCDAT,(R2)+ ;SAVE RESULT
        MOV      #62,@SBCDAT ;LOAD EXPECTED
        MOV      @V62,@SPREAD ;LOAD TOLERANCE
        JSR      @PC,@COMPAR ;TEST IF WITHIN LIMITS
        BR     25         ;;BR IF WITHIN LIMITS
        ERROR  10        ;D A DIFFERENTIAL LINEARITY ERROR
25:      RSL     R1
        CMP     R1,#2000 ;DONE ?
        BNE    15        ;;BR IF NOT DONE
        BIT    @BIT12,@SWR ;TEST FORCED PRINTOUT
        BEQ    TST27     ;;BR IF NOT FORCED PRINTOUT
        TYPE   01FMSG
        JSR    @R5,@TYPSTG ;TYPE A STRING OF OCTAL #
        RSLT2 ;STARTING
        IC     ;# OF LOCATIONS

        TYPE   01YMSG
        JSR    @R5,@TYPSTG ;TYPE A STRING OF OCTAL #
        RSLT3 ;# OF LOCATIONS
        IC     ;# OF LOCATIONS
        TYPE   01EMSG
        BR     TST27     ;;
126:     0
135:     0

```

*TEST 27 A TO D POSITIVE MULTIFLEXER SETTling TEST

```

*STRT: SCOPE
MOV #10,STIMES ::DO 10 ITERATIONS
MOV #1770,SVCYREG :LOAD Y DAC
JSR R4,SAR :SOFTWARE S.A.R.
VCYREG
.BYTE 0.6
I771
BIT?

MOV DACSAV,IOS :SAVE RESULTS
JSR R4,SAR :SOFTWARE S.A.R.
VCYREG
.BYTE CH4,CH5 :-2.5V CH 4 AND CH 5
I771 :EDGE
BIT?

MOV DACSAV,SBDAT :SAVE RESULT IN SBDAT
SUB IOS,SBDAT :SUB FIRST VALUE
MOV SBDAT,ADPMUX :SAVE RESULT
MOV #0,SBDAT :LOAD EXPECTED SETTling ERROR
MOV #77,SPREAD :LOAD TOLERANCE
JSR PC,COMPAR :TEST IF WITHIN LIMITS
BR TS+30 ::BR IF WITHIN
BR ERROR :AND POSITIVE SETTling ERROR
TS+30
::

```

```

005234 000004
005236 012737 000010 001164
005238 012737 001770 174270
005240 004437 010716
005242 001510
005244 000000 006
005246 001771
005248 000200

005236 013737 015122 005330
005244 004437 010716
005252 001510
005254 004 006
005256 001771
005258 000200

005260 013737 015122 001126
005266 163737 005330 001126
005274 013737 001126 015160
005302 012737 000000 001124
005310 012737 000077 011554
005318 004437 011655
005326 000103
005334 104011
005342 004011
005350 000000
005358 000000

```

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12299
12300

005332 000004
005334 012737 000010 001164
005342 012777 000010 174142
005350 004437 010716
005354 001510
005356 000 006
005360 000010
005362 000200

005364 013737 015122 005522
005372 004437 010716
005376 001510
005400 000 006
005404 000010
005404 000200

005406 013737 015122 001126
005414 163737 005522 001126
005422 013737 001126 015162
005430 012737 000000 001124
005436 012737 000077 011664
005444 004737 011666
005450 000401
005452 104011
005454 032777 010000 173454 15:
005462 001420
005464 104400 013436
005470 013746 015160
005474 104402
005476 000 000
005500 104400 013525
005504 013746 015162
005510 104402
005512 000 000
005514 104400 016536
005516 000401
005518 000401
005520 000000
005522 000000

```
::*****  
: *TEST 30 A TO D NEGATIVE MULTIPLEXER SETTling TEST  
: *****  
†TST30: SCOPE  
MOV #10, $TIMES ;; DO 10 ITERATIONS  
MOV #10, $VCYREG ;LOAD Y DAC  
JSR R4, $AR ;SOFTWARE S.A.R.  
VCXREG  
.BYTE 0,6  
LD  
BIT7  
  
MOV DACSAV, 10$ ;SAVE RESULTS  
JSR R4, $AR ;SOFTWARE S.A.R.  
VCXREG  
.BYTE CH3, CH6 ;+2.5V CH AND CH 6  
LD ;EDGE  
BIT7  
  
MOV DACSAV, $BDDAT ;SAVE RESULT IN $BDDAT  
SUB 10$, $BDDAT ;SUB FIRST VALUE  
MOV $BDDAT, ADNMUX ;SAVE RESULTS  
MOV #0, $GDDAT ;LOAD EXPECTED ZERO SETTling ERROR  
MOV #77, $SPREAD ;LOAD TOLERANCE  
JSR PC, $COMPAR ;TEST IF WITHIN LIMITS  
BR 1$ ;; BR IF WITHIN  
ERROR ;A/D NEGATIVE SETTling ERROR  
BIT #BIT12, $SWR ;TEST FOR FORCED TYPEOUT  
BEQ TST31 ;; BR IF NOT FORCED  
TYPE ADMSG  
MOV ADPMUX, -(SP) ;SAVE MUX SETTling  
TYPOS  
.BYTE 3, 0  
TYPE ADMSG  
MOV ACNMUX, -(SP) ;SAVE MUX SETTling  
TYPOS  
.BYTE 3, 0  
TYPE ACRLF  
BR †TST31 ;;  
  
10$: 0
```

E03

MA:NDCC-11-DZARC-8
DZARCB.P11 T31

MACY11 27(732) 21-SEP-76 16:44 PAGE 30
A/D RMS NOISE TEST ON CHANNEL 7 - BIPOLAR

```

*****
*TEST 31      A/D RMS NOISE TEST ON CHANNEL 7 - BIPOLAR
*****
↑TST31: SCOPE
MOV      #10,$TIMES      ;;DO 10 ITERATIONS

JSR      R4,SAR          ;S A R ROUTINE
VCYREG   ;Y AXIS
.BYTE    0,7            ;CHANNEL 7, FINE Y
1000     ;EDGE VALUE
C        ;RMS RESULTS

MOV      R5,$BDDAT      ;SAVE RESULTS (2X RMS NOISE, 1=1/50 LSB)
MOV      R5,CH7RMS      ;SAVE RESULTS (RMS NOISE, 1 = 1/100 LSB)
MOV      RMSMAX,$GDDAT  ;LOAD EXPECTED
CMP      $GDDAT,$BDDAT  ;COMPARE RESULTS
BGE      TST32          ;;BR IF NOISE WITHIN SPEC
ERROR    6              ;A/D RMS NOISE (BIPOLAR) IS GREATER
; THAN SPEC

```

```

*****
*TEST 32      A/D PEAK NOISE TEST ON CHANNEL 7 - BIPOLAR
*****
↑TST32: SCOPE
MOV      #10,$TIMES      ;;DO 10 ITERATIONS

JSR      R4,SAR          ;S A R ROUTINE
VCYREG   ;Y AXIS
.BYTE    0,7            ;CHANNEL 7, FINE Y
1000     ;EDGE VALUE
BIT15    ;PEAK RESULTS

MOV      R5,$BDDAT      ;SAVE RESULTS (PEAK NOISE, 1 = .01 LSB)
MOV      R5,CH7PEK      ;SAVE RESULTS
MOV      PEKMAX,$GDDAT  ;LOAD EXPECTED
CMP      $GDDAT,$BDDAT  ;COMPARE RESULTS
BGE      TST33          ;;BR IF NOISE WITHIN SPEC
ERROR    6              ;A/D PEAK NOISE (BIPOLAR) IS GREATER
; THAN SPEC

```

F03

MAINDEC-11-DZARC-B
DZARC8.P11 T33

MACY11 27(732) 21-SEP-76 16:44 PAGE 31
A/D RMS NOISE TEST ON CHANNEL 47 - UNIPOLAR

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```

*****
*TEST 33      A/D RMS NOISE TEST ON CHANNEL 47 - UNIPOLAR
*****
†ST33:  SCOPE
        MOV      #10,$TIMES      ;;DO 10 ITERATIONS
        JSR      R4,$AR          ;S A R ROUTINE
        VCYREG   ;Y AXIS
        BYTE    0,$CH47        ;CHANNEL 47, FINE Y
        3        ;EDGE VALUE
        0        ;RMS RESULTS
        MOV      R5,$BDDAT      ;SAVE RESULTS (RMS NOISE, 1=.01 LSB)
        MOV      R5,$CH47RM     ;SAVE RESULT
        MOV      RM$MAX,$GDDAT  ;LOAD EXPECTED
        CMP      $GDDAT,$BDDAT  ;COMPARE RESULTS
        BGE     TST34          ;;BR IF NOISE WITHIN SPEC
        ERROR   6              ;A/D RMS NOISE (UNIPOLAR) IS GREATER
                               ; THAN SPEC

```

```

*****
*TEST 34      A/D PEAK NOISE TEST ON CHANNEL 47 - UNIPOLAR
*****
†ST34:  SCOPE
        MOV      #10,$TIMES      ;;DO 10 ITERATIONS
        JSR      R4,$AR          ;S A R ROUTINE
        VCYREG   ;Y AXIS
        BYTE    0,$CH47        ;CHANNEL 47, FINE Y
        3        ;EDGE VALUE
        BIT15    ;PEAK RESULTS
        MOV      R5,$BDDAT      ;SAVE RESULTS (PEAK NOISE, 1 = .01 LSB)
        MOV      R5,$CH47PK     ;SAVE RESULT
        MOV      PEKMAX,$GDDAT  ;LOAD EXPECTED
        CMP      $GDDAT,$BDDAT  ;COMPARE RESULTS
        BGE     TST35          ;;BR IF NOISE WITHIN SPEC
        ERROR   6              ;A/D PEAK NOISE (UNIPOLAR) IS GREATER
                               ; THAN SPEC

```

| | | | |
|--------|--------|--------|--------|
| 005654 | 000004 | 000010 | 001164 |
| 005656 | 012737 | | |
| 005664 | 004437 | 010716 | |
| 005670 | 001512 | | |
| 005672 | 000 | 047 | |
| 005674 | 000003 | | |
| 005676 | 000000 | | |
| 005700 | 010537 | 001126 | |
| 005704 | 010537 | 015134 | |
| 005710 | 013737 | 015124 | 001124 |
| 005716 | 023737 | 001124 | 001126 |
| 005724 | 002001 | | |
| 005726 | 104006 | | |
| 005730 | 000004 | 000010 | 001164 |
| 005732 | 012737 | | |
| 005740 | 004437 | 010716 | |
| 005744 | 001512 | | |
| 005746 | 000 | 047 | |
| 005750 | 000003 | | |
| 005752 | 100000 | | |
| 005754 | 010537 | 001126 | |
| 005760 | 010537 | 015136 | |
| 005764 | 013737 | 015126 | 001124 |
| 005772 | 023737 | 001124 | 001126 |
| 006000 | 002001 | | |
| 006002 | 104006 | | |

G03

MAINDEC-11-DZARC-B
DZARCB.P11 T35

MACY11 27(732) 21-SEP-76 16:44 PAGE 32
X DAC RMS NOISE TEST ON CHANNEL 5

```

1364
1365
1366
1367 006004 000004
1368 006006 012737 000010 001164
1369 006014 012777 001000 173466
1370
1371 006022 004437 010716
1372 006026 001512
1373 006030 000 005
1374 006032 001000
1375 006034 000000
1376
1377 006036 010537 001126
1378 006042 163737 015130 001126
1379 006050 013737 001126 015142
1380 006056 013737 015124 001124
1381 006064 023737 001124 001126
1382 006072 002001
1383 006074 104006
1384
1385
1386
1387
1388
1389
1390 006076 000004
1391 006100 012737 000010 001164
1392
1393 006106 012777 001000 173374
1394 006114 004437 010716
1395 006120 001512
1396 006122 000 005
1397 006124 001000
1398 006126 100000
1399
1400 006130 010537 001126
1401 006134 163737 015130 001126
1402 006142 013737 001126 015142
1403 006150 013737 015126 001124
1404 006156 023737 001124 001126
1405 006164 002001
1406 006166 104006

```

```

*****
*TEST 35 X DAC RMS NOISE TEST ON CHANNEL 5
*****
†ST35: SCOPE
MOV #10, $TIMES ;;DO 10 ITERATIONS
MOV #1000, @VCXREG ;LOAD X POS
JSR R4, SAR ;S A R ROUTINE
VCYREG ;Y AXIS
.BYTE 0.5 ;CHANNEL 5, COARSE X AND FINE Y
1000 ;EDGE VALUE
0 ;RMS RESULTS
MOV R5, $BDDAT ;SAVE RESULTS (X DAC + A/D RMS NOISE, 1=.01 LSB)
SUB CH7RMS, $BDDAT ;X DAC RMS NOISE ONLY
MOV $BDDAT, XDACRM ;SAVE RESULT
MOV RMSMAX, $GDDAT ;LOAD EXPECTED
CMP $GDDAT, $BDDAT ;COMPARE RESULTS
BGE TST36 ;;BR IF NOISE WITHIN SPEC
ERROR 6 ;X D/A RMS NOISE IS GREATER
; THAN SPEC
*****
*TEST 36 X DAC PEAK NOISE TEST ON CHANNEL 5
*****
†ST36: SCOPE
MOV #10, $TIMES ;;DO 10 ITERATIONS
MOV #1000, @VCXREG ;LOAD X DAC
JSR R4, SAR ;S A R ROUTINE
VCYREG ;Y AXIS
.BYTE 0.5 ;CHANNEL 5, COARSE X AND FINE Y
1000 ;EDGE VALUE
BITS ;PEAK RESULTS
MOV R5, $BDDAT ;SAVE RESULTS (X DAC + A/D PEAK NOISE, 1=.01 LSB)
SUB CH7PEK, $BDDAT ;X DAC PEAK NOISE ONLY
MOV $BDDAT, XDACPK ;SAVE RESULT
MOV PEKMAX, $GDDAT ;LOAD EXPECTED
CMP $GDDAT, $BDDAT ;COMPARE RESULTS
BGE TST37 ;;BR IF NOISE WITHIN SPEC
ERROR 6 ;X D/A PEAK NOISE IS GREATER
; THAN SPEC

```

H03

MAINDEC-11-DZARC-B
DZARCB.P11 T37

MACY11 27(732) 21-SEP-76 16:44 PAGE 33
Y DAC RMS NOISE TEST ON CHANNEL 6

```

1409          ::*****
1409          :*TEST 37      Y DAC RMS NOISE TEST ON CHANNEL 6
1410          :*****
1411          †TST37: SCOPE
1412          MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1413
1414          MOV      #1000,$VCYREG    ;LOAD Y REG
1415          JSR      R4,SAR          ;S A R ROUTINE
1416          VCXREG    ;X AXIS
1417          .BYTE    0.6             ;CHANNEL 6 ,COARSE Y AND FINE X
1418          1000          ;EDGE VALUE
1419          0             ;RMS RESULTS
1420
1421          MOV      R5,$BDDAT        ;SAVE RESULTS (Y DAC + A/D RMS NOISE, 1=.01 LSB)
1422          SUB      CH7RMS,$BDDAT    ;Y DAC RMS NOISE ONLY
1423          MOV      $BDDAT,YDACRM    ;SAVE RESULT
1424          MOV      RMSMAX,$GDDAT    ;LOAD EXPECTED
1425          CMP      $GDDAT,$BDDAT    ;COMPARE RESULTS
1426          BGE     TST40            ;;BR IF NOISE WITHIN SPEC
1427          ERROR    6               ;Y D/A RMS NOISE IS GREATER
1428          ; THAN SPEC
1429
1430          ::*****
1431          :*TEST 40      Y DAC PEAK NOISE TEST ON CHANNEL 6
1432          :*****
1433          †TST40: SCOPE
1434          MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1435
1436          MOV      #1000,$VCYREG    ;LOAD Y AXIS
1437          JSR      R4,SAR          ;S A R ROUTINE
1438          VCXREG    ;X AXIS
1439          .BYTE    0.6             ;CHANNEL 6 ,COARSE Y AND FINE X
1440          1000          ;EDGE VALUE
1441          BIT15          ;PEAK RESULTS
1442
1443          MOV      R5,$BDDAT        ;SAVE RESULTS (Y DAC + A/D PEAK NOISE, 1=.01 LSB)
1444          SUB      CH7PEK,$BDDAT    ;Y DAC PEAK NOISE ONLY
1445          MOV      $BDDAT,YDACPK    ;SAVE RESULT
1446          MOV      PEKMAX,$GDDAT    ;LOAD EXPECTED
1447          CMP      $GDDAT,$BDDAT    ;COMPARE RESULTS
1448          BGE     15              ;;BR IF NOISE WITHIN SPEC
1449          ERROR    6               ;Y DAC PEAK NOISE IS GREATER
1450          ; THAN SPEC
1451
1452          006354 032777 010000 172554 15: BIT      #BIT12,$SWR    ;TEST FORCED PRINTOUT
1453          006362 001432          BEQ      TST41          ;;BR IF NOT FORCED
1454          006364 104400 013543          TYPE     ,NOIMSG
1455          006370 004537 011466          JSR      R5,BYTW0    ;TYPE 2 OCTAL COL.
1456          006374 015130          CH7RMS
1457          006376 015132          CH7PEK
1458          006400 104400 013664          TYPE     ,NOIMG1
1459          006404 004537 011466          JSR      R5,BYTW0    ;TYPE 2 OCTAL COL.
1460          006410 015134          CH47RM
1461          006412 015136          CH47PK
1462          006414 104400 013707          TYPE     ,NOIMG2
1463          006420 004537 011466          JSR      R5,BYTW0    ;TYPE 2 OCTAL COL.

```

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DZARCB.P11

T40

MACY11 27(732) 21-SEP-76 16:44 PAGE 34
Y DAC PEAK NOISE TEST ON CHANNEL 6

| | | | | |
|------|--------|--------|--------|--|
| 1464 | 006424 | 015140 | | |
| 1465 | 006426 | 015142 | | |
| 1466 | 006430 | 104400 | 013732 | |
| 1467 | 006434 | 004537 | 011466 | |
| 1468 | 006440 | 015144 | | |
| 1469 | 006442 | 015146 | | |
| 1470 | 006444 | 104400 | 016536 | |

```

XDACRM
XDACPK
TYPE      NOIMG3
JSR      R5,BY TWO
YDACRM
YDACPK
TYPE      ,ACRLF

```

```

*****
*TEST 41      BIPOLAR A/D OFFSET USING CH 7 WITH WAIT INTERRUPT
*****

```

| | | | | |
|------|--------|--------|--------|--------|
| 1471 | | | | |
| 1472 | | | | |
| 1473 | | | | |
| 1474 | | | | |
| 1475 | 006450 | 000004 | | |
| 1476 | 006452 | 012737 | 000010 | 001164 |
| 1477 | 006460 | 004437 | 010716 | |
| 1478 | 006464 | 001512 | | |
| 1479 | 006466 | 000 | 007 | |
| 1480 | 006470 | 001001 | | |
| 1481 | 006472 | 000200 | | |
| 1482 | | | | |
| 1483 | 006474 | 013737 | 015122 | 015150 |
| 1484 | 006502 | 013737 | 015122 | 001126 |
| 1485 | 006510 | 012737 | 001014 | 001124 |
| 1486 | 006516 | 013737 | 016774 | 011664 |
| 1487 | 006524 | 004737 | 011666 | |
| 1488 | 006530 | 000401 | | |
| 1489 | 006532 | 104012 | | |

```

†TST41:  SCOPE
          MOV      #10,$TIMES      ;;DO 10 ITERATIONS
          JSR      R4,$AR          ;SOFTWARE S.A.R.
          VCYREG
          .BYTE   0,CH7           ;CHANNEL 7, FINE Y
          1001                   ;EDGE 1000/1001 TRANSITION
          BIT7                     ;50-50
          MOV      DACSAV,OFSTBX   ;SAVE A/D OFFSET BIPOLAR
          MOV      DACSAV,$BDDAT   ;LOAD VALUE READ
          MOV      #1014,$GDDAT    ;LOAD EXPECTED
          MOV      V62,$SPREAD     ;LOAD +- VARIABLE, 1LSB
          JSR      PC,COMPAR       ;TEST IF WITHIN +- VALJE
          BR      TST42           ;;BR IF WITHIN LIMITS
          ERROR   12              ;A/D BIPOLAR OFFSET ERROR

```

```

*****
*TEST 42      UNIPOLAR A/D OFFSET USING CH 7 WITH WAIT INTERRUPT
*****

```

| | | | | |
|------|--------|--------|--------|--------|
| 1490 | | | | |
| 1491 | | | | |
| 1492 | | | | |
| 1493 | | | | |
| 1494 | 006534 | 000004 | | |
| 1495 | 006536 | 012737 | 000010 | 001164 |
| 1496 | 006544 | 004437 | 010716 | |
| 1497 | 006550 | 001512 | | |
| 1498 | 006552 | 000 | 047 | |
| 1499 | 006554 | 000001 | | |
| 1500 | 006556 | 000200 | | |
| 1501 | | | | |
| 1502 | 006560 | 013737 | 015122 | 015152 |
| 1503 | 006566 | 013737 | 015122 | 001126 |
| 1504 | 006574 | 012737 | 001014 | 001124 |
| 1505 | 006602 | 013737 | 016774 | 011664 |
| 1506 | 006610 | 004737 | 011666 | |
| 1507 | 006614 | 000401 | | |
| 1508 | 006616 | 104012 | | |

```

†TST42:  SCOPE
          MOV      #10,$TIMES      ;;DO 10 ITERATIONS
          JSR      R4,$AR          ;SOFTWARE S.A.R.
          VCYREG
          .BYTE   0,CH47         ;CHANNEL 7, UNIPOLAR FINE Y
          1                   ;EDGE 0/1 TRANSITION
          BIT7                     ;50-50
          MOV      DACSAV,OFSTUX   ;SAVE A/D OFFSET UNIPOLAR
          MOV      DACSAV,$BDDAT   ;LOAD VALUE READ
          MOV      #1014,$GDDAT    ;LOAD EXPECTED
          MOV      V62,$SPREAD     ;LOAD +- VARIABLE, 1 LSB
          JSR      PC,COMPAR       ;TEST IF WITHIN +- VALJE
          BR      TST43           ;;BR IF WITHIN LIMITS
          ERROR   12              ;A/D UNIPOLAR OFFSET ERROR

```

J03

MAINDEC-11-DZARC-B
DZARCB.P11 T43

MACY11 27(732) 21-SEP-76 16:44 PAGE 35
BIPOLAR A/D OFFSET USING CH 7 WITH WAIT LOOP

```

1509                                     ::*****
1510                                     ::*TEST 43      BIPOLAR A/D OFFSET USING CH 7 WITH WAIT LOOP
1511                                     ::*****
1512 006620 000004                          †ST43: SCOPE
1513 006622 012737 000010 001164          MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1514 006630 004437 010716                  JSR      R4,$AR         ;;SOFTWARE S.A.R.
1515 006634 001512                          VCYREG
1516 006636      000      007              .BYTE   0,CH7          ;CHANNEL 7, FINE Y
1517 006640 001001                          1001                ;EDGE, 1000/1001 TRANSITION
1518 006642 000201                          BIT7!BIT0           ;50-50, WAIT LOOP
1519
1520 006644 013737 015122 015154          MOV      DACSAV,OFSLBX ;SAVE A/D OFFSET BIPOLAR
1521 006652 013737 015122 001126          MOV      DACSAV,$BDDAT ;LOAD VALUE READ
1522 006660 012737 001014 001124          MOV      #1014,$GDDAT ;LOAD EXPECTED
1523 006666 013737 016774 011664          MOV      V62,$SPREAD  ;LOAD +- VARIABLE, 1 LSB
1524 006674 004737 011666                  JSR      PC,COMPARE   ;TEST IF WITHIN +- VALUE
1525 006700 000401                          BR       1$           ;;BR IF WITHIN LIMITS
1526 006702 104012                          ERROR    12          ;A/D BIPOLAR OFFSET ERROR
1527
1528 006704 163737 015150 001126 1$:     SUB      OFSTBX,$BDDAT ;OFFSET DUE TO WAIT LOOP
1529 006712 005037 001124                  CLR      $GDDAT
1530 006716 012737 000031 011664          MOV      #31,$SPREAD  ;1/2 LSB ALLOWED
1531 006724 004737 011666                  JSR      PC,COMPARE   ;WITHIN LIMITS
1532 006730 000401                          BR       1$           ;;BR IF WITHIN LIMITS
1533 006732 104012                          ERROR    12          ;A/D BIPOLAR OFFSET ERROR DUE TO WAIT LOOP
1534
1535                                     ::*****
1536                                     ::*TEST 44      UNIPOLAR A/D OFFSET USING CH 7 WITH WAIT LOOP
1537                                     ::*****
1538 006734 000004                          †ST44: SCOPE
1539 006736 012737 000010 001164          MOV      #10,$TIMES      ;;DO 10 ITERATIONS
1540 006744 004437 010716                  JSR      R4,$AR         ;;SOFTWARE S.A.R.
1541 006750 001512                          VCYREG
1542 006752      000      047              .BYTE   0,CH47       ;CHANNEL 7, UNIPOLAR, FINE Y
1543 006754 000001                          1                    ;EDGE, 0/1 TRANSITION
1544 006756 000201                          BIT7!BIT0           ;50-50, WAIT LOOP
1545
1546 006760 013737 015122 015156          MOV      DACSAV,OFSLUX ;SAVE A/D OFFSET UNIPOLAR
1547 006766 013737 015122 001126          MOV      DACSAV,$BDDAT ;LOAD VALUE READ
1548 006774 012737 001014 001124          MOV      #1014,$GDDAT ;LOAD EXPECTED
1549 007002 013737 016774 011664          MOV      V62,$SPREAD  ;LOAD +- VARIABLE, 1 LSB
1550 007010 004737 011666                  JSR      PC,COMPARE   ;TEST IF WITHIN +- VALUE
1551 007014 000401                          BR       1$           ;;BR IF WITHIN LIMITS
1552 007016 104012                          ERROR    12          ;A/D UNIPOLAR OFFSET ERROR
1553
1554 007020 163737 015152 001126 1$:     SUB      OFSTUX,$BDDAT ;OFFSET DUE TO WAIT LOOP
1555 007026 005037 001124                  CLR      $GDDAT
1556 007032 012737 000031 011664          MOV      #31,$SPREAD  ;1/2 LSB ALLOWED
1557 007040 004737 011666                  JSR      PC,COMPARE   ;WITHIN LIMITS
1558 007044 000401                          BR       1$           ;;BR IF WITHIN LIMITS
1559 007046 104012                          ERROR    12          ;A/D UNIPOLAR OFFSET ERROR DUE TO WAIT LOOP

```

K03

MAINDEC-11-DZARC-B
DZARC8.P11 T45

MACY11 27(732) 21-SEP-76 16:44 PAGE 36
X D A OFFSET USING CH 5

```
1560 ::*****
1561 :*TEST 45 X D/A OFFSET USING CH 5
1562 :*****
1563 †ST45: SCOPE
1564 007050 000004 MOV #10, $TIMES ;;DO 10 ITERATIONS
1565 007052 012737 000010 001164 MOV #1000, @VCXREG ;LOAD X REG
1566 007060 012777 001000 172422 JSR R4, SAR ;SOFTWARE S.A.R.
1567 007066 004437 010716 VCYREG ;Y AXIS
1568 007072 001512 .BYTE 0, CH5 ;CHANNEL 5, COARSE X AND FINE Y
1569 007074 000 005 1001 ;EDGE VALUE, 1000/1001 TRANSITION
1570 007076 001001 BIT7 ;50-50
1571 007102 013737 015122 001126 MOV DACSAV, $BDDAT ;SAVE RESULT (X DAC + A/D OFFSET)
1572 007110 163737 015150 001126 SUB OFSTBX, $BDDAT ;OFFSET DUE TO X DAC
1573 007116 005037 001124 CLR $GDDAT ;CLEAR EXPECTED
1574 007122 013737 016776 011664 MOV V77, SPREAD ;TOLERANCE DUE TO X DAC
1575 007130 004737 011666 JSR PC, COMPAR ;TEST IF WITHIN +- LIMITS
1576 007134 000401 BR †ST46 ;;BR IF WITHIN LIMITS
1577 007136 104012 ERROR 12 ;X DAC OFFSET ERROR
1578
1579 ::*****
1580 :*TEST 46 Y D/A OFFSET USING CH 6
1581 :*****
1582 †ST46: SCOPE
1583 007140 000004 MOV #10, $TIMES ;;DO 10 ITERATIONS
1584 007142 012737 000010 001164 MOV #1000, @VCYREG ;LOAD Y REG
1585 007150 012777 001000 172334 JSR R4, SAR ;SOFTWARE S.A.R.
1586 007156 004437 010716 VCYREG ;X AXIS
1587 007162 001510 .BYTE 0, CH6 ;CHANNEL 6, COARSE Y AND FINE X
1588 007164 000 006 1001 ;EDGE VALUE, 1000/1001 TRANSITION
1589 007166 001001 BIT7 ;50-50
1590 007170 000200 MOV DACSAV, $BDDAT ;SAVE RESULT (Y DAC + A/D OFFSET)
1591 007172 013737 015122 001126 SUB OFSTBX, $BDDAT ;OFFSET DUE TO Y DAC
1592 007200 163737 015150 001126 CLR $GDDAT ;CLEAR EXPECTED
1593 007206 005037 001124 MOV V77, SPREAD ;TOLERANCE DUE TO Y DAC
1594 007212 013737 016776 011664 JSR PC, COMPAR ;TEST IF WITHIN +- LIMITS
1595 007220 004737 011666 BR †ST47 ;;BR IF WITHIN LIMITS
1596 007224 000401 ERROR 12 ;Y DAC OFFSET ERROR
1597 007226 104012
```

```

1597
1598
1599
1600 007230 000004
1601 007232 012737 000010 001164
1602 007240 012777 001300 172242
1603 007246 012737 001600 001124
1604 007254 004537 011534
1605 007260 000011
1606 007262 013737 011656 001126
1607 007270 012737 000003 011664
1608 007276 004737 011666
1609 007302 000401
1610 007304 104013
1611 007306 162777 000100 172174
1612 007314 162737 000200 001124
1613 007322 100354
1614
1615
1616
1617
1618 007324 000004
1619 007326 012737 000010 001164
1620 007334 012777 001300 172150
1621 007342 012737 001600 001124
1622 007350 004537 011534
1623 007354 000012
1624 007356 013737 011656 001126
1625 007364 012737 000003 011664
1626 007372 004737 011666
1627 007376 000401
1628 007400 104013
1629 007402 162777 000100 172102
1630 007410 162737 000200 001124
1631 007416 100354

```

```

*****
*TEST 47 CALIBRATION USING CHANNEL 11 - X DAC VS. A/D
*****
↑ST47: SCOPE
MOV #10, $TIMES ;:DO 10 ITERATIONS
MOV #1300, @VCXREG ;LOAD X DAC
MOV #1600, $GDDAT ;LOAD EXPECTED
2$: JSR R5, CONVRT ;CONVERT 32X AND AVERAGE
CH11
MOV ADEND, $BDDAT ;READ VALUE OBTAINED
MOV #3, $SPREAD ;LOAD DEVIATION
JSR PC, COMPAR ;TEST IF WITHIN
BR 1$ ;BR IF WITHIN
ERROR 13 ;CALIBRATION ERROR - X DAC VS. A/D
1$: SUB #100, @VCXREG ;DECREMENT DAC
SUB #200, $GDDAT ;DEC. EXPECTED
BPL 2$ ;BR UNTIL DONE

*****
*TEST 50 CALIBRATION USING CHANNEL 12 - Y DAC VS. A/D
*****
↑ST50: SCOPE
MOV #10, $TIMES ;:DO 10 ITERATIONS
MOV #1300, @VCYREG ;LOAD Y DAC
MOV #1600, $GDDAT ;LOAD EXPECTED
2$: JSR R5, CONVRT ;CONVERT 32X AND AVERAGE
CH12
MOV ADEND, $BDDAT ;READ VALUE OBTAINED
MOV #3, $SPREAD ;LOAD DEVIATION
JSR PC, COMPAR ;TEST IF WITHIN
BR 1$ ;BR IF WITHIN
ERROR 13 ;CALIBRATION ERROR - Y DAC VS. A/D
1$: SUB #100, @VCYREG ;DECREMENT DAC
SUB #200, $GDDAT ;DEC. EXPECTED
BPL 2$ ;BR UNTIL DONE

```

M03

MAINDEC-11-DZARC-B
DZARCB.P11 T51

MACY11 27(732) 21-SEP-76 16:44 PAGE 38
LINEARITY TEST USING CH 6 - Y DAC VS. A/D

```

1632                                     ::*****
1633                                     ;*TEST 51 LINEARITY TEST USING CH 6 - Y DAC VS. A/D
1634                                     ;*****
1635 007420 000004 T51: SCOPE
1636 007422 012737 000010 001164 MOV #10,$TIMES ;;DO 10 ITERATIONS
1637 007430 012700 014702 MOV #NUMBF2,R0 ;LOAD EDGE VALUE PCINTER
1638 007434 012702 014726 MOV #RSLTO,R2 ;LOAD RESULT POINTER
1639
1640 007440 011037 007460 1$: MOV (R0),10$ ;LOAD EDGE EXPECTED
1641 007444 012077 172042 MOV (R0)+,JVCYREG ;LOAD DAC
1642 007450 004437 010716 JSR R4,SAR ;SOFTWARE S.A.R.
1643 007454 001510 VCXREG
1644 007456 000 006 .BYTE 0,CH6 ;CHANNEL 6, COARSE Y AND FINE X
1645 007460 000000 10$: 0 ;EDGE VALUE
1646 007462 000200 BIT7 ;50-50
1647
1648 007464 013722 015122 MOV DACSAV,(R2)+ ;SAVE RESULTS
1649 007470 005710 TST (R0) ;LAST RESULT?
1650 007472 100362 BPL 1$ ;BR UNTIL DONE
1651 007474 005037 001124 CLR $GDDAT ;ZERO NOMINAL LINEARITY ERROR
1652 007500 013737 016776 011664 MOV V77,SPREAD ;TOLERANCE FOR A/D + DAC
1653 007506 013700 014746 MOV RSLTO+20,R0 ;GET LAST VALUE
1654 007512 163700 014726 SUB RSLTO,R0 ;SUBTRACT FIRST VALUE
1655 007516 006200 ASR R0
1656 007520 006200 ASR R0
1657 007522 006200 ASR R0
1658 007524 005500 ADC R0 ;AVERAGE DIFFERENCE IN R0
1659 007526 012701 000002 MC, #2,R1
1660 007532 005037 014746 CLR RSLTO+20 ;START RUNNING SUM OF ERRORS
1661 007536 060037 014726 2$: ADD R0,RSLTO ;COMPUTE NOM. VALUE ON LINE BETW. END PTS.
1662 007542 163761 014726 014726 SUB RSLTO,RSLTO(R1) ;COMPUTE ERROR WITH RESP. TO END PT. LINE
1663 007550 066137 014726 014746 ADD RSLTO(R1),RSLTO+20 ;KEEP RUNNING SUM
1664 007556 005721 TST (R1)+ ;BUMP R1
1665 007560 020127 000020 CMP R1,#20 ;DONE?
1666 007564 001364 BNE 2$ ;BR IF NOT
1667 007566 005237 014746 ASR RSLTO+20
1668 007572 006237 014746 ASR RSLTO+20
1669 007576 006237 014746 ASR RSLTO+20
1670 007602 005537 014746 ADC RSLTO+20 ;AVERAGE ERROR WITH RESP. TO END PT. LINE
1671 007606 005037 014726 CLR RSLTO
1672 007612 005001 CLR R1
1673 007614 163761 014746 014726 3$: SUB RSLTO+20,RSLTO(R1) ;ERROR WITH RESP. TO "BEST STRAIGHT LINE"
1674
1675 007622 016137 014726 001126 MOV RSLTO(R1),$BDDAT
1676 007630 004737 011666 JSR PC,COMPAR ;COMPARE LINEARITY ERROR WITH + OR - TOLERANCE
1677 007634 000422 BR 4$
1678 007636 010102 MOV R1,R2
1679 007640 042702 177770 BIC #177770,R2 ;MASK BITS 0-2
1680 007644 062702 000060 ADD #60,R2 ;CONVERT TO ASCII
1681 007650 110237 012445 MOVB R2,$M14A
1682 007654 010102 MOV R1,R2 ;LOAD R2
1683 007656 006202 ASR R2
1684 007660 006202 ASR R2
1685 007662 006202 ASR R2
1686 007664 042702 177770 BIC #177770,R2 ;MASK
1687 007670 062702 000060 ADD #60,R2 ;MAKE ASCII

```

N03

MAINDEC-11-DZARC-B
DZARC8.P11

TS1

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LINEARITY TEST USING CH 6 - Y DAC VS. A/D

```

1688 007674 110237 012444      MOVB      R2,EM14B      ;SAVE #
1689 007700 104014      ERROR     14           ;LINEARITY ERROR USING CH 6 - YDAC VS. A/D
1690 007702 005721      4$:      TST      (R1)+      ;BUMP R1
1691 007704 020127 000020      CMP      R1,#20       ;DONE ?
1692 007710 001341      BNE      3$           ;;BR IF NOT DONE
1693
1694
1695 ;:*****
1696 ;:TEST 52      LINEARITY TEST USING CH 5 - X DAC VS. A/D
1697 ;:*****
1698 007712 000004      †ST52:  SCOPE
1699 007714 012737 000010 001164      MOV      #10,STIMES   ;;DO 10 ITERATIONS
1700 007722 012700 014702      MOV      #NUMBF2,R0   ;LOAD EDGE VALUE POINTER
1701 007726 012702 014764      MOV      #RSLT1,R2    ;LOAD RESULT POINTER
1702 007732 011037 007752      1$:      MOV      (R0),10$     ;LOAD EDGE EXPECTED
1703 007736 012077 171546      MOV      (R0)+,2VCXREG ;LOAD DAC
1704 007742 004437 010716      JSR      R4,SAR       ;SOFTWARE S.A.R.
1705 007746 001512      VCYREG
1706 007750      000      005      .BYTE    0,CH5       ;CHANNEL 5, COARSE & FINE Y
1707 007752 000000      10$:     0           ;EDGE VALUE
1708 007754 000200      BIT7      ;50-50
1709
1710 007756 013722 015122      MOV      DACSAV,(R2)+ ;SAVE RESULTS
1711 007762 005710      TST      (R0)        ;LAST RESULT ?
1712 007764 100362      BPL      1$         ;BR UNTIL DONE
1713 007766 005037 001124      CLR      $DDCAT      ;ZERO NOMINAL LINEARITY ERROR
1714 007772 013737 016776 011664      MOV      V77,SPREAD  ;TOLERANCE FOR A/D + DAC
1715 000000 013700 015004      MOV      RSLT1+20,R0 ;GET LAST VALUE
1716 000004 163700 014764      SUB      RSLT1,R0     ;SUBTRACT FIRST VALUE
1717 000010 006200      ASR      R0
1718 000012 006200      ASR      R0
1719 000014 005200      ASR      R0
1720 000016 005500      ADC      R0
1721 000020 012701 000002      MOV      #2,R1       ;AVERAGE DIFFERENCE IN PC
1722 000024 005037 015004      CLR      RSLT1+20    ;START RUNNING SUM OF ERRORS
1723 000030 067037 014764      2$:      ADD      R0,RSLT1    ;COMPUTE NOM. VALUE ON LINE BETW. ENC PTS.
1724 000034 163761 014764 014764      SUB      RSLT1,RSLT1(R1) ;COMPUTE ERROR WITH RESP. TO END PT. LINE
1725 000042 066137 014764 015004      ADD      RSLT1(R1),RSLT1+20 ;KEEP RUNNING SUM
1726 000050 005721      TST      (R1)+      ;BUMP R1
1727 000052 020127 000020      CMP      R1,#20       ;DONE ?
1728 000056 001364      BNE      3$         ;;BR IF NOT
1729 000060 006237 015004      ASR      RSLT1+20
1730 000064 006237 015004      ASR      RSLT1+20
1731 000070 006237 015004      ASR      RSLT1+20
1732 000074 005537 015004      ADC      RSLT1+20    ;AVERAGE ERROR WITH RESP. TO END PT. LINE
1733 000100 005037 014764      CLR      RSLT1
1734 000104 005001      CLR      R1
1735 000106 163761 015004 014764 3$:      SUB      RSLT1+20,RSLT1(R1) ;ERROR WITH RESP. TO "BEST STRAIGHT LINE"
1736 000114 016137 014764 001125      MOV      RSLT1,R1),$DDCAT
1737 000122 004737 011666      JSR      PC,COMPAR   ;COMPARE LINEARITY ERROR WITH + OR - TOLERANCE
1738 000126 000422      BR      4$
1739 000130 010102      MOV      R1,R2
1740 000132 042702 177770      BIC      #177770,R2  ;MASK BITS 0-2
1741 000136 062702 000060      ADD      #60,R2      ;CONVERT TO ASCII
1742 000142 110237 012445      MOVB     R2,EM14A
1743 000146 010102      MOV      R1,R2      ;LOAD R2

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010716 012437 015120
010722 012437 015166
010726 012437 015170
010732 012437 015172
010736 010046
010740 010146
010742 010246
010744 010346
010746 113737 015167 011662
010754 005037 011660
010760 113737 015167 011661
010766 005237 011660
010772 017737 004122 015120
011000 012777 011132 170460
011006 005037 015174
011012 005737 015172
011016 100003
011020 012703 000002
011024 000407
011026 012703 000121 103:
011032 105737 015172
011036 100002
011040 012703 000400
011044 012702 001000 155:
011050 005077 004044
011054 005001 145:
011056 012700 001000
011062 060277 004032
011066 032737 000001 015172 65:
011074 001407
011076 013777 011660 170370 275:
011104 105777 170364
011110 100375
011112 000410
011114 052737 000100 011660 205:
011122 013777 011660 170344
011130 000001
011132 022626 115:
011134 027737 170340 015170 125:
011142 002401
011144 005201
011146 105737 015166 15:
011152 001420

```
:S A R SUBROUTINE
:JSR R4,SAR
:AXIS POINTER
:CH, IN HIGH BYTE / DUMMY CH. IN LOW BYTE
:EDGE VALUE
:RESULT,MODE
:RETURN WITH R5 = DIFFERENCE

:BIT15 = 0      RMS
:BIT15 = 1      PEAK
:BIT7  = 1      SO-50
:BITC  = 1      :WAIT LOOP

SAR:  MOV      (R4)+,ADAC      :SAVE DAC ADDRESS POINTER
      MOV      (R4)+,SARCHN  :SAVE CHANNEL
      MOV      (R4)+,EDGE    :SAVE VALUE EDGE
      MOV      (R4)+,CONS    :SAVE RMS/PEAK SW
      MOV      R0,-(SP)      :SAVE GPR
      MOV      R1,-(SP)
      MOV      R2,-(SP)
      MOV      R3,-(SP)
      MOVVB   SARCHN+1,CHANL
      CLR      FCHANL
      MOVVB   SARCHN+1,FCHANL+1
      INC      FCHANL
      MOV      @ADAC,ADAC    :SET START BIT
      MOV      @15,SARBVCT  :GET BUS ADDRESS OF ACTIVE DAC
      CLR      FINS         :LOAD VECTOR
      TST     CONS         :CLEAR 2 PASS COUNTER FLAG
      BPL     10$          :TEST IF RMS
      MOV     #2,R3        :BR IF RMS
                          :LOAD A = .4% OF 512
      BR     15$
      MOV     #121,R3     :LOAD A = 16% OF 512
      TSTB   CONS        :TEST FOR SO-50
      BPL     15$
      MOV     #400,R3     :LOAD SO-50, A = 50% COUNT
      MOV     #1000,R2   :SET UP MSB
      CLR     @ADAC      :CLEAR DAC
      CLR     R1         :SET HIGH = 0
      MOV     #1000,R0   :SET UP 512 CONVERSIONS
      ADD    R2,@ADAC    :NEXT BIT OF ACTIVE DAC
      BIT    #BITC,CONS  :TEST BIT C
      BEQ    20$
      MOV    FCHANL,@ADCS :START CONVERSION
      TSTB  @ADCS
      BPL  27$
      BR   12$
      BIS  #BIT6,FCHANL  :SET INTR. ENABLE
      MOV  FCHANL,@ADCS :START CONVERSION
      WAIT
      CMP  (SP)+,(SP)+
      CMP  @ADCBR,EDGE  :IS RESULT < EDGE VALUE ?
      BLT  1$          :YES, BR
      INC  R1           :NO, INC HIGH FOR RESULT > OR = EDGE VALUE
      TSTB SARCHN      :TEST IF DUMMY CH. IS SET
      BEQ  3$          :BR IF NOT
```

G04

| | | | | | | | | |
|------|--------|--------|--------|--------|------|------|----------------|--------------------------------|
| 1953 | 011154 | 012777 | 011176 | 170304 | | MOV | #45, DARBVCT | :LOAD VECTOR |
| 1954 | 011162 | 113777 | 015166 | 170306 | | MOVB | SARCHN, DADCS1 | :LOAD MUX |
| 1955 | 011170 | 005277 | 170300 | | | INC | DADCS | :CONVERT |
| 1956 | 011174 | 000001 | | | | WAIT | | |
| 1957 | 011176 | 022636 | | | 45: | CMP | (SP)+, (SP)+ | :POP STACK |
| 1958 | 011200 | 027777 | 170274 | 170272 | 135: | CMP | DADDBR, DADDBR | :FAKE READ |
| 1959 | 011206 | 012777 | 011132 | 170252 | | MOV | #115, DARBVCT | :RESET VECTOR |
| 1960 | 011214 | 005300 | | | 35: | DEC | R0 | :DONE 512 TIMES ? |
| 1961 | 011216 | 001323 | | | | BNE | R0 | :NO |
| 1962 | 011220 | 020103 | | | | CMP | R1, R3 | :IS HIGH > CR = A ? |
| 1963 | 011222 | 002402 | | | | BLT | R3 | :NO LEAVE BIT ON |
| 1964 | 011224 | 160277 | 003670 | | | SUB | R2, DADAC | :YES TAKE IT OUT |
| 1965 | 011230 | 022702 | 000001 | | 25: | CMP | #1, R2 | :TEST FOR Z = 1. S.A.R. DONE ? |
| 1966 | 011234 | 001402 | | | | BEQ | R2 | :BR IF DONE |
| 1967 | 011236 | 006202 | | | | ASR | R2 | :NO SET Z /2 |
| 1968 | 011240 | 000705 | | | | BR | 145 | :TRY AGAIN |
| 1969 | 011242 | 017737 | 003652 | 015122 | 215: | MOV | DADAC, DACSAV | |
| 1970 | 011250 | 005737 | 015174 | | | TST | FINS | :FIRST OR SECOND TIME ? |
| 1971 | 011254 | 001020 | | | | BNE | 455 | :SECCND, BR |
| 1972 | 011256 | 017705 | 003636 | | | MOV | DADAC, R5 | :READ VALUE |
| 1973 | 011262 | 005237 | 015174 | | | INC | FINS | :SET FLAG |
| 1974 | 011266 | 005737 | 015172 | | | TST | CONS | :TEST IF PEAK/RMS |
| 1975 | 011272 | 100003 | | | | BPL | 415 | :BR IF RMS |
| 1976 | 011274 | 012703 | 000776 | | | MOV | #776, R3 | :LOAD 99.6% OF 512 |
| 1977 | 011300 | 000405 | | | | BR | 425 | |
| 1978 | 011302 | 012703 | 000657 | | 415: | MOV | #657, R3 | :SET A = 84% OF 512 |
| 1979 | 011306 | 105737 | 015172 | | 435: | TSTB | CONS | |
| 1980 | 011312 | 100404 | | | | BMI | 55 | |
| 1981 | 011314 | 000653 | | | 425: | BR | 155 | :DO MORE TESTING |
| 1982 | 011316 | 167705 | 003576 | | 455: | SUB | DADAC, R5 | :SUBTRACT DIFF |
| 1983 | 011322 | 005405 | | | | NEG | R5 | |
| 1984 | 011324 | 005077 | 170144 | | 55: | CLR | DADCS | :RETURN WITH 2 X NOISE IN R5 |
| 1985 | 011330 | 012603 | | | | MOV | (SP)+, R3 | |
| 1986 | 011332 | 012602 | | | | MOV | (SP)+, R2 | |
| 1987 | 011334 | 012601 | | | | MOV | (SP)+, R1 | |
| 1988 | 011336 | 012600 | | | | MOV | (SP)+, R0 | |
| 1989 | 011340 | 000204 | | | | RTS | R4 | :EXIT |

.SBTTL MISC. SUBROUTINES

```

1992 ;CONVERT R2 INTO DECIMAL DIGITS
1993
1994 011342 J12737 177774 011446 DECPRT: MOV #-4,DIGCNT ;LOAD COUNT
1995 011350 010146 MOV R1,-(SP) ;SAVE R1
1996 011352 012701 011462 MOV #DIGT1-1,R1 ;LOAD POINTER
1997 011356 012737 011452 011450 MOV #DECPNT+2,DECPNT ;LOAD POINTER
1998 011364 012737 177777 011444 1$: MOV #-1,DIGIT ;LOAD #
1999 011372 005237 011444 2$: INC DIGIT ;UPDATE IT
2000 011376 167702 000046 SJB @DECPNT,R2 ;SUB MAGNITUDE
2001 011402 100373 BPL 2$ ;BR IF +
2002 011404 067702 000040 ADD @DECPNT,R2 ;RESTORE
2003 011410 052737 000060 011444 BIS #60,DIGIT ;MAKE A #
2004 011416 113721 011444 MOV#B DIGIT,(R1)+ ;LOAD INTO STRING
2005 011422 005237 011446 INC DIGCNT ;UPDATE COUNT
2006 011425 001002 BNE 3$ ;BR IF NOT DONE
2007 011430 012601 MOV (SP)+,R1 ;RESTORE R1
2008 011432 000207 RTS PC ;EXIT
2009 011434 062737 000002 011450 3$: ADD #2,DECPNT ;UPDATE POINTER
2010 011442 000750 BR 1$ ;BR BACK

2011
2012 011444 000000 DIGIT: 0
2013 011446 000000 DIGCNT: 0
2014 011450 011452 DECPNT: .+2
2015 011452 001750 1000.
2016 011454 000144 100.
2017 011456 000012 10.
2018 011460 000001 1.

2019
2020 011462 000 DIGTO: .BYTE 0
2021 011463 000 DIGT1: .BYTE 0
2022 011464 000 DIGT2: .BYTE 0
2023 011465 000 DIGT3: .BYTE 0

2024
2025 011466 013546 BYTWO: MOV @ (R5)+,-(SP) ;SAVE ON STACK
2026 011470 104402 TYPOS
2027 011472 004 001 .BYTE 4,1
2028 011474 104400 014541 TYPE MULTSP
2029 011500 013546 MOV @ (R5)+,-(SP) ;SAVE ON STACK
2030 011502 104402 TYPOS
2031 011504 004 001 .BYTE 4,1
2032 011506 000205 RTS R5 ;EXIT

2033
2034 011510 012500 TYPSTG: MOV (R5)+,R0 ;GET ADDRESS POINTER
2035 011512 012501 MOV (R5)+,R1 ;LOAD # OF LOC.
2036 011514 012046 1$: MOV (R0)+,-(SP) ;SAVE ON STACK
2037 011516 104402 TYPOS
2038 011520 004 001 .BYTE 4,1
2039 011522 104400 016543 TYPE SP3MSG
2040 011526 005301 DEC R1 ;DONE
2041 011530 001371 BNE 1$
2042 011532 000205 2$: RTS R5 ;EXIT

```

2043 ;SUBROUTINE TO GET AVERAGE OF 32 CONVERSIONS

```

2044
2045 011534 012537 011652 CONVRT: MOV (R5)+,10$
2046 011540 013737 011652 011662 MOV 10$,CHANL
2047 011546 000337 011652 SLAB 10$
2048 011552 C12737 000040 011654 MOV #32,11$
2049 011560 005037 011656 CLR ADEND
2050 011564 013777 011652 167702 MOV 10$,2ADCS
2051 011572 005277 167676 2$: INC 2ADCS
2052 011576 105777 167672 1$: TSTB 2ADCS
2053 011602 100375 BPL 1$
2054 011604 067737 167670 011656 ADD 2ADDBR,ADEND
2055 011612 003337 011654 DEC 11$
2056 011616 001365 BNE 2$
2057 011620 006237 011656 ASR ADEND
2058 011624 006237 011656 ASR ADEND
2059 011630 006237 011656 ASR ADEND
2060 011634 006237 011656 ASR ADEND
2061 011640 006237 011656 ASR ADEND
2062 011644 005537 011656 ADC ADEND
2063 011650 000205 RTS RS ;ROUND UP

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2064
2065 011652 000000 10$: 0
2066 011654 000000 11$: 0
2067 011656 000000 ADEND: 0
2068 011660 000000 FCHANL: 0
2069 011662 000000 CHANL: 0
2070 011664 000000 SPREAD: 0

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2071
2072 011666 010046 COMPAR: MOV R0,-(SP)
2073 011670 010146 MOV R1,-(SP)
2074 011672 013700 001124 MOV $GDAT,R0 ;LOAD R0
2075 011676 013701 001126 MOV $BCDAT,R1 ;LOAD R1
2076 011702 160100 SUB R1,R0 ;SUBTRACT
2077 011704 100001 BPL 2$
2078 011706 005400 NEG R0
2079 011710 020037 011664 9$: CMP R0,SPREAD ;MAGNITUDE OF DIFF. IN RC
2080 011714 003405 BLE 10$
2081 011716 012601 9$: MOV (SP)+,R1
2082 011720 012600 MOV (SP)+,R0
2083 011722 062716 000002 ADD #2,(SP)
2084 011726 000207 RTS PC

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2085
2086 011730 012601 10$: MOV (SP)+,R1
2087 011732 012600 MOV (SP)+,R0
2088 011734 000207 RTS PC
2089 .SBTTL ASCII MESSAGES AND ERROR POINTERS

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| | | | | | |
|------|--------|--------|--------|--------|---|
| 0090 | | | | | |
| 0091 | 011736 | 051105 | 047522 | 020122 | EM1: .ASCIZ \ERROR ON A/D CHANNEL\ |
| 0092 | 011744 | 047117 | 040440 | 042057 | |
| 0093 | 011752 | 041440 | 040510 | 047116 | |
| 0094 | 011760 | 046105 | 000 | | |
| 0095 | 011763 | 126 | 020103 | 047514 | EM2: .ASCIZ /VC LOGIC SIGNAL HIGH OUTPUT TOO LOW/ |
| 0096 | 011770 | 044507 | 020103 | 044523 | |
| 0097 | 011776 | 047107 | 046101 | 044040 | |
| 0098 | 012004 | 043511 | 020110 | 052517 | |
| 0099 | 012012 | 050124 | 052125 | 052040 | |
| 0100 | 012020 | 047517 | 046040 | 053517 | |
| 0101 | 012026 | 000 | | | |
| 0102 | 012027 | 126 | 020103 | 052123 | EM3: .ASCIZ /VC STATUS REGISTER IN ERROR/ |
| 0103 | 012034 | 052101 | 051525 | 051040 | |
| 0104 | 012042 | 043505 | 051511 | 042524 | |
| 0105 | 012050 | 020122 | 047111 | 042440 | |
| 0106 | 012056 | 051122 | 051117 | 000 | |
| 0107 | 012063 | 105 | 052130 | 051105 | EM4: .ASCIZ /EXTERNAL A TO D START FAILED/ |
| 0108 | 012070 | 040516 | 020114 | 020101 | |
| 0109 | 012076 | 047524 | 042040 | 051440 | |
| 0110 | 012104 | 040524 | 052122 | 043040 | |
| 0111 | 012112 | 044501 | 042514 | 000104 | |
| 0112 | 012120 | 027501 | 020104 | 044504 | EM5: .ASCIZ \A/D DIFFERENTIAL LINEARITY ERROR\ |
| 0113 | 012126 | 043106 | 051105 | 047105 | |
| 0114 | 012134 | 044524 | 046101 | 046040 | |
| 0115 | 012142 | 047111 | 040505 | 044522 | |
| 0116 | 012150 | 054524 | 042440 | 051122 | |
| 0117 | 012156 | 051117 | 000 | | |
| 0118 | 012161 | 116 | 044517 | 042523 | EM6: .ASCIZ /NOISE LEVEL EXCEEDED LIMIT/ |
| 0119 | 012166 | 046040 | 053105 | 046105 | |
| 0120 | 012174 | 042440 | 041530 | 042505 | |
| 0121 | 012202 | 042504 | 020104 | 044514 | |
| 0122 | 012210 | 044515 | 000124 | | |
| 0123 | 012214 | 041526 | 046040 | 043517 | EM7: .ASCIZ /VC LOGIC SIGNAL LOW OUTPUT TOO HIGH/ |
| 0124 | 012222 | 041511 | 051440 | 043511 | |
| 0125 | 012230 | 040516 | 020114 | 047514 | |
| 0126 | 012236 | 020127 | 052517 | 050124 | |
| 0127 | 012244 | 052125 | 052040 | 047517 | |
| 0128 | 012252 | 044040 | 043511 | 000110 | |
| 0129 | 012260 | 027504 | 020101 | 044504 | EM10: .ASCIZ \D/A DIFFERENTIAL LINEARITY ERROR\ |
| 0130 | 012266 | 043106 | 051105 | 047105 | |
| 0131 | 012274 | 044524 | 046101 | 046040 | |
| 0132 | 012302 | 047111 | 040505 | 044522 | |
| 0133 | 012310 | 054524 | 042440 | 051122 | |
| 0134 | 012316 | 051117 | 000 | | |
| 0135 | 012321 | 101 | 042057 | 044440 | EM11: .ASCIZ \A/D INTER-CHANNEL SETTLING ERROR\ |
| 0136 | 012326 | 052116 | 051105 | 041455 | |
| 0137 | 012334 | 040510 | 047116 | 046105 | |
| 0138 | 012342 | 051440 | 052105 | 046124 | |
| 0139 | 012350 | 047111 | 020107 | 051105 | |
| 0140 | 012356 | 047522 | 000122 | | |
| 0141 | 012362 | 043117 | 051506 | 052105 | EM12: .ASCIZ /OFFSET ERROR/ |
| 0142 | 012370 | 042440 | 051122 | 051117 | |
| 0143 | 012376 | 000 | | | |
| 0144 | 012377 | 103 | 046101 | 041111 | EM13: .ASCIZ /CALIBRATION ERROR/ |
| 0145 | 012404 | 040522 | 044524 | 047117 | |

K04

| | | | | | | | | | | |
|------|--------|--------|--------|--------|--------|--------|-----------------------|----------------|----------------|--------|
| 2146 | 012412 | 042440 | 051122 | 051117 | | | | | | |
| 2147 | 012420 | 000 | | | | | | | | |
| 2148 | 012421 | 114 | 047111 | 040505 | EM14: | .ASCII | /LINEARITY ERROR AT / | | | |
| 2149 | 012426 | 044522 | 054524 | 042440 | | | | | | |
| 2150 | 012434 | 051122 | 051117 | 040440 | | | | | | |
| 2151 | 012442 | 020124 | | | | | | | | |
| 2152 | 012444 | 060 | | | EM14B: | .BYTE | 60 | | | |
| 2153 | 012445 | 060 | 060 | 060 | EM14A: | .BYTE | 60,60,60,0 | | | |
| 2154 | 012450 | 000 | | | | | | | | |
| 2155 | 012451 | 105 | 051122 | 041520 | DH1: | .ASCIZ | /ERRPC ARADD CHANL | NOMINAL SPREAD | ACTUAL | / |
| 2156 | 012456 | 020040 | 040440 | 040522 | | | | | | |
| 2157 | 012464 | 042104 | 020040 | 041440 | | | | | | |
| 2158 | 012472 | 040510 | 046116 | 020040 | | | | | | |
| 2159 | 012500 | 047040 | 046517 | 047111 | | | | | | |
| 2160 | 012506 | 046101 | 051440 | 051120 | | | | | | |
| 2161 | 012514 | 040505 | 020104 | 040440 | | | | | | |
| 2162 | 012522 | 052103 | 040525 | 000114 | | | | | | |
| 2163 | 012530 | 051105 | 050122 | 020103 | DH2: | .ASCIZ | /ERRPC ARADD | CHANL | 3V LEV. OUTPUT | / |
| 2164 | 012536 | 020040 | 051101 | 042101 | | | | | | |
| 2165 | 012544 | 020104 | 020040 | 041440 | | | | | | |
| 2166 | 012552 | 040510 | 046116 | 020040 | | | | | | |
| 2167 | 012560 | 053063 | 046040 | 053105 | | | | | | |
| 2168 | 012566 | 020056 | 052517 | 050124 | | | | | | |
| 2169 | 012574 | 052125 | 000 | | | | | | | |
| 2170 | 012577 | 105 | 051122 | 041520 | DH3: | .ASCIZ | ERRPC ARADD | GOOD | BAD | / |
| 2171 | 012604 | 020040 | 040440 | 040522 | | | | | | |
| 2172 | 012612 | 042104 | 020040 | 020040 | | | | | | |
| 2173 | 012620 | 047507 | 042117 | 020040 | | | | | | |
| 2174 | 012626 | 020040 | 040502 | 000104 | | | | | | |
| 2175 | 012634 | 051105 | 050122 | 020103 | DH5: | .ASCIZ | /ERRPC ARADD | # OF ST. | ALLOWED | / |
| 2176 | 012642 | 020040 | 051101 | 042101 | | | | | | |
| 2177 | 012650 | 020104 | 021440 | 047440 | | | | | | |
| 2178 | 012656 | 020106 | 052123 | 020056 | | | | | | |
| 2179 | 012664 | 046101 | 047514 | 042527 | | | | | | |
| 2180 | 012672 | 000104 | | | | | | | | |
| 2181 | 012674 | 051105 | 050122 | 020103 | DH6: | .ASCIZ | /ERRPC ARADD | LIMIT | MEASURED | / |
| 2182 | 012702 | 020040 | 051101 | 042101 | | | | | | |
| 2183 | 012710 | 020104 | 020040 | 044514 | | | | | | |
| 2184 | 012716 | 044515 | 020124 | 020040 | | | | | | |
| 2185 | 012724 | 042515 | 051501 | 051125 | | | | | | |
| 2186 | 012732 | 042105 | 000 | | | | | | | |
| 2187 | 012735 | 105 | 051122 | 041520 | D47: | .ASCIZ | /ERRPC ARADD | A/D CH. | .4 LEV | OUTPUT |
| 2188 | 012742 | 020040 | 040440 | 040522 | | | | | | |
| 2189 | 012750 | 042104 | 020040 | 040440 | | | | | | |
| 2190 | 012756 | 042057 | 041440 | 027110 | | | | | | |
| 2191 | 012764 | 027040 | 020064 | 042514 | | | | | | |
| 2192 | 012772 | 020126 | 047440 | 052125 | | | | | | |
| 2193 | 013000 | 052520 | 000124 | | | | | | | |
| 2194 | 013004 | 051105 | 050122 | 020103 | DH10: | .ASCIZ | /ERRPC ARADD | STEP | NOMINAL SPREAD | ACTUAL |
| 2195 | 013012 | 020040 | 051101 | 042101 | | | | | | |
| 2196 | 013020 | 020104 | 020040 | 051440 | | | | | | |
| 2197 | 013026 | 042524 | 020120 | 020040 | | | | | | |
| 2198 | 013034 | 047516 | 044515 | 040516 | | | | | | |
| 2199 | 013042 | 020114 | 050123 | 042522 | | | | | | |
| 2200 | 013050 | 042101 | 020040 | 041501 | | | | | | |
| 2201 | 013056 | 052524 | 046101 | 000 | | | | | | |

| | | | | | |
|------|--------|--------|--------|--------|--|
| 2314 | 014170 | 020104 | 044504 | 043106 | |
| 2315 | 014176 | 051105 | 047105 | 044524 | |
| 2316 | 014204 | 046101 | 046040 | 047111 | |
| 2317 | 014212 | 040505 | 044522 | 054524 | |
| 2318 | 014220 | 006472 | 000012 | | |
| 2319 | 014224 | 052123 | 052101 | 026505 | ADDIF: .ASCIZ STATE-WIDTH<(15)>(12) |
| 2320 | 014232 | 044527 | 052104 | 006510 | |
| 2321 | 014240 | 000012 | | | |
| 2322 | 014242 | 034050 | 020051 | 045523 | SKPMSG: .ASCIZ (8) SKIPPED STATE(S)<(15)>(12) |
| 2323 | 014242 | 050111 | 042520 | 020104 | |
| 2324 | 014250 | 052123 | 052101 | 024105 | |
| 2325 | 014256 | 024523 | 005015 | 000 | |
| 2326 | 014264 | 0250 | 024470 | 037040 | GRTMSG: .ASCIZ (8) > 2 LSB STATE(S)<(15)>(12) |
| 2327 | 014271 | 031040 | 046040 | 041123 | |
| 2328 | 014280 | 051440 | 040524 | 042524 | |
| 2329 | 014312 | 051450 | 006451 | 000012 | |
| 2330 | 014320 | 034050 | 020051 | 020074 | NARMSG: .ASCIZ (8) < 1/2 LSB NARROW STATE(S)<(15)>(12) |
| 2331 | 014326 | 027461 | 020062 | 051514 | |
| 2332 | 014334 | 020102 | 040516 | 051122 | |
| 2333 | 014342 | 052517 | 051440 | 040524 | |
| 2334 | 014350 | 042524 | 051450 | 006451 | |
| 2335 | 014356 | 000012 | | | |
| 2336 | 014360 | 034050 | 02 | 020076 | WIDMSG: .ASCIZ (9) > 1 1/2 LSB WIDE STATE(S)<(15)>(12) |
| 2337 | 014366 | 020061 | 027 | 020062 | |
| 2338 | 014380 | 051514 | 020102 | 044527 | |
| 2339 | 014402 | 042524 | 051440 | 040524 | |
| 2340 | 014410 | 042524 | 051450 | 006451 | |
| 2341 | 014416 | 000012 | | | |
| 2342 | 014424 | 054056 | 054056 | 020045 | FEH_F: .ASCIZ \XX.X% OF STATES WITHIN 1/2 LSB (SPEC = 95%)<(15)>(12) |
| 2343 | 014432 | 051440 | 051440 | 040524 | |
| 2344 | 014440 | 020123 | 020123 | 044527 | |
| 2345 | 014448 | 047111 | 047111 | 030440 | |
| 2346 | 014456 | 046040 | 046040 | 041123 | |
| 2347 | 014464 | 051450 | 051450 | 042520 | |
| 2348 | 014472 | 020076 | 020076 | 037040 | |
| 2349 | 014480 | 005015 | 005015 | 000 | |
| 2350 | 014488 | 027130 | 027130 | 022530 | PERFRT: .ASCIZ \XX.X% OF STATES WITHIN 1/4 LSB <(15)>(12) |
| 2351 | 014496 | 020106 | 020106 | 052123 | |
| 2352 | 014504 | 051505 | 051505 | 053440 | |
| 2353 | 014512 | 044510 | 044510 | 020116 | |
| 2354 | 014520 | 020064 | 020064 | 051514 | |
| 2355 | 014528 | 005015 | 005015 | 000 | |
| 2356 | 014536 | 020040 | 020040 | 020040 | MULTSP: .ASCIZ / |
| 2357 | 014544 | 020040 | 020040 | 020040 | |
| 2358 | 014552 | 020040 | 020040 | 020040 | |
| 2359 | 014560 | 020040 | 020040 | 020040 | |
| 2360 | 014568 | 020040 | 020040 | 020040 | |
| 2361 | 014576 | 001116 | 001464 | 011662 | DT1: .EVEN SERRPC,ARBADD,CHANL,SGDOAT,SPREAD,SBOCAT,0 |
| 2362 | 014584 | 001126 | 011664 | 001126 | |
| 2363 | 014592 | 000000 | | | |
| 2364 | 014600 | 001116 | 001464 | 011662 | DT2: SERRPC,ARBADD,CHANL,SGDOAT,SBOCAT,0 |
| 2365 | 014608 | 001126 | 001126 | 000000 | |
| 2366 | 014616 | 001116 | 001464 | 001124 | DT3: SERRPC,ARBADD,SGDOAT,SBOCAT,0 |
| 2367 | 014624 | 001126 | 000000 | | |
| 2368 | 014632 | 001116 | 001464 | 001126 | DT5: SERRPC,ARBADD,SBOCAT,SGDOAT,0 |
| 2369 | 014640 | 001126 | 001126 | | |


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DIFLIN: MOV (R5)+,VCREG1 ;LOAD COARSE REGISTER ADDRESS
        MOV (R5)+,VCREG2 ;LOAD FINE ADDRESS
        MOV (R5)+,DIFCHN ;LOAD CHANNEL
        MOV R5,($P)
        CLR JVCSTAT
        MOV JVCREG1,VCREG1 ;GET BUS ADDRESS
        MOV JVCREG2,VCREG2
        MOV #0,JVCREG1 ;CLEAR COARSE
        MOV #0,JVCREG2 ;CLEAR FINE
        MOV #ADBUFF,%1
        CLR %0
15: CLR (1)+ ;CLEAR MEMORY
    CMP #LAST,%1
    BNE 15

CONVR: JSR PC,CONVT
        BEQ 25 ;BRANCH IF RESULT = 0
        CMP R5,RO ;COMPARE RESULT AND POINT
        BMT 25 ;BRANCH FOR RESULT < POINT
        INCB ADBUFF(%5) ;INCREMENT CONTENTS OF Z
        BCC 25
        MOVB #377,ADBUFF(R5)
25: ADD #1,JVCREG2 ;UPDATE FINE REG.
    TST JVCREG2
    BNE CONVR ;BRANCH UNTIL FINISH
    CMP #1774,JVCREG1 ;TEST COARSE REG.
    BEQ READ
        ADD #14,JVCREG1 ;UPDATE COARSE REG.
        CLR JVCREG2 ;CLEAR FINE REG.
        JSR PC,CONVT
        ADD #4,R5 ;(4+RESULT) IN R5
        MOV #5,%0 ;LOAD POINT
        ADD #ADBUFF,%5
        MOV #10,%4
15: CLR B ;CLEAR B. MEM BYTE LOCATIONS
    DECB %4
    BNE 15
    BR CONVR

READ: BIT #BIT12,DSWR
        BEQ 135
        TYPE ADDIFM
135: CLR RO
        MOV #1776,%2
        MOV #ADBUFF+1,%3
        MOV #1,TEMP
        CLR QRTOK
        CLR DIFERR
        CLR NARROW
        CLR WIDE
        CLR ZOS
        CLR SKIPST
        CLR EXCESS
25: MOVB (R3),RO ;TEST IF STATE WIDTH > 2LSB
    CMP RO,#14
    BGT 15

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015526 105260 024102 INCB ABUFF4(RO) :UPDATE STATE-WIDTH BUFFER
015528 020037 015202 CMP RO,HILIM1 :TEST IF STATE WIDTH > 1 1/2 LSB
015530 003021 BGT 3$ :YES BR
015532 020037 015204 CMP RO,HILIM2 :TEST IF STATE WIDTH > 1 1/4 LSB
015534 003061 BGT 4$ :YES BR
015536 020037 017000 CMP RO,VI :IS IT A SKIPPED STATE?
015538 103416 SLO 10$ :YES BR
015540 020037 015176 CMP RO,LOLIM1 :TEST IF STATE WIDTH < 1/2 LSB
015542 002415 BLT 11$ :YES BR
015544 020037 015200 CMP RO,LOLIM2 :TEST IF STATE WIDTH < 3/4 LSB
015546 002450 BLT 4$ :YES BR
015548 005237 015206 INC JRTCK :STATE WIDTH BETWEEN 3/4 AND 1 1/4 LSB
015550 003445 BR 4$
015552 005237 015744 1$: INC EXCESS :UPDATE > 2 LSB WIDE STATE COUNT
015554 005237 015212 3$: INC WIDE :UPDATE > 1 1/2 LSB WIDE STATE COUNT
015556 000406 BR 12$
015558 005237 015740 10$: INC SKIPST :UPDATE SKIPPED STATE COUNT
015560 105700 11$: TSTB RO
015562 100767 BMI 1$
015564 005237 015200 INC NARROW :UPDATE < 1/2 LSB NARROW STATE COUNT
015566 005237 015742 12$: INC DIFERR :UPDATE OUTSIDE + OR - 1/2 LSB COUNT
015568 022777 010000 5$: BIT #BIT12,3SWR :TEST FOR FORCED TYPEOLT
015570 001424 BEQ 4$ ::BR IF NOT
015572 005737 015736 TST 20$ :TEST IF FIRST TIME ?
015574 001004 BNE 6$ :BR IF YES
015576 005237 015736 INC 20$
015578 104400 TYPE ADDIF
015580 012746 6$: MOV TEMP,-(SP) :SAVE CURRENT STATE
015582 104402 TYPOS
015584 004 .BYTE 4,1
015586 104400 016552 TYPE .TYANXX
015588 042700 177400 BIC #177400,RO
015590 010046 MOV RO,-(SP)
015592 104402 TYPOS
015594 003 .BYTE 3,1
015596 104400 016536 TYPE ACRLF
015598 005237 015216 4$: INC TEMP
015600 005723 TSTB (R3)+
015602 005302 DEC %2
015604 001276 BNE 2$
015606 032777 010000 163206 BIT #BIT12,3SWR :TEST BIT 12
015608 001006 BNE SWDIST :BR IF FORCED PRINTOUT
015610 000205 MOV (SP)+,R5
015612 000000 RTS R5
015614 000000 20$:
015616 000000 SKIPST: 0
015618 000000 DIFERR: 0
015620 000000 EXCESS: 0
015622 000000
015624 000000
015626 000000
015628 000000
015630 000000
015632 000000
015634 000000
015636 000000
015638 000000
015640 000000
015642 000000
015644 000000
015646 012703 024102 SWDIST: MOV #ABUFF4,%3
015648 005037 015216 CLR TEMP
015650 104400 016536 TYPE ACRLF
015652 012746 015740 MOV SKIPST,-(SP) :SAVE SKIPPED STATES
015654 104402 TYPOS
015656 003 .BYTE 3,0
015658 104400 014242 TYPE .SKPMSG

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| | | | | | | |
|------|--------|--------|--------|--------|--------------|-----------------------------|
| 2546 | 015776 | 013746 | 015744 | MOV | EXCESS,-(SP) | ;SAVE EXCESSIVE WIDE STATES |
| 2547 | 016002 | 104402 | | TYPOS | | |
| 2548 | 016004 | 003 | 000 | .BYTE | 3,0 | |
| 2549 | 016006 | 104400 | 014271 | TYPE | ,GRT2MG | |
| 2550 | 016012 | 013746 | 015210 | MOV | NARROW,-(SP) | |
| 2551 | 016016 | 104402 | | TYPOS | | |
| 2552 | 016020 | 003 | 000 | .BYTE | 3,0 | |
| 2553 | 016022 | 104400 | 014320 | TYPE | ,NARMSG | |
| 2554 | 016026 | 013746 | 015212 | MOV | WIDE,-(SP) | ;SAVE WIDE STATES |
| 2555 | 016032 | 104402 | | TYPOS | | |
| 2556 | 016034 | 003 | 000 | .BYTE | 3,0 | |
| 2557 | 016036 | 104400 | 014360 | TYPE | ,WIDMSG | |
| 2558 | 016042 | 012700 | 001776 | MOV | #1022,RO | |
| 2559 | 016046 | 163700 | 015212 | SUB | WIDE,RO | |
| 2560 | 016052 | 163700 | 015210 | SUB | NARROW,RO | |
| 2561 | 016056 | 004537 | 017044 | JSR | RS,PRCNT | ;CONVERT TO PERCENT |
| 2562 | 016062 | 014420 | | PERHLF | | |
| 2563 | 016064 | 104400 | 014420 | TYPE | ,PERHLF | |
| 2564 | 016070 | 013700 | 015206 | MOV | QRTOK,RO | |
| 2565 | 016074 | 004537 | 017044 | JSR | RS,PRCNT | |
| 2566 | 016100 | 014477 | | PERQRT | | |
| 2567 | 016102 | 104400 | 014477 | TYPE | ,PERQRT | |
| 2568 | 016106 | 104400 | | TYPE | | |
| 2569 | 016110 | 016554 | | STDMSG | | |
| 2570 | 016112 | 013702 | 015216 | MOV | TEMP,R2 | |
| 2571 | 016116 | 006302 | | ASL | R2 | |
| 2572 | 016120 | 004737 | 011342 | JSR | PC,DECPRT | ;CONVERT R2 TO DEC. |
| 2573 | 016124 | 113737 | 011463 | MOVSB | DIGT1,RSV1 | |
| 2574 | 016132 | 113737 | 011464 | MOVSB | DIGT2,RSV2 | |
| 2575 | 016140 | 113737 | 011465 | MOVSB | DIGT3,RSV3 | |
| 2576 | | | | | | |
| 2577 | 016146 | 111302 | | MOVSB | (R3),R2 | |
| 2578 | 016150 | 004737 | 011342 | JSR | PC,DECPRT | ;CONVERT R2 TO DEC. |
| 2579 | 016154 | 113737 | 011462 | MOVSB | DIGT0,RSX1 | |
| 2580 | 016162 | 113737 | 011463 | MOVSB | DIGT1,RSX2 | |
| 2581 | 016170 | 113737 | 011464 | MOVSB | DIGT2,RSX3 | |
| 2582 | 016176 | 113737 | 011465 | MOVSB | DIGT3,RSX4 | |
| 2583 | 016204 | 104400 | | TYPE | | |
| 2584 | 016206 | 016376 | | RSVMSG | | |
| 2585 | 016210 | 111305 | | MOVSB | (%3),R5 | |
| 2586 | 016212 | 005305 | | DEC | R5 | |
| 2587 | 016214 | 100403 | | BM: | 15 | |
| 2588 | 016216 | 104400 | | TYPE | | |
| 2589 | 016220 | 016541 | | TYANX | | |
| 2590 | 016222 | 000773 | | BR | 25 | |
| 2591 | 016224 | 023737 | 015176 | CMP | L0LIM1,TEMP | |
| 2592 | 016232 | 001412 | | BEQ | 45 | |
| 2593 | 016234 | 023737 | 015202 | CMP | H1LIM1,TEMP | |
| 2594 | 016242 | 001412 | | BEQ | 55 | |
| 2595 | 016244 | 023737 | 015214 | CMP | AMEAN,TEMP | |
| 2596 | 016252 | 001010 | | BNE | 35 | |
| 2597 | 016254 | 104400 | 016472 | TYPE | ,TYMEAN | |
| 2598 | 016260 | 000405 | | BR | 35 | |
| 2599 | 016262 | 104400 | 016414 | TYPE | ,TYLOW | |
| 2600 | 016266 | 000402 | | BR | 35 | |
| 2601 | 016270 | 104400 | 016442 | TYPE | ,TYHIGH | |

| | | | | | | | | |
|------|--------|--------|---------------|----------|--------|---------------------------|----------------|--|
| 2602 | 016274 | 005237 | 015216 | 35: | INC | TEMP | | |
| 2603 | 016300 | 105723 | | | TSTB | (3)+ | | |
| 2604 | 016302 | 022737 | 000145 015216 | | CMF | #145.TEMP | | |
| 2605 | 016310 | 001300 | | | BNE | 65 | | |
| 2606 | 016212 | 104400 | | | TYPE | | | |
| 2607 | 016314 | 016536 | | | ACRLF | | | |
| 2608 | 016316 | 104400 | 016516 | | TYPE | OVERNG | | |
| 2609 | 016322 | 013746 | 015744 | | MOV | EXCESS,-(SP) | | |
| 2610 | 016326 | 104402 | | | TYPOS | | | |
| 2611 | 016330 | 004 | 001 | | .BYTE | 4,1 | | |
| 2612 | 016332 | 012605 | | | MOV | (SP)+,R5 | | |
| 2613 | 016334 | 000205 | | | RTS | R5 | | |
| 2614 | | | | | | | | |
| 2615 | 016336 | 000000 | | 125: | 0 | | | |
| 2616 | | | | | | | | |
| 2617 | 016340 | 005277 | 163142 | CONVT: | INC | QVCSTAT | | |
| 2618 | 016344 | 105777 | 163136 | 25: | TSTB | QVCSTAT | | |
| 2619 | 016350 | 100375 | | | BPL | 25 | | |
| 2620 | 016352 | 005005 | | | CLR | %5 | | |
| 2621 | 016354 | 013777 | 016644 163112 | | MOV | DIFCHN,QADC5 | ;CONVERT | |
| 2622 | 016362 | 105777 | 163106 | 15: | TSTB | QADC5 | ;WAIT FOR DONE | |
| 2623 | 016366 | 100375 | | | BPL | 15 | | |
| 2624 | 016370 | 017705 | 163104 | | MOV | QADCBP,R5 | ;READ VALUE | |
| 2625 | 016374 | 000207 | | | RTS | PC | | |
| 2626 | | | | | | | | |
| 2627 | 016376 | 015 | 012 | RSVMSG: | .BYTE | 15,12 | | |
| 2628 | 016400 | 060 | 056 | RSV1: | .BYTE | 60,56 | | |
| 2629 | 016402 | 060 | | RSV2: | .BYTE | 60 | | |
| 2630 | 016403 | 060 | 055 | RSV3: | .BYTE | 60,55 | | |
| 2631 | 016405 | 060 | | RSX1: | .BYTE | 60 | | |
| 2632 | 016406 | 060 | | RSX2: | .BYTE | 60 | | |
| 2633 | 016407 | 060 | | RSX3: | .BYTE | 60 | | |
| 2634 | 016410 | 060 | 040 111 | RSX4: | .BYTE | 60,40,111,0 | | |
| 2635 | 016412 | 000 | | | | | | |
| 2636 | 016414 | 026455 | 026455 026455 | TYLOW: | .ASCIZ | "----- (1/2 LSB)" | | |
| 2637 | 016422 | 026455 | 026455 020040 | | | | | |
| 2638 | 016430 | 030450 | 031057 046040 | | | | | |
| 2639 | 016436 | 041123 | 000051 | | | | | |
| 2640 | 016442 | 026455 | 026455 026455 | TYHIGH: | .ASCIZ | "----- (1 1/2 LSB)" | | |
| 2641 | 016450 | 026455 | 026455 020040 | | | | | |
| 2642 | 016456 | 030450 | 030440 031057 | | | | | |
| 2643 | 016464 | 046040 | 041123 000051 | | | | | |
| 2644 | 016472 | 026455 | 026455 026455 | TYMEAN: | .ASCIZ | "----- (1 LSB)" | | |
| 2645 | 016500 | 026455 | 026455 020040 | | | | | |
| 2646 | 016506 | 030450 | 046040 041123 | | | | | |
| 2647 | 016514 | 000051 | | | | | | |
| 2648 | 016516 | 052517 | 020124 043117 | OVERNG: | .ASCIZ | /OUT OF RANGE - | | |
| 2649 | 016524 | 051040 | 047101 042507 | | | | | |
| 2650 | 016532 | 026440 | 000040 | | | | | |
| 2651 | 016536 | 015 | 012 000 | ACRLF: | .BYTE | 15,12,0 | | |
| 2652 | 016541 | 052 | 000 | TYANX: | .BYTE | 52,0 | | |
| 2653 | 016543 | 040 | 020040 000 | SP3MSG: | .ASCIZ | / / | | |
| 2654 | 016547 | 000 | 000 000 | | .BYTE | 0,0,0 | | |
| 2655 | 016552 | 055 | 000 | TYANXX: | .BYTE | 55,0 | | |
| 2656 | 016554 | 015 | 012 | STCMMSG: | .BYTE | 15,12 | | |
| 2657 | 016556 | 052123 | 052101 026505 | | .ASCII | /STATE-WIDTH DISTRIBUTION | | |

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2658 016564 044527 052104 020110
2659 016572 044504 052123 044522
2660 016600 052502 044524 047117
2661 016606 015 012
2662 016610 044527 052104 026510
2663 016616 052516 041115 051105
2664 016624 047440 020106 052123
2665 016632 052101 051505 000
2666 016640
2667 016640 000000
2668 016642 000000
2669 016644 000000
2670
2671
2672 016646 012701 016764
2673 016652 005737 016762
2674 016656 001017
2675 016660 012702 017004
2676 016664 112737 000062 013150
2677 016672 112737 000060 013152
2678 016700 112737 000065 013175
2679 016706 112737 000060 013176
2680 016714 000416
2681 016716 012702 017022
2682 016722 112737 000061 013150
2683 016730 112737 000070 013152
2684 016736 112737 000064 013175
2685 016744 112737 000064 013176
2686 016752 012221
2687 016754 005711
2688 016756 100375
2689 016760 000207
2690
2691 016762 000000
2692
2693 016764 001754
2694 016766 000024
2695 016770 000050
2696 016772 000024
2697 016774 000062
2698 016776 000077
2699 017000 000001
2700 017002 100000
2701
2702 017004 001754
2703 017006 000024
2704 017010 000050
2705 017012 000024
2706 017014 000062
2707 017016 000077
2708 017020 000001
2709
2710 017022 001760
2711 017024 000020
2712 017026 000040
2713 017030 000022

```

```

.BYTE 15,12
.ASCIZ WIDTH-NUMBER OF STATES/

```

```

.EVEN
VCREG1: 0
VCREG2: 0
DIFCHN: 0

```

```

.SBTTL PARAMETER ADJUSTMENT ROUTINE
WFADJ: MOV #V1754,R1 ;LOAD PARM. POINTER
TST WFTST ;TEST IF OPTION TEST AREA
BNE IS ;BR IF IT WAS
MOV #VARLT1,R2 ;LOAD "STANDARD" PARM. VALUES
MOVB #'2,BASBT1 ;LOAD BIAS MESSAGE
MOVB #'0,BASBT2
MOVB #'5,BASBT3 ;LOAD BIAS LIMIT
MOVB #'0,BASBT3+1
BR 25
15: MOV #VARLT2,R2 ;LOAD "OPTION TEST AREA" PARM. VALUES
MOVB #'1,BASBT1 ;LOAD BIAS MESSAGE
MOVB #'8,BASBT2
MOVB #'4,BASBT3 ;LOAD BIAS LIMIT
MOVB #'4,BASBT3+1
25: MOV (R2)+,(R1)+ ;LOAD INTO PARM. LIST
TST (R1) ;TEST FOR LAST
BPL 25 ;BR IF NOT
RTS PC ;EXIT

```

```
WFTST: 0
```

```

V1754: 1754 ;1760 IF OPTION TEST AREA SELECTED
V24: 24 ;20
V50: 50 ;40
V24: 24 ;20
V62: 62 ;45
V77: 77 ;45
V1: 1 ;15
BIT15 RM.

```

```

VARLT1: 1754
24
50
24
62
77
1
VARLT2: 1760
20
40
22

```

```

017032 000045 45
017034 000045 45
017036 000015 15
      .SBTTL SUBROUTINE TO CONVERT R0 TO DECIMAL PERCENTAGE OF 1022.
017040 000000 MSGPNT: 0
017042 000000 PCT: 0
017044 012537 017040 PRCNT: MOV (R5)+,MSGPNT ;GET MESSAGE POINTER
017050 010037 017042 MOV R0,PCT ;
017054 006200 ASR R1 ;
017056 006200 ASR R0 ;
017060 006200 ASR R0 ;
017062 006200 ASR R0 ;
017064 006200 ASR R0 ;
017066 006200 ASR R0 ;
017070 160037 017042 SUB R0,PCT ;
017074 006200 ASR R0 ;
017076 160037 017042 SUB R0,PCT ;PCT CONTAINS OCTAL %
017102 013702 017042 MOV PCT,R2 ;LOAD R2 WITH PERCENTAGE
017106 004737 011342 JSR PC,DECPRT ;CONVERT TO DEC.
017112 013700 017040 MOV MSGPNT,R0 ;LOAD MSG. POINTER
017116 022737 001750 017042 CMP #1000.,PCT ;TEST FOR 100 %
017124 001411 BEQ 15 ;BR IF 100 %
017126 113720 011463 MOVB DIGT1,(R0)+ ;LOAD A DIGIT
017132 113720 011464 MOVB DIGT2,(R0)+ ;
017136 113720 000056 MOVB #56,(R0)+ ;LOAD "."
017142 113720 011465 MOVB DIGT3,(R0)+ ;LOAD 1/10 % DIGIT
017146 000205 RTS R5 ;EXIT

017150 112720 000040 15: MOVB #40,(R0)+ ;LOAD "SPACE"
017154 112720 000061 MOVB #'1,(R0)+ ;LOAD '1' 100%
017160 112720 000060 MOVB #'0,(R0)+ ;
017164 112720 000060 MOVB #'0,(R0)+ ;
017170 000205 RTS R5 ;EXIT

```

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2800
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017172
017172 104405
017174 032777 040000 161734
017202 001114

017204 000416

017206 013746 000004
017212 012737 017232 000004
017220 005737 177050
017224 012637 000004
017230 000463
017232 022626
017234 012637 000004
017240 000423
017242
017242 032777 000400 161666
017250 001404
017252 127737 161660 001102
017260 001465
017262 105737 001103
017266 001421
017270 123737 001115 001103
017276 101015
017300 032777 001000 161630
017306 001404
017310 013737 001110 001106
017316 000446
017320 105037 001107
017324 005037 001154
017330 000415
017332 032777 004000 161576
017340 001011
017342 005737 001206
017346 001406
017350 005237 001104
017354 023737 001164 001104
017362 002024
017364 012737 000001 001104
017372 013737 017450 001164
017400 105237 001102

```
.SBTTL SCOPE HANDLER ROUTINE
*****
*THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
*AND LOAD THE TEST NUMBER($STSTM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW14=1 LOOP ON TEST
*SW11=1 INHIBIT ITERATIONS
*SW09=1 LOOP ON ERROR
*SW08=1 LOOP ON TEST IN SWR<7:0>
*CALL
* SCOPE ::SCOPE=IOT

$SCOPE:
CKSWR
1$: BIT #BIT14,$SWR ::LOOP ON PRESENT TEST?
BNE $OVER ::YES IF SW14=1
*****START OF CODE FOR THE XOR TESTER*****
$XTSTR: BR 6$
::IF RUNNING ON THE "XOR" TESTER CHANGE
::THIS INSTRUCTION TO A "NOP" (NOP=240)
::SAVE THE CONTENTS OF THE ERROR VECTOR
MOV 2$ERRVEC, -(SP)
MOV 3$ERRVEC
TST 2$177060
::SET FOR TIMEOUT
::TIME OUT ON XOR?
MOV (SP)+, 2$ERRVEC
BR $SVLAD
::RESTORE THE ERROR VECTOR
::GO TO THE NEXT TEST
5$: CMP (SP)+, (SP)+
::CLEAR THE STACK AFTER A TIME OUT
MOV (SP)+, 2$ERRVEC
BR 7$
::RESTORE THE ERROR VECTOR
::LOOP ON THE PRESENT TEST
6$: *****END OF CODE FOR THE XOR TESTER*****
BIT #BIT08,$SWR
::LOOP ON SPEC. TEST?
BEQ 2$
BR IF NO
CMPB $SWR,$STSTM
::ON THE RIGHT TEST? SWR<7:0>
BEQ $OVER
BR IF YES
2$: TSTB $ERFLG
::HAS AN ERROR OCCURRED?
BEQ 3$
BR IF NO
CMPB $ERMAX,$ERFLG
::MAX. ERRORS FOR THIS TEST OCCURRED?
BHI 3$
BR IF NO
BIT #BIT09,$SWR
::LOOP ON ERROR?
BEQ 4$
BR IF NO
7$: MOV $LPERR,$LPADR
::SET LOOP ADDRESS TO LAST SCOPE
BR $OVER
4$: CLRB $ERFLG
::ZERO THE ERROR FLAG
CLR $TIMES
::CLEAR THE NUMBER OF ITERATIONS TO MAKE
BR 1$
::ESCAPE TO THE NEXT TEST
3$: BIT #BIT11,$SWR
::INHIBIT ITERATIONS?
BNE 1$
BR IF YES
TST $PASS
::IF FIRST PASS OF PROGRAM
BEQ 1$
INCR $ICNT
::INCREMENT ITERATION COUNT
CMP $TIMES,$ICNT
::CHECK THE NUMBER OF ITERATIONS MADE
BGE $OVER
BR IF MORE ITERATION REQUIRED
1$: MOV #1,$ICNT
::REINITIALIZE THE ITERATION COUNTER
MOV $MXCNT,$TIMES
::SET NUMBER OF ITERATIONS TO DO
$SVLAD: INCB $STSTM
::COUNT TEST NUMBERS
```

K05

```

2805 017404 113737 001102 001204      MOVB  $STSTNM,$STSTN  ;; SET TEST NUMBER IN APT MAILBOX
2806 017412 011637 001106           MOV   (SP), $LPCADR  ;; SAVE SCOPE LOOP ADDRESS
2807 017416 011637 001110           MOV   (SP), $LPERR   ;; SAVE ERROR LOOP ADDRESS
2808 017422 005037 001166           CLR   $ESCAPE       ;; CLEAR THE ESCAPE FROM ERROR ADDRESS
2809 017426 112737 000001 001115     MOVB  #1,$E=MAX     ;; ONLY ALLOW ONE(1) ERROR ON NEXT TEST
2810 017434 013777 001102 161476 $OVER: MOV  $STSTNM,$DISPLAY ;; DISPLAY TEST NUMBER
2811 017442 013716 001106           MOV   $LPCADR,(SP)  ;; FUDGE RETURN ADDRESS
2812 017446 000002           RTI                    ;; FIXES PS
2813 017450 003720 $MXCNT: 2000.          ;; MAX. NUMBER OF ITERATIONS

.SBTTL  ERROR HANDLER - .JTINE

*****
*THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT.
*SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
*AND GO TO $ERRTYP ON ERROR
*THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
*SW15=1      HALT ON ERROR
*SW13=1      INHIBIT ERROR TYPEOUTS
*SW10=1      BELL ON ERROR
*SW09=1      LOOP ON ERROR
*CALL
*          ERJCR  N          ;; ERROR=EMT AND N=ERROR ITEM NUMBER

2829 017452 $ERROR:
2830 017452 105237 001103 75:      INCB  $ERFLG          ;; SET THE ERROR FLAG
2831 017456 001775      BEQ   75             ;; DON'T LET THE FLAG GO TO ZERO
2832 017460 013777 001102 161452     MOV   $STSTNM,$DISPLAY ;; DISPLAY TEST NUMBER AND ERROR FLAG
2833 017466 032777 002000 161442     BIT   #BIT10,$SWR     ;; BELL ON ERROR?
2834 017474 001402      BEJ   15            ;; NO - SKIP
2835 017476 104400 001170      TYPE  $SBELL        ;; RING BELL
2836 017502 005237 001112 15:      INC   $ERTTL        ;; COUNT THE NUMBER OF ERRORS
2837 017506 011637 001116     MOV   (SP), $ERRPC   ;; GET ADDRESS OF ERROR INSTRUCTION
2838 017512 162737 000002 001116     SUB   #2,$ERRPC
2839 017520 117737 161372 001114     MOVB  $ERRPC,$ITEMB  ;; STRIP AND SAVE THE ERROR ITEM CODE
2840 017526 032777 020000 161402     BIT   #BIT13,$SWR   ;; SKIP TYPEOUT IF SET
2841 017534 001004      BNE  20$           ;; SKIP TYPEOUTS
2842 017536 004737 020442     JSR   PC,$ERRTYP    ;; GO TO USER ERROR ROUTINE
2843 017542 104400 001175      TYPE  $SCALF

2844 017546 20$:
2845 017546 122737 000001 001220     CMPB  #APTENV,$ENV   ;; RUNNING IN APT MODE
2846 017554 001007      BNE  25            ;; NO SKIP APT ERROR REPORT
2847 017556 113737 001114 017570     MOVB  $ITEMB,21$    ;; SET ITEM NUMBER AS ERROR NUMBER
2848 017564 004737 021544     JSR   PC,$ATY4     ;; REPORT FATAL ERROR TO APT
2849 017570 000           21$:      .BYTE 0
2850 017571 000           .BYTE 0
2851 017572 000777      BR   22$           ;; APT ERROR LOOP
2852 017574 005777 161336     TST  $SWR          ;; HALT ON ERROR
2853 017600 100001      BPL  3$            ;; SKIP IF CONTINUE
2854 017602 000000      HALT                ;; HALT ON ERROR!
2855 017604 032777 001000 161324     3$:      BIT   #BIT09,$SWR  ;; LOOP ON ERROR SWITCH SET?
2856 017612 001402      BEQ  4$            ;; BR IF NO
2857 017614 013716 001110     MOV   $LPERR,(SP)  ;; FUDGE RETURN FOR LOOPING
2858 017620 005737 001166     TST  $ESCAPE       ;; CHECK FOR AN ESCAPE ADDRESS
2859 017624 001402      BEQ  5$            ;; BR IF NONE
2860 017626 013716 001166     MOV   $ESCAPE,(SP) ;; FUDGE RETURN ADDRESS FOR ESCAPE

```

```

2861 017632          5$:      CMP      #SENDAD, @#42      ;;ACT-11 AUTO-ACCEPT?
2862 017632 022737 010436 000042      BNE      6$          ;;BRANCH IF NO
2863 017640 001001          HALT          ;;YES
2864 017642 000000
2865 017644          6$:      RTI          ;;RETURN
2866 017544 000002
2867
2868 .SBTTL TTY INPUT ROUTINE
2869
2870 *****
2871 *SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
2872 *ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
2873 *SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
2874 *WHEN OPERATING IN TTY FLAG MODE.
2875 017646 022737 000176 001136 $CKSWR: CMP      #SWREG, SWR      ;; IS THE SOFT-SWR SELECTED?
2876 017654 001073          BNE      14$          ;; BRANCH IF NO
2877 017656 105777 161260          TSTB     @STKS          ;; CHAR THERE?
2878 017662 100070          BPL      14$          ;; IF NO, DON'T WAIT AROUND
2879 017664 117746 161254          2$:      MOVB     @STKB, -(SP)      ;; SAVE THE CHAR
2880 017670 042716 177600          BIC     #1C177, (SP)    ;; STRIP-OFF THE ASCII
2881 017674 022726 000007          CMP     #7, (SP)+      ;; IS IT A CONTROL G?
2882 017700 001061          BNE     14$          ;; NO, RETURN TO USER
2883 017702 104400 020311          TYPE   .SCNTLG        ;; YES, ECHO CONTROL G
2884
2885 017706 104400 020316          6$:      TYPE   $MSWR          ;; TYPE CURRENT CONTENTS
2886 017712 013746 000176          MOV     SWREG, -(SP)   ;; SAVE SWREG FOR TYPEOUT
2887 017716 104401          TYPOC          ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
2888 017720 104400 020327          TYPE   .SMNEW          ;; PROMPT FOR NEW SWR
2889 017724 005046          CLR     -(SP)         ;; CLEAR COUNTER
2890 017726 005046          CLR     -(SP)         ;; THE NEW SWR
2891 017730 104406          7$:      RDCHR          ;; GET NEXT CHAR
2892
2893 017732 022716 000025          8$:      CMP     #25, (SP)    ;; IS IT A CONTROL U?
2894 017736 001005          BNE     9$            ;; BRANCH IF NO
2895 017740 104400 020304          TYPE   .SCNTLU        ;; YES, ECHO IT
2896 017744 062706 000006          ADD     #6, SP        ;; IGNORE PREVIOUS INPUT
2897 017750 000756          BR      6$            ;; LET'S TRY IT AGAIN
2898
2899 017752 022716 000015          9$:      CMP     #15, (SP)   ;; IS IT A <CR>?
2900 017756 001011          BNE    11$           ;; BRANCH IF NO
2901 017760 005766 000004          TST     4(SP)         ;; YES, IS IT THE FIRST CHAR?
2902 017764 001403          BEQ     10$          ;; BRANCH IF YES
2903 017766 016677 000002 161142          MOV     2(SP), @SWR   ;; SAVE NEW SWR
2904 017774 062706 000006          10$:     ADD     #5, SP    ;; CLEAR UP STACK
2905 020000 000417          BR      13$          ;; RETURN TO USER
2906 020002 022716 000012          11$:     CMP     #12, (SP)  ;; IS IT A <LF>?
2907 020006 001017          BNE    15$           ;; BRANCH IF NO
2908 020010 005766 000004          TST     4(SP)         ;; YES, IS IT THE FIRST CHAR?
2909 020014 001403          BEQ     12$          ;; YES
2910 020016 016677 000002 161112          MOV     2(SP), @SWR   ;; SAVE NEW SWR
2911 020024 062706 000006          12$:     ADD     #6, SP    ;; CLEAR UP STACK
2912 020030 013716 000046          MOV     @#46, (SP)    ;; GET RESTART
2913 020034 062716 000010          ADD     #10, (SP)     ;; ADDRESS
2914 020040 104400 001175          13$:     TYPE   .SCRLF        ;; ECHO <CR> AND <LF>
2915 020044 000002          14$:     RTI          ;; RETURN
2916 020046 004737 021456          15$:     JSR     PC, $TYPEC ;; ECHO CHAR

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M05

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2917 020052 042726 177770      BIC      #177770,(SP)+  ;;RESTRICT TO 0-7
2918 020056 005766 000002      TST      2(SP)        ;;IS THIS THE FIRST CHAR
2919 020062 001403          BEQ      16$         ;;BRANCH IF YES
2920 020064 006316          ASL      (SP)        ;;NO, SHIFT PRESENT
2921 020066 006316          ASL      (SP)        ;;CHAR OVER TO MAKE
2922 020070 006316          ASL      (SP)        ;;ROOM FOR NEW ONE.
2923 020072 005266 000002      16$: INC      2(SP)        ;;KEEP COUNT OF CHAR
2924 020076 056616 177776      BIS      -2(SP),(SP) ;;SET IN NEW CHAR
2925 020102 000712          BR       7$         ;;GET THE NEXT ONE
2926
2927 *****
2928 *THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
2929 *CALL:
2930 *      RDCHR          ;;INPUT A SINGLE CHARACTER FROM THE TTY
2931 *      RETURN HERE   ;;CHARACTER IS ON THE STACK
2932 *                  ;;WITH PARITY BIT STRIPPED OFF
2933
2934 $RDCHR: MOV      (SP),-(SP)  ;;PUSH DOWN THE PC
2935 020106 016666 000004 000002  MOV      4(SP),2(SP)  ;;SAVE THE PS
2936 020114 105777 161022 1$:  TSTB     2$TKS      ;;WAIT FOR
2937 020120 100375          BPL      1$         ;;A CHARACTER
2938 020122 117766 161010 000004  MOVB     2$TKB,4(SP)  ;;READ THE TTY
2939 020130 042766 1776LL 000004  BIC      #1C<17>,4(SP) ;;GET RID OF JUNK IF ANY
2940 020136 026627 000004 000140  CMP      4(SP),#140  ;;IS IT UPPER CASE?
2941 020144 002407          BLT      2$         ;;BRANCH IF YES
2942 020146 026627 000004 000175  CMP      4(SP),#175  ;;IS IT A SPECIAL CHAR?
2943 020154 003003          BGT      2$         ;;BRANCH IF YES
2944 020156 042766 000040 000004  BIC      #40,4(SP)   ;;MAKE IT UPPER CASE
2945 020164 000002          RTI             ;;GO BACK TO USER
2946
2947 *****
2948 *THIS ROUTINE WILL INPUT A STRING FROM THE TTY
2949 *CALL:
2950 *      RDLIN         ;;INPUT A STRING FROM THE TTY
2951 *      RETURN HERE  ;;ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
2952 *                  ;;TERMINATOR WILL BE A BYTE OF ALL 0'S
2953
2954 $RDLIN: MOV      R3, -(SP)  ;;SAVE R3
2955 020170 012703 020274 1$:  MOV      #1$TTYIN,R3  ;;GET ADDRESS
2956 020174 022703 020304 2$:  CMP      #1$TTYIN+8,R3 ;;BUFFER FULL?
2957 020200 101405          BLOS     4$         ;;BR IF YES
2958 020202 104406          RDCHR   ;;GO READ ONE CHARACTER FROM THE TTY
2959 020204 112613          MOVB    (SP)+,R3    ;;GET CHARACTER
2960 020206 122713 000177 10$:  CMPB    #177,R3     ;;IS IT A RUBOUT
2961 020212 001003          BNE     3$         ;;SKIP IF NOT
2962 020214 104400 001174 4$:  TYPE    $QJES      ;;TYPE A '?'
2963 020220 000763          BR      1$         ;;CLEAR THE BUFFER AND LOOP
2964 020222 111337 020272 3$:  MOVB    (R3),9$    ;;ECHO THE CHARACTER
2965 020226 104400 020272          TYPE    3$
2966 020232 122723 000015  CMPB    #15,(R3)+  ;;CHECK FOR RETURN
2967 020236 001356          BNE     2$         ;;LOOP IF NOT RETURN
2968 020240 105063 177777          CLRB   -1(R3)     ;;CLEAR RETURN (THE 15)
2969 020244 104400 001176          TYPE    $LF       ;;TYPE A LINE FEED
2970 020250 012603          MOV     (SP)+,R3  ;;RESTORE R3
2971 020252 011646          MOV     (SP),-(SP) ;;ADJUST THE STACK AND PUT ADDRESS OF THE
2972 020254 016666 000004 000002  MOV     4(SP),2(SP) ;;FIRST ASCII CHARACTER ON IT
2973 020262 012766 020274 000004  MOV     #1$TTYIN,4(SP)

```


.SBTTL POWER DOWN AND UP ROUTINES

::*****
:POWER DOWN ROUTINE

020576 012737 020742 000024
020604 012737 000340 000026
020612 010046
020614 010046
020616 010046
020620 010046
020622 010046
020624 010046
020626 017746 160304
020630 010637 020746
020638 012737 020650 000024
020644 000000
020646 000776

\$PWRDN: MOV \$SILLUP, @PWRVEC :: SET FOR FAST UP
MOV @340, @PWRVEC+2 :: PRIO:7
MOV RC, -(SP) :: PUSH RC ON STACK
MOV R1, -(SP) :: PUSH R1 ON STACK
MOV R2, -(SP) :: PUSH R2 ON STACK
MOV R3, -(SP) :: PUSH R3 ON STACK
MOV R4, -(SP) :: PUSH R4 ON STACK
MOV R5, -(SP) :: PUSH R5 ON STACK
MOV @SWR, -(SP) :: PUSH @SWR ON STACK
MOV SP, \$SAVR6 :: SAVE SP
MOV \$PWRUP, @PWRVEC :: SET UP VECTOR
HALT
BR -2 :: HANG UP

::*****
:POWER UP ROUTINE

020650 012737 020742 000024
020656 013706 020746
020660 035037 020746
020666 005237 020746
020672 001375
020674 012677 160236
020676 012605
020678 012604
020680 012603
020682 012602
020684 012601
020686 012600
020688 012600
020690 012737 020576 000024
020692 012737 000340 000026
020694 004400
020696 020750
020698 002716
020700 001516
020702 000002
020704 000000
020706 000776
020708 000000
020710 035015 042522 052123
020712 051101 044524 053516
020714 040440 052106 051105
020716 050040 050517
020718 043040 044501
020720 042522 050015
020722
020724
020726
020728
020730
020732
020734
020736
020738
020740
020742
020744
020746
020748
020750
020752
020754
020756
020758
020760
020762
020764
020766
020768
020770
020772
020774
020776
020778
020780
020782
020784
020786
020788
020790
020792
020794
020796
020798
020800

\$PWRUP: MOV \$SILLUP, @PWRVEC :: SET FOR FAST DOWN
MOV \$SAVR6, SP :: GET SP
CLR \$SAVR6 :: WAIT LOOP FOR THE TTY
IS: INC \$SAVR6 :: WAIT FOR THE INC
BNE IS OF WORD
MOV (SP)+, @SWR :: POP STACK INTO @SWR
MOV (SP)+, R5 :: POP STACK INTO R5
MOV (SP)+, R4 :: POP STACK INTO R4
MOV (SP)+, R3 :: POP STACK INTO R3
MOV (SP)+, R2 :: POP STACK INTO R2
MOV (SP)+, R1 :: POP STACK INTO R1
MOV (SP)+, RC :: POP STACK INTO RC
MOV \$PWRDN, @PWRVEC :: SET UP THE POWER DOWN VECTOR
MOV @340, @PWRVEC+2 :: PRIO:7
TYPE PWRMSG :: REPORT THE POWER FAILURE
MOV (PC)+, (SP) :: POWER FAIL MESSAGE POINTER
SPWRAD: .WORD BEGIN :: RESTART AT BEGIN
RTI :: RESTART ADDRESS
\$SILLUP: HALT :: THE POWER UP SEQUENCE WAS STARTED
BR -2 :: BEFORE THE POWER DOWN WAS COMPLETE
\$SAVR6: 0
PWRMSG: .ASCII '15' 12 RESTARTING AFTER A POWER FAILURE '15' '12' '12'

.E.E'

| | | | | | | |
|--------|--------|---------|---------------|-------|-------------|------------------------------------|
| 021154 | 005704 | | | TST | R4 | :: SUPPRESS THIS 0? |
| 021156 | 001403 | | | BEQ | 5\$ | :: BR IF YES |
| 021160 | 005204 | 4\$: | | INC | R4 | :: DON'T SUPPRESS ANYMORE 0'S |
| 021162 | 052703 | | 000060 | BIS | *'0,R3 | :: MAKE THIS DIGIT ASCII |
| 021166 | 052703 | 5\$: | 000040 | BIS | *'0,R3 | :: MAKE ASCII IF NOT ALREADY |
| 021170 | 110337 | | 021236 | MOVB | R3,B\$ | :: SAVE FOR TYPING |
| 021176 | 104400 | | 021236 | TYPE | 9\$ | :: GO TYPE THIS DIGIT |
| 021180 | 105337 | 7\$: | 021240 | DECB | \$CNT | :: COUNT BY 1 |
| 021186 | 003347 | | | BGT | 2\$ | :: BR IF MORE TO DO |
| 021190 | 002402 | | | BLT | 6\$ | :: BR IF DONE |
| 021194 | 005204 | | | INC | R4 | :: INSURE LAST DIGIT ISN'T A BLANK |
| 021198 | 000744 | | | BR | 2\$ | :: GO DO THE LAST DIGIT |
| 021202 | 012605 | 8\$: | | MOV | (SP)+,R5 | :: RESTORE R5 |
| 021206 | 012604 | | | MOV | (SP)+,R4 | :: RESTORE R4 |
| 021210 | 012603 | | | MOV | (SP)+,R3 | :: RESTORE R3 |
| 021214 | 016666 | | 000002 000004 | MOV | 2(SP),4(SP) | :: SET THE STACK FOR RETURNING |
| 021218 | 012616 | | | MOV | (SP)+,(SP) | |
| 021222 | 000002 | 9\$: | | RTI | | :: RETURN |
| 021226 | 000 | | | .BYTE | 0 | :: STORAGE FOR ASCII DIGIT |
| 021230 | 000 | | | .BYTE | 0 | :: TERMINATOR FOR TYPE ROUTINE |
| 021234 | 000 | \$CNT: | | .BYTE | 0 | :: OCTAL DIGIT COUNTER |
| 021238 | 000 | \$FILL: | | .BYTE | 0 | :: ZERO FILL SWITCH |
| 021242 | 000000 | \$MODE: | | .WORD | 0 | :: NUMBER OF DIGITS TO TYPE |

.SBTTL TYPE ROUTINE

```

*****
*ROUTINE TO TYPE ASCII MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
*THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
*NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
*NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
*NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
*

```

```

*CALL:
*1) USING A TRAP INSTRUCTION
*   TYPE      ,MESADR      ;;MESADR IS FIRST ADDRESS OF AN ASCII STRING
*OR
*   TYPE
*   MESADR
*

```

| | | | | | | |
|--------|--------|---------------|---------|-------|------------------|----------------------------------|
| 021244 | 105737 | 001155 | \$TYPE: | TSTB | \$TPFLG | :: IS THERE A TERMINAL? |
| 021250 | 100002 | | | BPL | 1\$ | :: BR IF YES |
| 021252 | 000000 | | | HALT | | :: HALT HERE IF NO TERMINAL |
| 021254 | 000430 | | | BR | 3\$ | :: LEAVE |
| 021256 | 010046 | 1\$: | | MOV | RO,-(SP) | :: SAVE RO |
| 021260 | 017600 | 000002 | | MOV | 22(SP),RO | :: GET ADDRESS OF ASCII STRING |
| 021264 | 122737 | 000001 001220 | | CMPB | \$APTENV,\$ENV | :: RUNNING IN APT MODE |
| 021272 | 001011 | | | BNE | 62\$ | :: NO GO CHECK FOR APT CONSOLE |
| 021274 | 132737 | 000100 001221 | | BITB | \$APTPOOL,\$ENVM | :: SPOOL MESSAGE TO APT |
| 021302 | 001405 | | | BEQ | 62\$ | :: NO GO CHECK FOR CONSOLE |
| 021304 | 010037 | 021314 | | MOV | RO,61\$ | :: SETUP MESSAGE ADDRESS FOR APT |
| 021310 | 004737 | 021534 | | JSR | PC,\$ATY3 | :: SPOOL MESSAGE TO APT |
| 021314 | 000000 | | 61\$: | .WORD | 0 | :: MESSAGE ADDRESS |
| 021316 | 122737 | 000040 001221 | 62\$: | BITB | \$APTOS,\$ENVM | :: APT CONSOLE SUPPRESSED |
| 021324 | 001003 | | | BNE | 60\$ | :: YES, SKIP TYPE OUT |

```

3235 021326 112046      25:   MOVB   (RO)+,-(SP)   ;; PUSH CHARACTER TO BE TYPED ONTC STACK
3236 021330 001005      BNE    45             ;; BR IF IT ISN'T THE TERMINATOR
3237 021332 005726      TST    (SP)+         ;; IF TERMINATOR POP IT OFF THE STACK
3238 021334 012600      60$:   MOV    (SP)+,RO    ;; RESTORE RO
3239 021336 062716 000002  35:   ADD    #2,(SP)      ;; ADJUST RETURN PC
3240 021342 000002      RTI                    ;; RETURN
3241 021344 122716 000011  45:   CMPB   #HT,(SP)     ;; BRANCH IF <HT>
3242 021350 001430      BEQ    85             ;;
3243 021352 122716 000200  CMPB   #CRLF,(SP)   ;; BRANCH IF NOT <CRLF>
3244 021356 001006      BNE    55             ;;
3245 021360 005726      TST    (SP)+         ;; POP <CR>,<LF> EQUIV
3246 021362 104400      TYPE   $CRLF        ;; TYPE A CR AND LF
3247 021364 001175      $CRLF
3248 021366 105037 021522  CLRB   $CHARCNT     ;; CLEAR CHARACTER COUNT
3249 021372 000755      BR     25            ;; GET NEXT CHARACTER
3250 021374 004737 021456  55:   JSR    PC,$TYPEC    ;; GO TYPE THIS CHARACTER
3251 021400 123726 001154  55:   CMPB   $FILLC,(SP)+ ;; IS IT TIME FOR FILLER CHARS.?
3252 021404 001350      BNE    25            ;; IF NO GO GET NEXT CHAR.
3253 021406 013746 001152  MOV    $NULL,-(SP)  ;; GET # OF FILLER CHARS. NEEDED
3254                                ;; AND THE NULL CHAR.
3255 021412 105366 000001  75:   DECB   1(SP)        ;; DOES A NULL NEED TO BE TYPED?
3256 021416 002770      BLT    65            ;; BR IF NO--GO POP THE NULL OFF OF STACK
3257 021420 004737 021456  JSR    PC,$TYPEC    ;; GO TYPE A NULL
3258 021424 105337 021522  DECB   $CHARCNT     ;; DO NOT COUNT AS A COUNT
3259 021430 000770      BR     75            ;; LOOP

```

:HORIZONTAL TAB PROCESSOR

```

3263 021432 112716 000040  85:   MOVB   #' (SP)      ;; REPLACE TAB WITH SPACE
3264 021436 004737 021456  95:   JSR    PC,$TYPEC    ;; TYPE A SPACE
3265 021442 132737 000007 021522 BITB   #7,$CHARCNT  ;; BRANCH IF NOT AT
3266 021450 001372      BNE    95            ;; TAB STOP
3267 021452 005726      TST    (SP)+         ;; POP SPACE OFF STACK
3268 021454 000724      BR     25            ;; GET NEXT CHARACTER
3269 021456 105777 157464  $TYPEC: TSTB   $STPB       ;; WAIT UNTIL PRINTER IS READY
3270 021462 100375      BPL    $TYPEC
3271 021464 116677 000002 157456 MOVB   2(SP),3$TPB  ;; LOAD CHAR TO BE TYPED INTO DATA REG.
3272 021472 122766 000015 000002 CMFB   #CR,2(SP)    ;; IS CHARACTER A CARRIAGE RETURN?
3273 021500 001003      BNE    15            ;; BRANCH IF NO
3274 021502 105037 021522  CLRB   $CHARCNT     ;; YES--CLEAR CHARACTER COUNT
3275 021506 000406      BR     $TYPEX        ;; EXIT
3276 021510 122766 000012 000002 15:   CMPB   #LF,2(SP)    ;; IS CHARACTER A LINE FEED?
3277 021516 001402      BEQ    $TYPEX        ;; BRANCH IF YES
3278 021520 105227      INCB   (PC)+         ;; COUNT THE CHARACTER
3279 021522 000000  $CHARCNT: .WORD    0 ;; CHARACTER COUNT STORAGE
3280 021524 000207  $TYPEX: RTS    PC

```

.SBTTL APT COMMUNICATIONS ROUTINE

```

3286 021526 112737 000001 021772 $ATY1: MOVB   #1,$FFLG  ;; TO REPORT FATAL ERROR
3287 021534 112737 000001 021770 $ATY3: MOVB   #1,$MFLG  ;; TO TYPE A MESSAGE
3288 021542 000403      BR     $ATYC
3289 021544 112737 000001 021772 $ATY4: MOVB   #1,$FFLG  ;; TO ONLY REPORT FATAL ERROR
3290 021552 $ATYC:

```

```

3291 021552 310046      MOV      RO,-(SP)      ;; PUSH RO ON STACK
3292 021554 010146      MOV      R1,-(SP)      ;; PUSH R1 ON STACK
3293 021556 105737 021770    TSTB     $MFLG         ;; SHOULD TYPE A MESSAGE?
3294 021562 001450      BEQ      5$           ;; IF NOT: BR
3295 021564 122737 000001 001220    CMPB     #APTENV,$ENV  ;; OPERATING UNDER APT?
3296 021572 001031      BNE      3$           ;; IF NOT: BR
3297 021574 132737 000100 001221    9ITB     #APTSPool,$ENVM ;; SHOULD SPOOL MESSAGES?
3298 021602 001425      BEQ      3$           ;; IF NOT: BR
3299 021604 017600 000004      MOVB     24(SP),RO     ;; GET MESSAGE ADDR.
3300 021610 062766 000002 000004      ADD      #2,4(SP)      ;; BUMP RETURN ADDR.
3301 021616 005737 001200      TST      $MSGTYPE     ;; SEE IF DONE W/ LAST XMISSION?
3302 021622 001375      BNE      1$           ;; IF NOT: WAIT
3303 021624 010037 001214      MOV      RO,$MSGAD     ;; PUT ADDR IN MAILBOX
3304 021630 105720 2$:      TSTB     (RO)+         ;; FIND END OF MESSAGE
3305 021632 001376      BNE      2$
3306 021634 163700 001214      SUB      $MSGAD,RO     ;; SUB START OF MESSAGE
3307 021640 006200      ASR      RO           ;; GET MESSAGE LNTH IN WORDS
3308 021642 010037 001216      MOV      RO,$MSGGLT    ;; PUT LENGTH IN MAILBOX
3309 021646 012737 000004 001200    MOV      #4,$MSGTYPE  ;; TELL APT TO TAKE MSG.
3310 021654 000413      BR       5$
3311 021656 017637 000004 021702 3$:      MOV      24(SP),4$    ;; PUT MSG ADDR IN JSR LINKAGE
3312 021664 062766 000002 000004      ADD      #2,4(SP)      ;; BUMP RETURN ADDRESS
3313 021672 013746 177776      MOV      177776,-(SP) ;; PUSH 177776 ON STACK
3314 021676 004737 021244      JSR      PC,$TYPE     ;; CALL TYPE MACRO
3315 021702 000000 4$:      .WORD    0
3316 021704 5$:
3317 021704 105737 021772 10$:      TSTB     $FFLG         ;; SHOULD REPORT FATAL ERROR?
3318 021710 001416      BEQ      12$          ;; IF NOT: BR
3319 021712 005737 001220      TST      $ENV         ;; RUNNING UNDER APT?
3320 021716 001413      BEQ      12$          ;; IF NOT: BR
3321 021722 005737 001200 11$:      TST      $MSGTYPE     ;; FINISHED LAST MESSAGE?
3322 021724 001375      BNE      11$          ;; IF NOT: WAIT
3323 021726 017637 000004 001202      MOV      24(SP),$FATAL ;; GET ERROR #
3324 021734 062766 000002 000004      ADD      #2,4(SP)      ;; BUMP RETURN ADDR.
3325 021742 005237 001200      INC      $MSGTYPE     ;; TELL APT TO TAKE ERROR
3326 021746 105037 021772 12$:      CLRB     $FFLG         ;; CLEAR FATAL FLAG
3327 021752 105037 021771      CLRB     $LFLG        ;; CLEAR LOG FLAG
3328 021756 105037 021770      CLRB     $MFLG        ;; CLEAR MESSAGE FLAG
3329 021762 012601      MOV      (SP)+,R1     ;; POP STACK INTO R1
3330 021764 012600      MOV      (SP)+,RO     ;; POP STACK INTO RO
3331 021766 000207      RTS      PC           ;; RETURN
3332 021770 000      $MFLG:  .BYTE    0    ;; MESSG. FLAG
3333 021771 000      $LFLG:  .BYTE    0    ;; LOG FLAG
3334 021772 000      $FFLG:  .BYTE    0    ;; FATAL FLAG
3335 021774      .EVEN
3336 000200      APTSIZE=200
3337 000001      APTENV=001
3338 000100      APTSPCOL=100
3339 000040      APTCSUP=040

```

.SBTTL TRAP DECODER

```

*****
*THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
*AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
*OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL

```

```

3347
3348
3349 021774 010046
3350 021776 016500 000002
3351 022002 005740
3352 022004 111000
3353 022006 006300
3354 022010 016000 022016
3355 022014 000200
3356
3357
3358
3359
3360
3361
3362
3363
3364
3365 022016
3366 022016 021244
3367 022020 021042
3368 022022 021016
3369 022024 021056
3370 022026 010472
3371 022030 017646
3372 022032 020104
3373 022034 020166
3374 022036 020340
3375
3376 022040 000000
3377 022042 001020
3378 024102 000000
3379 024104 000200
3380 024504 000000
3381 000001

```

:*GO TO THAT ROUTINE.

```

$TRAP:  MOV    RO, -(SP)      ;; SAVE RO
        MOV    2(SP), RO    ;; GET TRAP ADDRESS
        TST   -(RO)        ;; BACKUP BY 2
        MOVB  (RO), RO     ;; GET RIGHT BYTE OF TRAP
        ASL   RO           ;; POSITION FOR INDEXING
        MOV   $TRAPD(RO), RO ;; INDEX TO TABLE
        RTS   RO           ;; GO TO ROUTINE

```

.SBTTL TRAP TABLE

:*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
:*BY THE "TRAP" INSTRUCTION.

```

; ROUTINE
; -----

```

```

$TRAPD: $TYPE    ;;CALL=TYPE    TRAP+0(104400)  TTY TYPEOUT ROUTINE
        $TYPC   ;;CALL=TYPC   TRAP+1(104401)  TYPE OCTAL NUMBER (WITH LEADING ZERCS.
        $TYPOS  ;;CALL=TYPOS  TRAP+2(104402)  TYPE OCTAL NUMBER (NO LEADING ZERCS.
        $TYPON  ;;CALL=TYPON  TRAP+3(104403)  TYPE OCTAL NUMBER (AS PER LAST CALL)
        $TYPDS  ;;CALL=TYPDS  TRAP+4(104404)  TYPE DECIMAL NUMBER (WITH SIGN)
        $CKSWR  ;;CALL=CKSWR  TRAP+5(104405)  TEST FOR CHANGE IN SOFT-SWR
        $RDCHR  ;;CALL=RDCHR  TRAP+6(104406)  TTY TYPEIN CHARACTER ROUTINE
        $RDLIN  ;;CALL=RDLIN  TRAP+7(104407)  TTY TYPEIN STRING ROUTINE
        $RDOCT  ;;CALL=RDOCT  TRAP+10(104410) READ AN OCTAL NUMBER FROM TTY

```

```

ACBUFF: 0
        .BLKW 1020
ABUFF4: 0
        .BLKW 200
LAST: 0
        .END

```


| | | |
|--------|------|------|
| .HEADE | 190# | |
| .SETUP | 190# | 632 |
| .SWRHI | 190# | 315 |
| .SWRLO | 328# | |
| .SACTI | 190# | 344 |
| .SAPT8 | 190# | 426# |
| .SAPTH | 190# | 356 |
| .SAPTY | 190# | 3282 |
| .SCATC | 190# | 328 |
| .SCMTA | 190# | 379 |
| .SEOP | 190# | 1786 |
| .SERRO | 190# | 2814 |
| .SERRT | 190# | 3023 |
| .SPARM | 190# | |
| .SPOWE | 190# | 3072 |
| .SRDCC | 190# | 2983 |
| .SREAD | 190# | 2867 |
| .SSAVE | 190# | |
| .SSCOP | 190# | 2749 |
| .SSPAC | 190# | |
| .SSWDO | 190# | |
| .STRAP | 190# | 3340 |
| .STYPD | 190# | 1828 |
| .STYPE | 190# | 3202 |
| .STYPO | 190# | 2124 |

L07

| INC | INC | INC | INC | INC | INC | INC | INC | INC | INC | INC | INC | INC | INC | INC | INC |
|------|------|------|------|------|------|------|------|------|------|------|------|------|------|-----|-----|
| 2506 | 2509 | 2510 | 2515 | 2526 | 2502 | 2617 | 2793 | 2936 | 2923 | 3096 | 3181 | 3139 | 3225 | | |
| 2490 | 2804 | 2830 | 3278 | 2526 | 2502 | 2617 | 2793 | 2936 | 2923 | 3096 | 3181 | 3139 | 3225 | | |
| 34 | 34 | 687 | 690 | 1791 | 1783 | 1820 | 871 | 882 | 886 | 897 | 901 | 912 | 916 | | |
| 930 | 951 | 837 | 841 | 852 | 856 | 867 | 1050 | 1055 | 1080 | 1095 | 1127 | 1137 | 1149 | | |
| 930 | 951 | 977 | 992 | 1006 | 1020 | 1035 | 1248 | 1255 | 1266 | 1290 | 1310 | 1330 | 1350 | | |
| 1180 | 1192 | 1201 | 1205 | 1218 | 1225 | 1236 | 1477 | 1487 | 1496 | 1506 | 1514 | 1524 | 1531 | | |
| 1139 | 1415 | 1437 | 1455 | 1459 | 1463 | 1467 | 1608 | 1622 | 1626 | 1642 | 1676 | 1704 | 1737 | | |
| 1139 | 1555 | 1566 | 1575 | 1585 | 1594 | 1604 | 2578 | 2634 | 2626 | 2842 | 2916 | 3231 | 3250 | | |
| 2264 | 3314 | 2450 | 2464 | 2561 | 2565 | 2572 | 2578 | 2734 | 2842 | 2848 | 2916 | 3231 | 3250 | | |
| 612 | 611 | 612 | 613 | 625 | 626 | 628 | 629 | 630 | 633 | 637 | 639 | 640 | 641 | | |
| 645 | 645 | 645 | 646 | 657 | 651 | 652 | 655 | 656 | 657 | 658 | 659 | 660 | 661 | | |
| 677 | 677 | 677 | 678 | 680 | 680 | 682 | 683 | 684 | 685 | 686 | 687 | 688 | 689 | | |
| 798 | 798 | 798 | 799 | 801 | 801 | 802 | 803 | 804 | 805 | 806 | 807 | 808 | 809 | | |
| 840 | 840 | 840 | 850 | 851 | 854 | 855 | 856 | 857 | 858 | 859 | 860 | 861 | 862 | | |
| 849 | 849 | 849 | 900 | 910 | 911 | 914 | 915 | 924 | 925 | 928 | 929 | 930 | 931 | | |
| 1008 | 1008 | 1008 | 950 | 961 | 965 | 965 | 965 | 976 | 979 | 988 | 989 | 990 | 991 | | |
| 1067 | 1067 | 1067 | 1017 | 1018 | 1019 | 1022 | 1022 | 1034 | 1037 | 1047 | 1048 | 1049 | 1050 | | |
| 1122 | 1122 | 1122 | 1025 | 1026 | 1029 | 1032 | 1032 | 1048 | 1052 | 1059 | 1060 | 1061 | 1062 | | |
| 1122 | 1122 | 1122 | 1026 | 1027 | 1030 | 1033 | 1033 | 1049 | 1053 | 1060 | 1061 | 1062 | 1063 | | |
| 1122 | 1122 | 1122 | 1027 | 1028 | 1031 | 1034 | 1034 | 1050 | 1054 | 1061 | 1062 | 1063 | 1064 | | |
| 1122 | 1122 | 1122 | 1028 | 1029 | 1032 | 1035 | 1035 | 1051 | 1055 | 1062 | 1063 | 1064 | 1065 | | |
| 1122 | 1122 | 1122 | 1029 | 1030 | 1033 | 1036 | 1036 | 1052 | 1056 | 1063 | 1064 | 1065 | 1066 | | |
| 1122 | 1122 | 1122 | 1030 | 1031 | 1034 | 1037 | 1037 | 1053 | 1057 | 1064 | 1065 | 1066 | 1067 | | |
| 1122 | 1122 | 1122 | 1031 | 1032 | 1035 | 1038 | 1038 | 1054 | 1058 | 1065 | 1066 | 1067 | 1068 | | |
| 1122 | 1122 | 1122 | 1032 | 1033 | 1036 | 1039 | 1039 | 1055 | 1059 | 1066 | 1067 | 1068 | 1069 | | |
| 1122 | 1122 | 1122 | 1033 | 1034 | 1037 | 1040 | 1040 | 1056 | 1060 | 1067 | 1068 | 1069 | 1070 | | |
| 1122 | 1122 | 1122 | 1034 | 1035 | 1038 | 1041 | 1041 | 1057 | 1061 | 1068 | 1069 | 1070 | 1071 | | |
| 1122 | 1122 | 1122 | 1035 | 1036 | 1039 | 1042 | 1042 | 1058 | 1062 | 1069 | 1070 | 1071 | 1072 | | |
| 1122 | 1122 | 1122 | 1036 | 1037 | 1040 | 1043 | 1043 | 1059 | 1063 | 1070 | 1071 | 1072 | 1073 | | |
| 1122 | 1122 | 1122 | 1037 | 1038 | 1041 | 1044 | 1044 | 1060 | 1064 | 1071 | 1072 | 1073 | 1074 | | |
| 1122 | 1122 | 1122 | 1038 | 1039 | 1042 | 1045 | 1045 | 1061 | 1065 | 1072 | 1073 | 1074 | 1075 | | |
| 1122 | 1122 | 1122 | 1039 | 1040 | 1043 | 1046 | 1046 | 1062 | 1066 | 1073 | 1074 | 1075 | 1076 | | |
| 1122 | 1122 | 1122 | 1040 | 1041 | 1044 | 1047 | 1047 | 1063 | 1067 | 1074 | 1075 | 1076 | 1077 | | |
| 1122 | 1122 | 1122 | 1041 | 1042 | 1045 | 1048 | 1048 | 1064 | 1068 | 1075 | 1076 | 1077 | 1078 | | |
| 1122 | 1122 | 1122 | 1042 | 1043 | 1046 | 1049 | 1049 | 1065 | 1069 | 1076 | 1077 | 1078 | 1079 | | |
| 1122 | 1122 | 1122 | 1043 | 1044 | 1047 | 1050 | 1050 | 1066 | 1070 | 1077 | 1078 | 1079 | 1080 | | |
| 1122 | 1122 | 1122 | 1044 | 1045 | 1048 | 1051 | 1051 | 1067 | 1071 | 1078 | 1079 | 1080 | 1081 | | |
| 1122 | 1122 | 1122 | 1045 | 1046 | 1049 | 1052 | 1052 | 1068 | 1072 | 1079 | 1080 | 1081 | 1082 | | |
| 1122 | 1122 | 1122 | 1046 | 1047 | 1050 | 1053 | 1053 | 1069 | 1073 | 1080 | 1081 | 1082 | 1083 | | |
| 1122 | 1122 | 1122 | 1047 | 1048 | 1051 | 1054 | 1054 | 1070 | 1074 | 1081 | 1082 | 1083 | 1084 | | |
| 1122 | 1122 | 1122 | 1048 | 1049 | 1052 | 1055 | 1055 | 1071 | 1075 | 1082 | 1083 | 1084 | 1085 | | |
| 1122 | 1122 | 1122 | 1049 | 1050 | 1053 | 1056 | 1056 | 1072 | 1076 | 1083 | 1084 | 1085 | 1086 | | |
| 1122 | 1122 | 1122 | 1050 | 1051 | 1054 | 1057 | 1057 | 1073 | 1077 | 1084 | 1085 | 1086 | 1087 | | |
| 1122 | 1122 | 1122 | 1051 | 1052 | 1055 | 1058 | 1058 | 1074 | 1078 | 1085 | 1086 | 1087 | 1088 | | |
| 1122 | 1122 | 1122 | 1052 | 1053 | 1056 | 1059 | 1059 | 1075 | 1079 | 1086 | 1087 | 1088 | 1089 | | |
| 1122 | 1122 | 1122 | 1053 | 1054 | 1057 | 1060 | 1060 | 1076 | 1080 | 1087 | 1088 | 1089 | 1090 | | |
| 1122 | 1122 | 1122 | 1054 | 1055 | 1058 | 1061 | 1061 | 1077 | 1081 | 1088 | 1089 | 1090 | 1091 | | |
| 1122 | 1122 | 1122 | 1055 | 1056 | 1059 | 1062 | 1062 | 1078 | 1082 | 1089 | 1090 | 1091 | 1092 | | |
| 1122 | 1122 | 1122 | 1056 | 1057 | 1060 | 1063 | 1063 | 1079 | 1083 | 1090 | 1091 | 1092 | 1093 | | |
| 1122 | 1122 | 1122 | 1057 | 1058 | 1061 | 1064 | 1064 | 1080 | 1084 | 1091 | 1092 | 1093 | 1094 | | |
| 1122 | 1122 | 1122 | 1058 | 1059 | 1062 | 1065 | 1065 | 1081 | 1085 | 1092 | 1093 | 1094 | 1095 | | |
| 1122 | 1122 | 1122 | 1059 | 1060 | 1063 | 1066 | 1066 | 1082 | 1086 | 1093 | 1094 | 1095 | 1096 | | |
| 1122 | 1122 | 1122 | 1060 | 1061 | 1064 | 1067 | 1067 | 1083 | 1087 | 1094 | 1095 | 1096 | 1097 | | |
| 1122 | 1122 | 1122 | 1061 | 1062 | 1065 | 1068 | 1068 | 1084 | 1088 | 1095 | 1096 | 1097 | 1098 | | |
| 1122 | 1122 | 1122 | 1062 | 1063 | 1066 | 1069 | 1069 | 1085 | 1089 | 1096 | 1097 | 1098 | 1099 | | |
| 1122 | 1122 | 1122 | 1063 | 1064 | 1067 | 1070 | 1070 | 1086 | 1090 | 1097 | 1098 | 1099 | 1100 | | |
| 1122 | 1122 | 1122 | 1064 | 1065 | 1068 | 1071 | 1071 | 1087 | 1091 | 1098 | 1099 | 1100 | 1101 | | |
| 1122 | 1122 | 1122 | 1065 | 1066 | 1069 | 1072 | 1072 | 1088 | 1092 | 1099 | 1100 | 1101 | 1102 | | |
| 1122 | 1122 | 1122 | 1066 | 1067 | 1070 | 1073 | 1073 | 1089 | 1093 | 1100 | 1101 | 1102 | 1103 | | |
| 1122 | 1122 | 1122 | 1067 | 1068 | 1071 | 1074 | 1074 | 1090 | 1094 | 1101 | 1102 | 1103 | 1104 | | |
| 1122 | 1122 | 1122 | 1068 | 1069 | 1072 | 1075 | 1075 | 1091 | 1095 | 1102 | 1103 | 1104 | 1105 | | |
| 1122 | 1122 | 1122 | 1069 | 1070 | 1073 | 1076 | 1076 | 1092 | 1096 | 1103 | 1104 | 1105 | 1106 | | |
| 1122 | 1122 | 1122 | 1070 | 1071 | 1074 | 1077 | 1077 | 1093 | 1097 | 1104 | 1105 | 1106 | 1107 | | |
| 1122 | 1122 | 1122 | 1071 | 1072 | 1075 | 1078 | 1078 | 1094 | 1098 | 1105 | 1106 | 1107 | 1108 | | |
| 1122 | 1122 | 1122 | 1072 | 1073 | 1076 | 1079 | 1079 | 1095 | 1099 | 1106 | 1107 | 1108 | 1109 | | |
| 1122 | 1122 | 1122 | 1073 | 1074 | 1077 | 1080 | 1080 | 1096 | 1100 | 1107 | 1108 | 1109 | 1110 | | |
| 1122 | 1122 | 1122 | 1074 | 1075 | 1078 | 1081 | 1081 | 1097 | 1101 | 1108 | 1109 | 1110 | 1111 | | |
| 1122 | 1122 | 1122 | 1075 | 1076 | 1079 | 1082 | 1082 | 1098 | 1102 | 1109 | 1110 | 1111 | 1112 | | |
| 1122 | 1122 | 1122 | 1076 | 1077 | 1080 | 1083 | 1083 | 1099 | 1103 | 1110 | 1111 | 1112 | 1113 | | |
| 1122 | 1122 | 1122 | 1077 | 1078 | 1081 | 1084 | 1084 | 1100 | 1104 | 1111 | 1112 | 1113 | 1114 | | |
| 1122 | 1122 | 1122 | 1078 | 1079 | 1082 | 1085 | 1085 | 1101 | 1105 | 1112 | 1113 | 1114 | 1115 | | |
| 1122 | 1122 | 1122 | 1079 | 1080 | 1083 | 1086 | 1086 | 1102 | 1106 | 1113 | 1114 | 1115 | 1116 | | |
| 1122 | 1122 | 1122 | 1080 | 1081 | 1084 | 1087 | 1087 | 1103 | 1107 | 1114 | 1115 | 1116 | 1117 | | |
| 1122 | 1122 | 1122 | 1081 | 1082 | 1085 | 1088 | 1088 | 1104 | 1108 | 1115 | 1116 | 1117 | 1118 | | |
| 1122 | 1122 | 1122 | 1082 | 1083 | 1086 | 1089 | 1089 | 1105 | 1109 | 1116 | 1117 | 1118 | 1119 | | |
| 1122 | 1122 | 1122 | 1083 | 1084 | 1087 | 1090 | 1090 | 1106 | 1110 | 1117 | 1118 | 1119 | 1120 | | |
| 1122 | 1122 | 1122 | 1084 | 1085 | 1088 | 1091 | 1091 | 1107 | 1111 | 1118 | 1119 | 1120 | 1121 | | |
| 1122 | 1122 | 1122 | 1085 | 1086 | 1089 | 1092 | 1092 | 1108 | 1112 | 1119 | | | | | |

| SUB | 151 | 152 | 153 | 154 | 155 | 156 | 157 | 158 | 159 | 160 | 161 | 162 | 163 | 164 | 165 | 166 | 167 | 168 | 169 | 170 | 171 | 172 | 173 | 174 | 175 | 176 | 177 | 178 | 179 | 180 | 181 | 182 | 183 | 184 | 185 | 186 | 187 | 188 | 189 | 190 | 191 | 192 | 193 | 194 | 195 | 196 | 197 | 198 | 199 | 200 | | | | | | | | | | | | | | | | |
|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|-----|
| 333 | 335 | 336 | 337 | 338 | 339 | 340 | 341 | 342 | 343 | 344 | 345 | 346 | 347 | 348 | 349 | 350 | 351 | 352 | 353 | 354 | 355 | 356 | 357 | 358 | 359 | 360 | 361 | 362 | 363 | 364 | 365 | 366 | 367 | 368 | 369 | 370 | 371 | 372 | 373 | 374 | 375 | 376 | 377 | 378 | 379 | 380 | 381 | 382 | 383 | 384 | 385 | 386 | 387 | 388 | 389 | 390 | 391 | 392 | 393 | 394 | 395 | 396 | 397 | 398 | 399 | 400 |

| | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
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| 2323 | 268 | 3317 | 3290 | 3287 | 3286 | 3285 | 3284 | 3283 | 3282 | 3281 | 3280 | 3279 | 3278 | 3277 | 3276 | 3275 | 3274 | 3273 | 3272 | 3271 | 3270 | 3269 | 3268 | 3267 | 3266 | 3265 | 3264 | 3263 | 3262 | 3261 | 3260 | 3259 | 3258 | 3257 | 3256 | 3255 | 3254 | 3253 | 3252 | 3251 | 3250 | 3249 | 3248 | 3247 | 3246 | 3245 | 3244 | 3243 | 3242 | 3241 | 3240 | 3239 | 3238 | 3237 | 3236 | 3235 | 3234 | 3233 | 3232 | 3231 | 3230 | 3229 | 3228 | 3227 | 3226 | 3225 | 3224 | 3223 | 3222 | 3221 | 3220 | 3219 | 3218 | 3217 | 3216 | 3215 | 3214 | 3213 | 3212 | 3211 | 3210 | 3209 | 3208 | 3207 | 3206 | 3205 | 3204 | 3203 | 3202 | 3201 | 3200 | 3199 | 3198 | 3197 | 3196 | 3195 | 3194 | 3193 | 3192 | 3191 | 3190 | 3189 | 3188 | 3187 | 3186 | 3185 | 3184 | 3183 | 3182 | 3181 | 3180 | 3179 | 3178 | 3177 | 3176 | 3175 | 3174 | 3173 | 3172 | 3171 | 3170 | 3169 | 3168 | 3167 | 3166 | 3165 | 3164 | 3163 | 3162 | 3161 | 3160 | 3159 | 3158 | 3157 | 3156 | 3155 | 3154 | 3153 | 3152 | 3151 | 3150 | 3149 | 3148 | 3147 | 3146 | 3145 | 3144 | 3143 | 3142 | 3141 | 3140 | 3139 | 3138 | 3137 | 3136 | 3135 | 3134 | 3133 | 3132 | 3131 | 3130 | 3129 | 3128 | 3127 | 3126 | 3125 | 3124 | 3123 | 3122 | 3121 | 3120 | 3119 | 3118 | 3117 | 3116 | 3115 | 3114 | 3113 | 3112 | 3111 | 3110 | 3109 | 3108 | 3107 | 3106 | 3105 | 3104 | 3103 | 3102 | 3101 | 3100 | 3099 | 3098 | 3097 | 3096 | 3095 | 3094 | 3093 | 3092 | 3091 | 3090 | 3089 | 3088 | 3087 | 3086 | 3085 | 3084 | 3083 | 3082 | 3081 | 3080 | 3079 | 3078 | 3077 | 3076 | 3075 | 3074 | 3073 | 3072 | 3071 | 3070 | 3069 | 3068 | 3067 | 3066 | 3065 | 3064 | 3063 | 3062 | 3061 | 3060 | 3059 | 3058 | 3057 | 3056 | 3055 | 3054 | 3053 | 3052 | 3051 | 3050 | 3049 | 3048 | 3047 | 3046 | 3045 | 3044 | 3043 | 3042 | 3041 | 3040 | 3039 | 3038 | 3037 | 3036 | 3035 | 3034 | 3033 | 3032 | 3031 | 3030 | 3029 | 3028 | 3027 | 3026 | 3025 | 3024 | 3023 | 3022 | 3021 | 3020 | 3019 | 3018 | 3017 | 3016 | 3015 | 3014 | 3013 | 3012 | 3011 | 3010 | 3009 | 3008 | 3007 | 3006 | 3005 | 3004 | 3003 | 3002 | 3001 | 3000 | 2999 | 2998 | 2997 | 2996 | 2995 | 2994 | 2993 | 2992 | 2991 | 2990 | 2989 | 2988 | 2987 | 2986 | 2985 | 2984 | 2983 | 2982 | 2981 | 2980 | 2979 | 2978 | 2977 | 2976 | 2975 | 2974 | 2973 | 2972 | 2971 | 2970 | 2969 | 2968 | 2967 | 2966 | 2965 | 2964 | 2963 | 2962 | 2961 | 2960 | 2959 | 2958 | 2957 | 2956 | 2955 | 2954 | 2953 | 2952 | 2951 | 2950 | 2949 | 2948 | 2947 | 2946 | 2945 | 2944 | 2943 | 2942 | 2941 | 2940 | 2939 | 2938 | 2937 | 2936 | 2935 | 2934 | 2933 | 2932 | 2931 | 2930 | 2929 | 2928 | 2927 | 2926 | 2925 | 2924 | 2923 | 2922 | 2921 | 2920 | 2919 | 2918 | 2917 | 2916 | 2915 | 2914 | 2913 | 2912 | 2911 | 2910 | 2909 | 2908 | 2907 | 2906 | 2905 | 2904 | 2903 | 2902 | 2901 | 2900 | 2899 | 2898 | 2897 | 2896 | 2895 | 2894 | 2893 | 2892 | 2891 | 2890 | 2889 | 2888 | 2887 | 2886 | 2885 | 2884 | 2883 | 2882 | 2881 | 2880 | 2879 | 2878 | 2877 | 2876 | 2875 | 2874 | 2873 | 2872 | 2871 | 2870 | 2869 | 2868 | 2867 | 2866 | 2865 | 2864 | 2863 | 2862 | 2861 | 2860 | 2859 | 2858 | 2857 | 2856 | 2855 | 2854 | 2853 | 2852 | 2851 | 2850 | 2849 | 2848 | 2847 | 2846 | 2845 | 2844 | 2843 | 2842 | 2841 | 2840 | 2839 | 2838 | 2837 | 2836 | 2835 | 2834 | 2833 | 2832 | 2831 | 2830 | 2829 | 2828 | 2827 | 2826 | 2825 | 2824 | 2823 | 2822 | 2821 | 2820 | 2819 | 2818 | 2817 | 2816 | 2815 | 2814 | 2813 | 2812 | 2811 | 2810 | 2809 | 2808 | 2807 | 2806 | 2805 | 2804 | 2803 | 2802 | 2801 | 2800 | 2799 | 2798 | 2797 | 2796 | 2795 | 2794 | 2793 | 2792 | 2791 | 2790 | 2789 | 2788 | 2787 | 2786 | 2785 | 2784 | 2783 | 2782 | 2781 | 2780 | 2779 | 2778 | 2777 | 2776 | 2775 | 2774 | 2773 | 2772 | 2771 | 2770 | 2769 | 2768 | 2767 | 2766 | 2765 | 2764 | 2763 | 2762 | 2761 | 2760 | 2759 | 2758 | 2757 | 2756 | 2755 | 2754 | 2753 | 2752 | 2751 | 2750 | 2749 | 2748 | 2747 | 2746 | 2745 | 2744 | 2743 | 2742 | 2741 | 2740 | 2739 | 2738 | 2737 | 2736 | 2735 | 2734 | 2733 | 2732 | 2731 | 2730 | 2729 | 2728 | 2727 | 2726 | 2725 | 2724 | 2723 | 2722 | 2721 | 2720 | 2719 | 2718 | 2717 | 2716 | 2715 | 2714 | 2713 | 2712 | 2711 | 2710 | 2709 | 2708 | 2707 | 2706 | 2705 | 2704 | 2703 | 2702 | 2701 | 2700 | 2699 | 2698 | 2697 | 2696 | 2695 | 2694 | 2693 | 2692 | 2691 | 2690 | 2689 | 2688 | 2687 | 2686 | 2685 | 2684 | 2683 | 2682 | 2681 | 2680 | 2679 | 2678 | 2677 | 2676 | 2675 | 2674 | 2673 | 2672 | 2671 | 2670 | 2669 | 2668 | 2667 | 2666 | 2665 | 2664 | 2663 | 2662 | 2661 | 2660 | 2659 | 2658 | 2657 | 2656 | 2655 | 2654 | 2653 | 2652 | 2651 | 2650 | 2649 | 2648 | 2647 | 2646 | 2645 | 2644 | 2643 | 2642 | 2641 | 2640 | 2639 | 2638 | 2637 | 2636 | 2635 | 2634 | 2633 | 2632 | 2631 | 2630 | 2629 | 2628 | 2627 | 2626 | 2625 | 2624 | 2623 | 2622 | 2621 | 2620 | 2619 | 2618 | 2617 | 2616 | 2615 | 2614 | 2613 | 2612 | 2611 | 2610 | 2609 | 2608 | 2607 | 2606 | 2605 | 2604 | 2603 | 2602 | 2601 | 2600 | 2599 | 2598 | 2597 | 2596 | 2595 | 2594 | 2593 | 2592 | 2591 | 2590 | 2589 | 2588 | 2587 | 2586 | 2585 | 2584 | 2583 | 2582 | 2581 | 2580 | 2579 | 2578 | 2577 | 2576 | 2575 | 2574 | 2573 | 2572 | 2571 | 2570 | 2569 | 2568 | 2567 | 2566 | 2565 | 2564 | 2563 | 2562 | 2561 | 2560 | 2559 | 2558 | 2557 | 2556 | 2555 | 2554 | 2553 | 2552 | 2551 | 2550 | 2549 | 2548 | 2547 | 2546 | 2545 | 2544 | 2543 | 2542 | 2541 | 2540 | 2539 | 2538 | 2537 | 2536 | 2535 | 2534 | 2533 | 2532 | 2531 | 2530 | 2529 | 2528 | 2527 | 2526 | 2525 | 2524 | 2523 | 2522 | 2521 | 2520 | 2519 | 2518 | 2517 | 2516 | 2515 | 2514 | 2513 | 2512 | 2511 | 2510 | 2509 | 2508 | 2507 | 2506 | 2505 | 2504 | 2503 | 2502 | 2501 | 2500 | 2499 | 2498 | 2497 | 2496 | 2495 | 2494 | 2493 | 2492 | 2491 | 2490 | 2489 | 2488 | 2487 | 2486 | 2485 | 2484 | 2483 | 2482 | 2481 | 2480 | 2479 | 2478 | 2477 | 2476 | 2475 | 2474 | 2473 | 2472 | 2471 | 2470 | 2469 | 2468 | 2467 | 2466 | 2465 | 2464 | 2463 | 2462 | 2461 | 2460 | 2459 | 2458 | 2457 | 2456 | 2455 | 2454 | 2453 | 2452 | 2451 | 2450 | 2449 | 2448 | 2447 | 2446 | 2445 | 2444 | 2443 | 2442 | 2441 | 2440 | 2439 | 2438 | 2437 | 2436 | 2435 | 2434 | 2433 | 2432 | 2431 | 2430 | 2429 | 2428 | 2427 | 2426 | 2425 | 2424 | 2423 | 2422 | 2421 | 2420 | 2419 | 2418 | 2417 | 2416 | 2415 | 2414 | 2413 | 2412 | 2411 | 2410 | 2409 | 2408 | 2407 | 2406 | 2405 | 2404 | 2403 | 2402 | 2401 | 2400 | 2399 | 2398 | 2397 | 2396 | 2395 | 2394 | 2393 | 2392 | 2391 | 2390 | 2389 | 2388 | 2387 | 2386 | 2385 | 2384 | 2383 | 2382 | 2381 | 2380 | 2379 | 2378 | 2377 | 2376 | 2375 | 2374 | 2373 | 2372 | 2371 | 2370 | 2369 | 2368 | 2367 | 2366 | 2365 | 2364 | 2363 | 2362 | 2361 | 2360 | 2359 | 2358 | 2357 | 2356 | 2355 | 2354 | 2353 | 2352 | 2351 | 2350 | 2349 | 2348 | 2347 | 2346 | 2345 | 2344 | 2343 | 2342 | 2341 | 2340 | 2339 | 2338 | 2337 | 2336 | 2335 | 2334 | 2333 | 2332 | 2331 | 2330 | 2329 | 2328 | 2327 | 2326 | 2325 | 2324 | 2323 | 2322 | 2321 | 2320 | 2319 | 2318 | 2317 | 2316 | 2315 | 2314 | 2313 | 2312 | 2311 | 2310 | 2309 | 2308 | 2307 | 2306 | 2305 | 2304 | 2303 | 2302 | 2301 | 2300 | 2299 | 2298 | 2297 | 2296 | 2295 | 2294 | 2293 | 2292 | 2291 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|------|-----|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|------|--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