

AAV11

DIAGNOSTIC TEST
MD-11-DVAAA-A

EP-DVAAA-A-DL-A

NOV 1978

COPYRIGHT © 1978



FICHE 1 OF 1

MADE IN U.S.A.

A grid of 60 microfilm frames arranged in 10 rows and 6 columns. Each frame contains a different view of a diagnostic test, likely a flight simulator or engine test, showing various data points, graphs, and text. The frames are separated by a dark border.

THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
PACKAGE (MAINDEC-11-DZGAC-C1), MAR 24, 1976.

SBTTL BASIC DEFINITIONS

INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***

001100

STACK= 1100
.EQUIV ENT,ERROR ;; BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE ;; BASIC DEFINITION OF SCOPE CALL

MISCELLANEOUS DEFINITIONS

000011
000012
000015
000200
177776

HT= 11 ;; CODE FOR HORIZONTAL TAB
LF= 12 ;; CODE FOR LINE FEED
CR= 15 ;; CODE FOR CARRIAGE RETURN
CALF= 200 ;; CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776 ;; PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLAT= 177774 ;; STACK LIMIT REGISTER
PIRQ= 177772 ;; PROGRAM INTERRUPT REQUEST REGISTER
DSMR= 177570 ;; HARDWARE SWITCH REGISTER
DDISP= 177570 ;; HARDWARE DISPLAY REGISTER

177774
177772
177570
177570

GENERAL PURPOSE REGISTER DEFINITIONS

000000
000001
000002
000003
000004
000005
000006
000007

R0= X0 ;; GENERAL REGISTER
R1= X1 ;; GENERAL REGISTER
R2= X2 ;; GENERAL REGISTER
R3= X3 ;; GENERAL REGISTER
R4= X4 ;; GENERAL REGISTER
R5= X5 ;; GENERAL REGISTER
R6= X6 ;; GENERAL REGISTER
R7= X7 ;; GENERAL REGISTER
.EQUIV R6,SP ;; STACK POINTER
.EQUIV R7,PC ;; PROGRAM COUNTER

PRIORITY LEVEL DEFINITIONS

000000
000040
000100
000140
000200
000240
000300
000340

PR0= 0 ;; PRIORITY LEVEL 0
PR1= 40 ;; PRIORITY LEVEL 1
PR2= 100 ;; PRIORITY LEVEL 2
PR3= 140 ;; PRIORITY LEVEL 3
PR4= 200 ;; PRIORITY LEVEL 4
PR5= 240 ;; PRIORITY LEVEL 5
PR6= 300 ;; PRIORITY LEVEL 6
PR7= 340 ;; PRIORITY LEVEL 7

"SWITCH REGISTER" SWITCH DEFINITIONS

100000
040000
020000
010000
004000
002000
001000
000400
000200

SW15= 100000
SW14= 400000
SW13= 200000
SW12= 100000
SW11= 500000
SW10= 200000
SW09= 100000
SW08= 400000
SW07= 200000

000000
000001
000002
000003
000004
000005
000006
000007
000008
000009
000010
000011
000012
000013
000014
000015
000016
000017
000018
000019
000020
000021
000022
000023
000024
000025
000026
000027
000028
000029
000030
000031
000032
000033
000034
000035
000036
000037
000038
000039
000040
000041
000042
000043
000044
000045
000046
000047
000048
000049
000050
000051
000052
000053
000054
000055
000056
000057
000058
000059
000060
000061
000062
000063
000064
000065
000066
000067
000068
000069
000070
000071
000072
000073
000074
000075
000076
000077
000078
000079
000080
000081
000082
000083
000084
000085
000086
000087
000088
000089
000090
000091
000092
000093
000094
000095
000096
000097
000098
000099
000100
000101
000102
000103
000104
000105
000106
000107
000108
000109
000110
000111
000112
000113
000114
000115
000116
000117
000118
000119
000120
000121
000122
000123
000124
000125
000126
000127
000128
000129
000130
000131
000132
000133
000134
000135
000136
000137
000138
000139
000140
000141
000142
000143
000144
000145
000146
000147
000148
000149
000150
000151
000152
000153
000154
000155
000156
000157
000158
000159
000160
000161
000162
000163
000164
000165
000166
000167
000168
000169
000170
000171
000172
000173
000174
000175
000176
000177
000178
000179
000180
000181
000182
000183
000184
000185
000186
000187
000188
000189
000190
000191
000192
000193
000194
000195
000196
000197
000198
000199
000200
000201
000202
000203
000204
000205
000206
000207
000208
000209
000210
000211
000212
000213
000214
000215
000216
000217
000218
000219
000220
000221
000222
000223
000224
000225
000226
000227
000228
000229
000230
000231
000232
000233
000234
000235
000236
000237
000238
000239
000240
000241
000242
000243
000244
000245
000246
000247
000248
000249
000250
000251
000252
000253
000254
000255
000256
000257
000258
000259
000260
000261
000262
000263
000264
000265
000266
000267
000268
000269
000270
000271
000272
000273
000274
000275
000276
000277
000278
000279
000280
000281
000282
000283
000284
000285
000286
000287
000288
000289
000290
000291
000292
000293
000294
000295
000296
000297
000298
000299
000300
000301
000302
000303
000304
000305
000306
000307
000308
000309
000310
000311
000312
000313
000314
000315
000316
000317
000318
000319
000320
000321
000322
000323
000324
000325
000326
000327
000328
000329
000330
000331
000332
000333
000334
000335
000336
000337
000338
000339
000340
000341
000342
000343
000344
000345
000346
000347
000348
000349
000350
000351
000352
000353
000354
000355
000356
000357
000358
000359
000360
000361
000362
000363
000364
000365
000366
000367
000368
000369
000370
000371
000372
000373
000374
000375
000376
000377
000378
000379
000380
000381
000382
000383
000384
000385
000386
000387
000388
000389
000390
000391
000392
000393
000394
000395
000396
000397
000398
000399
000400
000401
000402
000403
000404
000405
000406
000407
000408
000409
000410
000411
000412
000413
000414
000415
000416
000417
000418
000419
000420
000421
000422
000423
000424
000425
000426
000427
000428
000429
000430
000431
000432
000433
000434
000435
000436
000437
000438
000439
000440
000441
000442
000443
000444
000445
000446
000447
000448
000449
000450
000451
000452
000453
000454
000455
000456
000457
000458
000459
000460
000461
000462
000463
000464
000465
000466
000467
000468
000469
000470
000471
000472
000473
000474
000475
000476
000477
000478
000479
000480
000481
000482
000483
000484
000485
000486
000487
000488
000489
000490
000491
000492
000493
000494
000495
000496
000497
000498
000499
000500
000501
000502
000503
000504
000505
000506
000507
000508
000509
000510
000511
000512
000513
000514
000515
000516
000517
000518
000519
000520
000521
000522
000523
000524
000525
000526
000527
000528
000529
000530
000531
000532
000533
000534
000535
000536
000537
000538
000539
000540
000541
000542
000543
000544
000545
000546
000547
000548
000549
000550
000551
000552
000553
000554
000555
000556
000557
000558
000559
000560
000561
000562
000563
000564
000565
000566
000567
000568
000569
000570
000571
000572
000573
000574
000575
000576
000577
000578
000579
000580
000581
000582
000583
000584
000585
000586
000587
000588
000589
000590
000591
000592
000593
000594
000595
000596
000597
000598
000599
000600
000601
000602
000603
000604
000605
000606
000607
000608
000609
000610
000611
000612
000613
000614
000615
000616
000617
000618
000619
000620
000621
000622
000623
000624
000625
000626
000627
000628
000629
000630
000631
000632
000633
000634
000635
000636
000637
000638
000639
000640
000641
000642
000643
000644
000645
000646
000647
000648
000649
000650
000651
000652
000653
000654
000655
000656
000657
000658
000659
000660
000661
000662
000663
000664
000665
000666
000667
000668
000669
000670
000671
000672
000673
000674
000675
000676
000677
000678
000679
000680
000681
000682
000683
000684
000685
000686
000687
000688
000689
000690
000691
000692
000693
000694
000695
000696
000697
000698
000699
000700
000701
000702
000703
000704
000705
000706
000707
000708
000709
000710
000711
000712
000713
000714
000715
000716
000717
000718
000719
000720
000721
000722
000723
000724
000725
000726
000727
000728
000729
000730
000731
000732
000733
000734
000735
000736
000737
000738
000739
000740
000741
000742
000743
000744
000745
000746
000747
000748
000749
000750
000751
000752
000753
000754
000755
000756
000757
000758
000759
000760
000761
000762
000763
000764
000765
000766
000767
000768
000769
000770
000771
000772
000773
000774
000775
000776
000777
000778
000779
000780
000781
000782
000783
000784
000785
000786
000787
000788
000789
000790
000791
000792
000793
000794
000795
000796
000797
000798
000799
000800
000801
000802
000803
000804
000805
000806
000807
000808
000809
000810
000811
000812
000813
000814
000815
000816
000817
000818
000819
000820
000821
000822
000823
000824
000825
000826
000827
000828
000829
000830
000831
000832
000833
000834
000835
000836
000837
000838
000839
000840
000841
000842
000843
000844
000845
000846
000847
000848
000849
000850
000851
000852
000853
000854
000855
000856
000857
000858
000859
000860
000861
000862
000863
000864
000865
000866
000867
000868
000869
000870
000871
000872
000873
000874
000875
000876
000877
000878
000879
000880
000881
000882
000883
000884
000885
000886
000887
000888
000889
000890
000891
000892
000893
000894
000895
000896
000897
000898
000899
000900
000901
000902
000903
000904
000905
000906
000907
000908
000909
000910
000911
000912
000913
000914
000915
000916
000917
000918
000919
000920
000921
000922
000923
000924
000925
000926
000927
000928
000929
000930
000931
000932
000933
000934
000935
000936
000937
000938
000939
000940
000941
000942
000943
000944
000945
000946
000947
000948
000949
000950
000951
000952
000953
000954
000955
000956
000957
000958
000959
000960
000961
000962
000963
000964
000965
000966
000967
000968
000969
000970
000971
000972
000973
000974
000975
000976
000977
000978
000979
000980
000981
000982
000983
000984
000985
000986
000987
000988
000989
000990
000991
000992
000993
000994
000995
000996
000997
000998
000999
001000

404 000030
405 000034
406 000060
407 000064
408 000240
409
410 170440

ENTVEC= 30
TRAPVEC=34
TKVEC= 60
TPVEC= 64
PIRQVEC=240

ABASE=170440

:::EMULATOR TRAP (ENT) **ERROR**
:::TRAP TRAP
:::TTY KEYBOARD VECTOR
:::TTY PRINTER VECTOR
:::PROGRAM INTERRUPT REQUEST VECTOR

446
447
448
449
450
451
452
453
454
455
456
457
458
459
460
461
462
463
464
465
466
467
468
469
470
471
472
473
474
475
476
477
478

000046 000106
000046 000046
000046 005542
000052 000052
000052 000000
000052 000106
000052 001000

000024 001000
000024 000024
000024 000200
000044 000044
000044 001000
001000
001000 000000
001002 001174
001004 000030
001006 000010
001010 000030
001012 000031

.SBTTL ACT11 HOOKS

HOOKS REQUIRED BY ACT11

SSVPC=. ;SAVE PC
=46
SENDAD ;;1)SET LOC.46 TO ADDRESS OF SENDAD IN .SEOP
=52
.WORD 0 ;;2)SET LOC.52 TO ZERO
=SSVPC ;; RESTORE PC
=1000

.SBTTL APT PARAMETER BLOCK

SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT

.SX=. ;;SAVE CURRENT LOCATION
=24 ;;SET POWER FAIL TO POINT TO START OF PROGRAM
200 FOR APT START UP
=44 POINT TO APT INDIRECT ADDRESS PNTR.
SAPTHDR POINT TO APT HEADER BLOCK
=.SX RESET LOCATION COUNTER

SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
INTERFACE SPEC.

SAPTHD:
SHIBTS: .WORD 0 ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
SMBADR: .WORD SMAIL ;;ADDRESS OF APT MAILBOX (BITS 0-15)
STSTM: .WORD 30 ;;RUN TIM OF LONGEST TEST
SPASTH: .WORD 10 ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
SUNITH: .WORD 30 ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
.WORD SETEND-SMAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)

001256
001256
001260
001262
001264
001266
001270
001272
001274
001276
001300
001302
001304
001306
001310
001312
001314
001316
001320
001322
001324
001326
001330
001332
001334

.SBTTL ERROR POINTER TABLE

:#THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.
:#THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
:#LOCATION SITE#B, THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
:#NOTE1: IF SITE#B IS 0 THE ONLY PERTINENT DATA IS (SERAPC).
:#NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

::# EM ::POINTS TO THE ERROR MESSAGE
::# DH ::POINTS TO THE DATA HEADER
::# DT ::POINTS TO THE DATA
::# DF ::POINTS TO THE DATA FORMAT

SERRTB:

;ITEM 1
EM1 : BUT TIME-OUT WHEN REF. A DAC ADDRESS
DH2 :ERRPC BUSADR
DT1 :SERAPC SBD0AT
DF0

;ITEM 2
EM2 :DAC #0 REGISTER IN ERROR
DH1 :ERRPC BUSADR GOOD BAD
DT2 :SERAPC DAC0 SGDOAT SBD0AT
DF0

;ITEM 3
EM3 :DAC #1 REGISTER IN ERROR
DH1 :ERRPC BUSADR GOOD BAD
DT3 :SERAPC DAC1 SGDOAT SBD0AT
DF0

;ITEM 4
EM4 :DAC #2 REGISTER IN ERROR
DH1 :ERRPC BUSADR GOOD BAD
DT4 :SERAPC DAC2 SGDOAT SBD0AT
DF0

;ITEM 5
EM5 :DAC #3 REGISTER IN ERROR
DH1 :ERRPC BUSADR GOOD BAD
DT5 :SERAPC DAC3 SGDOAT SBD0AT
DF0

;ITEM 6
EM6 :SELECTED DAC OFFSET POT IS NOT ADJUSTED CORRECTLY
DH6 :ERRPC BUSADR EXPECT WAS SPREAD
DT6 :SERAPC DACBAD SGDOAT SBD0AT SPREAD
DF0

667	001432	005237	007020		TESTER: INC	WFTST	;INDICATE TESTER MODE
668	001436	000411			BR	BEGIN1	
669	001440	012737	000021	007006	ADDOK: MOV	#17, NUMBOK	;LOAD 16 MAX UNITS
670	001446	001403			BR	BEGINA	
671	001450	012737	000005	007006	BEGIN: MOV	#5, NUMBOK	;LOAD 4 MAX UNITS
672	001456	005037	007020		BEGINA: CLR	WFTST	
673	001462	005037	007012		BEGIN1: CLR	TEMP	
674	001468	005037	001420		CLR	EVER	
675	001472	000005			RESET		
676					.SBTTL INITIALIZE THE COMMON TAGS		
677					::CLEAR THE COMMON TAGS (SCHTAG) AREA		
678	001474	012706	001100		MOV	#SCHTAG, R6	::FIRST LOCATION TO BE CLEARED
679	001500	005026			CLR	(R6)+	::CLEAR MEMORY LOCATION
680	001502	022706	001140		CMP	#SMR, R6	::DONE?
681	001506	001374			BNE	-6	::LOOP BACK IF NO
682	001510	012706	001100		MOV	#STACK, SP	::SETUP THE STACK POINTER
683					::INITIALIZE A FEW VECTORS		
684	001514	012737	011524	000020	MOV	#SCOPE, #IOTVEC	::IOT VECTOR FOR SCOPE ROUTINE
685	001522	012737	000340	000022	MOV	#340, #IOTVEC+2	::LEVEL 7
686	001530	012737	012006	000030	MOV	#ERROR, #ENTVEC	::ENT VECTOR FOR ERROR ROUTINE
687	001536	012737	000340	000032	MOV	#340, #ENTVEC+2	::LEVEL 7
688	001544	012737	014412	000034	MOV	#TRAP, #TRAPVEC	::TRAP VECTOR FOR TRAP CALLS
689	001552	012737	000340	000036	MOV	#340, #TRAPVEC+2	::LEVEL 7
690	001560	012737	012336	000024	MOV	#SPINON, #PMRVEC	::POWER FAILURE VECTOR
691	001566	012737	000340	000026	MOV	#340, #PMRVEC+2	::LEVEL 7
692	001574	005037	001160		CLR	STIMES	::INITIALIZE NUMBER OF ITERATIONS
693	001600	005037	001162		CLR	SESCAPE	::CLEAR THE ESCAPE ON ERROR ADDRESS
694	001604	012737	000001	001115	MOV#	#1, #ERRMAX	::ALLOW ONE ERROR PER TEST
695	001612	012737	001612	001106	MOV	#, #LPAOR	::INITIALIZE THE LOOP ADDRESS FOR SCOPE
696	001620	012737	001620	001110	MOV	#, #LPER	::SETUP THE ERROR LOOP ADDRESS
697					::SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS		
698					::EQUAL TO A "-1" SETUP FOR A SOFTWARE SWITCH REGISTER.		
699	001626	013746	000004		MOV	#ERRVEC, -(SP)	::SAVE ERROR VECTOR
700	001632	012737	001666	000004	MOV	#648, #ERRVEC	::SET UP ERROR VECTOR
701	001640	012737	177570	001140	MOV	#SMR, SMR	::SETUP FOR A HARDWARE SWICH REGISTER
702	001646	012737	177570	001142	MOV	#DISP, DISPLAY	::AND A HARDWARE DISPLAY REGISTER
703	001654	022777	177777	177256	CMP	#-1, #SMR	::TRY TO REFERENCE HARDWARE SMR
704	001662	001012			BNE	668	::BRANCH IF NO TIMEOUT TRAP OCCURRED
705							::AND THE HARDWARE SMR IS NOT = -1
706	001664	000403			BR	658	::BRANCH IF NO TIMEOUT
707	001666	012716	001674	648:	MOV	#658, (SP)	::SET UP FOR TRAP RETURN
708	001672	000002			RTI		
709	001674	012737	000176	001140	658:	MOV	#SMREG, SMR
710	001702	012737	000174	001142	MOV	#DISPREG, DISPLAY	::POINT TO SOFTWARE SMR
711	001710	012637	000004	668:	MOV	(SP)+, #ERRVEC	::RESTORE ERROR VECTOR
712							
713	001714	005037	001202		CLR	SPASS	::CLEAR PASS COUNT
714	001720	012737	000200	001215	BITB	#APTSIZE, #ENVM	::TEST USER SIZE UNDER APT
715	001726	001403			BEG	678	::YES, USE NON-APT SWITCH
716	001730	012737	001216	001140	MOV	#SSMREG, SMR	::NO, USE APT SWITCH REGISTER
717	001736			678:			
718	001736	005037	007004		CLR	BADUNT	::RESET BAD INDICATOR
719	001742	000137	002044		JMP	INIT1	

```

720 ;SUBROUTINE TO LOAD A TRAP CATCHER
721
722 001746 012702 000252 LDTRAP: MOV      #252,R2      ;LOAD R2
723 001752 012701 000250      MOV      #250,R1      ;LOAD R1
724 001756 010221 000250  SS:      MOV      R2,(R1)+  ;LOAD .+2
725 001760 005021      CLR      (R1)+      ;LOAD HALT
726 001762 010102      MOV      R1,R2      ;LOAD R2
727 001764 005722      YST      (R2)+      ;BUMP R2
728 001766 020227 001002      CMP      R2,#1002   ;TEST FOR LAST
729 001772 001371      BNE      SS         ;BR UNTIL DONE
730
731 ;AND LOAD DEVICE ADDRESSES LOCATIONS
732
733 001774 013700 001250      MOV      @BASE,R0    ;GET BASE ADDRESS
734 002000 010037 001422      MOV      R0,DAC0    ;LOAD X ADDRESS
735 002004 010037 001424      MOV      R0,DAC1    ;LOAD Y ADDRESS
736 002010 010037 001426      MOV      R0,DAC2    ;LOAD DAC #2
737 002014 010037 001430      MOV      R0,DAC3    ;LOAD DAC #3
738 002020 062737 000002 001424      ADD      #2,DAC1
739 002026 062737 000004 001426      ADD      #4,DAC2
740 002034 062737 000006 001430      ADD      #6,DAC3
741 002042 000207      RTS      PC         ;EXIT
742
743 002044 004737 001746  INIT1: JSR      PC,LDTRAP
744 002050 005737 007012      TST      TEMP      ;TEST IF START OR RESTART
745 002054 001012      BNE      MTEST     ;RESTART
746 002056 005737 000042      TST      #042     ;TEST IF MONITOR
747 002062 001007      BNE      MTEST     ;BR IF NOT
748 002064 005737 007020      TST      MFTST    ;TEST IF ON TESTER
749 002070 001402      BEQ      IS       ;BR IF NOT
750 002072 104401      TYPE
751 002074 010011      MSGSM
752 002076 104401      IS:      TYPE
753 002100 007040      TITLE

```

```

.SBTTL DETERMINE THE NUMBER OF RAV11 ON THIS SYSTEM
754
755
756 002102 013737 001250 001126 MTEST: NOV SBASE,SBDDAT ;GET THE BASE ADDRESS
757 002110 005037 007010 CLR MASKNM ;CLEAR UNIT #
758 002114 005037 001206 CLR SUNIT ;LOAD TRAP RETURN
759 002120 012737 002164 000014 MOV #25,ERRVEC ;TEST IF ADDR EXISTS
760 002126 005777 176774 1S: TST SBDDAT ;UPDATE THE BUS ADDRESS
761 002132 063737 001416 001126 ADD VADDR,SBDDAT ;UPDATE UNIT COUNT
762 002140 005237 001206 INC SUNIT ;TEST IF "DO NOT SIZE"
763 002146 005737 001214 TST #ENV ;BR IF NO SIZING
764 002150 100413 BMI #S ;TEST IF MAX. NUMBER
765 002156 023737 007006 001206 CMP NUMBOK,SUNIT ;BR IF NOT
766 002160 001362 BNE #S ;BR IF MAX.
767 002166 000406 BR #S ;CLEAN THE STACK
768 002172 023737 2S: CMP (SP)+,(SP)+ ;TEST IF ANY EXIST
769 002178 005737 001206 TST SUNIT ;BR IF SOME ARE THERE
770 002184 001002 BNE #S ;BASE ADDRESS CAUSED AN BUS TRAP
771 002190 104001 BR #S ;IS SBASE CORRECT??
772 002196 000443 BR #S ;TEST IF # HAS BEEN REPORTED
773 002202 005737 001420 3S: TST EVER ;BR IF IT HAS
774 002208 100422 BMI #S ;TEST IF TESTER MODE
775 002214 005737 007020 TST #FTST ;BR IF TESTER
776 002220 001010 BNE #S ;TELL OPERATOR THE # OF RAV11'S
777 002226 104401 TYPE ;
778 002232 010224 FOUND1 ;
779 002238 013746 001206 MOV SUNIT,-(SP) ;
780 002244 104403 TYPOS ;
781 002250 002 .BYTE 2 ;
782 002256 000 .BYTE 0 ;
783 002262 104401 TYPE ;
784 002268 010250 FOUND2 ;
785 002274 013737 001206 001420 6S: NOV SUNIT,EVER ;SAVE THE # OF RAV11'S FOR LATER
786 002280 052737 100000 001420 BIS #BIT15,EVER ;SET "REPORTED # FLAG"
787 002286 000405 BR #S ;
788 002292 123737 001420 001206 4S: CMPB EVER,SUNIT ;TEST IF ANY HAVE GONE AWAY
789 002298 001401 BEQ #S ;BR IF ALL ARE STILL HERE
790 002304 104013 ERROR #3 ;EXISTING UNIT FAILED TO RESPOND NOW
791 002310 005037 001206 5S: CLR SUNIT ;RESET UNIT POINTER
792 002316 005037 001204 CLR SDEVCT ;MAKE APT HAPPY
793 002322 004737 001746 JSR PC,LDTRAP ;LOAD TRAP CATCHER AND BUS ADDRESSES
794 002330 012737 000001 007010 MOV #BIT0,MASKNM ;LOAD MASK NUMBER IF ERROR
    
```

```

795
796
797
798 002306 000004
799 002310 012737 002340 000004
800 002316 005777 177100
801 002322 005777 177076
802 002326 005777 177074
803 002332 005777 177072
804 002336 000407
805 002340 022626
806 002342 104001
807 002344 012737 000006 000004
808 002352 000137 004530
809 002356 012737 000006 000004
810
811
812
813 002364 000004
814 002366 005037 001124
815 002372 013777 001124 177022
816 002400 017737 177016 001126
817 002406 023737 001124 001126
818 002414 001401
819 002416 104002
820
821
822
823
824
825
826
827
828
829
830
831
832
833
834
835
836
837
838
839
840
841
842
843
844

```

```

*****
*TEST 1 TEST THAT THE AV11 RESPONDS TO THE CPU
*****
TST1: SCOPE
MOV #15,ERRVEC ;LOAD BUS TRAP RETURN
TST @DAC0 ;TEST DAC #0
TST @DAC1 ;TEST DAC #1
TST @DAC2 ;TEST DAC #2
TST @DAC3 ;TEST DAC #3
BR 25 ;BR AND RESTORE LOC. 4
15: CMP (SP)+,(SP)+ ;CLEAN THE STACK
ERROR 1 ;ERROR, BUS TIMEOUT WHEN ADDRESSING THE AV11
MOV #6,ERRVEC ;LOAD LOC 4
JMP REMAIN ;TEST IF ANY OTHER'S
25: MOV #6,ERRVEC ;LOAD RETURN
*****
*TEST 2 TEST THAT DAC0 REGISTER CAN BE CLEARED
*****
TST2: SCOPE
CLR $GDAT ;LOAD EXPECTED
MOV $GDAT,@DAC0 ;LOAD REG
MOV @DAC0,$BDAT ;READ REG
CMP $GDAT,$BDAT ;COMPARE
BEQ TST3 ;;BR IF EQUAL
ERROR 2 ;ERROR, DAC0 REGISTER NOT = 0
*****
*TEST 3 TEST THAT DAC0 REGISTER CAN BE LOADED WITH #7777
*****
TST3: SCOPE
MOV #7777,$GDAT ;LOAD EXPECTED
MOV $GDAT,@DAC0 ;LOAD REG
MOV @DAC0,$BDAT ;READ REG
CMP $GDAT,$BDAT ;COMPARE
BEQ TST4 ;;BR IF EQUAL
ERROR 2 ;ERROR, DAC0 REGISTER NOT = 7777
*****
*TEST 4 TEST THAT DAC0 REGISTER CAN HOLD A FLOATING 1 PATTERN
*****
TST4: SCOPE
MOV #100,$TIMES ;;DO 100 ITERATIONS
MOV #BIT11,$GDAT ;LOAD EXPECTED
15: MOV $GDAT,@DAC0 ;LOAD DAC0 REGISTER
MOV @DAC0,$BDAT ;READ THE REGISTER
CMP $GDAT,$BDAT ;COMPARE THE DATA
BEQ 25 ;;BR IF SAME
ERROR 2 ;ERROR, DAC0 REGISTER FAILED TO HOLD A FLOATING
25: ASR $GDAT ;CHANGE THE DATA
BNE 15 ;BR AND TEST MORE DATA

```

```

*****
*TEST 5 TEST THAT DAC0 CAN HOLD A FLOATING 1 PATTERN (DYNAMICLY)
*****
TST5: SCOPE
      MOV #100,STIMES ;;DO 100 ITERATIONS
      MOV #BIT11,SGDAT ;LOAD EXPECTED
      MOV SGDAT,DAC0 ;LOAD DAC0
1S:   MOV DAC0,SBDAT ;READ THE REGISTER
      CMP SGDAT,SBDAT ;COMPARE THE GOOD TO DAC0
      BEQ 2S ;;BR IF THE SAME
      MOV DAC0,SBDAT ;SAVE FOR TYPEOUT
      ERROR 2 ;DAC0 FAILED TO HOLD A FLOATING 1 PATTERN
      MOV SGDAT,DAC0 ;LOAD DAC0 AGAIN
2S:   ASR DAC0 ;CHANGE THE DATA
      ASR SGDAT ;CHANGE THE EXPECTED
      BNE 1S ;BR IF MORE DATA
*****
*TEST 6 TEST THE "SUB" INSTRUCTION WORKS ON DAC0
*****
TST6: SCOPE
      MOV #10,STIMES ;;DO 10 ITERATIONS
      MOV #7777,SGDAT ;LOAD EXPECTED
      MOV SGDAT,DAC0 ;LOAD DAC0
1S:   SUB #1,SGDAT ;SUB A VALUE
      SUB #1,DAC0 ;FROM EXPECTED AND DAC0
      MOV DAC0,SBDAT ;READ THE REGISTER
      CMP SGDAT,SBDAT ;COMPARE
      BEQ 2S ;;BR IF SAME
      ERROR 2 ;THE SUB INSTRUCTION FAILED ON DAC0
      MOV SGDAT,DAC0 ;LOAD THE REGISTER AGAIN
2S:   TST SGDAT ;TEST FOR MORE DATA
      BNE 1S ;;BR IF MORE DATA
*****
*TEST 7 TEST THAT DAC1 REGISTER CAN BE CLEARED
*****
TST7: SCOPE
      CLR SGDAT ;LOAD EXPECTED
      MOV SGDAT,DAC1 ;LOAD DAC1
      MOV DAC1,SBDAT ;READ REG
      CMP SGDAT,SBDAT ;COMPARE
      BEQ TST10 ;;BR IF EQUAL
      ERROR 3 ;ERROR, DAC1 REGISTER NOT = 0
*****
*TEST 10 TEST THAT DAC #1 REGISTER CAN BE LOADED WITH #7777
*****
TST10: SCOPE
      MOV #7777,SGDAT ;LOAD EXPECTED
      MOV SGDAT,DAC1 ;LOAD DAC #1
      MOV DAC1,SBDAT ;READ REG
      CMP SGDAT,SBDAT ;COMPARE
      BEQ TST11 ;;BR IF EQUAL
      ERROR 3 ;ERROR, DAC1 REGISTER NOT = 7777

```

```

859 002530 000004
860 002532 012737 000100 001160
861 002534 012737 004000 001124
862 002536 013777 001124 176646
863 002538 017737 176642 001126
864 002540 023737 001124 001126
865 002542 001407
866 002544 017737 176624 001126
867 002546 104002
868 002548 013777 001124 176612
869 002550 005277 176606
870 002552 005237 001124
871 002554 001355
872 002622 000004
873 002624 012737 000010 001160
874 002626 012737 007777 001124
875 002628 013777 001124 176554
876 002630 162737 000001 001124
877 002632 162777 000001 176540
878 002634 017737 176534 001126
879 002636 023737 001124 001126
880 002638 001404
881 002640 104002
882 002642 013777 001124 176512
883 002644 005737 001124
884 002646 001354
885 002716 000004
886 002720 005037 001124
887 002724 013777 001124 176472
888 002728 017737 176466 001126
889 002732 023737 001124 001126
890 002736 001401
891 002740 104003
892 002752 000004
893 002754 012737 007777 001124
894 002756 013777 001124 176434
895 002758 017737 176430 001126
896 002762 023737 001124 001126
897 003004 001401
898 003006 104003

```

```

897
898
899
900 003010 000004
901 003012 012737 000100 001160
902 003020 012737 004000 001124
903 003026 013777 001124 176370
904 003034 017737 176364 001126
905 003042 023737 001124 001126
906 003050 001401
907 003052 104003
908 003054 006237 001124
909 003060 001362
910
911
912
913 003062 000004
914 003064 012737 000100 001160
915 003072 012737 004000 001124
916 003100 013777 001124 176316
917 003106 017737 176312 001126
918 003114 023737 001124 001126
919 003122 001407
920 003124 017737 176274 001126
921 003132 104003
922 003134 013777 001124 176262
923 003142 006277 176256
924 003146 006237 001124
925 003152 001355
926
927
928 003154 000004
929 003156 012737 000010 001160
930 003164 012737 007777 001124
931 003172 013777 001124 176224
932 003200 162737 000001 001124
933 003206 162777 000001 176210
934 003214 017737 176204 001126
935 003222 023737 001124 001126
936 003230 001404
937 003232 104003
938 003234 013777 001124 176162
939 003242 005737 001124
940 003246 001354
941

```

```

*****
#TEST 11 TEST THAT DAC #1 REGISTER CAN HOLD A FLOATING 1 PATTERN
*****
TST11: SCOPE
MOV #100,STIMES ;;DO 100 ITERATIONS
MOV #BIT11,SGDAT ;;LOAD EXPECTED
1S: MOV SGDAT,2DAC1 ;;LOAD THE REGISTER
MOV 2DAC1,SBDAT ;;READ THE REGISTER
CMP SGDAT,SBDAT ;;COMPARE THE DATA
BEQ 2S ;;BR IF DATA IS SAME
ERROR 3 ;;ERROR, DAC #1 REGISTER FAILED TO HOLD A FLOATIN
2S: ASR SGDAT ;;CHANGE THE DATA
BNE 1S ;;BR AND TEST MORE DATA
*****
#TEST 12 TEST THAT DAC1 CAN HOLD A FLOATING 1 PATTERN (DYNAMICLY)
*****
TST12: SCOPE
MOV #100,STIMES ;;DO 100 ITERATIONS
MOV #BIT11,SGDAT ;;LOAD EXPECTED
1S: MOV SGDAT,2DAC1 ;;LOAD DAC1
MOV 2DAC1,SBDAT ;;READ THE REGISTER
CMP SGDAT,SBDAT ;;COMPARE THE GOOD TO DAC1
BEQ 2S ;;BR IF THE SAME
MOV 2DAC1,SBDAT ;;SAVE FOR TYPEOUT
ERROR 3 ;;DAC1 FAILED TO HOLD A FLOATING 1 PATTERN
2S: MOV SGDAT,2DAC1 ;;LOAD DAC1 AGAIN
ASR 2DAC1 ;;CHANGE THE DATA
ASR SGDAT ;;CHANGE THE EXPECTED
BNE 1S ;;BR IF MORE DATA
*****
#TEST 13 TEST THE "SUB" INSTRUCTION WORKS ON DAC1
*****
TST13: SCOPE
MOV #10,STIMES ;;DO 10 ITERATIONS
MOV #7777,SGDAT ;;LOAD EXPECTED
MOV SGDAT,2DAC1 ;;LOAD DAC1
1S: SUB #1,SGDAT ;;SUB A VALUE
SUB #1,2DAC1 ;;FROM EXPECTED AND DAC1
MOV 2DAC1,SBDAT ;;READ THE REGISTER
CMP SGDAT,SBDAT ;;COMPARE
BEQ 2S ;;BR IF SAME
ERROR 3 ;;THE SUB INSTRUCTION FAILED ON DAC1
2S: MOV SGDAT,2DAC1 ;;LOAD THE REGISTER AGAIN
TST SGDAT ;;TEST FOR MORE DATA
BNE 1S ;;BR IF MORE DATA

```

993
992
991
990
989
988
987
986
985
984
983
982
981
980
979
978
977
976
975
974
973
972
971
970
969
968
967
966
965
964
963
962
961
960
959
958
957
956
955
954
953
952
951
950
949
948
947
946
945
944
943
942
941
940
939
938
937
936
935
934
933
932
931
930
929
928
927
926
925
924
923
922
921
920
919
918
917
916
915
914
913
912
911
910
909
908
907
906
905
904
903
902
901
900
899
898
897
896
895
894
893
892
891
890
889
888
887
886
885
884
883
882
881
880
879
878
877
876
875
874
873
872
871
870
869
868
867
866
865
864
863
862
861
860
859
858
857
856
855
854
853
852
851
850
849
848
847
846
845
844
843
842
841
840
839
838
837
836
835
834
833
832
831
830
829
828
827
826
825
824
823
822
821
820
819
818
817
816
815
814
813
812
811
810
809
808
807
806
805
804
803
802
801
800
799
798
797
796
795
794
793
792
791
790
789
788
787
786
785
784
783
782
781
780
779
778
777
776
775
774
773
772
771
770
769
768
767
766
765
764
763
762
761
760
759
758
757
756
755
754
753
752
751
750
749
748
747
746
745
744
743
742
741
740
739
738
737
736
735
734
733
732
731
730
729
728
727
726
725
724
723
722
721
720
719
718
717
716
715
714
713
712
711
710
709
708
707
706
705
704
703
702
701
700
699
698
697
696
695
694
693
692
691
690
689
688
687
686
685
684
683
682
681
680
679
678
677
676
675
674
673
672
671
670
669
668
667
666
665
664
663
662
661
660
659
658
657
656
655
654
653
652
651
650
649
648
647
646
645
644
643
642
641
640
639
638
637
636
635
634
633
632
631
630
629
628
627
626
625
624
623
622
621
620
619
618
617
616
615
614
613
612
611
610
609
608
607
606
605
604
603
602
601
600
599
598
597
596
595
594
593
592
591
590
589
588
587
586
585
584
583
582
581
580
579
578
577
576
575
574
573
572
571
570
569
568
567
566
565
564
563
562
561
560
559
558
557
556
555
554
553
552
551
550
549
548
547
546
545
544
543
542
541
540
539
538
537
536
535
534
533
532
531
530
529
528
527
526
525
524
523
522
521
520
519
518
517
516
515
514
513
512
511
510
509
508
507
506
505
504
503
502
501
500
499
498
497
496
495
494
493
492
491
490
489
488
487
486
485
484
483
482
481
480
479
478
477
476
475
474
473
472
471
470
469
468
467
466
465
464
463
462
461
460
459
458
457
456
455
454
453
452
451
450
449
448
447
446
445
444
443
442
441
440
439
438
437
436
435
434
433
432
431
430
429
428
427
426
425
424
423
422
421
420
419
418
417
416
415
414
413
412
411
410
409
408
407
406
405
404
403
402
401
400
399
398
397
396
395
394
393
392
391
390
389
388
387
386
385
384
383
382
381
380
379
378
377
376
375
374
373
372
371
370
369
368
367
366
365
364
363
362
361
360
359
358
357
356
355
354
353
352
351
350
349
348
347
346
345
344
343
342
341
340
339
338
337
336
335
334
333
332
331
330
329
328
327
326
325
324
323
322
321
320
319
318
317
316
315
314
313
312
311
310
309
308
307
306
305
304
303
302
301
300
299
298
297
296
295
294
293
292
291
290
289
288
287
286
285
284
283
282
281
280
279
278
277
276
275
274
273
272
271
270
269
268
267
266
265
264
263
262
261
260
259
258
257
256
255
254
253
252
251
250
249
248
247
246
245
244
243
242
241
240
239
238
237
236
235
234
233
232
231
230
229
228
227
226
225
224
223
222
221
220
219
218
217
216
215
214
213
212
211
210
209
208
207
206
205
204
203
202
201
200
199
198
197
196
195
194
193
192
191
190
189
188
187
186
185
184
183
182
181
180
179
178
177
176
175
174
173
172
171
170
169
168
167
166
165
164
163
162
161
160
159
158
157
156
155
154
153
152
151
150
149
148
147
146
145
144
143
142
141
140
139
138
137
136
135
134
133
132
131
130
129
128
127
126
125
124
123
122
121
120
119
118
117
116
115
114
113
112
111
110
109
108
107
106
105
104
103
102
101
100
99
98
97
96
95
94
93
92
91
90
89
88
87
86
85
84
83
82
81
80
79
78
77
76
75
74
73
72
71
70
69
68
67
66
65
64
63
62
61
60
59
58
57
56
55
54
53
52
51
50
49
48
47
46
45
44
43
42
41
40
39
38
37
36
35
34
33
32
31
30
29
28
27
26
25
24
23
22
21
20
19
18
17
16
15
14
13
12
11
10
9
8
7
6
5
4
3
2
1
0

003250 000004
003251 005037
003252 013777
003253 017737
003254 017737
003272 023737
003300 001401
003302 104004

003304 000004
003306 012737
003314 013777
003322 017737
003330 023737
003336 001401
003340 104004

003342 000004
003344 012737
003352 012737
003360 013777
003366 017737
003374 023737
003402 001401
003404 104004
003406 006237
003412 001362

003414 000004
003416 012737
003424 012737
003432 013777
003440 017737
003446 023737
003454 001401
003456 017737
003464 104004
003466 013777
003474 006277
003500 006237
003504 001355

```
*****  
#TEST 14 TEST THAT THE DAC #2 REGISTER CAN BE CLEARED  
*****  
TST14: SCOPE  
CLR SGDDAT ;LOAD EXPECTED  
MOV SGDDAT, ZDAC2 ;LOAD REG  
MOV ZDAC2, SBDDAT ;READ REG  
CMP SGDDAT, SBDDAT ;COMPARE  
BEQ TST15 ;;BR IF EQUAL  
ERROR 4 ;ERROR, DAC #2 REGISTER NOT = 0
```

```
*****  
#TEST 15 TEST THAT THE DAC #2 REGISTER CAN BE LOADED WITH #7777  
*****  
TST15: SCOPE  
MOV #7777, SGDDAT ;LOAD EXPECTED  
MOV SGDDAT, ZDAC2 ;LOAD REG  
MOV ZDAC2, SBDDAT ;READ REG  
CMP SGDDAT, SBDDAT ;COMPARE  
BEQ TST16 ;;BR IF EQUAL  
ERROR 4 ;ERROR, DAC #2 REGISTER NOT = 7777
```

```
*****  
#TEST 16 TEST THAT THE DAC #2 REGISTER CAN HOLD A FLOATING 1 PATTERN  
*****  
TST16: SCOPE  
MOV #100, STIMES ;;DO 100 ITERATIONS  
MOV #BIT11, SGDDAT ;LOAD EXPECTED  
1S: MOV SGDDAT, ZDAC2 ;LOAD DAC2 REGISTER  
MOV ZDAC2, SBDDAT ;READ THE REGISTER  
CMP SGDDAT, SBDDAT ;COMPARE THE DATA  
BEQ 2S ;;BR IF SAME  
ERROR 4 ;ERROR, DAC #2 REGISTER FAILED TO HOLD A FLOATIN  
2S: ASR SGDDAT ;CHANGE THE DATA  
BNE 1S ;BR AND TEST MORE DATA
```

```
*****  
#TEST 17 TEST THAT DAC2 CAN HOLD A FLOATING 1 PATTERN (DYNAMICLY)  
*****  
TST17: SCOPE  
MOV #100, STIMES ;;DO 100 ITERATIONS  
MOV #BIT11, SGDDAT ;LOAD EXPECTED  
MOV SGDDAT, ZDAC2 ;LOAD DAC2  
1S: MOV ZDAC2, SBDDAT ;READ THE REGISTER  
CMP SGDDAT, SBDDAT ;COMPARE THE GOOD TO DAC2  
BEQ 2S ;;BR IF THE SAME  
MOV ZDAC2, SBDDAT ;SAVE FOR TYPEOUT  
ERROR 4 ;DAC2 FAILED TO HOLD A FLOATING 1 PATTERN  
2S: MOV SGDDAT, ZDAC2 ;LOAD DAC2 AGAIN  
ASR ZDAC2 ;CHANGE THE DATA  
ASR SGDDAT ;CHANGE THE EXPECTED  
BNE 1S ;BR IF MORE DATA
```



```

1047
1048
1049
1050 003746 000004
1051 003750 012737 000100 001160
1052 003756 012737 004000 001124
1053 003764 013777 001124 175436
1054 003772 017737 175432 001126
1055 004000 023737 001124 001126
1056 004006 001407
1057 004010 017737 175414 001126
1058 004016 104005
1059 004020 013777 001124 175402
1060 004026 006277 175376
1061 004032 006237 001124
1062 004036 001355
1063
1064
1065
1066 004040 000004
1067 004042 012737 000010 001160
1068 004050 012737 007777 001124
1069 004056 013777 001124 175344
1070 004064 162737 000001 001124
1071 004072 162777 000001 175330
1072 004100 017737 175324 001126
1073 004106 023737 001124 001126
1074 004114 001404
1075 004116 104005
1076 004120 013777 001124 175302
1077 004126 005737 001124
1078 004132 001354

*****
*TEST 24 TEST THAT DAC3 CAN HOLD A FLOATING 1 PATTERN (DYNAMICLY)
*****
TST24: SCOPE
MOV #100,STIMES ;;DO 100 ITERATIONS
MOV #BIT11,$GDDAT ;LOAD EXPECTED
MOV $GDDAT,$DAC3 ;LOAD DAC3
1S: MOV $DAC3,$BDDAT ;READ THE REGISTER
CMP $GDDAT,$BDDAT ;COMPARE THE GOOD TO DAC3
BEQ 2S ;;BR IF THE SAME
MOV $DAC3,$BDDAT ;SAVE FOR TYPEOUT
ERROR 5 ;DAC3 FAILED TO HOLD A FLOATING 1 PATTERN
MOV $GDDAT,$DAC3 ;LOAD DAC3 AGAIN
2S: ASR $DAC3 ;CHANGE THE DATA
ASR $GDDAT ;CHANGE THE EXPECTED
BNE 1S ;BR IF MORE DATA

*****
*TEST 25 TEST THE "SUB" INSTRUCTION WORKS ON DAC3
*****
TST25: SCOPE
MOV #10,STIMES ;;DO 10 ITERATIONS
MOV #7777,$GDDAT ;LOAD EXPECTED
MOV $GDDAT,$DAC3 ;LOAD DAC3
1S: SUB #1,$GDDAT ;SUB A VALUE
SUB #1,$DAC3 ;FROM EXPECTED AND DAC3
MOV $DAC3,$BDDAT ;READ THE REGISTER
CMP $GDDAT,$BDDAT ;COMPARE
BEQ 2S ;;BR IF SAME
ERROR 5 ;THE SUB INSTRUCTION FAILED ON DAC3
MOV $GDDAT,$DAC3 ;LOAD THE REGISTER AGAIN
2S: TST $GDDAT ;TEST FOR MORE DATA
BNE 1S ;;BR IF MORE DATA

```

:TEST 26 TEST THAT THE FOUR DAC REGISTERS CAN HOLD DIFFERENT DATA

```

TST26: SCOPE
MOV      #1111, @DAC0      ;LOAD DAC #0
MOV      #2222, @DAC1      ;LOAD DAC #1
MOV      #4444, @DAC2      ;LOAD DAC #2
MOV      #7777, @DAC3      ;LOAD DAC #3
MOV      @1111, @SGDAT      ;LOAD EXPECTED
MOV      @DAC0, @SBDAT      ;READ REG
CMP      @SGDAT, @SBDAT     ;COMPARE
BEQ      1S                ;;BR IF EQUAL
ERROR    2                  ;ERROR, SELECTED DAC #0 IN ERROR

1S:      MOV      #2222, @SGDAT ;LOAD EXPECTED
MOV      @DAC1, @SBDAT      ;READ REG
CMP      @SGDAT, @SBDAT     ;COMPARE
BEQ      2S                ;;BR IF EQUAL
ERROR    3                  ;ERROR, SELECTED DAC #1 IN ERROR

2S:      MOV      #4444, @SGDAT ;LOAD EXPECTED
MOV      @DAC2, @SBDAT      ;READ REG
CMP      @SGDAT, @SBDAT     ;COMPARE
BEQ      3S                ;;BR IF SAME
ERROR    4                  ;ERROR, SELECTED DAC #2 IN ERROR

3S:      MOV      #7777, @SGDAT ;LOAD EXPECTED
MOV      @DAC3, @SBDAT      ;READ REG
CMP      @SGDAT, @SBDAT     ;COMPARE
BEQ      TST27             ;;BR IF SAME
ERROR    5                  ;ERROR, SELECTED DAC #3 IN ERROR

```

```

1079
1080
1081
1082 004134 000004
1083 004136 012777 001111 175256
1084 004144 012777 002222 175252
1085 004152 012777 004444 175246
1086 004160 012777 007777 175242
1087 004166 012737 001111 001124
1088 004174 017737 175222 001126
1089 004202 023737 001124 001126
1090 004210 001401
1091 004212 104002
1092
1093 004214 012737 002222 001124 1S:
1094 004222 017737 175176 001126
1095 004230 023737 001124 001126
1096 004236 001401
1097 004240 104003
1098
1099 004242 012737 004444 001124 2S:
1100 004250 017737 175152 001126
1101 004256 023737 001124 001126
1102 004264 001401
1103 004266 104004
1104
1105 004270 012737 007777 001124 3S:
1106 004276 017737 175126 001126
1107 004304 023737 001124 001126
1108 004312 001401
1109 004314 104005

```

*TEST 27 TEST THAT RESET CLEARS DAC #0 REGISTER

TST27: SCOPE
MOV #10,STIMES ;;DO 10 ITERATIONS
MOV #-1,DAC0 ;LOAD EXPECTED
CLR SGDDAT ;LOAD EXPECTED
RESET ;READ REG
MOV DAC0,SDDAT ;COMPARE
CMP SGDDAT,SDDAT ;BR IF EQUAL
BEQ TST30 ;ERROR, RESET FAILED TO CLEAR DAC #0
ERROR 2

*TEST 30 TEST THAT RESET CLEARS DAC #1 REGISTER

TST30: SCOPE
MOV #10,STIMES ;;DO 10 ITERATIONS
MOV #-1,DAC1 ;LOAD EXPECTED
CLR SGDDAT ;LOAD EXPECTED
RESET ;READ REG
MOV DAC1,SDDAT ;COMPARE
CMP SGDDAT,SDDAT ;BR IF EQUAL
BEQ TST31 ;ERROR, RESET FAILED TO CLEAR DAC #1
ERROR 3

*TEST 31 TEST THAT RESET CLEARS DAC #2 REGISTER

TST31: SCOPE
MOV #10,STIMES ;;DO 10 ITERATIONS
MOV #-1,DAC2 ;LOAD THE REGISTER
CLR SGDDAT ;CLEAR EXPECTED
RESET ;READ THE REGISTER
MOV DAC2,SDDAT ;BR IF CLEARED
BEQ TST32 ;ERROR, RESET FAILED TO CLEAR DAC #2
ERROR 4

*TEST 32 TEST THAT RESET CLEARS DAC #3 REGISTER

TST32: SCOPE
MOV #10,STIMES ;;DO 10 ITERATIONS
MOV #-1,DAC3 ;LOAD THE REGISTER
CLR SGDDAT ;CLEAR THE EXPECTED
MOV #1,TEMP ;READ THE REGISTER
RESET ;BR IF CLEARED
MOV DAC3,SDDAT ;ERROR, RESET FAILED TO CLEAR DAC #3
BEQ TST33
ERROR 5

1110
1111
1112
1113 004316 000004
1114 004320 012737 000010 001160
1115 004324 012777 177777 175066
1116 004328 005037 001124
1117 004332 000005
1118 004336 017737 175054 001126
1119 004340 023737 001124 001126
1120 004356 001401
1121 004360 104002
1122
1123
1124
1125
1126 004368 000004
1127 004372 012737 000010 001160
1128 004376 012777 177777 175024
1129 004400 005037 001124
1130 004404 000005
1131 004408 017737 175012 001126
1132 004412 023737 001124 001126
1133 004428 001401
1134 004432 104003
1135
1136
1137
1138
1139 004428 000004
1140 004432 012737 000010 001160
1141 004436 012777 177777 174762
1142 004440 005037 001124
1143 004444 000005
1144 004448 017737 174750 001126
1145 004452 001401
1146 004456 104004
1147
1148
1149
1150
1151 004464 000004
1152 004468 012737 000010 001160
1153 004472 012777 177777 174726
1154 004502 005037 001124
1155 004506 012737 000001 007012
1156 004510 000005
1157 004514 017737 174706 001126
1158 004518 001401
1159 004522 104005
1160
1161


```

1188      ;*****
1189      ;*TEST 35      TEST THAT DAC #3 OUTPUT BITS (0-3) FUNCTION
1190      ;*****
1191      TST35:  SCOPE
1192      004646 000004      MOV      #1,STIMES      ;;DO 1 ITERATION
1193      004650 012737 000001 001160      MOV      #8113,STEMP    ;LOAD DAC PATTERN
1194      004656 012737 000010 007014      MOV      #8111,SGDDAT   ;LOAD EXPECTED PATTERN
1195      004664 012737 004000 001124
1196      004672 013777 007014 174530 15:      MOV      $TEMP,$DAC3    ;LOAD DAC REGISTER
1197      004700 017737 002120 001126      MOV      $DRIN,$DDOAT   ;READ THE REGISTER
1198      004706 042737 170377 001126      BIC      #170377,$DDOAT ;MASK OFF OTHER BITS
1199      004714 023737 001124 001126      CMP      $DDOAT,$DDOAT  ;COMPARE
1200      004722 001401      BEQ      25              ;;BR IF THE SAME
1201      004724 104013      ERROR    13              ;DAC #3 DIGITAL OUTPUT BITS IN ERROR
1202
1203      004726 006237 001124 25:      ASR      $DDOAT         ;ADJUST EXPECTED
1204      004732 006237 007014      ASR      $TEMP         ;ADJUST LOADED PATTERN
1205      004736 001355      BNE      15
1206
1207      ;*****
1208      ;*TEST 36      VERIFY THE RAV11 +15 SUPPLY
1209      ;*****
1210      TST36:  SCOPE
1211      004740 000004      MOV      #1,STIMES      ;;DO 1 ITERATION
1212      004742 012737 000001 001160      MOV      V5744,$GDDAT  ;LOAD EXPECTED
1213      004750 013737 007032 001124      JSR      R5,CONVRT     ;SAMPLE THE CHANNEL
1214      004756 004537 006302
1215      004762 000012      L2
1216      004764 013737 007034 007016      MOV      V144,SPREAD   ;LOAD TOLERANCE
1217      004772 004737 006520      JSR      PC,COMPAR     ;TEST IT
1218      004776 000401      BR      TST37          ;;BR
1219      005000 104011      ERROR    11              ;+15 VOLT SUPPLY IS WRONG
1220
1221      ;*****
1222      ;*TEST 37      VERIFY THE RAV11 -15 SUPPLY
1223      ;*****
1224      TST37:  SCOPE
1225      005002 000004      MOV      #1,STIMES      ;;DO 1 ITERATION
1226      005004 012737 000001 001160      MOV      V2034,$GDDAT  ;LOAD EXPECTED
1227      005012 013737 007036 001124      JSR      R5,CONVRT     ;SAMPLE THE CHANNEL
1228      005018 004537 006302
1229      005024 000011      L1
1230      005030 013737 007034 007016      MOV      V144,SPREAD   ;LOAD TOLERANCE
1231      005036 004737 006520      JSR      PC,COMPAR     ;TEST IT
1232      005042 000401      BR      TST40          ;;BR
1233      005048 104012      ERROR    12              ;-15 VOLT SUPPLY IS WRONG

```

1233
1234
1235
1236
1237
1238
1239
1240
1241
1242
1243
1244
1245
1246
1247
1248
1249
1250
1251
1252
1253
1254
1255
1256
1257

005044 000004
005046 012737 000001 001160
005054 005737 001202
005060 001006

005062 004537 005634
005064 001422
005070 010726
005072 010552
005074 000013

005076 000004
005100 012737 000001 001160
005106 005737 001202
005112 001005

005114 004537 005764
005120 001422
005122 010376
005124 000013

005126 000004
005130 012737 000001 001160
005136 004537 006104
005142 001422
005144 000013

```
*****  
:TEST 40 DAC0 OFFSET ADJUSTMENT  
*****  
TST40: SCOPE  
MOV #1,STIMES ;;DO 1 ITERATION  
TST SPASS ;TEST IF FIRST PASS  
BNE TST41 ;;BR IF NOT  
  
JSR RS,OFFDAC ;LOAD AND EXECUTE DAC OFFSET ADJ.  
DAC0 ;DAC ADDRESS  
SELDO ;TYPEOUT ADDRESS  
ADJR46 ;RES. TO ADJUST  
13 ;RESULT CHANNEL #  
  
*****  
:TEST 41 DAC0 GAIN ADJUSTMENT  
*****  
TST41: SCOPE  
MOV #1,STIMES ;;DO 1 ITERATION  
TST SPASS ;TEST IF FIRST PASS  
BNE TST42 ;;BR IF NOT  
  
JSR RS,GAIDAC ;LOAD AND EXECUTE DAC GAIN ADJ.  
DAC0 ;DAC ADDRESS  
ADJR34 ;RES. TO ADJUST  
13 ;CHANNEL # FOR RESULTS  
  
*****  
:TEST 42 DAC0 CALIBRATION  
*****  
TST42: SCOPE  
MOV #1,STIMES ;;DO 1 ITERATION  
JSR RS,CALDAC ;LOAD AND EXECUTE CALIBRATION  
DAC0 ;DAC ADDRESS  
13 ;CHANNEL # FOR RESULTS
```

E03

MAINDEC-11-DVAAA-A
DVAAA.P11 T43

RAV11 DIAGNOSTIC
DAC1 OFFSET ADJUSTMENT

MACY11 27(665) 12-OCT-76 13:42 PAGE 30

```
1268
1269
1270
1271 005146 000004
1272 005150 012737 000001 001160
1273 005156 005737 001202
1274 005162 001006
1275
1276 005164 004537 005634
1277 005170 001424
1278 005172 010744
1279 005174 010605
1280 005176 000014
1281
1282
1283
1284
1285
1286
1287
1288
1289
1290 005200 000004
1291 005202 012737 000001 001160
1292 005210 005737 001202
1293 005214 001005
1294
1295 005216 004537 005764
1296 005222 001424
1297 005224 010431
1298 005226 000014
1299
1300
1301
1302 005230 000004
1303 005232 012737 000001 001160
1304 005240 004537 006104
1305 005244 001424
1306 005246 000014

*****
#TEST 43 DAC1 OFFSET ADJUSTMENT
*****
TST43: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
TST SPASS ;TEST IF FIRST PASS
BNE TST44 ;;BR IF NOT

JSR RS,OFFDAC ;LOAD AND EXECUTE DAC OFFSET ADJ.
DAC1 ;DAC ADDRESS
SELD1 ;TYPEOUT ADDRESS
ADJR47 ;RES. TO ADJUST
14 ;RESULT CHANNEL #

*****
#TEST 44 DAC1 GAIN ADJUSTMENT
*****
TST44: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
TST SPASS ;TEST IF FIRST PASS
BNE TST45 ;;BR IF NOT

JSR RS,GAIDAC ;LOAD AND EXECUTE DAC GAIN ADJ.
DAC1 ;DAC ADDRESS
ADJR35 ;RES. TO ADJUST
14 ;CHANNEL # FOR RESULTS

*****
#TEST 45 DAC1 CALIBRATION
*****
TST45: SCOPE
MOV #1,STIMES ;;DO 1 ITERATION
JSR RS,CALDAC ;LOAD AND EXECUTE CALIBRATION
DAC1 ;DAC ADDRESS
14 ;CHANNEL # FOR RESULTS
```

F03

MAINDEC-11-DVAAA-A
DVAAA.P11 T46

ARV11 DIAGNOSTIC
DAC2 OFFSET ADJUSTMENT

MACY11 27(665) 12-OCT-76 13:42 PAGE 31

```
1303
1304
1305
1306 *****
1307 #TEST 46 DAC2 OFFSET ADJUSTMENT *****
1308 TST46: SCOPE
1309 MOV #1,STIMES ;;DO 1 ITERATION
1310 TST SPASS ;TEST IF FIRST PASS
1311 BNE TST47 ;;BR IF NOT
1312 JSR RS,OFFDAC ;LOAD AND EXECUTE DAC OFFSET ADJ.
1313 DAC2 ;DAC ADDRESS
1314 SELD2 ;TYPEOUT ADDRESS
1315 ADJR48 ;RES. TO ADJUST
1316 16 ;RESULT CHANNEL #
1317
1318 *****
1319 #TEST 47 DAC2 GAIN ADJUSTMENT *****
1320 TST47: SCOPE
1321 MOV #1,STIMES ;;DO 1 ITERATION
1322 TST SPASS ;TEST IF FIRST PASS
1323 BNE TST50 ;;BR IF NOT
1324 JSR RS,GAIDAC ;LOAD AND EXECUTE DAC GAIN ADJ.
1325 DAC2 ;DAC ADDRESS
1326 ADJR36 ;RES. TO ADJUST
1327 16 ;CHANNEL # FOR RESULTS
1328
1329 *****
1330 #TEST 50 DAC2 CALIBRATION *****
1331 TST50: SCOPE
1332 MOV #1,STIMES ;;DO 1 ITERATION
1333 JSR RS,CALDAC ;LOAD AND EXECUTE CALIBRATION
1334 DAC2 ;DAC ADDRESS
1335 16 ;CHANNEL # FOR RESULTS
1336
1337
```

Line	Address	Offset	Control	Label	Instruction	Comment
1306	005250	000004			SCOPE	
1307	005252	012737	000001	001160	MOV #1,STIMES	;;DO 1 ITERATION
1308	005260	005737	001202		TST SPASS	;TEST IF FIRST PASS
1309	005264	001006			BNE TST47	;;BR IF NOT
1311	005266	004537	005634		JSR RS,OFFDAC	;LOAD AND EXECUTE DAC OFFSET ADJ.
1312	005272	001426			DAC2	;DAC ADDRESS
1313	005274	010762			SELD2	;TYPEOUT ADDRESS
1314	005276	010640			ADJR48	;RES. TO ADJUST
1315	005300	000016			16	;RESULT CHANNEL #
1320	005302	000004			SCOPE	
1321	005304	012737	000001	001160	MOV #1,STIMES	;;DO 1 ITERATION
1322	005312	005737	001202		TST SPASS	;TEST IF FIRST PASS
1323	005316	001005			BNE TST50	;;BR IF NOT
1324	005320	004537	005764		JSR RS,GAIDAC	;LOAD AND EXECUTE DAC GAIN ADJ.
1325	005324	001426			DAC2	;DAC ADDRESS
1326	005326	010464			ADJR36	;RES. TO ADJUST
1327	005330	000016			16	;CHANNEL # FOR RESULTS
1332	005332	000004			SCOPE	
1333	005334	012737	000001	001160	MOV #1,STIMES	;;DO 1 ITERATION
1334	005342	004537	006104		JSR RS,CALDAC	;LOAD AND EXECUTE CALIBRATION
1335	005346	001426			DAC2	;DAC ADDRESS
1336	005350	000016			16	;CHANNEL # FOR RESULTS

```

1338      ;*****
1339      ;#TEST 51      DAC3 OFFSET ADJUSTMENT
1340      ;*****
1341      005352 000004      TST51: SCOPE
1342      005354 012737 000001 001160      MOV      #1,STIMES      ;;DO 1 ITERATION
1343      005362 005737 001202      TST      $PASS      ;TEST IF FIRST PASS
1344      005366 001006      BNE      TST52      ;;BR IF NOT
1345
1346      005370 004537 005634      JSR      RS,OFFDAC      ;LOAD AND EXECUTE DAC OFFSET ADJ.
1347      005374 001430      DAC3      ;DAC ADDRESS
1348      005376 011000      SELD3     ;TYPEOUT ADDRESS
1349      005400 010673      ADJR49    ;RES. TO ADJUST
1350      005402 000015      IS       ;RESULT CHANNEL #
1351
1352      ;*****
1353      ;#TEST 52      DAC3 GAIN ADJUSTMENT
1354      ;*****
1355      005404 000004      TST52: SCOPE
1356      005406 012737 000001 001160      MOV      #1,STIMES      ;;DO 1 ITERATION
1357      005414 005737 001202      TST      $PASS      ;TEST IF FIRST PASS
1358      005420 001005      BNE      TST53      ;;BR IF NOT
1359
1360      005422 004537 005764      JSR      RS,GAIDAC     ;LOAD AND EXECUTE DAC GAIN ADJ.
1361      005426 001430      DAC3      ;DAC ADDRESS
1362      005430 010517      ADJR37    ;RES. TO ADJUST
1363      005432 000015      IS       ;CHANNEL # FOR RESULTS
1364
1365      ;*****
1366      ;#TEST 53      DAC3 CALIBRATION
1367      ;*****
1368      005434 000004      TST53: SCOPE
1369      005436 012737 000001 001160      MOV      #1,STIMES      ;;DO 1 ITERATION
1370      005444 004537 006104      JSR      RS,CALDAC     ;LOAD AND EXECUTE CALIBRATION
1371      005450 001430      DAC3      ;DAC ADDRESS
1372      005452 000015      IS       ;CHANNEL # FOR RESULTS

```

1373
1374
1375
1376
1377
1378
1379
1380
1381
1382
1383
1384
1385
1386
1387
1388
1389
1390
1391
1392
1393
1394
1395
1396
1397
1398
1399
1400
1401
1402
1403
1404
1405
1406
1407
1408
1409
1410
1411
1412
1413
1414
1415
1416
1417
1418
1419
1420

005454
005454 000004
005456 005037 001102
005462 005037 001160
005466 005237 001202
005472 042737 100000 001202
005500 005327
005502 000001
005504 003022
005506 012737
005510 000001
005512 005512
005514 104401 005561
005520 013746 001202
005524 104405
005526 104401 005556
005528 013700 000042
005530 001406
005532 000005
005534 004710
005536 000240
005538 000240
005540 000240
005542 000240
005544 000240
005546 000240
005548 000240
005550 000137
005552 005576
005554 377 377 000
005556 015 042412 042116
005558 050040 051501 020123
005560 000043
005576 005737 007020
005602 001012
005604 104401 010176
005610 013746 001112
005614 104405
005616 104401 010210
005622 013746 007004
005626 104406
005630 000137 002044

.SBTTL END OF PASS ROUTINE

; INCREMENT THE PASS NUMBER (SPASS)
; TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)
; IF THERES A MONITOR GO TO IT
; IF THERE ISN'T JUMP TO INIT7
SEOP:
SCOPE
CLR STSTN ; ZERO THE TEST NUMBER
CLR STIMES ; ZERO THE NUMBER OF ITERATIONS
INC SPASS ; INCREMENT THE PASS NUMBER
BIC #100000, SPASS ; DON'T ALLOW A NEG. NUMBER
DEC (PC)+ ; LOOP?
SEOPCT: .WORD 1
BGT SDOAGN ; YES
MOV (PC)+, 2(PC)+ ; RESTORE COUNTER
SENDCT: .WORD 1
SEOPCT
TYPE SENDNG ; TYPE "END PASS #"
MOV SPASS, -(SP) ; SAVE SPASS FOR TYPEOUT
TYPDS ; GO TYPE--DECIMAL ASCII WITH SIGN
TYPE SENULL ; TYPE A NULL CHARACTER
SGET42: MOV #42, R0 ; GET MONITOR ADDRESS
BEG SDOAGN ; BRANCH IF NO MONITOR
RESET ; CLEAR THE WORLD
SENDAD: JSR PC, (R0) ; GO TO MONITOR
NOP ; SAVE ROOM
NOP ; FOR
NOP ; ACT11
SDOAGN: JMP 2(PC)+ ; RETURN
SRTNAD: .WORD INIT7
SENULL: .BYTE -1, -1, 0 ; NULL CHARACTER STRING
SENDNG: .ASCIZ <15><12>/END PASS #/
INIT7: TST WFTST ; TEST IF ON TESTER
BNE IS
TYPE, ERRTOT
MOV SERTTL, -(SP)
TYPDS
TYPE, NESGD
MOV BADUNT, -(SP) ; SAVE BADUNT FOR TYPEOUT
TYPBN ; GO TYPE--BINARY ASCII
IS: JMP INIT1 ; TEST IT AGAIN

```

1421          .SBTTL SUBROUTINE TO ADJUST THE DAC'S OFFSET POTS
1422
1423 OFFDAC:  MOV      (R5)+,10$           ;GET BUS ADDRESS
1424          MOV      @10$,10$           ;
1425          MOV      10$,DACBAD         ;LOAD BUS ADDRESS IF ERROR
1426          MOV      (R5)+,11$         ;GET POINTER TO ASCII MESSAGE
1427          MOV      (R5)+,12$         ;GET POINTER TO RES. MESSAGE
1428          MOV      (R5)+,13$         ;GET AND SAVE CHANNEL #
1429          TYPE                                ;TELL OPERATOR TO SELECT DAC N
1430
1431          11$:  SELD0                                ;LOAD A VOLTAGE OF NS.1200 VOLTS.
1432          JSR      RS,SNDVLT
1433          MOV      #0000,@10$         ;LOAD THE SELECTED DAC TO NULL
1434          TYPE                                ;TELL OPERATOR TO ADJUST RXX FOR NULL
1435
1436          12$:  ADJR46
1437          JSR      PC,CSPACE          ;WAIT UNTIL THE IS READY
1438          MOV      #0000,SGDAT        ;LOAD EXPECTED VALUE
1439          JSR      RS,CONVRT          ;SAMPLE THE CHANNEL
1440
1441          13$:  13
1442          MOV      #2,SPREAD
1443          JSR      PC,COMPAR          ;TEST RESULTS
1444          BR      2$                  ;BR IF WITHIN THE LIMIT
1445          ERROR  6                    ;SELECTED DAC OFFSET POT WAS NOT ADJUSTED INCORRECTLY
1446          TYPE
1447          TRYAGN
1448          BR      1$                  ;LOOP AGAIN
1449          2$:  RTS      RS              ;EXIT
1450          10$:  0
1451
1452          .SBTTL SUBROUTINE TO ADJUST THE GAIN ADJUSTMENT POTS
1453
1454 GAIDAC:  MOV      (R5)+,10$           ;GET BUS ADDRESS
1455          MOV      @10$,10$           ;
1456          MOV      10$,DACBAD         ;LOAD BUS ADDRESS IF ERROR
1457          MOV      (R5)+,11$         ;GET ASCII RES. ADDRESS
1458          MOV      (R5)+,12$         ;GET CHANNEL
1459          JSR      RS,SNDVLT          ;LOAD + 5.1175 VOLTS
1460          MOV      #51175, @10$       ;LOAD THE DAC
1461          TYPE                                ;TELL OPERATOR WHICH RXX TO ADJUST FOR NULL
1462          11$:  ADJR34
1463          JSR      PC,CSPACE          ;WAIT FOR OPERATOR
1464          MOV      #7777,SGDAT        ;LOAD EXPECTED
1465          JSR      RS,CONVRT          ;CONVERT THE VALUE
1466
1467          12$:  13
1468          MOV      #2,SPREAD          ;LOAD LIMIT
1469          JSR      PC,COMPAR          ;TEST RESULTS
1470          BR      2$                  ;BR IF WITHIN LIMITS
1471          ERROR  7                    ;SELECTED DAC GAIN POT WAS NOT ADJUSTED PROPERLY
1472          TYPE
1473          TRYAGN
1474          BR      1$                  ;EXIT
1475          2$:  RTS      RS
1476          10$:  0

```

.SBTTL SUBROUTINE TO TEST THE D/A CALIBRATION

```

1475
1476
1477 006104 012537 006210 CALDAC: MOV (R5)+,10$ ;GET BUS ADDRESS
1478 006110 012737 000074 006210 MOV 210$,10$ ;
1479 006116 013737 006210 007002 MOV 10$,DACBAD ;LOAD BUS ADDRESS IF ERROR
1480 006124 012537 006150 MOV (R5)+,11$ ;GET CHANNEL #
1481
1482 006130 012777 007400 000052 MOV #7400,210$ ;LOAD THE DAC
1483 006136 012737 007400 001124 MOV #7400,$GDDAT ;LOAD THE EXPECTED VALUE
1484
1485 006144 004537 006302 1$: JSR R5,CONVRT ;SAMPLE THE CHANNEL
1486 006150 000013 11$: 13
1487
1488 006152 012737 000003 007016 MOV #3,SPREAD ;LOAD TOLERANCE
1489 006160 004737 006520 JSR PC,COMPAR ;TEST THE RESULTS
1490 006164 000401 BR 2$ ;;BR
1491 006166 104010 ERROR 10 ;NON-LINEARITY IN DAC DETECTED
1492 006170 162777 000400 000012 2$: SUB #400,210$ ;ADJUST THE CONTENTS
1493 006176 162737 000400 001124 SUB #400,$GDDAT ;ADJUST THE EXPECTED
1494 006204 001357 BNE 1$ ;;BR IF NOT DONE
1495 006206 000205 RTS ;EXIT
1496
1497 006210 000000 10$: 0

```

.SBTTL SUBROUTINE TO LOAD A VOLTAGE INTO THE VOLTAGE SOURCE

```

1500
1501 006212 012500 SNOVLT: MOV (R5)+,R0 ;LOAD THE POINTER
1502 006214 112001 2$: MOVB (R0)+,R1 ;GET SOME DATA
1503 006216 001421 BEQ 3$ ;BR IF TERM
1504 006220 110177 000576 MOVB R1,2FILZ ;LOAD THE DATA
1505 006224 012701 001000 MOV #1000,R1
1506 006230 005301 5$: DEC R1 ;DELAY
1507 006232 001376 BNE 5$
1508 006234 052777 000200 000560 BIS #BIT7,2FILZ ;SET BIT 7
1509 006242 012701 001000 MOV #1000,R1 ;LOAD DELAY
1510 006246 005301 1$: DEC R1 ;DELAY
1511 006250 001376 BNE 1$
1512 006252 042777 000200 000542 BIC #BIT7,2FILZ
1513 006260 000755 BR 2$
1514
1515 006262 012701 000000 3$: MOV #0,R1 ;LOAD DELAY
1516 006266 152777 000177 000526 BISB #177,2FILZ ;DISABLE BITS
1517 006274 005301 4$: DEC R1 ;DELAY
1518 006276 001376 BNE 4$
1519 006300 000205 RTS ;EXIT
1520

```

.SBTTL SUBROUTINE TO CONVERT CHANNEL N ON THE TESTER A/D

```

1531 006302 012537 006414 CONVRT: MOV (R5)+,10S ;GET THE CHANNEL #
1532 006306 000337 006414 SWAB 10S
1533 006312 042737 170377 006414 BIC @170377,10S ;MASK OUT OTHER BITS
1534 006320 013777 006414 000500 MOV 10S,2A0CS ;SELECT CHANNEL
1535 006326 005037 006416 CLR 11S
1536 006332 012737 000200 006420 MOV @BIT7,12S ;LOAD SHIFT COUNTER
1537 006338 105277 000462 1S: INCB 2A0CS ;CONVERT CHANNEL
1538 006344 105777 000456 2S: TSTB 2A0CS ;WAIT FOR DONE
1539 006350 100375 BPL 2S
1540 006356 067737 000452 006416 ROR 2A0BR,11S ;UPDATE CONVERSION
1541 006362 006237 006420 ASR 12S ;FINISHED ?
1542 006368 001365 BNE 1S
1543 006374 000257 CCC
1544 006378 006037 006416 ROR 11S
1545 006384 006237 006416 ASR 11S
1546 006390 006237 006416 ASR 11S
1547 006396 006237 006416 ASR 11S ;JUSTIFY DATA
1548 006400 006237 006416 MOV 11S,SBDDAT ;LOAD ACTUAL <ADJUSTED>
1549 006404 013737 006416 001126 RTS AS ;EXIT
1550 006412 000205
1551 006414 000000 10S: 0
1552 006416 000000 11S: 0
1553 006420 000000 12S: 0
    
```

.SBTTL SUBROUTINE TO LOOP UNTIL OPERATOR TYPES AN "SPACE"

```

1554 006422 104401 007746 CSPACE: TYPE, LDSPAC ;TELL OPERATOR TO HIT SPACE BAR
1555 006426 012737 000014 006516 3S: MOV @14,11S ;LOAD DELAY COUNTER
1556 006434 005037 006514 CLR 10S
1557 006440 105777 172500 1S: TSTB 25TKS ;WAIT FOR OPERATOR
1558 006444 100410 BMI 2S ;BR IF FLAG IS SET
1559 006446 005337 006514 DEC 10S ;DELAY
1560 006452 001372 BNE 1S ;BR IF NOT DONE
1561 006454 005337 006516 DEC 11S ;DELAY AGAIN
1562 006460 001367 BNE 1S
1563 006462 104014 ERROR 14 ;MAKE UP OPERATOR
1564 006464 000760 BR 3S ;LOOP
1565 006466 017737 172454 006514 2S: MOV 25TKB,10S ;READ THE CHARACTER
1566 006474 042737 177600 006514 BIC @177600,10S ;MASK OF OTHER BITS
1567 006502 022737 000040 006514 CMP #40,10S ;TEST FOR "SPACE"
1568 006510 001346 BNE 3S ;LOOP
1569 006512 000207 RTS PC ;EXIT
1570 006514 000000 10S: 0
1571 006516 000010 11S: BIT3
    
```

```

1567                    .SBTTL    SUBROUTINE TO COMPARE TWO LOCATIONS BY THE SPREAD
1568
1569    006520    010046                    COMPAR:    MOV        R0,-(SP)                    ;SAVE R0
1570    006522    010146                               MOV        R1,-(SP)                    ;SAVE R1
1571    006524    013700    001124                               MOV        $C0DAT,R0                 ;GET EXPECTED VALUE
1572    006530    013701    001126                               MOV        $B0DAT,R1                 ;GET THE UNKNOWN
1573    006534    160100                               SUB        R1,R0                     ;SUBTRACT
1574    006536    100001                               BPL        B5                        ;
1575    006540    005400                               NEG        R0                        ;
1576    006542    020037    007016                    B5:        CMP        R0,SPREAD               ;TEST IF DIFFERENCE IF > SHAN SPREAD
1577    006546    003405                               BLE        10$                      ;
1578    006550    012601                    9$:        MOV        (SP)+,R1                   ;RESTORE R1
1579    006552    012600                               MOV        (SP)+,R0                   ;RESTORE R0
1580    006554    062716    000002                               ADD        #2,(SP)                   ;MAKE AN ERROR EXIT
1581    006560    000207                               RTS        PC                       ;EXIT
1582
1583    006562    012601                    10$:      MOV        (SP)+,R1                   ;
1584    006564    012600                               MOV        (SP)+,R0                   ;
1585    006566    000207                               RTS        PC                       ;EXIT FOR GOOD LIMIT TEST
1586
1587                    .SBTTL    FULL SCALE RAMP ON EACH RAMP
1588
1589    006570    012706    001100                    FULRMP:    MOV        #STACK,SP                 ;LOAD POINTER
1590    006574    004737    001746                               JSR        PC,LDTRAP                 ;LOAD BUS ADDRESS
1591    006600    013700    001422                    1$:        MOV        DAC0,R0                   ;GET BUS ADDRESS
1592    006604    004737    006642                               JSR        PC,10$                    ;LOAD THE RAMP ON DAC #1
1593    006610    013700    001424                               MOV        DAC1,R0                   ;GET BUS ADDRESS
1594    006614    004737    006642                               JSR        PC,10$                    ;LOAD THE RAMP ON DAC #1
1595    006620    013700    001426                               MOV        DAC2,R0                   ;GET BUS ADDRESS
1596    006624    004737    006642                               JSR        PC,10$                    ;LOAD THE RAMP ON DAC #2
1597    006630    013700    001430                               MOV        DAC3,R0                   ;GET THE BUS ADDRESS
1598    006634    004737    006642                               JSR        PC,10$                    ;LOAD THE RAMP ON DAC #3
1599    006640    000757                               BR        1$                        ;BR BACK
1600
1601    006642    005010                    10$:      CLR        (R0)                      ;CLEAR DAC
1602    006644    062710    000010                    11$:      ADD        #10,(R0)                  ;UPDATE THE DATA
1603    006650    005710                               TST        (R0)                      ;TEST IF DONE
1604    006652    001374                               BNE        11$                      ;BR IF NOT
1605    006654    000207                               RTS        PC                       ;EXIT

```

```

1606
1607
1608
1609 006656 012706 001100
1610 006662 004737 001746
1611 006666 104410
1612 006670 017700 172244
1613 006674 010077 172522
1614 006700 010077 172520
1615 006704 010077 172516
1616 006710 010077 172514
1617 006714 000764
1618
1619
1620
1621 006716 012706 001100
1622 006722 004737 001746
1623 006726 104410
1624 006730 017700 172204
1625 006734 004737 006750
1626 006740 005000
1627 006742 004737 006750
1628 006746 000767
1629
1630 006750 010077 172446
1631 006754 010077 172444
1632 006760 010077 172442
1633 006764 010077 172440
1634 006770 012700 000020
1635 006774 005300
1636 006776 100376
1637 007000 000207
1638
1639 007002 170440
1640 007004 000000
1641 007006 000004
1642 007010 000001
1643 007012 000000
1644 007014 000000
1645 007016 000000
1646 007020 000000
1647 007022 167772
1648 007024 167774
1649 007026 170500
1650 007030 170502
1651 007032 005744
1652 007034 000144
1653 007036 002034
1654
1655

.SBTTL STATIC DAC CALIBRATION
STATIC: MOV #STACK, SP ;LOAD STACK POINTER
        JSR PC, LDTRAP ;LOAD BUS ADDRESSES
1S:     CKSWR ;TEST FOR CTRL G
        MOV #SMR, RO ;READ SWITCHES
        MOV RO, #DAC0 ;LOAD DAC #0
        MOV RO, #DAC1 ;LOAD DAC #1
        MOV RO, #DAC2 ;LOAD DAC #2
        MOV RO, #DAC3 ;LOAD DAC #3
        BR 1S

.SBTTL DYNAMIC DAC CALIBRATION
DYNCAL: MOV #STACK, SP ;LOAD STACK POINTER
        JSR PC, LDTRAP ;LOAD BUS ADDRESSES
1S:     CKSWR ;TEST FOR CTRL G
        MOV #SMR, RO ;READ SMR
        JSR PC, 10S ;LOAD THE SMR VALUE TO ALL DACS
        CLR RO ;CLEAR RO
        JSR PC, 10S ;LOAD ALL DAC'S WITH 0
        BR 1S

10S:    MOV RO, #DAC0 ;LOAD DAC #0
        MOV RO, #DAC1 ;LOAD DAC #1
        MOV RO, #DAC2 ;LOAD DAC #2
        MOV RO, #DAC3 ;LOAD DAC #3
        MOV #20, RO ;LOAD DELAY COUNTER
11S:    DEC RO ;DELAY
        BPL 11S ;WAIT
        RTS PC ;EXIT

DACBAD: ABASE
BADUNT: 0
NUMBOK: 4
MASKNH: BIT0
TEMP: 0
STEMP: 0
SPREAD: 0
WFTST: 0
FILZ: 167772
DRIN: 167774
ADCS: 170500
ADBR: 170502
V5744: 5744
V144: 144
V2034: 2034

```

1656						
1657					.SBTTL	ASCII MESSAGES
1658						
1659	007040	005015	040412	053101	TITLE:	.ASCIZ <15><12><12>'RAV11 DIAGNOSTIC TEST, (MAINDEC-11-DVAAA-A0)'<<15><12>
1660	007046	030461	042040	040511		
1661	007054	047107	051517	044524		
1662	007062	020103	042524	052123		
1663	007070	020054	046450	044501		
1664	007076	042116	041505	030455		
1665	007104	026461	053104	040501		
1666	007112	026501	030101	006451		
1667	007120	000012				
1668	007122	052502	020123	044524	EM1:	.ASCIZ /BUS TIME-OUT WHEN REFERENCING A DAC ADDRESS/
1669	007130	042515	047455	052125		
1670	007136	053440	042510	020116		
1671	007144	042522	042506	042522		
1672	007152	041516	047111	020107		
1673	007160	020101	040504	020101		
1674	007166	042101	051104	051505		
1675	007174	000123				
1676	007176	040504	030103	051040	EM2:	.ASCIZ /DAC0 REGISTER IN ERROR/
1677	007204	043505	051511	042524		
1678	007212	020122	047111	042440		
1679	007220	051122	051117	000		
1680	007225	104	041501	020061	EM3:	.ASCIZ /DAC1 REGISTER IN ERROR/
1681	007232	042522	044507	052123		
1682	007240	051105	044440	020116		
1683	007246	051105	047522	000122		
1684	007254	040504	031103	051040	EM4:	.ASCIZ /DAC2 REGISTER IN ERROR/
1685	007262	043505	051511	042524		
1686	007270	020122	047111	042440		
1687	007276	051122	051117	000		
1688	007303	104	041501	020063	EM5:	.ASCIZ /DAC3 REGISTER IN ERROR/
1689	007310	042522	044507	052123		
1690	007316	051105	044440	020116		
1691	007324	051105	047522	000122		
1692	007332	042523	042514	052103	EM6:	.ASCIZ /SELECTED DAC OFFSET POT WAS ADJUSTED INCORRECTLY/
1693	007340	042105	042040	041501		
1694	007346	047440	043106	042523		
1695	007354	020124	047520	020124		
1696	007362	040527	020123	042101		
1697	007370	052512	052123	042105		
1698	007376	044440	041516	051117		
1699	007404	042522	052103	054514		
1700	007412	000				
1701	007413	123	046105	041505	EM7:	.ASCIZ /SELECTED DAC GAIN POT WAS ADJUSTED INCORRECTLY/
1702	007420	042524	020104	040504		
1703	007426	020103	040507	047111		
1704	007434	050040	052117	053440		
1705	007442	051501	040440	045104		
1706	007450	051525	042524	020104		
1707	007456	047111	047503	051122		
1708	007464	041505	046124	000131		
1709	007472	042523	042514	052103	EM10:	.ASCIZ /SELECTED DAC HAS A LINEARITY PROBLEM/

ASCII MESSAGES

```

1710 007500 042105 042040 041501
1711 007506 044040 051501 040440
1712 007514 046040 047111 040505
1713 007522 044522 054524 050040
1714 007530 047522 046102 046505
1715 007536 000000
1716 007537 000000 032461 053040
1717 007544 046117 020124 052523
1718 007552 050120 054514 044440
1719 007560 020123 047111 047503
1720 007566 051122 041505 000124
1721 007574 030455 020065 047526
1722 007602 052114 051440 050125
1723 007610 046120 020131 051511
1724 007616 044440 041516 051117
1725 007624 042522 052103 000000
1726 007631 041501 021440
1727 007638 020000 044504 044507
1728 007645 040000 020114 052517
1729 007653 050122 041040
1730 007660 051122 051117
1731 007667 044000 051122 051117
1732 007675 000000
1733 007682 000000 040527
1734 007690 040000 040527
1735 007697 050113 040527
1736 007705 050113 040527
1737 007712 051117 040527
1738 007720 040527 040527
1739 007727 040527 040527
1740 007735 040527 040527
1741 007742 040527 040527
1742 007750 040527 040527
1743 007757 040527 040527
1744 007765 040527 040527
1745 010000 047500 040527 040527
1746 010001 000000 040527 040527
1747 010002 000000 040527 040527
1748 010003 000000 040527 040527
1749 010004 000000 040527 040527
1750 010005 000000 040527 040527
1751 010006 000000 040527 040527
1752 010007 000000 040527 040527
1753 010008 000000 040527 040527
1754 010009 000000 040527 040527
1755 010010 000000 040527 040527
1756 010011 000000 040527 040527
1757 010012 000000 040527 040527
1758 010013 000000 040527 040527
1759 010014 000000 040527 040527
1760 010015 000000 040527 040527
1761 010016 000000 040527 040527
1762 010017 000000 040527 040527
1763 010018 000000 040527 040527

```

EM11: .ASCIZ /+15 VOLT SUPPLY IS INCORRECT/

EM12: .ASCIZ /-15 VOLT SUPPLY IS INCORRECT/

EM13: .ASCIZ /DAC #3 DIGITAL OUTPUT BITS IN ERROR/

EM14: .ASCIZ <?><?><?>/MAKE UP OPERATOR AND ADJUST THE POT/<?><?>

LDSPAC: .ASCIZ / DEPRESS THE "SPACE-BAR" WHEN DONE/

MSGSM: .ASCII <?><15><12>/TESTER SM1-2 AND SM1-5 ONLY MUST BE ON/

.ASCII <?><15><12>/SM2-2 SM2-4 AND SM2-5 MUST BE ON/

.ASCIZ <15><12><?>/CONNECT RAV11 TO J09 OF TESTER ONLY/<15><12>

1764	010106	052123	051105	047440	
1765	010107	046116	006531	000012	
1766	010108	021440	042440	051122	ERRTOT: .ASCIZ / # ERRORS/
1767	010204	051117	000123		
1768	010210	041040	042101	052440	MSGD: .ASCIZ / BAD UNITS /
1769	010216	044516	051524	000040	
1770	010221	015	01		FOUND1: .BYTE 15,12
1771	010226	051120	043	040522	.ASCIZ /PROGRAM DETECTED /
1772	010231	020111	043	040522	
1773	010236	034033	042101	000040	
1774	010241	034033	04451	040501	FOUND2: .ASCIZ /(8) AV11(S) /
1775	010246	030528	04051	024523	
1776	010251	020040	000		
1777	010257	105	051120	041520	DH1: .ASCIZ /ERRPC BUSADR EXPECT WAS/
1778	010274	041011	051522	042101	
1779	010300	020123	042440	050130	
1780	010310	041520	020123	020040	
1781	010316	040527	000123		
1782	010322	051105	050123	004503	DH2: .ASCIZ /ERRPC BUSADR/
1783	010330	022502	040523	051104	
1784	010336	000			
1785	010337	105	051122	041520	DH6: .ASCIZ /ERRPC BUSADR EXPECT WAS SPREAD/
1786	010344	041011	051522	042101	
1787	010352	004522	054105	042520	
1788	010360	052103	053411	051501	
1789	010366	051411	051120	040505	
1790	010374	000104			
1791	010376	005015	040440	045104	ADJR34: .ASCIZ <15><12>/ ADJUST R34 FOR A NULL /
1792	010404	051525	020123	031522	
1793	010412	020064	047506	020122	
1794	010420	020101	052516	046114	
1795	010426	020040	000		
1796	010431	015	020012	042101	ADJR35: .ASCIZ <15><12>/ ADJUST R35 FOR A NULL /
1797	010436	0052512	052123	051040	
1798	010444	032463	043040	051117	
1799	010452	040440	047040	046125	
1800	010460	020114	000040		
1801	010464	005015	040440	045104	ADJR36: .ASCIZ <15><12>/ ADJUST R36 FOR A NULL /
1802	010472	051525	020123	031522	
1803	010500	020066	047506	020122	
1804	010506	020101	052516	046114	
1805	010514	020040	000		
1806	010517	015	020012	042101	ADJR37: .ASCIZ <15><12>/ ADJUST R37 FOR A NULL /
1807	010521	0052512	052123	051040	
1808	010532	032463	043040	051117	
1809	010540	040440	047040	046125	
1810	010546	020114	000040		
1811	010550	005015	040440	045104	ADJR46: .ASCIZ <15><12>/ ADJUST R46 FOR A NULL /
1812	010556	051525	020123	032122	
1813	010566	020066	047506	020122	
1814	010574	020101	052516	046114	
1815	010580	020040	000		
1816	010589	015	020012	042101	ADJR47: .ASCIZ <15><12>/ ADJUST R47 FOR A NULL /
1817	010612	052512	052123	051040	

1818	010620	033464	043040	051117	
1819	010626	040440	047040	046125	
1820	010634	020114	000040		
1821	010640	005015	040440	045104	ADJR48: .ASCIZ <15><12>/ ADJUST R48 FOR A NULL /
1822	010646	051525	020124	032122	
1823	010654	020070	047506	020122	
1824	010662	020101	052516	046114	
1825	010670	020040	000		
1826	010673	015	020012	042101	ADJR49: .ASCIZ <15><12>/ ADJUST R49 FOR A NULL /
1827	010700	052512	052123	051040	
1828	010706	034464	043040	051117	
1829	010714	040440	047040	046125	
1830	010722	020114	000040		
1831					
1832	010726	005015	042523	042514	SELDO: .ASCIZ <15><12>/SELECT DAC0/
1833	010734	052103	042040	041501	
1834	010742	000060			
1835	010744	005015	042523	042514	SEL01: .ASCIZ <15><12>/SELECT DAC1/
1836	010752	052103	042040	041501	
1837	010760	000061			
1838	010762	005015	042523	042514	SEL02: .ASCIZ <15><12>/SELECT DAC2/
1839	010770	052103	042040	041501	
1840	010776	000062			
1841	011000	005015	042523	042514	SEL03: .ASCIZ <15><12>/SELECT DAC3/
1842	011006	052103	042040	041501	
1843	011014	000063			
1844	011016	005015	042101	052512	TRYAGN: .ASCIZ <15><12>/ADJUST THAT SAME POT AGAIN PLEASE/<15><12>
1845	011024	052123	052040	040510	
1846	011032	020124	040523	042515	
1847	011040	050040	052117	040440	
1848	011046	040507	047111	050040	
1849	011054	042514	051501	006505	
1850	011062	000012			
1851		000001			
1852		000003			
1853	011064	001			
1854	011065	116	030465	030062	NS1200: .BYTE STX .ASCII /NS12000V/
1855	011072	030060	126		
1856	011075	003	000		
1857	011077	001			
1858	011100	032520	030461	032467	PS1175: .BYTE ETX,0 .ASCII /PS11750V/
1859	011106	053060			
1860	011110	003	000		
1861					
1862	011112	001116	001126	000000	DT1: SERRPC, SBDDAT, 0
1863	011120	001116	001422	001124	DT2: SERRPC, DAC0, SGDDAT, SBDDAT, 0
1864	011126	001126	000000		
1865	011132	001116	001424	001124	DT3: SERRPC, DAC1, SGDDAT, SBDDAT, 0
1866	011146	001126	000000		
1867	011144	001116	001426	001124	DT4: SERRPC, DAC2, SGDDAT, SBDDAT, 0
1868	011152	001126	000000		
1869	011156	001116	001430	001124	DT5: SERRPC, DAC3, SGDDAT, SBDDAT, 0
1870	011164	001126	000000		
1871	011170	001116	007002	001124	DT6: SERRPC, DACBAD, SGDDAT, SBDDAT, SPREAD, 0

1872	011176	001126	007016	000000
1873	011204	000000	000000	000000
1874	011212	000000	000000	000000
1875	011220	000000	000000	000000

DF0: 0,0,0,0,0,0,0,0

.SBTTL BINARY TO ASCII AND TYPE ROUTINE

```

*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 16-BIT
*BINARY-ASCII NUMBER AND TYPE IT.
*CALL:

```

```

*      NOV    NUMBER,-(SP)    ;;NUMBER TO BE TYPED
*      TYPBN
*                               ;;TYPE IT

```

1886	011224	010146		
1887	011226	016601	000006	
1888	011230	000261		
1889	011234	112737	000060	011276
1890	011236	006101		
1891	011238	001406		
1892	011240	105537	011276	
1893	011242	104401	011276	
1894	011244	000241		
1895	011246	000765		
1896	011248	012601		
1897	011250	016666	000002	000004
1898	011252	012616		
1899	011254	000002		
1900	011276	000	000	

```

STYPBN: NOV    R1,-(SP)    ;;SAVE R1 ON THE STACK
        NOV    6(SP),R1    ;;GET THE INPUT NUMBER
        SEC    C           ;;SET "C" SO CAN KEEP TRACK OF THE NUMBER OF BITS
        NOV    0'0,SBIN    ;;SET CHARACTER TO AN ASCII "0".
        ROL    R1         ;;GET THIS BIT
        BEQ    2S         ;;DONE?
        ROR    SBIN        ;;NO--SET THE CHARACTER EQUAL TO THIS BIT
        TYPE    ,SBIN     ;;GO TYPE THIS BIT
        CLC             ;;CLEAR "C" SO CAN KEEP TRACK OF BITS
        BR     1S         ;;GO DO THE NEXT BIT
2S:     NOV    (SP)+,R1    ;;POP THE STACK INTO R1
        NOV    2(SP),4(SP) ;;ADJUST THE STACK
        NOV    (SP)+,(SP)
        RTI             ;;RETURN TO USER
SBIN:   .BYTE    0,0      ;;STORAGE FOR ASCII CHAR. AND TERMINATOR
.SBTTL  CONVERT BINARY TO DECIMAL AND TYPE ROUTINE

```

```

*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
*SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
*NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
*BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
*REPLACED WITH SPACES.
*CALL:

```

```

*      NOV    NUM,-(SP)    ;;PUT THE BINARY NUMBER ON THE STACK
*      TYPDS
*                               ;;GO TO THE ROUTINE

```

1913	011300			
1914	011300	010046		
1915	011300	010146		
1916	011300	010246		
1917	011300	010346		
1918	011310	010546		
1919	011312	012746	020200	
1920	011316	016606	000020	
1921	011322	100004		
1922	011324	005405		
1923	011326	112766	000055	000001
1924	011334	005000		
1925	011336	012703	011514	

```

STYPDS: NOV    R0,-(SP)    ;;PUSH R0 ON STACK
        NOV    R1,-(SP)    ;;PUSH R1 ON STACK
        NOV    R2,-(SP)    ;;PUSH R2 ON STACK
        NOV    R3,-(SP)    ;;PUSH R3 ON STACK
        NOV    R4,-(SP)    ;;PUSH R4 ON STACK
        NOV    R5,-(SP)    ;;PUSH R5 ON STACK
        NOV    020200,-(SP) ;;SET BLANK SWITCH AND SIGN
        NOV    20(SP),R5    ;;GET THE INPUT NUMBER
        BPL    1S         ;;BR IF INPUT IS POS.
        NEG    R5         ;;MAKE THE BINARY NUMBER POS.
        NOV    0'-,1(SP)   ;;MAKE THE ASCII NUMBER NEG.
        CLR    R0         ;;ZERO THE CONSTANTS INDEX
1S:     NOV    050BLK,R3   ;;SETUP THE OUTPUT POINTER

```

```

1926 011342 112723 000040          MOVB  8' ,(R3)+      ;; SET THE FIRST CHARACTER TO A BLANK
1927 011344 005002          CLR   R2            ;; CLEAR THE BCD NUMBER
1928 011346 016001 011504          MOV  SDTBL(R0),R1   ;; GET THE CONSTANT
1929 011348 160105          SUB  R1,R5         ;; FORM THIS BCD DIGIT
1930 011350 002402          BLT  4$           ;; BR IF DONE
1931 011352 005202          INC  R2            ;; INCREASE THE BCD DIGIT BY 1
1932 011354 000774          BR   3$           ;;
1933 011356 060105          4$:  ADD  R1,R5     ;; ADD BACK THE CONSTANT
1934 011358 005702          TST  R2            ;; CHECK IF BCD DIGIT=0
1935 011370 001002          BNE  5$           ;; FALL THROUGH IF 0
1936 011372 105716          TSTB (SP)         ;; STILL DOING LEADING 0'S?
1937 011374 100407          BMI  7$           ;; BR IF YES
1938 011376 106316          5$:  ASLB (SP)      ;; MSD?
1939 011400 103003          BCC  6$           ;; BR IF NO
1940 011402 116663 000001 177777  MOVB  1(SP),-1(R3)  ;; YES--SET THE SIGN
1941 011410 052702 000060 6$:  BIS  8'0,R2      ;; MAKE THE BCD DIGIT ASCII
1942 011414 052702 000040 7$:  BIS  8' ,R2      ;; MAKE IT A SPACE IF NOT ALREADY A DIGIT
1943 011420 110223          MOVB  R2,(R3)+     ;; PUT THIS CHARACTER IN THE OUTPUT BUFFER
1944 011422 005720          TST  (R0)+        ;; JUST INCREMENTING
1945 011424 020027 000010          CMP  R0,#10       ;; CHECK THE TABLE INDEX
1946 011426 002746          BEQ  8$           ;; GO DO THE NEXT DIGIT
1947 011428 003002          BGT  9$           ;; GO TO EXIT
1948 011430 010502          MOV  R5,R2        ;; GET THE LSD
1949 011432 000764          BR   6$           ;; GO CHANGE TO ASCII
1950 011434 105726          8$:  TSTB (SP)+     ;; WAS THE LSD THE FIRST NON-ZERO?
1951 011436 100003          BPL  9$           ;; BR IF NO
1952 011438 116663 177777 177776 9$:  MOVB  -1(SP),-2(R3) ;; YES--SET THE SIGN FOR TYPING
1953 011440 105701          CLRB (R3)         ;; SET THE TERMINATOR
1954 011442 012850          MOV  (SP)+,R5     ;; POP STACK INTO R5
1955 011444 012850          MOV  (SP)+,R3     ;; POP STACK INTO R3
1956 011446 012850          MOV  (SP)+,R2     ;; POP STACK INTO R2
1957 011448 012850          MOV  (SP)+,R1     ;; POP STACK INTO R1
1958 011450 012850          MOV  (SP)+,R0     ;; POP STACK INTO R0
1959 011452 104401 011514          TYPE SDBLK        ;; NOW TYPE THE NUMBER
1960 011454 016664 000002 000004          MOV  2(SP),4(SP)  ;; ADJUST THE STACK
1961 011456 000000          MOV  (SP)+,(SP)
1962 011458 023420          RTI
1963 011460 001750          SDTBL: 10000.
1964 011462 000144          1000.
1965 011464 000012          100.
1966 011466 000004          10.
1967 011468          SDBLK: BLKW 4
1968 011470          .SBTTL SCOPE HANDLER ROUTINE

```

```

1970 *****
1971 #THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
1972 #AND LOAD THE TEST NUMBER($STNUM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
1973 #AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
1974 #THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
1975 #SM14=1 LOOP ON TEST
1976 #SM11=1 INHIBIT ITERATIONS
1977 #SM09=1 LOOP ON ERROR
1978 #SM08=1 LOOP ON TEST IN SMR<7:0>
1979 #CALL

```

```

1980 ;# SCOPE ;;SCOPE=IOT
1981 $SCOPE:
1982 011524 104410 CKSMR ;;TEST FOR CHANGE IN SOFT-SMR
1983 011524 104410 CKSMR
1984 011530 032777 040000 167402 1$: BIT #BIT14,SMR ;;LOOP ON PRESENT TEST?
1985 011536 001114 BNE SOVER ;;YES IF SM14=1
1986 011540 000416 :####START OF CODE FOR THE XOR TESTER####
1987 011542 013746 000004 SXTSTR: BR 6$ IF RUNNING ON THE "XOR" TESTER CHANGE
1988 011546 012737 011566 000004 MOV 2#ERRVEC,-(SP) THIS INSTRUCTION TO A "NOP" (NOP=240)
1989 011554 005737 177060 TST 2#177060 SAVE THE CONTENTS OF THE ERROR VECTOR
1990 011560 012637 000004 MOV (SP)+,2#ERRVEC SET FOR TIMEOUT
1991 011564 000463 BR $SVLAD TIME OUT ON XOR?
1992 011566 022626 5$: CMP (SP)+,(SP)+ RESTORE THE ERROR VECTOR
1993 011570 012637 000004 MOV (SP)+,2#ERRVEC GO TO THE NEXT TEST
1994 011574 000423 BR 7$ CLEAR THE STACK AFTER A TIME OUT
1995 011576 032777 000400 167334 6$:;####END OF CODE FOR THE XOR TESTER#### RESTORE THE ERROR VECTOR
1996 011604 001404 BEQ 2$ LOOP ON SPEC. TEST?
1997 011606 127737 167326 001102 CMPS 2SMR,STSTNM BR IF NO
1998 011614 001465 BEQ SOVER ON THE RIGHT TEST? SMR<7:0>
1999 011616 105737 001103 2$: TSTB SERFLG BR IF YES
2000 011622 001421 BEQ 3$ HAS AN ERROR OCCURRED?
2001 011624 123737 001115 001103 CMPS SERMAX,SERFLG MAX. ERRORS FOR THIS TEST OCCURRED?
2002 011632 101015 BHI 3$ BR IF NO
2003 011634 032777 001000 167276 BIT #BIT09,SMR LOOP ON ERROR?
2004 011642 001404 BEQ 4$ BR IF NO
2005 011644 013737 001110 001106 7$: MOV SLPERR,SLPADR SET LOOP ADDRESS TO LAST SCOPE
2006 011652 000446 BR SOVER
2007 011654 105037 001103 4$: CLAB SERFLG ;;ZERO THE ERROR FLAG
2008 011660 005037 001160 CLR STIMES CLEAR THE NUMBER OF ITERATIONS TO MAKE
2009 011664 000415 BR 1$ ESCAPE TO THE NEXT TEST
2010 011666 032777 004000 167244 3$: BIT #BIT11,SMR INHIBIT ITERATIONS?
2011 011674 001011 BNE 1$ BR IF YES
2012 011676 005737 001202 TST SPASS IF FIRST PASS OF PROGRAM
2013 011702 001406 BEQ 1$ INHIBIT ITERATIONS
2014 011704 005237 001104 INC SICNT INCREMENT ITERATION COUNT
2015 011710 023737 001160 001104 CMP STIMES,SICNT CHECK THE NUMBER OF ITERATIONS MADE
2016 011716 002024 BGE SOVER BR IF MORE ITERATION REQUIRED
2017 011720 012737 000001 001104 1$: MOV #1,SICNT REINITIALIZE THE ITERATION COUNTER
2018 011726 013737 012004 001160 SSVLAD: MOV SPOCNT,STIMES SET NUMBER OF ITERATIONS TO DO
2019 011734 105237 001102 INCB STSTNM COUNT TEST NUMBERS
2020 011740 113737 001102 001200 MOVB STSTNM,STSTNM SET TEST NUMBER IN APT MAILBOX
2021 011746 011637 001106 MOV (SP),SLPADR SAVE SCOPE LOOP ADDRESS
2022 011752 011637 001110 MOV (SP),SLPERR SAVE ERROR LOOP ADDRESS
2023 011758 005037 001162 CLR ESCAPE CLEAR THE ESCAPE FROM ERROR ADDRESS
2024 011764 112737 000001 001115 MOVB #1,SERMAX ONLY ALLOW ONE(1) ERROR ON NEXT TEST
2025 011770 013777 001102 167144 SOVER: MOV STSTNM,DISPLAY DISPLAY TEST NUMBER
2026 011776 013716 001106 MOV SLPADR,(SP) FUDGE RETURN ADDRESS
2027 012002 000002 RTI FIXES PS
2028 012004 003720 SMXCNT: 2000. ;;MAX. NUMBER OF ITERATIONS
2029 .SBTTL ERROR HANDLER ROUTINE

```

2034
2035
2036
2037
2038
2039
2040
2041
2042
2043
2044
2045
2046
2047
2048
2049
2050
2051
2052
2053
2054
2055
2056
2057
2058
2059
2060
2061
2062
2063
2064
2065
2066
2067
2068
2069
2070
2071
2072
2073
2074
2075
2076
2077
2078
2079
2080
2081
2082
2083
2084
2085
2086
2087

012006	104410		
012006	053737	007010	007004
012010	105237	001103	
012016	001775		
012022	013777	001102	167110
012028	032777	002000	167100
012034	001402		
012040	104401	001164	
012046	005237	001112	
012052	011637	001116	
012058	162737	000002	001116
012064	117737	167026	001114
012070	032777	020000	167040
012100	001004		
012106	004737	012202	
012112	104401	001171	
012118	122737	000001	001214
012124	001007		
012130	113737	001114	012134
012136	004737	014162	
012142	000		
012148	000		
012154	000777		
012160	005777	166774	
012166	100002		
012172	000000		
012178	104410		
012184	032777	001000	166760
012190	001402		
012196	013716	001110	
012202	005737	001162	
012208	001402		
012214	013716	001162	
012220			
012226	000002		

```

*****
; THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,
; SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL
; AND GO TO SERRTYP ON ERROR
; THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:
; SM15=1      HALT ON ERROR
; SM13=1      INHIBIT ERROR TYPEOUTS
; SM10=1      BELL ON ERROR
; SM09=1      LOOP ON ERROR
; CALL
; ERROR      N      ;; ERROR=EMT AND N=ERROR ITEM NUMBER

```

```

SERROR:
        CKSMR      ;; TEST FOR CHANGE IN SOFT-SMR
        BIS        MASKNM,BADUNT
        INCB       SERFLG      ;; SET THE ERROR FLAG
        BEQ        7S         ;; DON'T LET THE FLAG GO TO ZERO
        MOV        $STNM,$DISPLAY  ;; DISPLAY TEST NUMBER AND ERROR FLAG
        BIT        @BIT10,@SMR  ;; BELL ON ERROR?
        BEQ        1S         ;; NO - SKIP
        TYPE       $BELL       ;; RING BELL
        INC        $ERTTL      ;; COUNT THE NUMBER OF ERRORS
        MOV        (SP),SERRPC  ;; GET ADDRESS OF ERROR INSTRUCTION
        SUB        @SERRPC,SITEMB
        MOVB       @SERRPC,SITEMB  ;; STRIP AND SAVE THE ERROR ITEM CODE
        BIT        @BIT13,@SMR  ;; SKIP TYPEOUT IF SET
        BNE        20S        ;; SKIP TYPEOUTS
        JSR        PC,SERRTYP  ;; GO TO USER ERROR ROUTINE
        TYPE       ,SCLF

        CMPB       @APTENV,SENV  ;; RUNNING IN APT MODE
        BNE        21S        ;; NO SKIP APT ERROR REPORT
        MOVB       SITEMB,21S  ;; SET ITEM NUMBER AS ERROR NUMBER
        JSR        PC,SATY4     ;; REPORT FATAL ERROR TO APT

        .BYTE     0
        .BYTE     0
        BR         22S
        TST        @SMR
        BPL        3S
        HALT
        CKSMR      ;; TEST FOR CHANGE IN SOFT-SMR
        BIT        @BIT09,@SMR  ;; LOOP ON ERROR SWITCH SET?
        BEQ        4S         ;; BR IF NO
        MOV        $LPERR,(SP)  ;; FUDGE RETURN FOR LOOPING
        TST        $ESCAPE     ;; CHECK FOR AN ESCAPE ADDRESS
        BEQ        5S         ;; BR IF NONE
        MOV        $ESCAPE,(SP) ;; FUDGE RETURN ADDRESS FOR ESCAPE

        RTI      ;; RETURN
.SBttl  ERROR MESSAGE TYPEOUT ROUTINE

```

```

*****
; THIS ROUTINE USES THE "ITEM CONTROL BYTE" (SITEMB) TO DETERMINE WHICH

```

;;ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (SERRTB),
;;AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

SERRTYP:

```

TYPE      SCRLF      ;; "CARRIAGE RETURN" & "LINE FEED"
MOV       R0,-(SP)    ;; SAVE R0
CLR       R0          ;; PICKUP THE ITEM INDEX
BISB     2(SITEMB,R0)
BNE      1S          ;; IF ITEM NUMBER IS ZERO, JUST
                          TYPE THE PC OF THE ERROR
MOV       SERRPC,-(SP) ;; SAVE SERRPC FOR TIMEOUT
                          ERROR ADDRESS
                          GO TYPE--OCTAL ASCII(ALL DIGITS)
                          GET OUT
1S:       BR         6S      ;; ADJUST THE INDEX SO THAT IT WILL
                          WORK FOR THE ERROR TABLE
                          FORM TABLE POINTER
                          PICKUP "ERROR MESSAGE" POINTER
                          SKIP TIMEOUT IF NO POINTER
                          TYPE THE "ERROR MESSAGE"
2S:       WORD      0      ;; "ERROR MESSAGE" POINTER GOES HERE
                          "CARRIAGE RETURN" & "LINE FEED"
3S:       MOV       (R0)+,4S  ;; PICKUP "DATA HEADER" POINTER
                          SKIP TIMEOUT IF 0
                          TYPE THE "DATA HEADER"
4S:       WORD      0      ;; "DATA HEADER" POINTER GOES HERE
                          "CARRIAGE RETURN" & "LINE FEED"
5S:       MOV       (R0),R0   ;; PICKUP "DATA TABLE" POINTER
                          GO TYPE THE DATA
6S:       MOV       (SP)+,R0  ;; RESTORE R0
                          "CARRIAGE RETURN" & "LINE FEED"
7S:       RTS       PC      ;; RETURN
                          SAVE 2(R0)+ FOR TIMEOUT
                          GO TYPE--OCTAL ASCII(ALL DIGITS)
                          IS THERE ANOTHER NUMBER?
                          BR IF NO
                          TYPE TWO(2) SPACES
                          LOOP
8S:       .ASCIZ   / /      ;; TWO(2) SPACES
                          .EVEN

```

.SBTTL POWER DOWN AND UP ROUTINES

POWER DOWN ROUTINE

```

SPWRON:  MOV       $STILLUP 2(PWRVEC) ;; SET FOR FAST UP
          MOV       $340 2(PWRVEC+2) ;; PRIO:7
          MOV       R0,-(SP)
          MOV       R1,-(SP)
          MOV       R2,-(SP)
          MOV       R3,-(SP)
          MOV       R4,-(SP)
          PUSH     R0 ON STACK
          PUSH     R1 ON STACK
          PUSH     R2 ON STACK
          PUSH     R3 ON STACK
          PUSH     R4 ON STACK

```

```

0088
0089
0090
0091
0092
0093
0094
0095
0096
0097
0098
0099
0100
0101
0102
0103
0104
0105
0106
0107
0108
0109
0110
0111
0112
0113
0114
0115
0116
0117
0118
0119
0120
0121
0122
0123
0124
0125
0126
0127
0128
0129
0130
0131
0132
0133
0134
0135
0136
0137
0138
0139
0140
0141
012202
012204
012206
012210
012212
012216
012220
012224
012226
012228
012230
012232
012234
012236
012240
012244
012250
012254
012258
012262
012266
012270
012272
012274
012300
012302
012304
012306
012312
012314
012314
012316
012320
012322
012324
012330
012332
012336
012344
012348
012352
012356
012360
012364
010401
010046
005000
153700
001004
013746
104402
000426
005300
006300
006300
006300
006300
062700
012037
001404
104401
000000
104401
012037
001404
104401
000000
104401
001171
012037
012272
001404
104401
000000
104401
001171
011000
001004
012600
104401
000207
013046
104402
005710
001770
104401
000771
020040
012336
012737
012737
010046
010146
010246
010346
010446
012502
000024
000340
000026

```

012364	010546		
012366	017746	166546	
012372	010637	012506	
012376	012737	012410	000024
012404	000000		
012406	000776		

```

MOV R5, -(SP)          ;; PUSH R5 ON STACK
MOV @SMR, -(SP)        ;; PUSH @SMR ON STACK
MOV SP, @SAVR6         ;; SAVE SP
MOV @SPWRUP, @@PWRVEC ;; SET UP VECTOR
HALT
BR -.2                 ;; HANG UP

```

012710	012737	012502	000024
012712	013706	012506	
012714	005037	012506	
012716	005237	012506	
012718	001375		
012720	012677	166500	
012722	012605		
012724	012604		
012726	012603		
012728	012602		
012730	012601		
012732	012600		
012734	012737	012336	000024
012736	012737	000340	000026
012738	104401		
012740	012510		
012742	012716		
012744	001450		
012746	000002		
012748	000000		
012750	000776		
012752	000000		
012754	005015	042522	052123
012756	051101	044524	043516
012758	040440	052106	051105
012760	040440	050040	053517
012762	051105	043040	044501
012764	052514	042522	005015
012766	000012		

```

*****
: POWER UP ROUTINE
SPWRUP: MOV @SILLUP, @@PWRVEC ;; SET FOR FAST DOWN
        MOV @SAVR6, SP      ;; GET SP
        CLR @SAVR6         ;; WAIT LOOP FOR THE TTY
15:     INC @SAVR6          ;; WAIT FOR THE INC
        BNE 15             ;; OF WORD
        MOV (SP)+, @SMR    ;; POP STACK INTO @SMR
        MOV (SP)+, R5      ;; POP STACK INTO R5
        MOV (SP)+, R4      ;; POP STACK INTO R4
        MOV (SP)+, R3      ;; POP STACK INTO R3
        MOV (SP)+, R2      ;; POP STACK INTO R2
        MOV (SP)+, R1      ;; POP STACK INTO R1
        MOV (SP)+, R0      ;; POP STACK INTO R0
        MOV @SPWRDN, @@PWRVEC ;; SET UP THE POWER DOWN VECTOR
        MOV @340, @@PWRVEC+2 ;; Prio: ?
        TYPE PWRMSG        ;; REPORT THE POWER FAILURE
        MOV (PC)+, (SP)    ;; POWER FAIL MESSAGE POINTER
        SPWRAD: .WORD BEGIN ;; RESTART AT BEGIN
        SILLUP: HALT      ;; RESTART ADDRESS
        BR -.2            ;; THE POWER UP SEQUENCE WAS STARTED
                          ;; BEFORE THE POWER DOWN WAS COMPLETE
        @SAVR6: 0         ;; PUT THE SP HERE
        PWRMSG: .ASCIZ <15><12>/RESTARTING AFTER A POWER FAILURE/<15><12><12>

```

.EVEN

.SBTTL BINARY TO OCTAL (ASCII) AND TYPE

```

*****
* THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
* OCTAL (ASCII) NUMBER AND TYPE IT.
* STYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
* CALL:
*     MOV NUM, -(SP)      ;; NUMBER TO BE TYPED
*     TYPOS              ;; CALL FOR TYPEOUT
*     .BYTE N            ;; N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*     .BYTE M            ;; M=1 OR 0
*                          ;; 1=TYPE LEADING ZEROS
*                          ;; 0=SUPPRESS LEADING ZEROS

```

012364
012366
012372
012376
012404
012406
012710
012712
012714
012716
012718
012720
012722
012724
012726
012728
012730
012732
012734
012736
012738
012740
012742
012744
012746
012748
012750
012752
012754
012756
012758
012760
012762
012764
012766
012768
012770
012772
012774
012776
012778
012780
012782
012784
012786
012788
012790
012792
012794
012796
012798
012800

2260	012762	012603			MOV	(SP)+,R3	::RESTURE R3
2261	012764	016666	000002	000004	MOV	2(SP),4(SP)	::SET THE STACK FOR RETURNING
2262	012772	012616			MOV	(SP)+,(SP)	
2263	012774	000002			RTI		::RETURN
2264	012776	000			BS:	.BYTE 0	::STORAGE FOR ASCII DIGIT
2265	012777	000				.BYTE 0	::TERMINATOR FOR TYPE ROUTINE
2266	013000	000			SOCNT:	.BYTE 0	::OCTAL DIGIT COUNTER
2267	013001	000			SOFILL:	.BYTE 0	::ZERO FILL SWITCH
2268	013002	000000			SOMODE:	WORD 0	::NUMBER OF DIGITS TO TYPE
2269					.SBTTL	TYPE ROUTINE	
2270					*****		
2271					#ROUTINE TO TYPE ASCIZ MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.		
2272					#THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.		
2273					#NOTE1: SNUL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.		
2274					#NOTE2: SFILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.		
2275					#NOTE3: SFILLC CONTAINS THE CHARACTER TO FILL AFTER.		
2276					#		
2277					#CALL:		
2278					#1) USING A TRAP INSTRUCTION		
2279						TYPE ,MESADR	::MESADR IS FIRST ADDRESS OF AN ASCIZ STRING
2280					#OR		
2281						TYPE	
2282						MESADR	
2283					#		
2284					#		
2285					#		
2286	013004	105737	001157		STYPE:	TSTB STPFLG	:: IS THERE A TERMINAL?
2287	013010	100002				BPL IS	:: BR IF YES
2288	013012	000000				HALT	:: HALT HERE IF NO TERMINAL
2289	013014	000430				BR 3S	:: LEAVE
2290	013016	010046			IS:	MOV RO,-(SP)	:: SAVE RO
2291	013020	017600	000002			MOV 22(SP),RO	:: GET ADDRESS OF ASCIZ STRING
2292	013024	122737	000001	001214		CMPSB 8APTENV,SENV	:: RUNNING IN APT MODE
2293	013032	001011				BNE 62S	:: NO GO CHECK FOR APT CONSOLE
2294	013034	132737	000100	001215		BITB 8APTSPool,SENVH	:: SPOOL MESSAGE TO APT
2295	013042	001405				BEG 62S	:: NO GO CHECK FOR CONSOLE
2296	013044	010037	013054			MOV RO,61S	:: SETUP MESSAGE ADDRESS FOR APT
2297	013050	004737	014152			JSR PC,SATY3	:: SPOOL MESSAGE TO APT
2298	013054	000000			61S:	WORD 0	:: MESSAGE ADDRESS
2299	013056	132737	000040	001215	62S:	BITB 8APTCsUP,SENVH	:: APT CONSOLE SUPPRESSED
2300	013064	001003				BNE 60S	:: YES,SKIP TYPE OUT
2301	013066	112046			2S:	MOVB (RO)+,-(SP)	:: PUSH CHARACTER TO BE TYPED ONTO STACK
2302	013070	001005				BNE 4S	:: BR IF IT ISN'T THE TERMINATOR
2303	013072	005726				TST (SP)+	:: IF TERMINATOR POP IT OFF THE STACK
2304	013074	012600			60S:	MOV (SP)+,RO	:: RESTORE RO
2305	013076	062716	000002		3S:	ADC 82,(SP)	:: ADJUST RETURN PC
2306	013102	000002				RTI	:: RETURN
2307	013104	122716	000011		4S:	CMPSB 8HT,(SP)	:: BRANCH IF <HT>
2308	013110	001430				BEG 8S	
2309	013112	122716	000200			CMPSB 8CRLF,(SP)	:: BRANCH IF NOT <CRLF>
2310	013116	001006				BNE 5S	
2311	013120	005726				TST (SP)+	:: POP <CR><LF> EQUIV
2312	013122	104401				TYPE	:: TYPE A CR AND LF
2313	013124	001171				SCRLF	

```

2304 013126 105037 013262 CLR B SCHARCNT :: CLEAR CHARACTER COUNT
2305 013132 000755 BR 25 :: GET NEXT CHARACTER
2306 013134 004737 013216 5S: JSR PC,STYPEC :: GO TYPE THIS CHARACTER
2307 013140 123726 001156 6S: CMP B SFILLC,(SP)+ :: IS IT TIME FOR FILLER CHARS.?
2308 013144 001350 BNE 25 :: IF NO GO GET NEXT CHAR.
2309 013146 013746 001154 MOV SNULL,-(SP) :: GET # OF FILLER CHARS. NEEDED
2310 :: AND THE NULL CHAR.
2311 013152 105366 000001 7S: DECB 1(SP) :: DOES A NULL NEED TO BE TYPED?
2312 013156 002770 BLT 65 :: BR IF NO--GO POP THE NULL OFF OF STACK
2313 013160 004737 013216 JSR PC,STYPEC :: GO TYPE A NULL
2314 013164 105337 013262 DECB SCHARCNT :: DO NOT COUNT AS A COUNT
2315 013170 000770 BR 75 :: LOOP

```

;HORIZONTAL TAB PROCESSOR

```

2319 013172 112716 000040 8S: MOVB 8'(SP) :: REPLACE TAB WITH SPACE
2320 013176 004737 013216 9S: JSR PC,STYPEC :: TYPE A SPACE
2321 013202 132737 000007 013262 BIT B 8',SCHARCNT :: BRANCH IF NOT AT
2322 013210 001372 BNE 9S :: TAB STOP
2323 013212 005726 TST (SP)+ :: POP SPACE OFF STACK
2324 013214 000724 BR 25 :: GET NEXT CHARACTER
2325 013216 105777 165726 STYPEC: TST B 2STPS :: WAIT UNTIL PRINTER IS READY
2326 013222 100375 BPL STYPEC
2327 013224 116677 000002 165720 MOVB 2(SP),2STPB :: LOAD CHAR TO BE TYPED INTO DATA REG.
2328 013232 122766 000015 000002 CMP B 8CR,2(SP) :: IS CHARACTER A CARRIAGE RETURN?
2329 013240 001003 BNE 1S :: BRANCH IF NO
2330 013242 105037 013262 CLRB SCHARCNT :: YES--CLEAR CHARACTER COUNT
2331 013246 000406 BR STYPEX :: EXIT
2332 013250 122766 000012 000002 1S: CMP B 8LF,2(SP) :: IS CHARACTER A LINE FEED?
2333 013256 001402 BEQ STYPEX :: BRANCH IF YES
2334 013260 105227 INCB (PC)+ :: COUNT THE CHARACTER
2335 013262 000000 SCHARCNT: WORD 0 :: CHARACTER COUNT STORAGE
2336 013264 000207 STYPEX: RTS PC

```

.SBTTL TTY INPUT ROUTINE

```

2338
2339 :: *****
2340 .ENABL LSB

```

```

2341 :: *****
2342 #SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
2343 #ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
2344 #SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
2345 #WHEN OPERATING IN TTY FLAG MODE.

```

```

2348 013266 022737 000176 001140 SCKSWR: CMP 8SMREG,SWR :: IS THE SOFT-SWR SELECTED?
2349 013274 001074 BNE 155 :: BRANCH IF NO
2350 013276 105777 165642 TST B 2STKS :: CHAR THERE?
2351 013302 100071 BPL 155 :: IF NO, DON'T WAIT AROUND
2352 013304 117746 165636 MOVB 2STKB,-(SP) :: SAVE THE CHAR
2353 013310 042716 177600 BIC 8IC177,(SP) :: STRIP-OFF THE ASCII
2354 013314 022726 000007 CMP 87,(SP)+ :: IS IT A CONTROL G?
2355 013320 001063 BNE 155 :: NO, RETURN TO USER
2356 013322 123727 001134 000001 CMP B SAUTOB,81 :: ARE WE RUNNING IN AUTO-MODE?
2357 013330 001456 BEQ 155 :: BRANCH IF YES

```

2358									
2359	013332	104401	014013		TYPE	,SCNTLG		:: ECHO THE CONTROL-G (↑G)	
2360	013336	104401	014020	SGTSWR:	TYPE	,SMSWR		:: TYPE CURRENT CONTENTS	
2361	013342	013746	000176		MOV	SWREG,-(SP)		:: SAVE SWREG FOR TYPEOUT	
2362	013346	104402			TYPOC			:: GO TYPE--OCTAL ASCII(ALL DIGITS)	
2363	013350	104401	014031		TYPE	,SMNEW		:: PROMPT FOR NEW SWR	
2364	013354	005046		19S:	CLR	-(SP)		:: CLEAR COUNTER	
2365	013356	005046			CLR	-(SP)		:: THE NEW SWR	
2366	013360	105777	165560	7S:	TSTB	STKS		:: CHAR THERE?	
2367	013364	100375			BPL	7S		:: IF NOT TRY AGAIN	
2368									
2369	013366	117746	165554		MOVB	STKB,-(SP)		:: PICK UP CHAR	
2370	013372	042716	177600		BIC	81C177,(SP)		:: MAKE IT 7-BIT ASCII	
2371									
2372									
2373									
2374	013376	021627	000025	9S:	CMF	(SP),#25		:: IS IT A CONTROL-U?	
2375	013402	001005			BNE	10S		:: BRANCH IF NOT	
2376	013404	104401	014006		TYPE	,SCNTLU		:: YES, ECHO CONTROL-U (↑U)	
2377	013410	062706	000006	20S:	ADD	86,SP		:: IGNORE PREVIOUS INPUT	
2378	013414	000757			BR	19S		:: LET'S TRY IT AGAIN	
2379									
2380									
2381	013416	021627	000015	10S:	CMF	(SP),#15		:: IS IT A <CR>?	
2382	013422	001022			BNE	16S		:: BRANCH IF NO	
2383	013424	005766	000004		TST	4(SP)		:: YES, IS IT THE FIRST CHAR?	
2384	013430	001403			BEQ	11S		:: BRANCH IF YES	
2385	013432	016677	000002	165500	MOV	2(SP),STSR		:: SAVE NEW SWR	
2386	013440	062706	000006	11S:	ADD	86,SP		:: CLEAR UP STACK	
2387	013444	104401	001171	14S:	TYPE	,SCALF		:: ECHO <CR> AND <LF>	
2388	013450	123727	001135	000001	CMFB	\$INTAG,#1		:: RE-ENABLE TTY KBD INTERRUPTS?	
2389	013456	001003			BNE	15S		:: BRANCH IF NOT	
2390	013460	012777	000100	165456	MOV	8100,STKS		:: RE-ENABLE TTY KBD INTERRUPTS	
2391	013466	000002		15S:	RTI			:: RETURN	
2392	013470	004737	013216	16S:	JSR	PC,STYPEC		:: ECHO CHAR	
2393	013474	021627	000060		CMF	(SP),#60		:: CHAR < 0?	
2394	013500	002420			BLT	18S		:: BRANCH IF YES	
2395	013502	021627	000067		CMF	(SP),#67		:: CHAR > 7?	
2396	013506	003015			BGT	18S		:: BRANCH IF YES	
2397	013510	042726	000060		BIC	860,(SP)+		:: STRIP-OFF ASCII	
2398	013514	005766	000002		TST	2(SP)		:: IS THIS THE FIRST CHAR	
2399	013520	001403			BEQ	17S		:: BRANCH IF YES	
2400	013522	006316			ASL	(SP)		:: NO, SHIFT PRESENT	
2401	013524	006316			ASL	(SP)		:: CHAR OVER TO MAKE	
2402	013526	006316			ASL	(SP)		:: ROOM FOR NEW ONE.	
2403	013530	005266	000002	17S:	INC	2(SP)		:: KEEP COUNT OF CHAR	
2404	013534	056616	177776		BIS	-2(SP),(SP)		:: SET IN NEW CHAR	
2405	013540	000707			BR	7S		:: GET THE NEXT ONE	
2406	013542	104401	001170	18S:	TYPE	,SQUES		:: TYPE ?<CR><LF>	
2407	013546	000720			BR	20S		:: SIMULATE CONTROL-U	
2408				.DSABL	LSB				
2409									
2410									
2411									

;;*****

013666
013665
013664
013663
013662
013661
013660
013659
013658
013657
013656
013655
013654
013653
013652
013651
013650
013649
013648
013647
013646
013645
013644
013643
013642
013641
013640
013639
013638
013637
013636
013635
013634
013633
013632
013631
013630
013629
013628
013627
013626
013625
013624
013623
013622
013621
013620
013619
013618
013617
013616
013615
013614
013613
013612
013611
013610
013609
013608
013607
013606
013605
013604
013603
013602
013601
013600
013599
013598
013597
013596
013595
013594
013593
013592
013591
013590
013589
013588
013587
013586
013585
013584
013583
013582
013581
013580
013579
013578
013577
013576
013575
013574
013573
013572
013571
013570

011646
016666 000004 000002
105777 165360
100375
117746 165354 000004
042716 177600 000004
022627 000004 000023
001013
105777 165326
100375
117746 165322
042716 177600
022627 000021
001356
000750
022627 000004 000140 35:
002407
022627 000004 000175
003003
042716 000040 000004
000002 45:

THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY

CALL:

RDCHR
RETURN HERE

INPUT A SINGLE CHARACTER FROM THE TTY
CHARACTER IS ON THE STACK
WITH PARITY BIT STRIPPED OFF

SRDCHR: MOV (SP), -(SP)
MOV 4(SP), 2(SP)
15: TSTB 2(STKS)
BPL 15
MOV 2(STKB), 4(SP)
BIC 8(C177), 4(SP)
CMP 4(SP), 823
BNE 25
TSTB 2(STKS)
BPL 25
MOV 2(STKB), -(SP)
BIC 8(C177), (SP)
CMP (SP)+, 821
BNE 25
BR 15
CMP 4(SP), 8140 35:
BLT 45
CMP 4(SP), 8175
BGT 45
BIC 840, 4(SP)
45: RTI

PUSH DOWN THE PC
SAVE THE PS
WAIT FOR
A CHARACTER
READ THE TTY
GET RID OF JUNK IF ANY
IS IT A CONTROL-5?
BRANCH IF NO
WAIT FOR A CHARACTER
LOOP UNTIL ITS THERE
GET CHARACTER
MAKE IT 7-BIT ASCII
IS IT A CONTROL-9?
IF NOT DISCARD IT
YES, RESUME
IS IT UPPER CASE?
BRANCH IF YES
IS IT A SPECIAL CHAR?
BRANCH IF YES
MAKE IT UPPER CASE
GO BACK TO USER

THIS ROUTINE WILL INPUT A STRING FROM THE TTY

CALL:

RDLIN
RETURN HERE

INPUT A STRING FROM THE TTY
ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
TERMINATOR WILL BE A BYTE OF ALL 0'S

SRDLIN: MOV R3, -(SP)
15: MOV 8(TTYIN), R3
25: CMP 8(TTYIN), 8., R3
BLOS 45
RDCHR
MOV (SP)+, (R3)
105: CMPB 8177, (R3)
BNE 35
TYPE 8QUES
BR 15
35: MOV (R3), 95
TYPE 95
CMPB 815, (R3)+
BNE 25
CLRB -1(R3)
TYPE 8LF
MOV (SP)+, R3
MOV (SP), -(SP)
MOV 4(SP), 2(SP)

SAVE R3
GET ADDRESS
BUFFER FULL?
BR IF YES
GO READ ONE CHARACTER FROM THE TTY
GET CHARACTER
IS IT A RUBOUT
SKIP IF NOT
TYPE A '??'
CLEAR THE BUFFER AND LOOP
ECHO THE CHARACTER

CHECK FOR RETURN
LOOP IF NOT RETURN
CLEAR RETURN (THE 15)
TYPE A LINE FEED
RESTORE R3
ADJUST THE STACK AND PUT ADDRESS OF THE
FIRST ASCII CHARACTER ON IT

```

013764 012766 013776 000004
013772 000002
013774 000
013776 000
013778 000010
014000 052536 005015 000
014013 136 006507 000012
014020 005015 053523 020122
014028 020079 000
014031 040 047040 053505
014036 036440 000040
    
```

```

MOV      #STTYIN,4(SP)
RTI
95:      .BYTE 0
          .BYTE 0
          .BLKB 8
          .ASCIZ /1U<15><12>
          .ASCIZ /1G<15><12>
          .ASCIZ <15><12>/SMR = /
          .ASCIZ / NEW = /
    
```

```

::RETURN
::STORAGE FOR ASCII CHAR. TO TYPE
::TERMINATOR
::RESERVE 8 BYTES FOR TTY INPUT
::CONTROL "U"
::CONTROL "G"
    
```

```

.SBTTL READ AN OCTAL NUMBER FROM THE TTY

::*****
::THIS ROUTINE WILL READ AN OCTAL (ASCII) NUMBER FROM THE TTY AND
::CHANGE IT TO BINARY.
::CALL:
::      RDOCT
::      RETURN HERE
::      READ AN OCTAL NUMBER
::      LOW ORDER BITS ARE ON TOP OF THE STACK
::      HIGH ORDER BITS ARE IN SHIOCT
    
```

```

01404 011646 000004 000002
01404 016666
01404 010046
01405 010146
01406 010246
01406 104412
01406 012600
01406 005001
01406 005002
01407 112046
01407 001412
01407 006301
01407 006102
01410 006301
01410 006102
01410 006301
01410 006102
01411 042716 177770
01411 062601
01411 000764
01412 005709
01412 010166 000012
01412 010237 014142
01412 012602
01412 012601
01412 012600
01415 000002
01415 000000
    
```

```

SRDOCT: MOV      (SP),-(SP)
          MOV      4(SP),2(SP)
          MOV      R0,-(SP)
          MOV      R1,-(SP)
          MOV      R2,-(SP)
15:      ROLIN
          MOV      (SP)+,R0
          CLR      R1
          CLR      R2
25:      MOV      (R0)+,-(SP)
          BEQ      35
          RSL      R1
          RSL      R2
          RSL      R1
          RSL      R2
          RSL      R1
          RSL      R2
          BIC      81C7,(SP)
          ROR      (SP)+,R1
          BR      25
35:      TST      (SP)+
          MOV      R1,12(SP)
          MOV      R2,SHIOCT
          MOV      (SP)+,R2
          MOV      (SP)+,R1
          MOV      (SP)+,R0
          RTI
SHIOCT: .WORD 0
.SBTTL APT COMMUNICATIONS ROUTINE
    
```

```

::PROVIDE SPACE FOR THE
::INPUT NUMBER
::PUSH R0 ON STACK
::PUSH R1 ON STACK
::PUSH R2 ON STACK
::READ AN ASCII LINE
::GET ADDRESS OF 1ST CHARACTER
::CLEAR DATA WORD
::PICKUP THIS CHARACTER
::IF ZERO GET OUT
::#2
::#4
::#8
::STRIP THE ASCII JUNK
::ADD IN THIS DIGIT
::LOOP
::CLEAN TERMINATOR FROM STACK
::SAVE THE RESULT
::POP STACK INTO R2
::POP STACK INTO R1
::POP STACK INTO R0
::RETURN
::HIGH ORDER BITS GO HERE
    
```

```

01414 112737 000001 014410
01415 112737 000001 014406
    
```

```

::*****
SATY1: MOV      81,SFFLG
SATY3: MOV      81,SNFLG
    
```

```

::TO REPORT FATAL ERROR
::TO TYPE A MESSAGE
    
```

```

014160 000403 BR SATYC
014162 112737 000001 014410 SATY4: MOVB #1,SFFLG ;;TO ONLY REPORT FATAL ERROR
014170 SATYC: MOV RD,-(SP) ;;PUSH RD ON STACK
014172 010046 MOV R1,-(SP) ;;PUSH R1 ON STACK
014174 105737 014406 TSTB SMFLG ;;SHOULD TYPE A MESSAGE?
014200 001450 BEQ 55 ;;IF NOT: BR
014202 122737 000001 001214 CMPB #APTENV,SENV ;;OPERATING UNDER APT?
014210 001031 BNE 35 ;;IF NOT: BR
014212 132737 000100 001215 BITB #APTPOOL,SENVH ;;SHOULD SPOOL MESSAGES?
014220 001425 BEQ 35 ;;IF NOT: BR
014222 017600 000004 MOV #4(SP),RD ;;GET MESSAGE ADDR.
014224 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDR.
014226 005737 001174 15: TST SMSGTYPE ;;SEE IF DONE W/ LAST XMISSION?
014228 001375 BNE 15 ;;IF NOT: WAIT
014230 010037 001210 NOV RD,SMSGAD ;;PUT ADDR IN MAILBOX
014232 105720 25: TSTB (RD)+ ;;FIND END OF MESSAGE
014234 001376 BNE 25
014236 163700 001210 SUB SMSGAD,RD ;;SUB START OF MESSAGE
014238 006200 ASR RD ;;GET MESSAGE LNTH IN WORDS
014240 010037 001212 NOV RD,SMSG LGT ;;PUT LENGTH IN MAILBOX
014242 012737 000004 001174 NOV #4,SMSGTYPE ;;TELL APT TO TAKE MSG.
014244 000413 BR 55
014246 017637 000004 014320 35: NOV #4(SP),4S ;;PUT MSG ADDR IN JSR LINKAGE
014248 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDRESS
014250 013746 177776 NOV 177776,-(SP) ;;PUSH 177776 ON STACK
014252 004737 013004 JSR PC,STYPE ;;CALL TYPE MACRO
014254 000000 45: .WORD 0
014256 55:
014258 105737 014410 105: TSTB SFFLG ;;SHOULD REPORT FATAL ERROR?
014260 001416 BEQ 125 ;;IF NOT: BR
014262 005737 001214 TST SENV ;;RUNNING UNDER APT?
014264 001413 BEQ 125 ;;IF NOT: BR
014266 005737 001174 115: TST SMSGTYPE ;;FINISHED LAST MESSAGE?
014268 001375 BNE 115 ;;IF NOT: WAIT
014270 017637 000004 001176 NOV #4(SP),SFATAL ;;GET ERROR #
014272 062766 000002 000004 ADD #2,4(SP) ;;BUMP RETURN ADDR.
014274 005237 001174 INC SMSGTYPE ;;TELL APT TO TAKE ERROR
014276 105037 014410 125: CLRB SFFLG ;;CLEAR FATAL FLAG
014278 105037 014407 CLRB SLFLG ;;CLEAR LOG FLAG
014280 105037 014406 CLRB SMFLG ;;CLEAR MESSAGE FLAG
014400 012601 MOV (SP)+,R1 ;;POP STACK INTO R1
014402 012600 MOV (SP)+,R0 ;;POP STACK INTO R0
014404 000207 RTS ;;RETURN
014406 000 SMFLG: .BYTE 0
014407 000 SLFLG: .BYTE 0
014410 000 SFFLG: .BYTE 0
014412 .EVEN
000200 APTSIZE=200
000001 APTENV=001
000100 APTPOOL=100
000040 APTCSLP=040

```

.SBTTL TRAP DECODER

;THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
;AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
;OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
;GO TO THAT ROUTINE.

014412 010046
014414 016600 000002
014416 005740
014418 111000
014420 006300
014422 016000 014446
014424 000200

STRAP: MOV RO, -(SP) ;SAVE RO
MOV 2(SP), RO ;GET TRAP ADDRESS
TST -(RO) ;BACKUP BY 2
MOVB (RO), RO ;GET RIGHT BYTE OF TRAP
ASL RO ;POSITION FOR INDEXING
MOV STRPAD(RO), RO ;INDEX TO TABLE
RTS RO ;GO TO ROUTINE

;;THIS IS USE TO HANDLE THE "GETPRI" MACRO

014434 011646
014436 016666 000004 000002
014444 000002

STRAP2: MOV (SP), -(SP) ;MOVE THE PC DOWN
MOV 4(SP), 2(SP) ;MOVE THE PSW DOWN
RTI ;RESTORE THE PSW

.SBTTL TRAP TABLE

;THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
;BY THE "TRAP" INSTRUCTION.

	ROUTINE		
014446	014434	STRPAD: .WORD	STRAP2
014448	013004	STYPE	::CALL=TYPE TRAP+1(104401) TTY TYPEOUT ROUTINE
014450	012802	STYPOC	::CALL=TYPOC TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
014452	012556	STYPOS	::CALL=TYPOS TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
014454	012616	STYPON	::CALL=TYPON TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
014456	011300	STYPDS	::CALL=TYPDS TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)
014458	011224	STYPBN	::CALL=TYPBN TRAP+6(104406) TYPE BINARY (ASCII) NUMBER
014464	013336	SGTSNR	::CALL=GTSNR TRAP+7(104407) GET SOFT-SNR SETTING
014466	013266	SCKSNR	::CALL=CKSNR TRAP+10(104410) TEST FOR CHANGE IN SOFT-SNR
014470	013550	SROCHR	::CALL=ROCHR TRAP+11(104411) TTY TYPEIN CHARACTER ROUTINE
014472	013670	SROLIN	::CALL=ROLIN TRAP+12(104412) TTY TYPEIN STRING ROUTINE
014474	014042	SRODOCT	::CALL=RDOCT TRAP+13(104413) READ AN OCTAL NUMBER FROM TTY
	000001		

.END

014412 010046
014414 016600
014416 005740
014418 111000
014420 006300
014422 016000
014424 000200
014434 011646
014436 016666
014444 000002
014446 014434
014448 013004
014450 012802
014452 012556
014454 012616
014456 011300
014458 011224
014464 013336
014466 013266
014470 013550
014472 013670
014474 014042
000001

ABASE = 170440
 ACDM1 = 000000
 ACDM2 = 000000
 ACPUOP = 000000
 ADOR = 007030
 ADCS = 007026
 ADDOK = 001440
 ADDM0 = 000000
 ADDM1 = 000000
 ADDM10 = 000000
 ADDM11 = 000000
 ADDM12 = 000000
 ADDM13 = 000000
 ADDM14 = 000000
 ADDM15 = 000000
 ADDM16 = 000000
 ADDM17 = 000000
 ADDM18 = 000000
 ADDM19 = 000000
 ADDM20 = 000000
 ADDM21 = 000000
 ADDM22 = 000000
 ADDM23 = 000000
 ADDM24 = 000000
 ADDM25 = 000000
 ADDM26 = 000000
 ADDM27 = 000000
 ADDM28 = 000000
 ADDM29 = 000000
 ADEVCT = 000000
 ADEVH = 000000
 ADJR34 = 010376
 ADJR35 = 010431
 ADJR36 = 010464
 ADJR37 = 010517
 ADJR46 = 010552
 ADJR47 = 010605
 ADJR48 = 010640
 ADJR49 = 010673
 AENV = 000000
 AENVH = 000000
 AFATAL = 000000
 AMPDR1 = 000000
 AMPDR2 = 000000
 AMPDR3 = 000000
 AMPDR4 = 000000
 AMPAS1 = 000000
 AMPAS2 = 000000
 AMPAS3 = 000000
 AMPAS4 = 000000
 AMSCAD = 000000
 AMSCLC = 000000
 AMSGTY = 000000
 AMTYP1 = 000000
 AMTYP2 = 000000
 AMTYP3 = 000000
 AMTYP4 = 000000
 APASS = 000000
 APRIOR = 000000
 APTCSU = 000040

4108
 11
 2289

526
 569
 541
 16508
 15298
 6698
 17918
 18118
 25718

567
 663
 664
 665
 666
 1639
 1530
 16498

N05

MAINDEC-11-DVAAA-A
DVAAA.P11

AAV11
CROSS REFERENCE

DIAGNOSTIC
TABLE

MACY11 27(665) 12-OCT-76 13:42 PAGE 65

	964#	978#	994#	1011#	1022#	1033#	1047#	1063#	1079#	1110#	1123#	1137#	1149#
	1163#	1180#	1188#	1207#	1220#	1233#	1247#	1260#	1268#	1282#	1295#	1303#	1317#
	1330#	1338#	1352#	1365#									
\$OCNT 013000	2214#	2243#	2256#										
\$OMODE 013002	2209#	2213#	2218	2221*	2232*	2258#							
\$OVER 011770	1986	2002	2010	2020	2029#								
\$PASS 001202	531#	713*	1238	1252	1273	1287	1308	1322	1343	1357	1385*	1386*	1394
	1407	2016	2033										
\$PASTM 001006	476#												
\$PMRAD 012476	2168#												
\$PMRDN 012336	690	2135#	2163										
\$PMRNG 012472	2166#												
\$PMRUP 012410	2145	2151#											
\$QUES 001170	519#	2084	2338	2406	2455	2471							
\$ROCHR 013550	2419#	2614											
\$RODEC= ***** U	2617												
\$ROLIN 013670	2447#	2615											
\$RODOCT 014042	2487#	2616											
\$RODSZ = 000010	2440#												
\$RTNAD 005554	1406#												
\$R2A = ***** U	2617												
\$SAVRE= ***** U	2617												
\$SAVR6 012506	2144#	2152	2153#	2154#	2172#								
\$SCOPE 011524	684	1982#											
\$SETUP= 000117	588#	676#	683	684	686	688	690	692	693	695	1383	1983	2048
	2075	2083	2343	2477									
\$STUP = 177777	588#	676#											
\$SVLAD 011734	1994	2023#											
\$SVPC = 000106	450#	455											
\$SMR = 167400	289#	299	416	417	418	419	420	421	422	516	517	518	692
	693	695	696	799	814	825	836	849	865	881	891	901	914
	930	946	957	968	982	998	1015	1026	1037	1051	1067	1083	1114
	1127	1141	1153	1167	1184	1192	1211	1224	1237	1251	1264	1272	1286
	1299	1307	1321	1334	1342	1356	1369	1378	1384	1399	1405	1407	1974
	1975	1976	1977	1978	1985	1997	1999	2000	2003	2004	2005	2012	2013
	2014	2026	2029	2032	2039	2040	2041	2042	2043	2053	2060	2072	2076
	2084	2169											
\$SMREG 001216	539#	716											
\$SMRHK= 000000	422	423	1978	1979	2001								
\$STEMP 007014	1193#	1196	1204#	1644#									
\$TESTN 001200	530#	2024#											
\$TIMES 001160	516#	692#	836#	849#	865#	901#	914#	930#	968#	982#	998#	1037#	1051#
	1067#	1114#	1127#	1141#	1153#	1167#	1184#	1192#	1211#	1224#	1237#	1251#	1264#
	1272#	1286#	1299#	1307#	1321#	1334#	1342#	1356#	1369#	1384#	2012#	2019	2022#
	2032												
\$TKB 001146	509#	1559	2341	2352	2369	2423	2429						
\$TKS 001144	508#	1551	2341	2350	2366	2390#	2421	2427					
\$TN = 000054	289#	299	772	795	799#	810	814#	818	821	825#	829	832	836#
	845	849#	861	865#	877	881#	885	887	891#	895	897	901#	910
	914#	926	930#	942	946#	950	953	957#	961	964	968#	978	982#
	994	998#	1011	1015#	1019	1022	1026#	1030	1033	1037#	1047	1051#	1063
	1067#	1079	1083#	1108	1110	1114#	1120	1123	1127#	1133	1137	1141#	1146
	1149	1153#	1159	1163	1167#	1170	1180	1184#	1186	1188	1192#	1207	1211#
	1217	1220	1224#	1230	1233	1237#	1239	1247	1251#	1253	1260	1264#	1268

ADJR	17918	1796	1801	1806	1811	1816	1821	1826							
COMEN	18	4098													
DYNIC	6678	845	910	978	1047										
ENDCOM	18	4098													
ERROR	3038	771	790	806	819	830	842	856	873	886	896	907	921	938	951
	962	974	989	1006	1020	1031	1043	1058	1075	1091	1097	1103	1109	1121	1134
	1147	1160	1201	1218	1231	1443	1469	1491	1557						
ESCAPE	18	4098													
GETPRY	18	4098													
GETSAR	18	4098													
MULT	18	4098													
NEWTST	18	4098	795	810	821	832	845	861	877	887	897	910	926	942	953
	964	978	994	1011	1022	1033	1047	1063	1079	1110	1123	1137	1149	1163	1180
	1188	1207	1220	1233	1247	1260	1268	1282	1295	1303	1317	1330	1338	1352	1365
POP	18	4098	1954	2155	2157	2510	2551	2562							
PUSH	18	4098	1913	2137	2143	2489	2522	2524	2545						
REPORT	18	4098													
SCOPE	3048	798	813	824	835	848	864	880	890	900	913	929	945	956	967
	981	997	1014	1029	1036	1050	1066	1082	1113	1126	1140	1152	1166	1183	1191
	1210	1223	1236	1250	1263	1271	1285	1298	1306	1320	1333	1341	1355	1368	1382
SELD	18328	1835	1838	1841											
SETPRI	18	4098													
SETTRA	2598	2605	2606	2607	2608	2609	2611	2613	2614	2615	2616				
SETUP	18	4098	678												
SKIP	18	4098	767	772	787	789	804	818	828	841	854	872	876	885	895
	906	919	937	941	950	961	973	987	1005	1009	1019	1030	1042	1056	1074
	1078	1090	1096	1102	1108	1120	1133	1146	1159	1170	1186	1200	1217	1230	1239
	1253	1274	1288	1309	1323	1344	1358	1412	1468	1490	1494				
SLASH	18	4098													
SPACE	4098														
STARS	18	4098	448	459	461	468	481	522	526	795	797	810	812	821	823
	832	834	845	847	861	868	877	892	895	899	899	910	910	912	924
	928	944	944	953	955	963	966	979	980	994	996	1011	1013	1022	1026
	1033	1035	1047	1049	1063	1065	1079	1081	1110	1112	1123	1125	1137	1139	1149
	1151	1163	1165	1180	1182	1188	1199	1207	1209	1220	1223	1233	1235	1247	1249
	1260	1262	1268	1270	1282	1287	1298	1307	1309	1320	1317	1333	1335	1347	1349
	1340	1352	1354	1365	1367	1374	1379	1383	1383	1395	1317	1319	1330	1332	1338
	2340	2343	2411	2440	2479	2517	2575	1903	1970	2095	2086	2133	2149	2184	2261
SUBTST	6678	861	926	994	1063										
SUPER	11808	1233	1268	1303	1338										
SURSU	18	4098	6978												
TAMTRP	2598														
TYPBIN	18	4098	1418												
TYPDEC	18	4098	1394												
TYPNAM	18	4098													
TYPNUM	18	4098													
TYPOCS	18	4098													
TYPOCT	18	4098	2098	2122	2361										
TYPTXT	18	4098													
SSCHRE	4798														
SSCHTH	4798														
SSESCA	18	4098													
SSMENT	18	4098	795	810	821	832	845	861	877	887	897	910	926	942	953
	964	978	994	1011	1022	1033	1047	1063	1079	1110	1123	1137	1149	1163	1180

E06

MAINDEC-11-DVAAA-A
DVAAA.P11

AAV11
CROSS REFERENCE

DIAGNOSTIC
TABLE

MACY11 27(665) 12-OCT-76 13:42 PAGE 69

ROCB	1893														
ROO	739	740	761	1172	1173	1174	1175	1532	1580	1602	1933	2106	2210	2220	
ROA	2377	2386	2505	2532	2544	2556									
ROB	2103	2104	2105	2400	2401	2402	2498	2500	2502	2585					
ROE	858	859	908	923	924	975	991	992	1044	1060	1061	1203	1204	1533	
ROF	1538	2539													
ROC	749	789	818	829	841	854	872	885	895	906	919	937	950	961	
BOC	987	1005	1019	1030	1042	1056	1074	1090	1096	1102	1108	1120	1133	1146	
BOE	1159	1170	1200	1398	1503	1891	2000	2002	2004	2008	2017	2051	2054	2080	
BOF	2108	2113	2126	2237	2285	2298	2333	2357	2384	2399	2497	2526	2530	2550	
BGC	2020														
BGT	1389	1947	2244	2396	2437										
BHT	2006														
BIC	1198	1386	1512	1525	1560	2234	2353	2370	2397	2424	2430	2438	2504		
BIS	786	1508	1941	1942	2049	2239	2240	2404							
BISB	1516	2095													
BIT	1985	1999	2007	2014	2053	2060	2076								
BITB	714	2284	2289	2321	2529										
BLF	1577														
BLOS	2450														
BFL	1930	1946	2245	2312	2394	2435									
BFI	764	774	1552	1937											
BMI	681	704	729	745	747	766	770	776	844	860	876	909	925	941	
BML	993	1009	1045	1062	1078	1186	1205	1239	1274	1288	1309	1323	1344	1358	
BPL	1413	1494	1507	1511	1518	1534	1554	1556	1604	1935	1986	2015	2061	2066	
BPL	2096	2118	2155	2235	2283	2290	2292	2300	2322	2329	2349	2355	2375	2382	
BPL	2389	2426	2432	2454	2460	2528	2534	2537	2551						
BPL	1531	1574	1636	1921	1951	2073	2233	2277	2326	2351	2367	2422	2428	1490	
BPL	668	670	706	767	772	787	804	1217	1230	1442	1446	1468	1472	1513	
BPL	1558	1599	1617	1628	1895	1932	1949	1988	1994	1997	2010	2013	2071	2128	
BPL	2147	2171	2211	2226	2247	2279	2305	2315	2324	2331	2378	2405	2407	2456	
BPL	2506	2520	2542												
BPL	1535														
BPL	1894														
BPL	673	674	679	692	693	713	718	725	757	758	791	792	814	881	
BPL	1015	1116	1129	1143	1155	1177	1383	1384	1527	1550	1601	1626	1924	1927	
BPL	2012	2094	2153	2224	2364	2365	2494	2495							
BPL	1953	2304	2330	2461	2558	2559	2560								
BPL	680	728	765	768	805	817	828	840	853	871	884	894	905	918	
BPL	949	960	972	986	1004	1018	1029	1041	1055	1073	1089	1095	1101	1107	
BPL	1132	1199	1561	1576	1945	1995	2019	2348	2354	2374	2381	2393	2395	2425	
BPL	2434	2436	2449												
BPL	1169	2001	2005	2065	2282	2297	2299	2307	2328	2332	2356	2388	2453	2459	
BPL	788														
BPL	1506	1510	1517	1553	1555	1635	2102								
BPL	2243	2311	2314												
BPL	2074	2146	2170	2278											
BPL	762	1168	1171	1385	1931	2018	2056	2154	2238	2246	2403	2557			
BPL	2023	2050	2334												
BPL	434	435	436	439	442	719	808	1178	1187	1405	1420				

.IFF

.IFT
.IFTF
.IFF

.IRP

.LIST

.MACRO
.MCALL
.NEXT
.NLIST

1985	1997	1999	2000	2001	2003	2004	2005	2014	2016	2024	2026	2031	2032	2033
2035	2038	2049	2053	2060	2063	2065	2072	2076	2083	2084	2086	2101	2101	2117
2133	2143	2144	2149	2156	2157	2167	2169	2173	2184	2184	2261	2282	2340	2342
2433	2443	2444	2449	2456	2440	2448	2453	2454	2470	2470	2471	2477	2479	2482
2596	2604	2605	2606	2607	2608	2609	2610	2611	2612	2613	2614	2615	2616	2617
2611	2613	2614	2615	2616	2618	2619	2620	2621	2622	2623	2624	2625	2626	2627
2628	2629	2630	2631	2632	2633	2634	2635	2636	2637	2638	2639	2640	2641	2642
2643	2644	2645	2646	2647	2648	2649	2650	2651	2652	2653	2654	2655	2656	2657
2658	2659	2660	2661	2662	2663	2664	2665	2666	2667	2668	2669	2670	2671	2672
2673	2674	2675	2676	2677	2678	2679	2680	2681	2682	2683	2684	2685	2686	2687
2688	2689	2690	2691	2692	2693	2694	2695	2696	2697	2698	2699	2700	2701	2702
2703	2704	2705	2706	2707	2708	2709	2710	2711	2712	2713	2714	2715	2716	2717
2718	2719	2720	2721	2722	2723	2724	2725	2726	2727	2728	2729	2730	2731	2732
2733	2734	2735	2736	2737	2738	2739	2740	2741	2742	2743	2744	2745	2746	2747
2748	2749	2750	2751	2752	2753	2754	2755	2756	2757	2758	2759	2760	2761	2762
2763	2764	2765	2766	2767	2768	2769	2770	2771	2772	2773	2774	2775	2776	2777
2778	2779	2780	2781	2782	2783	2784	2785	2786	2787	2788	2789	2790	2791	2792
2793	2794	2795	2796	2797	2798	2799	2800	2801	2802	2803	2804	2805	2806	2807
2808	2809	2810	2811	2812	2813	2814	2815	2816	2817	2818	2819	2820	2821	2822
2823	2824	2825	2826	2827	2828	2829	2830	2831	2832	2833	2834	2835	2836	2837
2838	2839	2840	2841	2842	2843	2844	2845	2846	2847	2848	2849	2850	2851	2852
2853	2854	2855	2856	2857	2858	2859	2860	2861	2862	2863	2864	2865	2866	2867
2868	2869	2870	2871	2872	2873	2874	2875	2876	2877	2878	2879	2880	2881	2882
2883	2884	2885	2886	2887	2888	2889	2890	2891	2892	2893	2894	2895	2896	2897
2898	2899	2900	2901	2902	2903	2904	2905	2906	2907	2908	2909	2910	2911	2912
2913	2914	2915	2916	2917	2918	2919	2920	2921	2922	2923	2924	2925	2926	2927
2928	2929	2930	2931	2932	2933	2934	2935	2936	2937	2938	2939	2940	2941	2942
2943	2944	2945	2946	2947	2948	2949	2950	2951	2952	2953	2954	2955	2956	2957
2958	2959	2960	2961	2962	2963	2964	2965	2966	2967	2968	2969	2970	2971	2972
2973	2974	2975	2976	2977	2978	2979	2980	2981	2982	2983	2984	2985	2986	2987
2988	2989	2990	2991	2992	2993	2994	2995	2996	2997	2998	2999	3000	3001	3002
3003	3004	3005	3006	3007	3008	3009	3010	3011	3012	3013	3014	3015	3016	3017
3018	3019	3020	3021	3022	3023	3024	3025	3026	3027	3028	3029	3030	3031	3032
3033	3034	3035	3036	3037	3038	3039	3040	3041	3042	3043	3044	3045	3046	3047
3048	3049	3050	3051	3052	3053	3054	3055	3056	3057	3058	3059	3060	3061	3062
3063	3064	3065	3066	3067	3068	3069	3070	3071	3072	3073	3074	3075	3076	3077
3078	3079	3080	3081	3082	3083	3084	3085	3086	3087	3088	3089	3090	3091	3092
3093	3094	3095	3096	3097	3098	3099	3100	3101	3102	3103	3104	3105	3106	3107
3108	3109	3110	3111	3112	3113	3114	3115	3116	3117	3118	3119	3120	3121	3122
3123	3124	3125	3126	3127	3128	3129	3130	3131	3132	3133	3134	3135	3136	3137
3138	3139	3140	3141	3142	3143	3144	3145	3146	3147	3148	3149	3150	3151	3152
3153	3154	3155	3156	3157	3158	3159	3160	3161	3162	3163	3164	3165	3166	3167
3168	3169	3170	3171	3172	3173	3174	3175	3176	3177	3178	3179	3180	3181	3182
3183	3184	3185	3186	3187	3188	3189	3190	3191	3192	3193	3194	3195	3196	3197
3198	3199	3200	3201	3202	3203	3204	3205	3206	3207	3208	3209	3210	3211	3212
3213	3214	3215	3216	3217	3218	3219	3220	3221	3222	3223	3224	3225	3226	3227
3228	3229	3230	3231	3232	3233	3234	3235	3236	3237	3238	3239	3240	3241	3242
3243	3244	3245	3246	3247	3248	3249	3250	3251	3252	3253	3254	3255	3256	3257
3258	3259	3260	3261	3262	3263	3264	3265	3266	3267	3268	3269	3270	3271	3272
3273	3274	3275	3276	3277	3278	3279	3280	3281	3282	3283	3284	3285	3286	3287
3288	3289	3290	3291	3292	3293	3294	3295	3296	3297	3298	3299	3300	3301	3302
3303	3304	3305	3306	3307	3308	3309	3310	3311	3312	3313	3314	3315	3316	3317
3318	3319	3320	3321	3322	3323	3324	3325	3326	3327	3328	3329	3330	3331	3332
3333	3334	3335	3336	3337	3338	3339	3340	3341	3342	3343	3344	3345	3346	3347
3348	3349	3350	3351	3352	3353	3354	3355	3356	3357	3358	3359	3360	3361	3362
3363	3364	3365	3366	3367	3368	3369	3370	3371	3372	3373	3374	3375	3376	3377
3378	3379	3380	3381	3382	3383	3384	3385	3386	3387	3388	3389	3390	3391	3392
3393	3394	3395	3396	3397	3398	3399	3400	3401	3402	3403	3404	3405	3406	3407
3408	3409	3410	3411	3412	3413	3414	3415	3416	3417	3418	3419	3420	3421	3422
3423	3424	3425	3426	3427	3428	3429	3430	3431	3432	3433	3434	3435	3436	3437
3438	3439	3440	3441	3442	3443	3444	3445	3446	3447	3448	3449	3450	3451	3452
3453	3454	3455	3456	3457	3458	3459	3460	3461	3462	3463	3464	3465	3466	3467
3468	3469	3470	3471	3472	3473	3474	3475	3476	3477	3478	3479	3480	3481	3482
3483	3484	3485	3486	3487	3488	3489	3490	3491	3492	3493	3494	3495	3496	3497
3498	3499	3500	3501	3502	3503	3504	3505	3506	3507	3508	3509	3510	3511	3512
3513	3514	3515	3516	3517	3518	3519	3520	3521	3522	3523	3524	3525	3526	3527
3528	3529	3530	3531	3532	3533	3534	3535	3536	3537	3538	3539	3540	3541	3542
3543	3544	3545	3546	3547	3548	3549	3550	3551	3552	3553	3554	3555	3556	3557
3558	3559	3560	3561	3562	3563	3564	3565	3566	3567	3568	3569	3570	3571	3572
3573	3574	3575	3576	3577	3578	3579	3580	3581	3582	3583	3584	3585	3586	3587
3588	3589	3590	3591	3592	3593	3594	3595	3596	3597	3598	3599	3600	3601	3602
3603	3604	3605	3606	3607	3608	3609	3610	3611	3612	3613	3614	3615	3616	3617
3618	3619	3620	3621	3622	3623	3624	3625	3626	3627	3628	3629	3630	3631	3632
3633	3634	3635	3636	3637	3638	3639	3640	3641	3642	3643	3644	3645	3646	3647
3648	3649	3650	3651	3652	3653	3654	3655	3656	3657	3658	3659	3660	3661	3662
3663	3664	3665	3666	3667	3668	3669	3670	3671	3672	3673	3674	3675	3676	3677
3678	3679	3680	3681	3682	3683	3684	3685	3686	3687	3688	3689	3690	3691	3692
3693	3694	3695	3696	3697	3698	3699	3700	3701	3702	3703	3704	3705	3706	3707
3708	3709	3710	3711	3712	3713	3714	3715	3716	3717	3718	3719	3720	3721	3722
3723	3724	3725	3726	3727	3728	3729	3730	3731	3732	3733	3734	3735	3736	3737
3738	3739	3740	3741	3742	3743	3744	3745	3746	3747	3748	3749	3750	3751	3752
3753	3754	3755	3756	3757	3758	3759	3760	3761	3762	3763	3764	3765	3766	3767
3768	3769	3770	3771	3772	3773	3774	3775	3776	3777	3778	3779	3780	3781	3

	1237	1247	1251	1260	1264	1268	1272	1282	1286	1295	1299	1303	1307	1317	1321
	1330	1334	1338	1342	1352	1356	1365	1369	1383	1399	1587	1655	1978	2083	2440
.PAGE	2596	2604	2605	2606	2607	2608	2609	2610	2611	2612	2613	2614	2615	2616	2617
.REM	479	572													
.REPT	429														
.SBTTL	299	412	423	432	446	457	479	523	572	676	742	754	795	810	821
	832	845	861	877	887	897	910	926	942	953	964	978	994	1011	1022
	1033	1047	1063	1079	1110	1123	1137	1149	1163	1180	1188	1207	1220	1233	1247
	1260	1268	1282	1295	1303	1317	1330	1338	1352	1365	1373	1421	1450	1475	1499
	1521	1546	1567	1587	1607	1619	1655	1657	1877	1901	1968	2033	2084	2131	2182
.TITLE	259	2338	2477	2515	2573	2596									
.WORD	289														
	429	430	431	454	473	474	475	476	477	478	487	490	491	492	493
	496	497	498	499	500	501	502	505	506	507	528	529	530	531	532
	533	534	535	539	540	541	554	558	561	564	565	566	567	568	569
	1388	1391	1406	2110	2115	2166	2168	2258	2288	2335	2514	2547	2603		

ERRORS DETECTED: 0

#, DVAAA.SEG/SOL/CRF/NL: TOC/DS:ERFZ=DVAAA.SML, DVAAA.P11

RUN-TIME: 54 64 6 SECONDS

CORE USED: 33K

