

FP11

LDEXP

MD-11-DCFPK-B

EP-DCFPK-B-DL-A

OCT 1976

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FICHE 1 OF 1

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The microfiche card contains 40 frames of data, arranged in 8 rows and 5 columns. Each frame displays a small table or chart with text and numbers. The data is organized in a structured format, likely representing a series of measurements or calculations over time or across different categories. The frames are arranged in a grid, with each frame containing a small table or chart. The data is too small to read clearly but appears to be organized in a structured format.

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IDENTIFICATION

PRODUCT CODE: MAINDEC-11-0000
 PRODUCT NAME: P-11 BASIC INSTRUCTION TESTS
 DATE CREATED: MARCH 12, 1973
 MAINTAINED: DIAGNOSTIC GROUP
 AUTHORS: BOB BRAIN & KEV CHAPMAN

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 1973

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MAINDEC NO.	INSTRUCTIONS TESTED
DCFPB	LDFPS, STFPS, SETI, SETL SETF, SETD, CFCC
DCFPB	STST
DCFPB	LOF, LDD, STF, STD
DCFPB	ADD, ADD, SUBF, SUBC
DCFDE	CMDF, CMPD
DCFPF	MULF, MULD
DCFPG	DIVF, DIVD
DCFPH	CLRF, CLRD, TSTF, TSTD
DCFPY	ABSF, ABSD, NEGF, NEGD
DCFPY	LCCF, LCCD, STCF, STCD
DCFPY	LCIF, LCID, LCCF, LCCD
DCFPY	STCF, STCD, STCI, STCD
DCFPY	DEXP, STXP
DCFPY	CCF, CDD

FP11 BASIC INSTRUCTION TEST DCFPA - DCFPL
DESCRIPTION

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1. ABSTRACT

THESE PROGRAMS TEST THE FP11 IN ALL MODES WITH FIXED NUMBER PATTERNS. THE PROGRAMS SHOULD BE RUN IN ORDER FOR AT LEAST 2 PASSES WITH ALL SWITCHES DOWN.

2. REQUIREMENTS

2.1 EQUIPMENT

POP11/45 STANDARD COMPUTER WITH FP11 OPTION

2.2 STORAGE

PROGRAM STORAGE - THE ROUTINES USE MEMORY 0 - 17776

2.3 PRELIMINARY PROGRAMS

NONE

3. LOADING PROCEDURE

USE STANDARD PROCEDURE FOR ABS TAPES.

4. STARTING PROCEDURE

4.1 CONTROL SWITCH SETTINGS

SEE 5.1.1 (ALL DOWN FOR WORST CASE TESTING)

4.2 STARTING ADDRESS

THE PROGRAM SHOULD ALWAYS BE STARTED AT 200.

4.3 PROGRAM AND/OR OPERATOR ACTION

- 1) LOAD PROGRAM INTO MEMORY USING ABS LOADER.
- 2) LOAD ADDRESS 200.
- 3) SET SWITCHES (SEE SEC 5.1.1) ALL DOWN FOR WORST CASE
- 4) PRESS START.
- 5) THE PROGRAM WILL LOOP AND BELL WILL RING ONCE EVERY PASS
- 6) A MINIMUM OF TWO PASSES SHOULD ALWAYS BE RUN.

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TEST OF LDEXP, STEXP

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7) THE DISPLAY ON THE 11 45 WILL SHOW THE ITERATION COUNT ON
THE LEFT BYTE AND TEST NUMBER ON THE RIGHT. TO USE, SET THE

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DATA DISPLAY SWITCH TO THE DISPLAY POSITION.

5. OPERATING PROCEDURE

5.1 OPERATIONAL SWITCH SETTINGS

AT SA 200 .. ALL SWITCHES DOWN IS WORST CASE TESTING. IF AN ERROR OCCURS, THAT TEST WILL BE LOOPED UPON UNTIL COMPLETION OF 256 CONSECUTIVE PASSES WITH NO ERRORS OF THE SUBTEST IF SW(9) SET TO A 1. THE BELL WILL RING UPON COMPLETION OF A PASS.

5.1.1 SWITCH SETTINGS ARE:

- SW(15) = 1 HALT ON ERROR
- SW(14) = 1 SCOPE LOOP
- SW(13) = 1 INHIBIT PRINTOUT
- SW(12) = 1 INHIBIT TRACE TRAPPING
- SW(11) = 1 INHIBIT ITERATIONS OF SUBTEST
- SW(10) = 1 BELL ON ERROR
- SW(9) = 0 BELL ON PASS COMPLETE
- SW(8) = 1 LOOP ON ERROR
- SW(7) = 0 LOOP ON TEST IN SW(7:0)
- SW(6) = 0 LOAD SW(7:0) INTO UB REGISTER

5.2 SUBROUTINE ABSTRACTS

5.2.1 SCOPE

THIS SUBROUTINE CALL IS PLACED BETWEEN EACH SUBTEST IN THE INSTRUCTION SECTION. IT RECORDS THE STARTING ADDRESS OF EACH SUBTEST AS IT IS BEING ENTERED IN LOCATION "LAD". IF A SCOPE LOOP IS REQUESTED, THE CURRENT SUBTEST WILL BE LOOPED UPON. SW(11) ON A 1 INHIBITS ITERATION OF SUBTESTS. THE CONTENTS OF LAD MAY BE USED TO DETERMINE THE LAST SUBTEST SUCCESSFULLY COMPLETED.

5.2.2 HLT

THIS ROUTINE PRINTS OUT AN ERROR MESSAGE. SEE 5.1.1 IF A 1 IS EXECUTED, THE SUBTEST WILL BE LOOPED UPON UNTIL 256 CONSECUTIVE GOOD PASSES ARE COMPLETED IF SW(9) IS ON A 1. TO INHIBIT TYPEOUTS, PUT SW(13) ON A 1.

Vertical text on the left margin, possibly a page number or reference code.

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5.2.3 TRTRAP

IF SW(12) IS ON A 0, THE T BIT WILL BE SET ON ALTERNATE PASSES. WHEN SET, IT CAUSES A TRAP AFTER EACH INSTRUCTION. THE FIRST INSTRUCTION EXECUTED UPON TRAPPING IS AN "RTT" WHICH RETURNS TO THE INTERRUPTED SEQUENCE OF INSTRUCTIONS. THIS SEQUENCE IS CONTINUED UNTIL THE END OF THE PROGRAM IS REACHED.

5.2.4 TRAPCATCHER

A ".+2" - "HALT" SEQUENCE IS REPEATED FROM 0 - 776 TO CATCH ANY UNEXPECTED TRAPS. THIS ANY UNEXPECTED TRAPS OR INTERRUPTS WILL HALT AT THE VECTOR + 2.

5.2.5 FLOATING POINT TRAP (TO 244)

THE FP11 INTERRUPT DISABLE BIT IS ALWAYS SET IN ALL OF THESE TESTS (EXCEPT DCFPA) SO NO TRAPS TO 244 SHOULD OCCUR. IF AN INTERRUPT OCCURS, THE PROGRAM WILL HALT AT 766 IN THE ROUTINE CALLED FLTERR AND DISPLAY THE FPS REGISTER IN RC.

6. ERRORS

6.1 ERROR PRINTOUT

THE FORMAT IS AS FOLLOWS:

ADR FPS ANS1 ANS2 ANS3 ANS4 ANS5 ANS6 ANS7 ANS8
FEC FEA

WHERE:

- ADR = ADDRESS OF ERROR HLT
- FPS = FLOATING POINT STATUS
- FEC = FLOATING EXCEPTION CODES (ERROR CODES)
- FEA = FLOATING EXCEPTION ADDRESS (ERROR ADDRESS)
- ANS1-8 = ERROR DATA READ FROM THE FP11. FROM 0-8 OF THESE MAY BE TYPED DEPENDING ON THE NUMBER FOLLOWING THE HLT; I.E., HLT+3 WOULD TYPE ANS1-ANS3.

TO FIND THE FAILING TEST, LOOK AT THE LISTING ABOVE THE ADDRESS TYPED.

Vertical text on the left margin, likely a page number or reference code, appearing as a series of characters.

.TITLE MAINDEC-11-DCFPK-8 TEST OF LDEXP, STEXP
:COPYRIGHT 1972, DIGITAL EQUIPMENT CORP., MAYNARD, MASS
:PROGRAM BY KEN CHAPMAN
.REM*

SWITCH	USE
8	0 - LOAD UB REGISTER WITH SW<7:0> 1 - LOOP ON TEST IN SW<7:0>
9	LOOP ON ERROR
10	0 - BELL ON PASS COMPLETE 1 - BELL ON ERROR
11	INHIBIT ITERATIONS
12	INHIBIT TRACE TRAP
13	INHIBIT ERROR TYPEOLTS
14	LOOP ON TEST
15	HALT ON ERROR

OUTPUT FORM:

ADR FPS ANS1 ANS2 ANS3 ANS4 ANS5 ANS6 ANS7 ANS8
FEC FEA

BIT	FPS	REASON	CODE	FEC	ERROR
0		CARRY	0		ADDRESS ERROR
1		OVERFLOW	2		OPCODE ERROR
2		ZERO	4		DIVIDE BY ZERO
3		NEGATIVE	6		CONVERSION ERROR
4		MAINTAINANCE MODE	10		OVERFLOW
5		TRUNCATE MODE	12		UNDERFLOW
6		LONG INTEGER MODE	14		UNDEFINED VARIABLE .-C.
7		DOUBLE PRECISION MODE	16		UBREAK TRAP
8		INTERUPT ON CONVERSION ERROR			
9		INTERUPT ON OVERFLOW			
10		INTERUPT ON UNDERFLOW			
11		INTERUPT ON UNDEFINED VARIABLE			
12					
13					
14		INTERUPT DISABLE			
15		ERROR_FLAG*			

```

000001      .ENABL  ABS
000002      N=      1
000003      PS=     177776
000004      SWR=     177570
000005      DISPLAY=SWR
000006      SCOPE=   TRAP
000007      HLT=     EMT
000008      TYPE=    IOT
000009      BELL=    207
000010      FPS=    %0
000011      R0=     %0
000012      R1=     %1
000013      R2=     %2
000014      R3=     %3
000015      R4=     %4
000016      R5=     %5
000017      TTY=    %5
000018      SP=     %6
000019      PC=     %7
000020      ACO=    %0
000021      AC1=    %1
000022      AC2=    %2
000023      AC3=    %3
000024      AC4=    %4
000025      AC5=    %5
000026      SW15=   100000
000027      SW14=   40000
000028      SW13=   20000
000029      SW12=   10000
000030      SW11=   4000
000031      SW10=   2000
000032      SW09=   1000
000033      SW08=   400
000034      LDUB=   170003
000035      STAO=   170005
000036      STQ0=   170007
000037      MRS=   170006
000038      LDSC=   170004

000000      .=      0
000200      .=      200

000200 000167 000622      JMP      BEG

000760 000760      .=      760
000762 170200      FLTERR: STFPS  FPS
000766 170367 000034      STST   FEC
000770 000000      HALT
000002 000002      RTI

```

;TRAP CATCHER FROM G - 776

:TEST 1: TEST LDEXP (LOAD EXPONENT), FLOATING MODE
: 000000 --> 025252,125252 ==> 040052,125252
: FPS = 047400, SRC = M2-R7, AC = AC2
:*****

001174 104400
001176 000402

SCOPE
BR TST1

001200 025252 125252

DAT1: 025252,125252

001204 170127 047417
001210 172667 177764
001214 176627 000000
001220 170200
001222 022700 047400
001226 001401
001230 104000

TST1: LDFPS #047417 ;LOAD FLOATING POINT STATUS
LDF DAT1, AC2 ;LOAD 025252,125252 INTO AC2
FPI1: LDEXP #000000,AC2 ;LOAD 000000 INTO THE EXPONENT OF AC2
STFPS FPS ;STORE FLOATING POINT STATUS
CMP #047400,FPS ;CHECK FLOATING POINT STATUS
BEQ .+4 ;BRANCH IF OK
HLT ;FPS NOT EQUAL TO 047400

001232 174267 177544
001236 022767 040052 177536
001244 001401
001246 104002

STF AC2, ANS1 ;STORE AC2 INTO ANS1,ANS2
CMP #040052,ANS1 ;CHECK ANS1
BEQ .+4 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 040052

001250 022767 125252 177526
001256 001401
001260 104002

CMP #125252,ANS2 ;CHECK ANS2
BEQ .+4 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 125252

:TEST 2: TEST LDEXP (LOAD EXPONENT), FLOATING MODE
: 000005 --> 137777,177777 ==> 141377,177777
: FPS = 047410, SRC = M2-R7, AC = AC1
:*****

001262 104400
001264 000402

SCOPE
BR TST2

001266 137777 177777

DAT2: 137777,177777

001272 170127 047417
001276 172567 177764
001302 176527 000005
001306 170200
001310 022700 047410
001314 001401
001316 104000

TST2: LDFPS #047417 ;LOAD FLOATING POINT STATUS
LDF DAT2, AC1 ;LOAD 137777,177777 INTO AC1
FPI2: LDEXP #000005,AC1 ;LOAD 000005 INTO THE EXPONENT OF AC1
STFPS FPS ;STORE FLOATING POINT STATUS
CMP #047410,FPS ;CHECK FLOATING POINT STATUS
BEQ .+4 ;BRANCH IF OK
HLT ;FPS NOT EQUAL TO 047410

001320 174167 177456
001324 022767 141377 177450
001332 001401
001334 104002

STF AC1, ANS1 ;STORE AC1 INTO ANS1,ANS2
CMP #141377,ANS1 ;CHECK ANS1
BEQ .+4 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 141377

001336 022767 177777 177440
001344 001401
001346 104002

CMP #177777,ANS2 ;CHECK ANS2
BEQ .+4 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 177777

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TEST SECTION

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:TEST 3: TEST LDEXP (LOAD EXPONENT), FLOATING MODE
: 177601 --> 000000,000000 ==> 000200,000000
: FPS = 047400, SRC = M2-R7, AC = AC2
:*****

001350	104400			SCOPE			
001352	000402			BR	TST3		
001354	000000	000000		DATA:	000000,000000		
001360	170127	047417		TST3:	LDFPS #047417		;LOAD FLOATING POINT STATUS
001364	172667	177764			LDF DAT3, AC2		;LOAD 000000,000000 INTO AC2
001370	176627	177601		FPI3:	LDEXP #177601,AC2		;LOAD 177601 INTO THE EXPONENT OF AC2
001374	170200				STFPS FPS		;STORE FLOATING POINT STATUS
001376	022700	047400			CMP #047400,FPS		;CHECK FLOATING POINT STATUS
001402	001401				BEQ .+4		;BRANCH IF OK
001404	104000				HLT		;FPS NOT EQUAL TO 047400
001406	174267	177370		STF	AC2, ANS1		;STORE AC2 INTO ANS1,ANS2
001412	022767	000200	177362	CMF	#000200,ANS1		;CHECK ANS1
001420	001401			BEQ	.+4		;BRANCH IF OK
001422	104002			HLT+2			;ANS1 NOT EQUAL TO 000200
001424	022767	000000	177352	CMP	#000000,ANS2		;CHECK ANS2
001432	001401			BEQ	.+4		;BRANCH IF OK
001434	104002			HLT+2			;ANS2 NOT EQUAL TO 000000

:TEST 4: TEST LDEXP (LOAD EXPONENT), FLOATING MODE
: 000177 --> 000000,000000 ==> 077600,000000
: FPS = 047400, SRC = M2-R7, AC = AC2
:*****

001436	104400			SCOPE			
001440	000402			BR	TST4		
001442	000000	000000		DATA:	000000,000000		
001446	170127	047417		TST4:	LDFPS #047417		;LOAD FLOATING POINT STATUS
001452	172667	177764			LDF DAT4, AC2		;LOAD 000000,000000 INTO AC2
001456	176627	000177		FPI4:	LDEXP #000177,AC2		;LOAD 000177 INTO THE EXPONENT OF AC2
001462	170200				STFPS FPS		;STORE FLOATING POINT STATUS
001464	022700	047400			CMP #047400,FPS		;CHECK FLOATING POINT STATUS
001470	001401				BEQ .+4		;BRANCH IF OK
001472	104000				HLT		;FPS NOT EQUAL TO 047400
001474	174267	177302		STF	AC2, ANS1		;STORE AC2 INTO ANS1,ANS2
001500	022767	077600	177274	CMF	#077600,ANS1		;CHECK ANS1
001506	001401			BEQ	.+4		;BRANCH IF OK
001510	104002			HLT+2			;ANS1 NOT EQUAL TO 077600
001512	022767	000000	177264	CMP	#000000,ANS2		;CHECK ANS2
001520	001401			BEQ	.+4		;BRANCH IF OK
001522	104002			HLT+2			;ANS2 NOT EQUAL TO 000000

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*****
:TEST 5:          TEST LDEXP (LOAD EXPONENT), FLOATING MODE
:          177777 --> 040000,000020 ==> 037600,000020
:          FPS = 047400, SRC = M2-R7, AC = ACC
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001524 104400          SCOPE
001526 000402          BR      TST5

001530 040000 000020  DAT5:  040000,000020

001534 170127 047417  TST5:  LDFPS  #047417          ;LOAD FLOATING POINT STATUS
001540 172467 177764  LDF     DAT5,   ACC          ;LOAD 040000,000020 INTO ACC
001544 176427 177777  FPI5:  LDEXP  #177777,ACC      ;LOAD 177777 INTO THE EXPONENT OF ACC
001550 170200          STFPS  FPS                ;STORE FLOATING POINT STATUS
001552 022700 047400  CMP     #047400,FPS         ;CHECK FLOATING POINT STATUS
001556 001401          BEQ     .+4                ;BRANCH IF OK
001560 104000          HLT                    ;FPS NOT EQUAL TO 047400

001562 174067 177214          STF     ACC,   ANS1         ;STORE ACC INTO ANS1,ANS2
001566 022767 037600 177206  CMP     #037600,ANS1       ;CHECK ANS1
001574 001401          BEQ     .+4                ;BRANCH IF OK
001576 104002          HLT+2              ;ANS1 NOT EQUAL TO 037600

001600 022767 000020 177176  CMP     #000020,ANS2       ;CHECK ANS2
001606 001401          BEQ     .+4                ;BRANCH IF OK
001610 104002          HLT+2              ;ANS2 NOT EQUAL TO 000020

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*****
:TEST 6:          TEST LDEXP (LOAD EXPONENT), FLOATING MODE
:          177706 --> 125252,125252 ==> 121452,125252
:          FPS = 047410, SRC = M2-R7, AC = ACC
*****

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001612 104400          SCOPE
001614 000402          BR      TST6

001616 125252 125252  DAT6:  125252,125252

001622 170127 047417  TST6:  LDFPS  #047417          ;LOAD FLOATING POINT STATUS
001626 172467 177764  LDF     DAT6,   ACC          ;LOAD 125252,125252 INTO ACC
001632 176427 177706  FPI6:  LDEXP  #177706,ACC      ;LOAD 177706 INTO THE EXPONENT OF ACC
001636 170200          STFPS  FPS                ;STORE FLOATING POINT STATUS
001640 022700 047410  CMP     #047410,FPS         ;CHECK FLOATING POINT STATUS
001644 001401          BEQ     .+4                ;BRANCH IF OK
001646 104000          HLT                    ;FPS NOT EQUAL TO 047410

001650 174067 177126          STF     ACC,   ANS1         ;STORE ACC INTO ANS1,ANS2
001654 022767 121452 177120  CMP     #121452,ANS1       ;CHECK ANS1
001662 001401          BEQ     .+4                ;BRANCH IF OK
001664 104002          HLT+2              ;ANS1 NOT EQUAL TO 121452

001666 022767 125252 177110  CMP     #125252,ANS2       ;CHECK ANS2
001674 001401          BEQ     .+4                ;BRANCH IF OK
001676 104002          HLT+2              ;ANS2 NOT EQUAL TO 125252

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```

*****
EST 10:00:35  YES  DEEXP  OAC  EXPONENT)  FLOATING  MODE
FP1:000200  ==  140425  125252  ==>  100025  125252
FP2:000200  ==  140425  125252  ==>  100025  125252
FP3:000200  ==  140425  125252  ==>  100025  125252
*****

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```

001766  104400
001770  000400
001772  052525  052525
001776  170127  047417
002002  172467  177754
002006  176427  000200
002012  170200
002014  170367  177002
002020  022700  147416
002024  001401
002026  104000
002030  022767  000010  176754
002036  001401
002040  104000
002042  022767  002006  176754
002050  001401
002052  104000

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SCOPE
SP  TS17
DATA:  052525,052525
TEST:  100025,125252
FP1:000200  ==  140425  125252  ==>  100025  125252
FP2:000200  ==  140425  125252  ==>  100025  125252
FP3:000200  ==  140425  125252  ==>  100025  125252
*****
LOAD FLOATING POINT STATUS
LOAD 052525,052525 INTO ACC
LOAD 000135 INTO THE EXPONENT OF ACC
STORE FLOATING POINT STATUS
CHECK FLOATING POINT STATUS
BRANCH IF OK
FPS NOT EQUAL TO 047400
*****
STORE ACC INTO ANS1,ANS2
CHECK ANS1
BRANCH IF OK
ANS1 NOT EQUAL TO 067325
*****
CHECK ANS2
BRANCH IF OK
ANS2 NOT EQUAL TO 052525
*****

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*****
EST 10:00:35  YES  DEEXP  OAC  EXPONENT)  FLOATING  MODE
FP1:000200  ==  140425  125252  ==>  100025  125252
FP2:000200  ==  140425  125252  ==>  100025  125252
FP3:000200  ==  140425  125252  ==>  100025  125252
*****

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001766  104400
001770  000400
001772  140425  125252
001776  170127  047417
002002  172467  177754
002006  176427  000200
002012  170200
002014  170367  177002
002020  022700  147416
002024  001401
002026  104000
002030  022767  000010  176754
002036  001401
002040  104000
002042  022767  002006  176754
002050  001401
002052  104000

```

```

SCOPE
SP  TS17
DATA:  140425,125252
TEST:  100025,125252
FP1:000200  ==  140425  125252  ==>  100025  125252
FP2:000200  ==  140425  125252  ==>  100025  125252
FP3:000200  ==  140425  125252  ==>  100025  125252
*****
LOAD FLOATING POINT STATUS
LOAD 140425,125252 INTO ACC
LOAD 000200 INTO THE EXPONENT OF ACC
STORE FLOATING POINT STATUS
STORE EXCEPTION CODES
CHECK FLOATING POINT STATUS
BRANCH IF OK
FPS NOT EQUAL TO 147416
*****
CHECK FLOATING EXCEPTION CODE
BRANCH IF OK
FEC NOT EQUAL TO 10
*****
CHECK FLOATING EXCEPTION ADDRESS
BRANCH IF OK
FEA NOT EQUAL TO FPI:0
*****

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002054	174067	176722		STF	AC0	ANS1	:STORE AC0 INTO ANS1,ANS2
002060	022767	100025	176714	CMP	#100025	ANS1	:CHECK ANS1
002066	001401			BEQ	.+4		:BRANCH IF OK
002070	104002			HLT+2			:ANS1 NOT EQUAL TO 100025
002072	022767	125252	176704	CMP	#125252	ANS2	:CHECK ANS2
002100	001401			BEQ	.+4		:BRANCH IF OK
002102	104002			HLT+2			:ANS2 NOT EQUAL TO 125252

:TEST 11: TEST LDEXP (LOAD EXPONENT), FLOATING MODE
: 177601 --> 050505,050505 ==> 000305,050505
: FPS = 047400, SRC = M2-R7, AC = AC2

002104	104400			SCOPE			
002106	000402			BR	TST11		
002110	050505	050505		DAT11:	050505,050505		
002114	170127	047417		TST11:	LDFPS	#047417	:LOAD FLOATING POINT STATUS
002120	172667	177754		LDF	DAT11	AC2	:LOAD 050505,050505 INTO AC2
002124	176627	177501		FPI11:	LDEXP	#177601,AC2	:LOAD 177601 INTO THE EXPONENT OF AC2
002130	170200			STFPS	FPS		:STORE FLOATING POINT STATUS
002132	022700	047400		CMP	#047400	FPS	:CHECK FLOATING POINT STATUS
002136	001401			BEQ	.+4		:BRANCH IF OK
002140	104000			HLT			:FPS NOT EQUAL TO 047400
002142	174267	176634		STF	AC2	ANS1	:STORE AC2 INTO ANS1,ANS2
002146	022767	000305	176626	CMP	#000305	ANS1	:CHECK ANS1
002154	001401			BEQ	.+4		:BRANCH IF OK
002156	104002			HLT+2			:ANS1 NOT EQUAL TO 000305
002160	022767	050505	176616	CMP	#050505	ANS2	:CHECK ANS2
002166	001401			BEQ	.+4		:BRANCH IF OK
002170	104002			HLT+2			:ANS2 NOT EQUAL TO 050505

:TEST 12: TEST LDEXP (LOAD EXPONENT), FLOATING MODE
: 177600 --> 040525,125252 ==> 000125,125252
: FPS = 147404, SRC = M2-R7, AC = ACC
: FEC = 12, FEA = FPI12

002172	104400			SCOPE			
002174	000402			BR	TST12		
002176	040525	125252		DAT12:	040525,125252		
002202	170127	047417		TST12:	LDFPS	#047417	:LOAD FLOATING POINT STATUS
002206	172467	177764		LDF	DAT12	ACC	:LOAD 040525,125252 INTO ACC
002212	176427	177600		FPI12:	LDEXP	#177600,ACC	:LOAD 177600 INTO THE EXPONENT OF ACC
002216	170200			STFPS	FPS		:STORE FLOATING POINT STATUS
002220	170257	176576		STSY	FEC		:STORE EXCEPTION CODES
002224	022700	147404		CMP	#147404	FPS	:CHECK FLOATING POINT STATUS

```

002230 001401      BEQ      .+4      :BRANCH IF OK
002232 104000      HLT
002234 022767 000012 176560  CMP      #12,      FEC      :CHECK FLOATING EXCEPTION CODE
002242 001401      BEQ      .+4      :BRANCH IF OK
002244 104000      HLT      :FEC NOT EQUAL TO 12
002246 022767 002212 176550  CMP      #FPI12, FEA      :CHECK FLOATING EXCEPTION ADDRESS
002254 001401      BEQ      .+4      :BRANCH IF OK
002256 104000      HLT      :FEA NOT EQUAL TO FPI12
002260 174067 176516      STF      ACC,      ANS1    :STORE ACC INTO ANS1,ANS2
002264 022767 000125 176510  CMP      #000125,ANS1    :CHECK ANS1
002272 001401      BEQ      .+4      :BRANCH IF OK
002274 104002      HLT+2    :ANS1 NOT EQUAL TO 000125
002276 022767 125252 176500  CMP      #125252,ANS2    :CHECK ANS2
002304 001401      BEQ      .+4      :BRANCH IF OK
002306 104002      HLT+2    :ANS2 NOT EQUAL TO 125252

```

```

*****
TEST 13:      TEST LDEXP (LOAD EXPONENT), FLOATING MODE
              177575 --> 050505,050505 ==> 077305,050505
              FPS = 147400,   SAC = M2-R7,   AC = AC2
              FEC = 12,      FEA = FPI13
*****

```

```

002310 104400      SCOPE
002312 000402      BR      TST13
002314 050505 050505  DAT13: 050505,050505
002320 170127 047417  TST13: LDFPS   #047417      :LOAD FLOATING POINT STATUS
002324 172667 177764      LDF      DAT13, AC2      :LOAD 050505,050505 INTO AC2
002330 176627 177575  FPI13:  LDEXP   #177575,AC2    :LOAD 177575 INTO THE EXPONENT OF AC2
002334 170200      STFPS   FPS              :STORE FLOATING POINT STATUS
002336 170367 176460  STSY    FEC              :STORE EXCEPTION CODES
002342 022700 147400  CMP      #147400,FPS     :CHECK FLOATING POINT STATUS
002346 001401      BEQ      .+4      :BRANCH IF OK
002350 104000      HLT      :FPS NOT EQUAL TO 147400
002352 022767 000012 176442  CMP      #12,      FEC      :CHECK FLOATING EXCEPTION CODE
002360 001401      BEQ      .+4      :BRANCH IF OK
002362 104000      HLT      :FEC NOT EQUAL TO 12
002364 022767 002330 176432  CMP      #FPI13, FEA      :CHECK FLOATING EXCEPTION ADDRESS
002372 001401      BEQ      .+4      :BRANCH IF OK
002374 104000      HLT      :FEA NOT EQUAL TO FPI13
002376 174267 176400      STF      ACC,      ANS1    :STORE ACC INTO ANS1,ANS2
002402 022767 077305 176372  CMP      #077305,ANS1    :CHECK ANS1
002410 001401      BEQ      .+4      :BRANCH IF OK
002412 104002      HLT+2    :ANS1 NOT EQUAL TO 077305
002414 022767 050505 176362  CMP      #050505,ANS2    :CHECK ANS2
002422 001401      BEQ      .+4      :BRANCH IF OK

```

E02

WJANDEC-11-205PA-B
207PKB.F11

TEST OF LDEXP, STEXP
TEST SECTION

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002424 104002

HLT+2

;ANS2 NOT EQUAL TO 050505

TEST 14: TEST LDEXP (LOAD EXPONENT), FLOATING MODE
000201 --> 140425,125252 ==> 100225,125252
FPS = 046412, SRC = M2-R7, AC = ACC

002430 104400
002432 000402

SCOPE
BR TST14

002432 140425 125252

DAT14: 140425,125252

002436 170127 046405
002442 172467 177764
002446 176427 000201
002452 170200
002454 022700 046412
002460 001401
002462 104000

TST14: LDFPS #046405 ;LOAD FLOATING POINT STATUS
LDF DAT14, ACC ;LOAD 140425,125252 INTO ACC
FPI14: LDEXP #000201,ACC ;LOAD 000201 INTO THE EXPONENT OF ACC
STFPS FPS ;STORE FLOATING POINT STATUS
CMP #046412,FPS ;CHECK FLOATING POINT STATUS
BEQ .+4 ;BRANCH IF OK
HLT ;FPS NOT EQUAL TO 046412

002464 174067 176312
002470 022767 100225 176304
002476 001401
002500 104002

STF ACC,ANS1 ;STORE ACC INTO ANS1,ANS2
CMP #100225,ANS1 ;CHECK ANS1
BEQ .+4 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 100225

002502 022767 125252 176274
002510 001401
002512 104002

CMP #125252,ANS2 ;CHECK ANS2
BEQ .+4 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 125252

TEST 15: TEST LDEXP (LOAD EXPONENT), FLOATING MODE
177576 --> 040525,125252 ==> 000000,000000
FPS = 045404, SRC = M2-R7, AC = ACC

002514 104400
002516 000402

SCOPE
BR TST15

002520 040525 125252

DAT15: 040525,125252

002524 170127 045417
002530 172667 177764
002534 176627 177576
002540 170200
002542 022700 045404
002546 001401
002550 104000

TST15: LDFPS #045417 ;LOAD FLOATING POINT STATUS
LDF DAT15, ACC ;LOAD 040525,125252 INTO ACC
FPI15: LDEXP #177576,ACC ;LOAD 177576 INTO THE EXPONENT OF ACC
STFPS FPS ;STORE FLOATING POINT STATUS
CMP #045404,FPS ;CHECK FLOATING POINT STATUS
BEQ .+4 ;BRANCH IF OK
HLT ;FPS NOT EQUAL TO 045404

002552 174267 176224
002556 022767 000000 176216
002564 001401
002566 104002

STF ACC,ANS1 ;STORE ACC INTO ANS1,ANS2
CMP #000000,ANS1 ;CHECK ANS1
BEQ .+4 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 000000

002570 022767 000000 176206
002576 001401

CMP #000000,ANS2 ;CHECK ANS2
BEQ .+4 ;BRANCH IF OK

F02

MAINDEC-11-DCFPK-B
DCFPK.P11

TEST OF LDEXP, STEXP
TEST SECTION

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002600 104002

HLT+2

;ANS2 NOT EQUAL TO 000000

:TEST 16: TEST LDEXP (LOAD EXPONENT), FLOATING MODE
: 177706 --> 125252,125252 ==> 121452,125252
: FPS = 047410, SRC = MO-R2, AC = ACO
:*****

002602 104400
002604 000402

SCOPE
BR TST16

002606 125252 125252

DAT16: 125252,125252

002612 170127 047417
002616 172467 177764
002622 012702 177706
002626 176402
002630 170200
002632 022700 047410
002636 001401
002640 104000

TST16: LDFPS #047417 ;LOAD FLOATING POINT STATUS
LDF DAT16, ACO ;LOAD 125252,125252 INTO ACO
MOV #177706,R2 ;PUT EXPONENT IN R2
FPI16: LDEXP R2, ACO ;LOAD 177706 INTO THE EXPONENT OF ACO
STFPS FPS ;STORE FLOATING POINT STATUS
CMP #047410,FPS ;CHECK FLOATING POINT STATUS
BEQ .+4 ;BRANCH IF OK
HLT ;FPS NOT EQUAL TO 047410

002642 174067 176134
002646 022767 121452 176126
002654 001401
002656 104002

STF ACO, ANS1 ;STORE ACO INTO ANS1,ANS2
CMP #121452,ANS1 ;CHECK ANS1
BEQ .+4 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 121452

002660 022767 125252 176116
002666 001401
002670 104002

CMP #125252,ANS2 ;CHECK ANS2
BEQ .+4 ;BRANCH IF OK
HLT+2 ;ANS2 NOT EQUAL TO 125252

:TEST 17: TEST LDEXP (LOAD EXPONENT), FLOATING MODE
: 000135 --> 052525,052525 ==> 067325,052525
: FPS = 047400, SRC = MO-R3, AC = AC2
:*****

002672 104400
002674 000402

SCOPE
BR TST17

002676 052525 052525

DAT17: 052525,052525

002702 170127 047417
002706 172667 177764
002712 012703 000135
002716 176603
002720 170200
002722 022700 047400
002726 001401
002730 104000

TST17: LDFPS #047417 ;LOAD FLOATING POINT STATUS
LDF DAT17, AC2 ;LOAD 052525,052525 INTO AC2
MOV #000135,R3 ;PUT EXPONENT IN R3
FPI17: LDEXP R3, AC2 ;LOAD 000135 INTO THE EXPONENT OF AC2
STFPS FPS ;STORE FLOATING POINT STATUS
CMP #047400,FPS ;CHECK FLOATING POINT STATUS
BEQ .+4 ;BRANCH IF OK
HLT ;FPS NOT EQUAL TO 047400

002732 174267 176044
002736 022767 067325 176036
002744 001401
002746 104002

STF AC2, ANS1 ;STORE AC2 INTO ANS1,ANS2
CMP #067325,ANS1 ;CHECK ANS1
BEQ .+4 ;BRANCH IF OK
HLT+2 ;ANS1 NOT EQUAL TO 067325

```

002750 022767 052525 176026      CMP      #052525,ANS2      ;CHECK ANS2
002756 001401      BEQ      .+4              ;BRANCH IF OK
002760 104002      HLT+2      ;ANS2 NOT EQUAL TO 052525

```

```

:*****
:TEST 20:      TEST STEXP (STORE EXPONENT), FLOATING MODE
:      000000,000000 --> 177600
:      FPS = 047410,   FCC = 10,   AC = ACC,   DST = M6-P7
:*****

```

```

002762 104400      SCOPE
002764 000402      BR      TST20

002765 000000 000000      DAT20: 000000,000000

002772 170127 047417      TST20: LDFPS      #047417      ;LOAD FLOATING POINT STATUS
002776 172467 177764      LDF      DAT20,   ACC      ;LOAD 000000,000000 INTO ACC
003002 175067 175774      FPI20: STEXP      ACC,   ANS1      ;STORE THE EXPONENT OF ACC IN ANS1
003006 013767 177776 175770      MOV      @#PS,   ANS2      ;GET CPU STATUS
003014 042767 177760 175762      BIC      #177760,ANS2      ;SAVE CONDITION CODES
003022 170200      STFPS      FPS      ;STORE FLOATING POINT STATUS
003024 022700 047410      CMP      #047410,FPS      ;CHECK FLOATING POINT STATUS
003030 001401      BEQ      .+4              ;BRANCH IF OK
003032 104000      HLT      ;FPS NOT EQUAL TO 047410

003034 022767 177600 175740      CMP      #177600,ANS1      ;CHECK ANS1
003042 001401      BEQ      .+4              ;BRANCH IF OK
003044 104001      HLT+1      ;ANS1 NOT EQUAL TO 177600

003046 022767 000010 175730      CMP      #10,   ANS2      ;CHECK ANS2
003054 001401      BEQ      .+4              ;BRANCH IF OK
003056 104002      HLT+2      ;WPOING CONDITION CODES!

```

```

:*****
:TEST 21:      TEST STEXP (STORE EXPONENT), FLOATING MODE
:      140134,034343 --> 000000
:      FPS = 047404,   FCC = 4,   AC = ACC,   DST = M6-P7
:*****

```

```

003060 104400      SCOPE
003062 000402      BR      TST21

003064 140134 034343      DAT21: 140134,034343

003070 170127 047417      TST21: LDFPS      #047417      ;LOAD FLOATING POINT STATUS
003074 172667 177764      LDF      DAT21,   ACC      ;LOAD 140134,034343 INTO ACC
003100 175267 175676      FPI21: STEXP      ACC,   ANS1      ;STORE THE EXPONENT OF ACC IN ANS1
003104 013767 177776 175672      MOV      @#PS,   ANS2      ;GET CPU STATUS
003112 042767 177760 175664      BIC      #177760,ANS2      ;SAVE CONDITION CODES
003120 170200      STFPS      FPS      ;STORE FLOATING POINT STATUS
003122 022700 047404      CMP      #047404,FPS      ;CHECK FLOATING POINT STATUS
003126 001401      BEQ      .+4              ;BRANCH IF OK
003130 104000      HLT      ;FPS NOT EQUAL TO 047404

003132 022767 000000 175642      CMP      #000000,ANS1      ;CHECK ANS1
003140 001401      BEQ      .+4              ;BRANCH IF OK

```

H02

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DCFPKB.F11

TEST OF LDEXP. STEXP
TEST SECTION

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```
003142 104001          HLT+1          ;ANSI NOT EQUAL TO 000000
003144 022767 000004 175632  CMP      #4,      ANS2      ;CHECK ANS2
003152 001401          BEQ      .+4          ;BRANCH IF OK
003154 104002          HLT+2          ;WRONG CONDITION CODES!
```

```
*****
:TEST 22:          TEST STEXP (STORE EXPONENT), FLOATING MODE
:          040200,125252 --> 000001
:          FPS = 047400,   FCC = 0,          AC = ACO,          DST = M6-R7
*****
```

```
003156 104400          SCOPE
003160 000402          BR      TST22
003162 040200 125252  DAT22: 040200,125252
003166 170127 047417  TST22: LDFPS  #047417      ;LOAD FLOATING POINT STATUS
003172 172467 177764  LDF      DAT22,   ACO      ;LOAD 040200,125252 INTO ACO
003176 175067 175600  FPI22: STEXP  ACO,   ANS1   ;STORE THE EXPONENT OF ACO IN ANS1
003202 013767 177776 175574  MOV      #FPS,   ANS2   ;GET CPU STATUS
003210 042767 177760 175566  BIC      #177760,ANS2   ;SAVE CONDITION CODES
003216 170200          STFPS  FPS          ;STORE FLOATING POINT STATUS
003220 022700 047400  CMP      #047400,FPS    ;CHECK FLOATING POINT STATUS
003224 001401          BEQ      .+4          ;BRANCH IF OK
003226 104000          HLT          ;FPS NOT EQUAL TO 047400
003230 022767 000001 175544  CMP      #000001,ANS1   ;CHECK ANS1
003236 001401          BEQ      .+4          ;BRANCH IF OK
003240 104001          HLT+1         ;ANS1 NOT EQUAL TO 000001
003242 022767 000000 175534  CMP      #0,      ANS2   ;CHECK ANS2
003250 001401          BEQ      .+4          ;BRANCH IF OK
003252 104002          HLT+2         ;WRONG CONDITION CODES!
```

```
*****
:TEST 23:          TEST STEXP (STORE EXPONENT), FLOATING MODE
:          042525,052525 --> 000012
:          FPS = 047400,   FCC = 0,          AC = ACO,          DST = M6-R7
*****
```

```
003254 104400          SCOPE
003256 000402          BR      TST23
003260 042525 052525  DAT23: 042525,052525
003264 170127 047417  TST23: LDFPS  #047417      ;LOAD FLOATING POINT STATUS
003270 172467 177764  LDF      DAT23,   ACO      ;LOAD 042525,052525 INTO ACO
003274 175067 175502  FPI23: STEXP  ACO,   ANS1   ;STORE THE EXPONENT OF ACO IN ANS1
003300 013767 177776 175476  MOV      #FPS,   ANS2   ;GET CPU STATUS
003306 042767 177760 175470  BIC      #177760,ANS2   ;SAVE CONDITION CODES
003314 170200          STFPS  FPS          ;STORE FLOATING POINT STATUS
003316 022700 047400  CMP      #047400,FPS    ;CHECK FLOATING POINT STATUS
003322 001401          BEQ      .+4          ;BRANCH IF OK
003324 104000          HLT          ;FPS NOT EQUAL TO 047400
```

```

003326 022767 000012 175446      CMP      #000012,ANS1      ;CHECK ANS1
003334 001401                      BEQ      .+4              ;BRANCH IF OK
003336 104001                      HLT+1                    ;ANS1 NOT EQUAL TO 000012

003340 022767 000000 175436      CMP      #0,      ANS2      ;CHECK ANS2
003346 001401                      BEQ      .+4              ;BRANCH IF OK
003350 104002                      HLT+2                    ;WRONG CONDITION CODES!

```

```

:*****
:TEST 24:      TEST STEXP (STORE EXPONENT), FLOATING MODE
:              125252,125252 --> 177725
:              FPS = 047410,   FCC = 10,      AC = AC2,      DST = M6-R7
:*****

```

```

003352 104400      SCOPE
003354 000402      BR      TST24

003356 125252 125252      DAT24: 125252,125252

003362 170127 047417      TST24: LDFPS  #047417      ;LOAD FLOATING POINT STATUS
003366 172667 177764      LDF      DAT24,  AC2      ;LOAD 125252,125252 INTO AC2
003372 175267 175404      FPI24: STEXP  AC2,  ANS1      ;STORE THE EXPONENT OF AC2 IN ANS1
003376 013767 177776 175400      MOV      #FPS,  ANS2      ;GET CPU STATUS
003404 042767 177760 175372      BIC      #177760,ANS2     ;SAVE CONDITION CODES
003412 170200      STFPS  FPS              ;STORE FLOATING POINT STATUS
003414 022700 047410      CMP      #047410,FPS      ;CHECK FLOATING POINT STATUS
003420 001401                      BEQ      .+4              ;BRANCH IF OK
003422 104000                      HLT                          ;FPS NOT EQUAL TO 047410

003424 022767 177725 175350      CMP      #177725,ANS1     ;CHECK ANS1
003432 001401                      BEQ      .+4              ;BRANCH IF OK
003434 104001                      HLT+1                    ;ANS1 NOT EQUAL TO 177725

003436 022767 000010 175340      CMP      #10,      ANS2     ;CHECK ANS2
003444 001401                      BEQ      .+4              ;BRANCH IF OK
003446 104002                      HLT+2                    ;WRONG CONDITION CODES!

```

```

:*****
:TEST 25:      TEST STEXP (STORE EXPONENT), FLOATING MODE
:              052525,052525 --> 000052
:              FPS = 047400,   FCC = 00,      AC = ACC,      DST = M6-R7
:*****

```

```

003450 104400      SCOPE
003452 000402      BR      TST25

003454 052525 052525      DAT25: 052525,052525

003460 170127 047417      TST25: LDFPS  #047417      ;LOAD FLOATING POINT STATUS
003464 172467 177764      LDF      DAT25,  ACC      ;LOAD 052525,052525 INTO ACC
003470 175067 175306      FPI25: STEXP  ACC,  ANS1      ;STORE THE EXPONENT OF ACC IN ANS1
003474 013767 177776 175302      MOV      #FPS,  ANS2      ;GET CPU STATUS
003502 042767 177760 175274      BIC      #177760,ANS2     ;SAVE CONDITION CODES
003510 170200      STFPS  FPS              ;STORE FLOATING POINT STATUS
003512 022700 047400      CMP      #047400,FPS      ;CHECK FLOATING POINT STATUS
003516 001401                      BEQ      .+4              ;BRANCH IF OK

```

```

003520 104000          HLT          :FPS NOT EQUAL TO 047400
003522 022767 000052 175252      CMP      #000052,ANS1      ;CHECK ANS1
003530 001401          BEQ      .+4          ;BRANCH IF OK
003532 104001          HLT+1        ;ANS1 NOT EQUAL TO 000052

003534 022767 000000 175242      CMP      #00.      ANS2      ;CHECK ANS2
003542 001401          BEQ      .+4          ;BRANCH IF OK
003544 104002          HLT+2        ;WRONG CONDITION CODES!

```

```

:*****
:TEST 26:      TEST STEXP (STORE EXPONENT), FLOATING MODE
:              177777,177777 --> 000177
:              FPS = 047400, FCC = 00,      AC = AC3,      DST = M6-R7
:*****

```

```

003546 104400          SCOPE
003550 000402          BR      TST26

003552 177777 177777      DAT26: 177777,177777

003556 170127 047417      TST26: LDFPS      #047417      ;LOAD FLOATING POINT STATUS
003562 172767 177764      LDF      DAT26, AC3      ;LOAD 177777,177777 INTO AC3
003566 175367 175210      FPI26: STEXP      AC3, ANS1      ;STORE THE EXPONENT OF AC3 IN ANS1
003572 013767 177776 175204      MOV      @#PS, ANS2      ;GET CPU STATUS
003600 042767 177760 175176      BIC      #177760,ANS2      ;SAVE CONDITION CODES
003606 170200          STFPS      FPS      ;STORE FLOATING POINT STATUS
003610 022700 047400      CMP      #047400,FPS      ;CHECK FLOATING POINT STATUS
003614 001401          BEQ      .+4          ;BRANCH IF OK
003616 104000          HLT          ;FPS NOT EQUAL TO 047400

003620 022767 000177 175154      CMP      #000177,ANS1      ;CHECK ANS1
003626 001401          BEQ      .+4          ;BRANCH IF OK
003630 104001          HLT+1        ;ANS1 NOT EQUAL TO 000177

003632 022767 000000 175144      CMP      #00.      ANS2      ;CHECK ANS2
003640 001401          BEQ      .+4          ;BRANCH IF OK
003642 104002          HLT+2        ;WRONG CONDITION CODES!

```

```

:*****
:TEST 27:      TEST STEXP (STORE EXPONENT), FLOATING MODE
:              000200,000000 --> 177601
:              FPS = 047410, FCC = 10,      AC = AC1,      DST = M6-R7
:*****

```

```

003644 104400          SCOPE
003646 000402          BR      TST27

003650 000200 000000      DAT27: 000200,000000

003654 170127 047417      TST27: LDFPS      #047417      ;LOAD FLOATING POINT STATUS
003660 172567 177764      LDF      DAT27, AC1      ;LOAD 000200,000000 INTO AC1
003664 175167 175112      FPI27: STEXP      AC1, ANS1      ;STORE THE EXPONENT OF AC1 IN ANS1
003670 013767 177776 175106      MOV      @#PS, ANS2      ;GET CPU STATUS
003676 042767 177760 175100      BIC      #177760,ANS2      ;SAVE CONDITION CODES
003704 170200          STFPS      FPS      ;STORE FLOATING POINT STATUS

```

K02

MAINDEC-11-DCFPK-B
DCFPKB.P11

TEST OF LDEXP, STEXP
TEST SECTION

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003706	022700	047410		CMP	#047410,FPS	;CHECK FLOATING POINT STATUS
003712	001401			BEQ	+.4	;BRANCH IF OK
003714	104000			HLT		;FPS NOT EQUAL TO 047410
003716	022767	177601	175056	CMP	#177601,ANS1	;CHECK ANS1
003724	001401			BEQ	+.4	;BRANCH IF OK
003726	104001			HLT+1		;ANS1 NOT EQUAL TO 177601
003730	022767	000010	175046	CMP	#10, ANS2	;CHECK ANS2
003736	001401			BEQ	+.4	;BRANCH IF OK
003740	104002			HLT+2		;WRONG CONDITION CODES!

```

*****
:TEST 30:      TEST STEXP (STORE EXPONENT), FLOATING MODE
:      125252,125252 --> 177725
:      FPS = 047410,   FCC = 10,      AC = AC3,      DST = MO-R1
*****

```

003742	104400			SCOPE		
003744	000402			BR	TST30	
003746	125252	125252		DAT30:	125252,125252	
003752	170127	047417		TST30:	LDFPS #047417	;LOAD FLOATING POINT STATUS
003756	172767	177764			LDF DAT30, AC3	;LOAD 125252,125252 INTO AC3
003762	175301				STEXP AC3, R1	;STORE THE EXPONENT OF AC3 IN R1
003764	013767	177776	175012		MOV #FPS, ANS2	;GET CPU STATUS
003772	042767	177760	175004		BIC #177760,ANS2	;SAVE CONDITION CODES
004000	010167	174776			MOV R1, ANS1	;STORE EXPONENT FOR TYPING
004004	170200				STFPS FPS	;STORE FLOATING POINT STATUS
004006	022700	047410			CMP #047410,FPS	;CHECK FLOATING POINT STATUS
004012	001401				BEQ +.4	;BRANCH IF OK
004014	104000				HLT	;FPS NOT EQUAL TO 047410
004016	022767	177725	174756		CMP #177725,ANS1	;CHECK ANS1
004024	001401				BEQ +.4	;BRANCH IF OK
004026	104001				HLT+1	;ANS1 NOT EQUAL TO 177725
004030	022767	000010	174746		CMP #10, ANS2	;CHECK ANS2
004036	001401				BEQ +.4	;BRANCH IF OK
004040	104002				HLT+2	;WRONG CONDITION CODES!

```

*****
:TEST 31:      TEST STEXP (STORE EXPONENT), FLOATING MODE
:      052525,052525 --> 000052
:      FPS = 047400,   FCC = 00,      AC = AC2,      DST = MO-R4
*****

```

004042	104400			SCOPE		
004044	000402			BR	TST31	
004046	052525	052525		DAT31:	052525,052525	
004052	170127	047417		TST31:	LDFPS #047417	;LOAD FLOATING POINT STATUS
004056	172667	177764			LDF DAT31, AC2	;LOAD 052525,052525 INTO AC2
004062	175204				STEXP AC2, R4	;STORE THE EXPONENT OF AC2 IN R4

004064	013767	177776	174712	MOV	Q#PS, ANS2	;GET CPU STATUS
004072	042767	177760	174704	BIC	#177760,ANS2	;SAVE CONDITION CODES
004100	010467	174676		MOV	R4, ANS1	;STORE EXPONENT FOR TYPING
004104	170200			S -PS	FPS	;STORE FLOATING POINT STATUS
004106	022700	047400		CMP	#047400,FPS	;CHECK FLOATING POINT STATUS
004112	001401			BEQ	+.4	;BRANCH IF OK
004114	104000			HLT		;FPS NOT EQUAL TO 047400

004116	022767	000052	174656	CMP	#000052,ANS1	;CHECK ANS1
004124	001401			BEQ	+.4	;BRANCH IF OK
004126	104001			HLT+1		;ANS1 NOT EQUAL TO 000052

004130	022767	000000	174646	CMP	#00, ANS2	;CHECK ANS2
004136	001401			BEQ	+.4	;BRANCH IF OK
004140	104002			HLT+2		;WRONG CONDITION CODES!

:TEST 32: TEST STEXP (STORE EXPONENT), FLOATING MODE
: 143125,052525 --> 000014
: FPS = 047400, FCC = 00, AC = ACO, DST = M2-R7
:*****

004142	104400			SCOPE		
004144	000402			BR	TST32	

004146	143125	052525		DAT32:	143125,052525	
--------	--------	--------	--	--------	---------------	--

004152	170127	047417		TST32:	LDFPS #047417	;LOAD FLOATING POINT STATUS
004156	172467	177764			LDF DAT32, ACO	;LOAD 143125,052525 INTO ACO
004162	005067	000002			CLR ANR32	
004166	175027			FPI32:	STEXP ACC, (PC)+	;STORE THE EXPONENT OF ACC IN .+2
004170	000000			ANR32:	0	;THE EXPONENT GOES HERE
004172	000403				BR .+10	
004174	000000				HALT	
004176	000000				HALT	
004200	000000				HALT	

004202	013767	177776	174574	MOV	Q#PS, ANS2	;GET CPU STATUS
004210	042767	177760	174566	BIC	#177760,ANS2	;SAVE CONDITION CODES
004216	170200			STFPS	FPS	;STORE FLOATING POINT STATUS
004220	022700	047400		CMP	#047400,FPS	;CHECK FLOATING POINT STATUS
004224	001401			BEQ	+.4	;BRANCH IF OK
004226	104000			HLT		;FPS NOT EQUAL TO 047400

004230	016767	177734	174544	MOV	ANR32,ANS1	
004236	022767	000014	174536	CMP	#000014,ANS1	;CHECK ANS1
004244	001401			BEQ	+.4	;BRANCH IF OK
004246	104001			HLT+1		;ANS1 NOT EQUAL TO 000014

004250	022767	000000	174526	CMP	#00, ANS2	;CHECK ANS2
004256	001401			BEQ	+.4	;BRANCH IF OK
004260	104002			HLT+2		;WRONG CONDITION CODES!

:TEST 33: TEST STEXP (STORE EXPONENT), FLOATING MODE
: 032615,125252 --> 177753
: FPS = 047410, FCC = 10, AC = AC1, DST = M2-R7
:*****

```

004262 104400          SCOPE
00-264 000402          BR      TST33

004266 032615 125252  DAT33: 032615,125252

004272 170127 047417  TST33: LDFPS  #047417      ;LOAD FLOATING POINT STATUS
004276 172567 177764      LDF      DAT33, AC1      ;LOAD 032615,125252 INTO AC1
004302 005067 000002      CLR      ANR33
004306 175127      FPI33: STEXP AC1, (PC)+ ;STORE THE EXPONENT OF AC1 IN .+2
004310 000000      ANR33: 0           ;THE EXPONENT GOES HERE
004312 000403      BR      .+10
004314 000000      HALT
004316 000000      HALT
004320 000000      HALT
004322 013767 177776 174454  MOV      2#PS, ANS2      ;GET CPU STATUS
004330 042767 177760 174446  BIC      #177760,ANS2   ;SAVE CONDITION CODES
004336 170200      STFPS   FPS           ;STORE FLOATING POINT STATUS
004340 022700 047410  CMP      #047410,FPS    ;CHECK FLOATING POINT STATUS
004344 001401      BEQ     .+4           ;BRANCH IF OK
004346 104000      HLT     ;FPS NOT EQUAL TO 047410

004350 016767 177734 174424  MOV      ANR33,ANS1
004356 022767 177753 174416  CMP      #177753,ANS1   ;CHECK ANS1
004364 001401      BEQ     .+4           ;BRANCH IF OK
004366 104001      HLT+1      ;ANS1 NOT EQUAL TO 177753

004370 022767 000010 174406  CMP      #10, ANS2      ;CHECK ANS2
004376 001401      BEQ     .+4           ;BRANCH IF OK
004400 104002      HLT+2      ;WRONG CONDITION CODES!

```

```

*****
;TEST 34: TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
;000000 --> 040252,125252,125252,125252 ==> 040052,125252,125252,125252
;FPS = 047600, SRC = M2-R7, AC = ACO
*****

```

```

004402 104400          SCOPE
004404 000404          BR      TST34

004406 040252 125252 125252  DAT34: 040252,125252,125252,125252
004414 125252

004416 170127 047617  TST34: LDFPS  #047617      ;LOAD FLOATING POINT STATUS
004422 172467 177760      LDD      DAT34, ACC      ;LOAD 040252,125252,125252,125252 INTO ACC
004426 176427 000000  FPI34: LDEXP  #000000,ACC  ;LOAD 000000 INTO THE EXPONENT OF ACC
004432 170200      STFPS   FPS           ;STORE FLOATING POINT STATUS
004434 022700 047600  CMP      #047600,FPS    ;CHECK FLOATING POINT STATUS
004440 001401      BEQ     .+4           ;BRANCH IF OK
004442 104000      HLT     ;FPS NOT EQUAL TO 047600

004444 174067 174332      STD      ACC, ANS1      ;STORE ACC INTO ANS1 THRU ANS4
004450 022767 040052 174324  CMP      #040052,ANS1   ;CHECK ANS1
004456 001401      BEQ     .+4           ;BRANCH IF OK
004460 104004      HLT+4      ;ANS1 NOT EQUAL TO 040052

```

```

004462 022767 125252 174314    CMP    #125252,ANS2    ;CHECK ANS2
004470 001401    BEQ    .+4            ;BRANCH IF OK
004472 104004    HLT+4                ;ANS2 NOT EQUAL TO 125252

004474 022767 125252 174304    CMP    #125252,ANS3    ;CHECK ANS3
004502 001401    BEQ    .+4            ;BRANCH IF OK
004504 104004    HLT+4                ;ANS3 NOT EQUAL TO 125252

004506 022767 125252 174274    CMP    #125252,ANS4    ;CHECK ANS4
004514 001401    BEQ    .+4            ;BRANCH IF OK
004516 104004    HLT+4                ;ANS4 NOT EQUAL TO 125252

```

```

:*****
:TEST 35:      TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
:              000005 --> 137777,177777,177777,177777 ==> 141377,177777,177777,177777
:              FPS = 047610,   SRC = M2-R7,   AC = AC3
:*****

```

```

004520 104400    SCOPE
004522 000404    BR      TST35

004524 137777 177777 177777  DAT35: 137777,177777,177777,177777
004532 177777

004534 170127 047617  TST35: LDFPS  #047617    ;LOAD FLOATING POINT STATUS
004540 172767 177760    LDD    DAT35, AC3    ;LOAD 137777,177777,177777,177777 INTO AC3
004544 176727 000005  FPI35: LDEXP  #000005,AC3 ;LOAD 000005 INTO THE EXPONENT OF AC3
004550 170200    STFPS  FPS          ;STORE FLOATING POINT STATUS
004552 022700 047610  CMP    #047610,FPS  ;CHECK FLOATING POINT STATUS
004556 001401    BEQ    .+4          ;BRANCH IF OK
004560 104000    HLT                    ;FPS NOT EQUAL TO 047610

004562 174367 174214  STD    AC3, ANS1    ;STORE AC3 INTO ANS1 THRU ANS4
004566 022767 141377 174206  CMP    #141377,ANS1 ;CHECK ANS1
004574 001401    BEQ    .+4          ;BRANCH IF OK
004576 104004    HLT+4                ;ANS1 NOT EQUAL TO 141377

004600 022767 177777 174176  CMP    #177777,ANS2 ;CHECK ANS2
004606 001401    BEQ    .+4          ;BRANCH IF OK
004610 104004    HLT+4                ;ANS2 NOT EQUAL TO 177777

004612 022767 177777 174166  CMP    #177777,ANS3 ;CHECK ANS3
004620 001401    BEQ    .+4          ;BRANCH IF OK
004622 104004    HLT+4                ;ANS3 NOT EQUAL TO 177777

004624 022767 177777 174156  CMP    #177777,ANS4 ;CHECK ANS4
004632 001401    BEQ    .+4          ;BRANCH IF OK
004634 104004    HLT+4                ;ANS4 NOT EQUAL TO 177777

```

```

:*****
:TEST 36:      TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
:              177601 --> 061616,143434,052525,107070 ==> 000216,143434,052525,107070
:              FPS = 047600,   SRC = M2-R7,   AC = AC2
:*****

```

```

004754 104000          SCOPE
004756 000404          BR      TST37

004760 161616 161616 161616 DAT37: 161616,161616,161616,161616

004770 170127 047617          TST37: LDFPS #047617          :LOAD FLOATING POINT STATUS
004774 172567 177760          LDD   DAT37, AC1          :LOAD 161616,161616,161616,161616 INTO AC1
005000 176527 000177          FPI37: LDEXP #000177,AC1      :LOAD 000177 INTO THE EXPONENT OF AC1
005004 170200          STFPS FPS                :STORE FLOATING POINT STATUS
005006 022700 047610          CMP   #047610,FPS        :CHECK FLOATING POINT STATUS
005012 001401          BEQ   .+4                :BRANCH IF OK
005014 104000          HLT                    :FPS NOT EQUAL TO 047610

005016 174167 173760          STD   AC1, ANS1          :STORE AC1 INTO ANS1 THRU ANS4
005022 022767 177616 173752          CMP   #177616,ANS1      :CHECK ANS1
005030 001401          BEQ   .+4                :BRANCH IF OK
005032 104000          HLT                    :ANS1 NOT EQUAL TO 177616

```

```

*****
TEST 37: TEST LDEXP (LOAD EXPONENT) DOUBLE MODE
000177 --> 161616,161616,161616,161616 ==> 177616,161616,161616,161616
FPS = 047610, SAC = M2-R7, AC = AC1
*****

```

```

004754 104000          SCOPE
004756 000404          BR      TST37

004760 161616 161616 161616 DAT37: 161616,161616,161616,161616

004770 170127 047617          TST37: LDFPS #047617          :LOAD FLOATING POINT STATUS
004774 172567 177760          LDD   DAT37, AC1          :LOAD 161616,161616,161616,161616 INTO AC1
005000 176527 000177          FPI37: LDEXP #000177,AC1      :LOAD 000177 INTO THE EXPONENT OF AC1
005004 170200          STFPS FPS                :STORE FLOATING POINT STATUS
005006 022700 047610          CMP   #047610,FPS        :CHECK FLOATING POINT STATUS
005012 001401          BEQ   .+4                :BRANCH IF OK
005014 104000          HLT                    :FPS NOT EQUAL TO 047610

005016 174167 173760          STD   AC1, ANS1          :STORE AC1 INTO ANS1 THRU ANS4
005022 022767 177616 173752          CMP   #177616,ANS1      :CHECK ANS1
005030 001401          BEQ   .+4                :BRANCH IF OK
005032 104000          HLT                    :ANS1 NOT EQUAL TO 177616

```

```

005034 022767 161616 173742    CMP    #161616,ANS2    ;CHECK ANS2
005042 001401    BEQ    .+4            ;BRANCH IF OK
005044 104004    HLT+4            ;ANS2 NOT EQUAL TO 161616

005046 022767 161616 173732    CMP    #161616,ANS3    ;CHECK ANS3
005054 001401    BEQ    .+4            ;BRANCH IF OK
005056 104004    HLT+4            ;ANS3 NOT EQUAL TO 161616

005060 022767 161616 173722    CMP    #161616,ANS4    ;CHECK ANS4
005066 001401    BEQ    .+4            ;BRANCH IF OK
005070 104004    HLT+4            ;ANS4 NOT EQUAL TO 161616

```

```

:*****
:TEST 40:      TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
:              177777 --> 040000,000020,000020,000020 ==> 037600,000020,000020,000020
:              FPS = 047600,   SAC = M2-R7,   AC = ACC
:*****

```

```

005072 104400    SCOPE
005074 000404    BR      TST40

005076 040000 000020 000020 DAT40: 040000,000020,000020,000020
005104 000020

```

```

005106 170127 047617    TST40: LDFPS    #047617    ;LOAD FLOATING POINT STATUS
005112 172467 177760    LDD     DAT40, ACC      ;LOAD 040000,000020,000020,000020 INTO ACC
005116 176427 177777    FPI40: LDEXP    #177777,ACC   ;LOAD 177777 INTO THE EXPONENT OF ACC
005122 170200    STFPS   FPS            ;STORE FLOATING POINT STATUS
005124 022700 047600    CMP     #047600,FPS     ;CHECK FLOATING POINT STATUS
005130 001401    BEQ     .+4            ;BRANCH IF OK
005132 104000    HLT     ;FPS NOT EQUAL TO 047600

```

```

005134 174067 173642    STD     ACC, ANS1      ;STORE ACC INTO ANS1 THRU ANS4
005140 022767 037600 173634    CMP     #037600,ANS1   ;CHECK ANS1
005146 001401    BEQ     .+4            ;BRANCH IF OK
005150 104004    HLT+4    ;ANS1 NOT EQUAL TO 037600

```

```

005152 022767 000020 173624    CMP     #000020,ANS2   ;CHECK ANS2
005160 001401    BEQ     .+4            ;BRANCH IF OK
005162 104004    HLT+4    ;ANS2 NOT EQUAL TO 000020

```

```

005164 022767 000020 173614    CMP     #000020,ANS3   ;CHECK ANS3
005172 001401    BEQ     .+4            ;BRANCH IF OK
005174 104004    HLT+4    ;ANS3 NOT EQUAL TO 000020

```

```

005176 022767 000020 173604    CMP     #000020,ANS4   ;CHECK ANS4
005204 001401    BEQ     .+4            ;BRANCH IF OK
005206 104004    HLT+4    ;ANS4 NOT EQUAL TO 000020

```

```

:*****
:TEST 41:      TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
:              177706 --> 125252,125252,125252,125252 ==> 121452,125252,125252,125252
:              FPS = 047610,   SAC = M2-R7,   AC = ACC
:*****

```

```

005210 104400    SCOPE

```

```

005212 000404 BR TST41
005214 125252 125252 125252 DAT41: 125252,125252,125252,125252
005222 125252
005224 170127 047617 TST41: LDFPS #047617 :LOAD FLOATING POINT STATUS
005230 172767 177760 LDD DAT41, AC3 :LOAD 125252,125252,125252,125252 INTO AC3
005234 176727 177706 FPI41: LDEXP #177706,AC3 :LOAD 177706 INTO THE EXPONENT OF AC3
005240 170200 STFPS FPS :STORE FLOATING POINT STATUS
005242 022700 047610 CMP #047610,FPS :CHECK FLOATING POINT STATUS
005246 001401 BEQ .+4 :BRANCH IF OK
005250 104000 HLT :FPS NOT EQUAL TO 047610

005252 174367 173524 STD AC3,ANS1 :STORE AC3 INTO ANS1 THRU ANS4
005256 022767 121452 173516 CMP #121452,ANS1 :CHECK ANS1
005264 001401 BEQ .+4 :BRANCH IF OK
005266 104004 HLT+4 :ANS1 NOT EQUAL TO 121452

005270 022767 125252 173506 CMP #125252,ANS2 :CHECK ANS2
005276 001401 BEQ .+4 :BRANCH IF OK
005300 104004 HLT+4 :ANS2 NOT EQUAL TO 125252

005302 022767 125252 173476 CMP #125252,ANS3 :CHECK ANS3
005310 001401 BEQ .+4 :BRANCH IF OK
005312 104004 HLT+4 :ANS3 NOT EQUAL TO 125252

005314 022767 125252 173466 CMP #125252,ANS4 :CHECK ANS4
005322 001401 BEQ .+4 :BRANCH IF OK
005324 104004 HLT+4 :ANS4 NOT EQUAL TO 125252

```

```

*****
:TEST 42: TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
: 000135 --> 052525,052525,052525,052525 ==> 067325,052525,052525,052525
: FPS = 047600, SRC = M2-R7, AC = AC2
*****

```

```

005326 104400 SCOPE
005330 000404 BR TST42
005332 052525 052525 052525 DAT42: 052525,052525,052525,052525
005340 052525
005342 170127 047617 TST42: LDFPS #047617 :LOAD FLOATING POINT STATUS
005346 172667 177760 LDD DAT42, AC2 :LOAD 052525,052525,052525,052525 INTO AC2
005352 176627 000135 FPI42: LDEXP #000135,AC2 :LOAD 000135 INTO THE EXPONENT OF AC2
005356 170200 STFPS FPS :STORE FLOATING POINT STATUS
005360 022700 047600 CMP #047600,FPS :CHECK FLOATING POINT STATUS
005364 001401 BEQ .+4 :BRANCH IF OK
005366 104000 HLT :FPS NOT EQUAL TO 047600

005370 174267 173406 STD AC2,ANS1 :STORE AC2 INTO ANS1 THRU ANS4
005374 022767 067325 173400 CMP #067325,ANS1 :CHECK ANS1
005402 001401 BEQ .+4 :BRANCH IF OK
005404 104004 HLT+4 :ANS1 NOT EQUAL TO 067325

005406 022767 052525 173370 CMP #052525,ANS2 :CHECK ANS2

```

E03

MAINDEC-11-DCFA-B
DCFAKB.P11

TEST OF LDEXP. STEXP
TEST SECTION

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```

005414 001401      BEQ      .+4.      :BRANCH IF OK
005416 104004      HLT+4      :ANS2 NOT EQUAL TO 052525

005420 022767 052525 173360    CMP      #052525,ANS3 :CHECK ANS3
005426 001401      BEQ      .+4      :BRANCH IF OK
005430 104004      HLT+4      :ANS3 NOT EQUAL TO 052525

005432 022767 052525 173350    CMP      #052525,ANS4 :CHECK ANS4
005434 001401      BEQ      .+4      :BRANCH IF OK
005436 104004      HLT+4      :ANS4 NOT EQUAL TO 052525

```

```

*****
:TEST 43:      TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
:              000200 --> 140425,125252,125252,125252 ==> 100025,125252,125252,125252
:              FPS = 147616, SRC = M2-R7, AC = AC1
:              FEC = 10, FEA = FPI43
*****

```

```

005444 104400      SCOPE
005446 000404      BR      TST43

005450 140425 125252 125252 DAT43: 140425,125252,125252,125252
005456 125252

```

```

005460 170127 047617 TST43: LDFPS #047617 :LOAD FLOATING POINT STATUS
005464 172567 177760 LDD DAT43, AC1 :LOAD 140425,125252,125252,125252 INTO AC1
005470 176527 000200 FPI43: LDEXP #000200,AC1 :LOAD 000200 INTO THE EXPONENT OF AC1
005474 170200 STFPS FPS :STORE FLOATING POINT STATUS
005476 170367 173320 STST FEC :STORE EXCEPTION CODES
005502 022700 147616 CMP #147616,FPS :CHECK FLOATING POINT STATUS
005506 001401 BEQ .+4 :BRANCH IF OK
005510 104000 HLT :FPS NOT EQUAL TO 147616

```

```

005512 022767 000010 173302 CMP #10, FEC :CHECK FLOATING EXCEPTION CODE
005520 001401 BEQ .+4 :BRANCH IF OK
005522 104000 HLT :FEC NOT EQUAL TO 10

```

```

005524 022767 005470 173272 CMP #FPI43, FEA :CHECK FLOATING EXCEPTION ADDRESS
005532 001401 BEQ .+4 :BRANCH IF OK
005534 104000 HLT :FEA NOT EQUAL TO FPI43

```

```

005536 174167 173240 STD AC1, ANS1 :STORE AC1 INTO ANS1 THRU ANS4
005542 022767 100025 173232 CMP #100025,ANS1 :CHECK ANS1
005550 001401 BEQ .+4 :BRANCH IF OK
005552 104004 HLT+4 :ANS1 NOT EQUAL TO 100025

```

```

005554 022767 125252 173222 CMP #125252,ANS2 :CHECK ANS2
005562 001401 BEQ .+4 :BRANCH IF OK
005564 104004 HLT+4 :ANS2 NOT EQUAL TO 125252

```

```

005566 022767 125252 173212 CMP #125252,ANS3 :CHECK ANS3
005574 001401 BEQ .+4 :BRANCH IF OK
005576 104004 HLT+4 :ANS3 NOT EQUAL TO 125252

```

```

005600 022767 125252 173202 CMP #125252,ANS4 :CHECK ANS4
005606 001401 BEQ .+4 :BRANCH IF OK

```

F03

MAINDEC-11-DOFPA-B
DOFPA.B.F11

TEST OF LDEXP, STEXP
TEST SECTION

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005610 104004

HLT+4

;ANS4 NOT EQUAL TO 125252

```

*****
:TEST 44: TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
:      177600 --> 040525,125252,125252,125252 ==> 000125,125252,125252,125252
:      FPS = 147604, SRC = M2-R7, AC = ACC
:      FEC = 12, FEA = FPI44
*****

```

005612 104400
005614 000404

SCOPE
BR TST44

005616 040525 125252 125252 DAT44: 040525,125252,125252,125252
005624 125252

005626 170127 047617
005632 172467 177760
005636 176427 177600
005642 170200
005644 170367 173152
005650 022700 147604
005654 001401
005656 104000

```

TST44: LDFPS #047617 ;LOAD FLOATING POINT STATUS
LDD DAT44, ACC ;LOAD 040525,125252,125252,125252 INTO ACC
FPI44: LDEXP #177600,ACC ;LOAD 177600 INTO THE EXPONENT OF ACC
STFPS FPS ;STORE FLOATING POINT STATUS
STST FEC ;STORE EXCEPTION CODES
CMP #147604, FPS ;CHECK FLOATING POINT STATUS
BEQ .+4 ;BRANCH IF OK
HLT ;FPS NOT EQUAL TO 147604

```

005660 022767 000012 173134
005666 001401
005670 104000

```

CMP #12, FEC ;CHECK FLOATING EXCEPTION CODE
BEQ .+4 ;BRANCH IF OK
HLT ;FEC NOT EQUAL TO 12

```

005672 022767 005636 173124
005700 001401
005702 104000

```

CMP #FPI44, FEA ;CHECK FLOATING EXCEPTION ADDRESS
BEQ .+4 ;BRANCH IF OK
HLT ;FEA NOT EQUAL TO FPI44

```

005704 174067 173072
005710 022767 000125 173064
005716 001401
005720 104004

```

STD ACC, ANS1 ;STORE ACC INTO ANS1 THRU ANS4
CMP #000125, ANS1 ;CHECK ANS1
BEQ .+4 ;BRANCH IF OK
HLT+4 ;ANS1 NOT EQUAL TO 000125

```

005722 022767 125252 173054
005730 001401
005732 104004

```

CMP #125252, ANS2 ;CHECK ANS2
BEQ .+4 ;BRANCH IF OK
HLT+4 ;ANS2 NOT EQUAL TO 125252

```

005734 022767 125252 173044
005742 001401
005744 104004

```

CMP #125252, ANS3 ;CHECK ANS3
BEQ .+4 ;BRANCH IF OK
HLT+4 ;ANS3 NOT EQUAL TO 125252

```

005746 022767 125252 173034
005754 001401
005756 104004

```

CMP #125252, ANS4 ;CHECK ANS4
BEQ .+4 ;BRANCH IF OK
HLT+4 ;ANS4 NOT EQUAL TO 125252

```

```

*****
:TEST 45: TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
:      177575 --> 050505,050505,050505,050505 ==> 077305,050505,050505,050505
:      FPS = 147600, SRC = M2-R7, AC = ACC
:      FEC = 12, FEA = FPI45
*****

```

G03

MAINDEC-11-DCPK-B
DCPKB.P11

TEST OF LDEXP, STEXP
TEST SECTION

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```

005760 104400          SCOPE
005762 000404          BR      TST45

005764 050505 050505 050505 DAT45: 050505,050505,050505,050505
005772 050505

005774 170127 047617      TST45: LDFPS #047617      ;LOAD FLOATING POINT STATUS
006000 172767 177760      LDD   DAT45, AC3      ;LOAD 050505,050505,050505,050505 INTO AC3
006004 176727 177575      FPI45: LDEXP #177575,AC3 ;LOAD 177575 INTO THE EXPONENT OF AC3
006010 170200          STFPS FPS           ;STORE FLOATING POINT STATUS
006012 170367 173004      ST    FEC           ;STORE EXCEPTION CODES
006016 022700 147600      CMP   #147600,FPS    ;CHECK FLOATING POINT STATUS
006022 001401          BEQ   .+4           ;BRANCH IF OK
006024 104000          HLT

006026 022767 000012 172766      CMP   #12, FEC       ;CHECK FLOATING EXCEPTION CODE
006034 001401          BEQ   .+4           ;BRANCH IF OK
006036 104000          HLT       ;FEC NOT EQUAL TO 12

006040 022767 006004 172756      CMP   #FPI45, FEA    ;CHECK FLOATING EXCEPTION ADDRESS
006046 001401          BEQ   .+4           ;BRANCH IF OK
006050 104000          HLT       ;FEA NOT EQUAL TO FPI45

006052 174367 172724          STD   AC3, ANS1      ;STORE AC3 INTO ANS1 THRU ANS4
006056 022767 077305 172716      CMP   #077305,ANS1  ;CHECK ANS1
006064 001401          BEQ   .+4           ;BRANCH IF OK
006066 104004          HLT+4      ;ANS1 NOT EQUAL TO 077305

006070 022767 050505 172706      CMP   #050505,ANS2  ;CHECK ANS2
006076 001401          BEQ   .+4           ;BRANCH IF OK
006100 104004          HLT+4      ;ANS2 NOT EQUAL TO 050505

006102 022767 050505 172676      CMP   #050505,ANS3  ;CHECK ANS3
006110 001401          BEQ   .+4           ;BRANCH IF OK
006112 104004          HLT+4      ;ANS3 NOT EQUAL TO 050505

006114 022767 050505 172666      CMP   #050505,ANS4  ;CHECK ANS4
006122 001401          BEQ   .+4           ;BRANCH IF OK
006124 104004          HLT+4      ;ANS4 NOT EQUAL TO 050505

```

```

:*****
:TEST 46:      TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
:              000203 --> 140425,125252,125252,125252 ==> 100625,125252,125252,125252
:              FPS = 046612,   SRC = M2-R7,   AC = AC2
:*****

```

```

006126 104400          SCOPE
006130 000404          BR      TST46

006132 140425 125252 125252 DAT46: 140425,125252,125252,125252
006140 125252

006142 170127 046605      TST46: LDFPS #046605      ;LOAD FLOATING POINT STATUS
006146 172667 177760      LDD   DAT46, AC2      ;LOAD 140425,125252,125252,125252 INTO AC2
006152 176627 000203      FPI46: LDEXP #000203,AC2 ;LOAD 000203 INTO THE EXPONENT OF AC2
006156 170200          STFPS FPS           ;STORE FLOATING POINT STATUS

```

H03

MAINDEC-11-DCFPK-B
DCFPK.B.P11

TEST OF LDEXP, STEXP
TEST SECTION

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006160	022700	046612		CMP	#046612,FPS	:CHECK FLOATING POINT STATUS
006164	001401			BEQ	+.4	:BRANCH IF OK
006166	104000			HLT		:FPS NOT EQUAL TO 046612
006170	174267	172606		STD	AC2, ANS1	:STORE AC2 INTO ANS1 THRU ANS4
006174	022767	100625	172600	CMP	#100625,ANS1	:CHECK ANS1
006202	001401			BEQ	+.4	:BRANCH IF OK
006204	104004			HLT+4		:ANS1 NOT EQUAL TO 100625
006206	022767	125252	172570	CMP	#125252,ANS2	:CHECK ANS2
006214	001401			BEQ	+.4	:BRANCH IF OK
006216	104004			HLT+4		:ANS2 NOT EQUAL TO 125252
006220	022767	125252	172560	CMP	#125252,ANS3	:CHECK ANS3
006226	001401			BEQ	+.4	:BRANCH IF OK
006230	104004			HLT+4		:ANS3 NOT EQUAL TO 125252
006232	022767	125252	172550	CMP	#125252,ANS4	:CHECK ANS4
006240	001401			BEQ	+.4	:BRANCH IF OK
006242	104004			HLT+4		:ANS4 NOT EQUAL TO 125252

```
*****  
:TEST 47: TEST LDEXP (LOAD EXPONENT), DOUBLE MODE  
: 177577 --> 040525,125252,125252,125252 ==> 000000,000000,000000,000000  
: FPS = 045604, SRC = M2-R7, AC = ACC  
:*****
```

006244	104400			SCOPE		
006246	000404			BR	TST47	
006250	040525	125252	125252	DAT47:	040525,125252,125252,125252	
006256	125252					
006260	170127	045617		TST47: LDFPS	#045617	:LOAD FLOATING POINT STATUS
006264	172467	177760		LDD	DAT47, ACC	:LOAD 040525,125252,125252,125252 INTO ACC
006270	176427	177577		FP:47: LDEXP	#177577,ACC	:LOAD 177577 INTO THE EXPONENT OF ACC
006274	170200			STFPS	FPS	:STORE FLOATING POINT STATUS
006276	022700	045604		CMP	#045604,FPS	:CHECK FLOATING POINT STATUS
006302	001401			BEQ	+.4	:BRANCH IF OK
006304	104000			HLT		:FPS NOT EQUAL TO 045604
006306	174067	172470		STD	ACC, ANS1	:STORE ACC INTO ANS1 THRU ANS4
006312	022767	000000	172462	CMP	#000000,ANS1	:CHECK ANS1
006320	001401			BEQ	+.4	:BRANCH IF OK
006322	104004			HLT+4		:ANS1 NOT EQUAL TO 000000
006324	022767	000000	172452	CMP	#000000,ANS2	:CHECK ANS2
006332	001401			BEQ	+.4	:BRANCH IF OK
006334	104004			HLT+4		:ANS2 NOT EQUAL TO 000000
006336	022767	000000	172442	CMP	#000000,ANS3	:CHECK ANS3
006344	001401			BEQ	+.4	:BRANCH IF OK
006346	104004			HLT+4		:ANS3 NOT EQUAL TO 000000
006350	022767	000000	172432	CMP	#000000,ANS4	:CHECK ANS4
006356	001401			BEQ	+.4	:BRANCH IF OK

006360 104004

HLT+4

;ANS4 NOT EQUAL TO 000000

```

:*****
:TEST S0:      TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
:      177706 --> 156212,121212,121212,121212 ==> 121412,121212,121212,121212
:      FPS = 047610,   SAC = MO-R5,   AC = ACC
:*****

```

006362 104400
006364 000404

SCOPE
BR TST50

006366 156212
006374 121212

121212 121212 DAT50: 156212,121212,121212,121212

006376 170127 047617
006402 172467 177760
006406 012705 177706
006412 176405
006414 170200
006416 022700 047610
006422 001401
006424 104000

```

TST50: LDFPS #047617 ;LOAD FLOATING POINT STATUS
        LOD DAT50, ACC ;LOAD 156212,121212,121212,121212 INTO ACC
        MOV #177706,R5 ;PUT EXPONENT INTO R5
FPIS0: LDEXP R5, ACC ;LOAD 177706 INTO THE EXPONENT OF ACC
        STFPS FPS ;STORE FLOATING POINT STATUS
        CMP #047610,FPS ;CHECK FLOATING POINT STATUS
        BEQ .+4 ;BRANCH IF OK
        HLT ;FPS NOT EQUAL TO 047610

```

006426 174067 172350
006432 022767 121412 172342
006440 001401
006442 104004

```

STD ACC, ANS1 ;STORE ACC INTO ANS1 THRU ANS4
CMP #121412,ANS1 ;CHECK ANS1
BEQ .+4 ;BRANCH IF OK
HLT+4 ;ANS1 NOT EQUAL TO 121412

```

006444 022767 121212 172332
006452 001401
006454 104004

```

CMP #121212,ANS2 ;CHECK ANS2
BEQ .+4 ;BRANCH IF OK
HLT+4 ;ANS2 NOT EQUAL TO 121212

```

006456 022767 121212 172322
006464 001401
006466 104004

```

CMP #121212,ANS3 ;CHECK ANS3
BEQ .+4 ;BRANCH IF OK
HLT+4 ;ANS3 NOT EQUAL TO 121212

```

006470 022767 121212 172312
006476 001401
006500 104004

```

CMP #121212,ANS4 ;CHECK ANS4
BEQ .+4 ;BRANCH IF OK
HLT+4 ;ANS4 NOT EQUAL TO 121212

```

```

:*****
:TEST S1:      TEST LDEXP (LOAD EXPONENT), DOUBLE MODE
:      000135 --> 052525,052525,052525,052525 ==> 067325,052525,052525,052525
:      FPS = 047600,   SAC = MO-R2,   AC = ACC2
:*****

```

006502 104400
006504 000404

SCOPE
BR TST51

006506 052525
006514 052525

052525 052525 DAT51: 052525,052525,052525,052525

006516 170127 047617
006522 172667 177760
006526 012702 000135
006532 176602

```

TST51: LDFPS #047617 ;LOAD FLOATING POINT STATUS
        LOD DAT51, ACC2 ;LOAD 052525,052525,052525,052525 INTO ACC2
        MOV #000135,R2 ;PUT EXPONENT INTO R2
FPIS1: LDEXP R2, ACC2 ;LOAD 000135 INTO THE EXPONENT OF ACC2

```

```

006534 170200          STFPS  FPS          ;STORE FLOATING POINT STATUS
006536 022700 047600  CMP      #047600,FPS ;CHECK FLOATING POINT STATUS
006542 001401          BEQ      .+4          ;BRANCH IF OK
006544 104000          HLT      ;FPS NOT EQUAL TO 047600

006546 174267 172230  STD     AC2, ANS1      ;STORE AC2 INTO ANS1 THRU ANS4
006552 022767 067325 172222  CMP     #067325,ANS1 ;CHECK ANS1
006560 001401          BEQ      .+4          ;BRANCH IF OK
006562 104004          HLT+4     ;ANS1 NOT EQUAL TO 067325

006564 022767 052525 172212  CMP     #052525,ANS2 ;CHECK ANS2
006572 001401          BEQ      .+4          ;BRANCH IF OK
006574 104004          HLT+4     ;ANS2 NOT EQUAL TO 052525

006576 022767 052525 172202  CMP     #052525,ANS3 ;CHECK ANS3
006604 001401          BEQ      .+4          ;BRANCH IF OK
006606 104004          HLT+4     ;ANS3 NOT EQUAL TO 052525

006610 022767 052525 172172  CMP     #052525,ANS4 ;CHECK ANS4
006616 001401          BEQ      .+4          ;BRANCH IF OK
006620 104004          HLT+4     ;ANS4 NOT EQUAL TO 052525

```

```

:*****
:TEST 52: TEST STEXP (STORE EXPONENT), DOUBLE MODE
:          000000,000000,000000,000000 --> 177600
:          FPS = 047610, FCC = 10, AC = AC1, DST = M6-R7
:*****

```

```

006622 104400          SCOPE
006624 000404          BR      TST52

006626 000000 000000 000000 000000 DAT52: 000000,000000,000000,000000
006634 000000

```

```

TST52: LDFPS  #047617          ;LOAD FLOATING POINT STATUS
LD      DAT52, AC1          ;LOAD 000000,000000,000000,000000 INTO AC1
STEXP  AC1, ANS1          ;STORE THE EXPONENT OF AC1 IN ANS1
MOV     2#PS, ANS2        ;GET CPU STATUS
BIC     #177760,ANS2      ;SAVE CONDITION CODES
STFPS  FPS                ;STORE FLOATING POINT STATUS
CMP     #047610,FPS       ;CHECK FLOATING POINT STATUS
BEQ     .+4                ;BRANCH IF OK
HLT     ;FPS NOT EQUAL TO 047610

006636 170127 047617          ;LOAD FLOATING POINT STATUS
006642 172567 177760          ;LOAD 000000,000000,000000,000000 INTO AC1
006646 175167 172130          ;STORE THE EXPONENT OF AC1 IN ANS1
006652 013767 177776 172124  ;GET CPU STATUS
006660 042767 177760 172116  ;SAVE CONDITION CODES
006666 170200          ;STORE FLOATING POINT STATUS
006670 022700 047610          ;CHECK FLOATING POINT STATUS
006674 001401          ;BRANCH IF OK
006676 104000          ;FPS NOT EQUAL TO 047610

```

```

006700 022767 177600 172074  CMP     #177600,ANS1 ;CHECK ANS1
006706 001401          BEQ      .+4          ;BRANCH IF OK
006710 104001          HLT+1     ;ANS1 NOT EQUAL TO 177600

006712 022767 000010 172064  CMP     #10, ANS2    ;CHECK ANS2
006720 001401          BEQ      .+4          ;BRANCH IF OK
006722 104002          HLT+2     ;WRONG CONDITION CODES!

```

```

:*****
:TEST 53: TEST STEXP (STORE EXPONENT), DOUBLE MODE
:          040161,061434,143434,125252 --> 000000
:          FPS = 047604, FCC = 04, AC = AC3, DST = M6-R7
:*****

```

```

006724 104400          SCOPE
006726 000404          BR      TST53

006730 040161 061434 143434 DAT53: 040161,061434,143434,125252
006736 125252

006740 170127 047617      TST53: LDFPS #047617      ;LOAD FLOATING POINT STATUS
006744 172767 177760      LDD  DAT53, AC3      ;LOAD 040161,061434,143434,125252 INTO AC3
006750 175367 172026      STEXP AC3, ANS1      ;STORE THE EXPONENT OF AC3 IN ANS1
006754 013767 177776 172022 MOV  2#PS, ANS2      ;GET CPU STATUS
006762 042767 177760 172014 BIC  #177760,ANS2    ;SAVE CONDITION CODES
006770 170200      STFPS FPS          ;STORE FLOATING POINT STATUS
006772 022700 047604      CMP  #047604,FPS     ;CHECK FLOATING POINT STATUS
006776 001401      BEQ  .+4           ;BRANCH IF OK
007000 104000      HLT                    ;FPS NOT EQUAL TO 047604

007002 022767 000000 171772      CMP  #000000,ANS1    ;CHECK ANS1
007010 001401      BEQ  .+4           ;BRANCH IF OK
007012 104001      HLT+1          ;ANS1 NOT EQUAL TO 000000

007014 022767 000004 171762      CMP  #04, ANS2       ;CHECK ANS2
007022 001401      BEQ  .+4           ;BRANCH IF OK
007024 104002      HLT+2          ;WRONG CONDITION CODES!

```

```

*****
:TEST 54: TEST STEXP (STORE EXPONENT), DOUBLE MCDE
:          040200,125250,125250,125252 --> 000001
:          FPS = 047600, FCC = 00, AC = AC2, DST = M6-R7
*****

```

```

007026 104400          SCOPE
007030 000404          BR      TST54

007032 040200 125250 125250 DAT54: 040200,125250,125250,125252
007040 125252

007042 170127 047617      TST54: LDFPS #047617      ;LOAD FLOATING POINT STATUS
007046 172667 177760      LDD  DAT54, AC2      ;LOAD 040200,125250,125250,125252 INTO AC2
007052 175267 171724      STEXP AC2, ANS1      ;STORE THE EXPONENT OF AC2 IN ANS1
007056 013767 177776 171720 MOV  2#PS, ANS2      ;GET CPU STATUS
007064 042767 177760 171712 BIC  #177760,ANS2    ;SAVE CONDITION CODES
007072 170200      STFPS FPS          ;STORE FLOATING POINT STATUS
007074 022700 047600      CMP  #047600,FPS     ;CHECK FLOATING POINT STATUS
007100 001401      BEQ  .+4           ;BRANCH IF OK
007102 104000      HLT                    ;FPS NOT EQUAL TO 047600

007104 022767 000001 171670      CMP  #000001,ANS1    ;CHECK ANS1
007112 001401      BEQ  .+4           ;BRANCH IF OK
007114 104001      HLT+1          ;ANS1 NOT EQUAL TO 000001

007116 022767 000000 171660      CMP  #00, ANS2       ;CHECK ANS2
007124 001401      BEQ  .+4           ;BRANCH IF OK
007126 104002      HLT+2          ;WRONG CONDITION CODES!

```

```

*****
:TEST 55:      TEST STEXP (STORE EXPONENT), DOUBLE MODE
:      042525,052525,052525,052525 --> 000012
:      FPS = 047600,   FCC = 00,   AC = AC3,   DST = M6-R7
*****

```

```

007130 104400          SCOPE
007132 000404          BR      TST55

007134 042525 052525 052525 DAT55: 042525,052525,052525,052525
007142 052525

007144 170127 047617          TST55: LDFPS   #047617          ;LOAD FLOATING POINT STATUS
007150 172767 177760          LDD     DAT55, AC3          ;LOAD 042525,052525,052525,052525 INTO AC3
007154 175367 171622          STEXP  AC3,   ANS1          ;STORE THE EXPONENT OF AC3 IN ANS1
007160 013767 177776 171616  MOV    @#PS, ANS2          ;GET CPU STATUS
007166 042767 177760 171610  BIC    #177760,ANS2        ;SAVE CONDITION CODES
007174 170200          STFPS  FPS                ;STORE FLOATING POINT STATUS
007176 022700 047600          CMP    #047600,FPS         ;CHECK FLOATING POINT STATUS
007202 001401          BEQ    .+4                ;BRANCH IF OK
007204 104000          HLT                    ;FPS NOT EQUAL TO 047600

007206 022767 000012 171566  CMP    #000012,ANS1        ;CHECK ANS1
007214 001401          BEQ    .+4                ;BRANCH IF OK
007216 104001          HLT+1                  ;ANS1 NOT EQUAL TO 000012

007220 022767 000000 171556  CMP    #00,   ANS2         ;CHECK ANS2
007226 001401          BEQ    .+4                ;BRANCH IF OK
007230 104002          HLT+2                  ;WRONG CONDITION CODES!

```

```

*****
:TEST 56:      TEST STEXP (STORE EXPONENT), DOUBLE MODE
:      125252,125252,125252,125252 --> 177725
:      FPS = 047610,   FCC = 10,   AC = AC1,   DST = M6-R7.
*****

```

```

007232 104400          SCOPE
007234 000404          BR      TST56

007236 125252 125252 125252 DAT56: 125252,125252,125252,125252
007244 125252

007246 170127 047617          TST56: LDFPS   #047617          ;LOAD FLOATING POINT STATUS
007252 172567 177760          LDD     DAT56, AC1          ;LOAD 125252,125252,125252,125252 INTO AC1
007256 175167 171520          STEXP  AC1,   ANS1          ;STORE THE EXPONENT OF AC1 IN ANS1
007262 013767 177776 171514  MOV    @#PS, ANS2          ;GET CPU STATUS
007270 042767 177760 171506  BIC    #177760,ANS2        ;SAVE CONDITION CODES
007276 170200          STFPS  FPS                ;STORE FLOATING POINT STATUS
007300 022700 047610          CMP    #047610,FPS         ;CHECK FLOATING POINT STATUS
007304 001401          BEQ    .+4                ;BRANCH IF OK
007306 104000          HLT                    ;FPS NOT EQUAL TO 047610

007310 022767 177725 171464  CMP    #177725,ANS1        ;CHECK ANS1
007316 001401          BEQ    .+4                ;BRANCH IF OK
007320 104001          HLT+1                  ;ANS1 NOT EQUAL TO 177725

```

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TEST OF LDEXP, STEXP
TEST SECTION

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007322	022767	000010	171454	CMP	#10,	ANS2	;CHECK ANS2
007330	001401			BEQ	+.4		;BRANCH IF OK
007332	104002			HLT+2			;WRONG CONDITION CODES!

```

*****
;TEST 57:      TEST STEXP (STORE EXPONENT), DOUBLE MODE
;              052525,052525,052525,052525 --> 000052
;              FPS = 047600,   FCC = 00,       AC = AC3,       DST = M6-R7
*****

```

007334	104400			SCOPE			
007336	000404			BR		TST57	
007340	052525	052525	052525	DAT57:		052525,052525,052525,052525	
007346	052525						

007350	170127	047617		TST57:	LDFPS	#047617	;LOAD FLOATING POINT STATUS
007354	172767	177760			LDD	DAT57, AC3	;LOAD 052525,052525,052525,052525 INTO AC3
007360	175367	171416			STEXP	AC3, ANS1	;STORE THE EXPONENT OF AC3 IN ANS1
007364	013767	177776	171412		MOV	#PS, ANS2	;GET CPU STATUS
007372	042767	177760	171404		BIC	#177760,ANS2	;SAVE CONDITION CODES
007400	170200				STFPS	FPS	;STORE FLOATING POINT STATUS
007402	022700	047600			CMP	#047600,FPS	;CHECK FLOATING POINT STATUS
007406	001401				BEQ	+.4	;BRANCH IF OK
007410	104000				HLT		;FPS NOT EQUAL TO 047600

007412	022767	000052	171362	CMP	#000052,ANS1		;CHECK ANS1
007420	001401			BEQ	+.4		;BRANCH IF OK
007422	104001			HLT+1			;ANS1 NOT EQUAL TO 000052

007424	022767	000000	171352	CMP	#00,	ANS2	;CHECK ANS2
007432	001401			BEQ	+.4		;BRANCH IF OK
007434	104002			HLT+2			;WRONG CONDITION CODES!

```

*****
;TEST 60:      TEST STEXP (STORE EXPONENT), DOUBLE MODE
;              177777,177777,177777,177777 --> 000177
;              FPS = 047600,   FCC = 00,       AC = AC1,       DST = M6-R7
*****

```

007436	104400			SCOPE			
007440	000404			BR		TST60	
007442	177777	177777	177777	DAT60:		177777,177777,177777,177777	
007450	177777						

007452	170127	047617		TST60:	LDFPS	#047617	;LOAD FLOATING POINT STATUS
007456	172567	177760			LDD	DAT60, AC1	;LOAD 177777,177777,177777,177777 INTO AC1
007462	175167	171314			STEXP	AC1, ANS1	;STORE THE EXPONENT OF AC1 IN ANS1
007466	013767	177776	171310		MOV	#PS, ANS2	;GET CPU STATUS
007474	042767	177760	171302		BIC	#177760,ANS2	;SAVE CONDITION CODES
007502	170200				STFPS	FPS	;STORE FLOATING POINT STATUS
007504	022700	047600			CMP	#047600,FPS	;CHECK FLOATING POINT STATUS
007510	001401				BEQ	+.4	;BRANCH IF OK
007512	104000				HLT		;FPS NOT EQUAL TO 047600

```

007514 022767 000177 171260    CMP    #000177,ANS1    ;CHECK ANS1
007522 001401                BEQ    .+4             ;BRANCH IF OK
007524 104001                HLT+1                 ;ANS1 NOT EQUAL TO 000177

007526 022767 000000 171250    CMP    #00,    ANS2    ;CHECK ANS2
007534 001401                BEQ    .+4             ;BRANCH IF OK
007536 104002                HLT+2                 ;WRONG CONDITION CODES!

```

```

*****
;TEST 61:    TEST STEXP (STORE EXPONENT), DOUBLE MODE
;           000200,000000,000000,000000 --> 177601
;           FPS = 047610,    FCC = 10,    AC = AC3,    DST = M6-R7
*****

```

```

007540 104400                SCOPE
007542 000404                BR    TST61

007544 000200 000000 000000  DAT61: 000200,000000,000000,000000
007552 000000

```

```

007554 170127 047617    TST61: LDFPS    #047617    ;LOAD FLOATING POINT STATUS
007560 172767 177760    LDD    DAT61,    AC3    ;LOAD 000200,000000,000000,000000 INTC AC3
007564 175367 171212    STEXP   AC3,    ANS1    ;STORE THE EXPONENT OF AC3 IN ANS1
007570 013767 177776 171206    MOV    @#PS,    ANS2    ;GET CPU STATUS
007576 042767 177760 171200    BIC    #177760,ANS2    ;SAVE CONDITION CODES
007604 170200                STFPS   FPS             ;STORE FLOATING POINT STATUS
007606 022700 047610    CMP    #047610,FPS     ;CHECK FLOATING POINT STATUS
007612 001401                BEQ    .+4             ;BRANCH IF OK
007614 104000                HLT                    ;FPS NOT EQUAL TO 047610

```

```

007616 022767 177601 171156    CMP    #177601,ANS1    ;CHECK ANS1
007624 001401                LEQ    .+4             ;BRANCH IF OK
007626 104001                HLT+1                 ;ANS1 NOT EQUAL TO 177601

007630 022767 000010 171146    CMP    #10,    ANS2    ;CHECK ANS2
007636 001401                BEQ    .+4             ;BRANCH IF OK
007640 104002                HLT+2                 ;WRONG CONDITION CODES!

```

```

*****
;TEST 62:    TEST STEXP (STORE EXPONENT), DOUBLE MODE
;           125252,125252,125252,125252 --> 177725
;           FPS = 047610,    FCC = 10,    AC = AC0,    DST = M0-R3
*****

```

```

007642 104400                SCOPE
007644 000404                BR    TST62

007646 125252 125252 125252  DAT62: 125252,125252,125252,125252
007654 125252

```

```

007656 170127 047617    TST62: LDFPS    #047617    ;LOAD FLOATING POINT STATUS
007662 172467 177760    LDD    DAT62,    AC0    ;LOAD 125252,125252,125252,125252 INTC AC0
007666 175003    STEXP   AC0,    R3     ;STORE THE EXPONENT OF AC0 IN R3
007670 013767 177776 171106    MOV    @#PS,    ANS2    ;GET CPU STATUS
007676 042767 177760 171100    BIC    #177760,ANS2    ;SAVE CONDITION CODES
007704 010367 171072    MOV    R3,    ANS1    ;SAVE R3 FOR TYPING

```

```

007746 104400
007750 000404
007752 001401
007756 104001
007758 000000 170342
007760 001401
007762 000000 170342
007764 001401
007766 000000 170342
007768 001401
007770 000000 170342
007772 001401
007774 000000 170342
007776 001401
007778 000000 170342
007780 001401
007782 000000 170342
007784 001401
007786 000000 170342
007788 001401
007790 000000 170342
007792 001401
007794 000000 170342
007796 001401
007798 000000 170342
007800 001401
007802 000000 170342
007804 001401
007806 000000 170342
007808 001401
007810 000000 170342
007812 001401
007814 000000 170342
007816 001401
007818 000000 170342
007820 001401
007822 000000 170342
007824 001401
007826 000000 170342
007828 001401
007830 000000 170342
007832 001401
007834 000000 170342
007836 001401
007838 000000 170342
007840 001401
007842 000000 170342
007844 001401
007846 000000 170342
007848 001401
007850 000000 170342
007852 001401
007854 000000 170342
007856 001401
007858 000000 170342
007860 001401
007862 000000 170342
007864 001401
007866 000000 170342
007868 001401
007870 000000 170342
007872 001401
007874 000000 170342
007876 001401
007878 000000 170342
007880 001401
007882 000000 170342
007884 001401
007886 000000 170342
007888 001401
007890 000000 170342
007892 001401
007894 000000 170342
007896 001401
007898 000000 170342
007900 001401
007902 000000 170342
007904 001401
007906 000000 170342
007908 001401
007910 000000 170342
007912 001401
007914 000000 170342
007916 001401
007918 000000 170342
007920 001401
007922 000000 170342
007924 001401
007926 000000 170342
007928 001401
007930 000000 170342
007932 001401
007934 000000 170342
007936 001401
007938 000000 170342
007940 001401
007942 000000 170342
007944 001401
007946 000000 170342
007948 001401
007950 000000 170342
007952 001401
007954 000000 170342
007956 001401
007958 000000 170342
007960 001401
007962 000000 170342
007964 001401
007966 000000 170342
007968 001401
007970 000000 170342
007972 001401
007974 000000 170342
007976 001401
007978 000000 170342
007980 001401
007982 000000 170342
007984 001401
007986 000000 170342
007988 001401
007990 000000 170342
007992 001401
007994 000000 170342
007996 001401
007998 000000 170342
008000 001401

```

```

*****
TEST 63: TEST STEXP (STORE EXPONENT), DOUBLE MODE
052525,052525,052525,052525 --> 000052
FPS = 047600, FCC = 00, AC = AC1, CST = MC-R4
*****

```

```

007746 104400
007750 000404
007752 052525 052525 052525 052525 052525
007756 170127 047617
007760 172567 177760
007764 175104
007768 013767 177776 171002
007772 042767 177760 170774
007776 010467 170766
007780 170200
007784 022700 047600
007788 001401
007792 104000
007796 022767 000052 170746
007800 001401
007804 104001
007808 022767 000000 170736
007812 001401
007816 104002
007820 104400
007824 000404
007828 050252 125252 125252 052525
007832 050252
007836 050252
007840 050252
007844 050252
007848 050252
007852 050252
007856 050252
007860 050252
007864 050252
007868 050252
007872 050252
007876 050252
007880 050252
007884 050252
007888 050252
007892 050252
007896 050252
007900 050252
007904 050252
007908 050252
007912 050252
007916 050252
007920 050252
007924 050252
007928 050252
007932 050252
007936 050252
007940 050252
007944 050252
007948 050252
007952 050252
007956 050252
007960 050252
007964 050252
007968 050252
007972 050252
007976 050252
007980 050252
007984 050252
007988 050252
007992 050252
007996 050252
008000 050252

```

```

*****
TEST 64: TEST STEXP (STORE EXPONENT), DOUBLE MODE
050252,125252,125252,052525 --> 000041
FPS = 047600, FCC = 00, AC = AC2, CST = M2-R7
*****

```

```

008002 104400
008004 000404
008006 050252 125252 125252 052525
008010 050252
008014 050252
008018 050252
008022 050252
008026 050252
008030 050252
008034 050252
008038 050252
008042 050252
008046 050252
008050 050252
008054 050252
008058 050252
008062 050252
008066 050252
008070 050252
008074 050252
008078 050252
008082 050252
008086 050252
008090 050252
008094 050252
008098 050252
008100 050252

```

```

010066 170127 047617 TST64: LDFPS #047617 :LOAD FLOATING POINT STATUS
010072 172667 177760 LDD DAT64, AC2 :LOAD 050252,125252,125252,052525 INTO AC2
010076 005067 000002 CLR ANR64
010102 175227 STEXP AC2, (PC)+ :STORE THE EXPONENT OF AC2 IN .+2
010104 000000 ANR64: 0
010106 000403 BR .+10
010110 000000 HALT
010112 000000 HALT
010114 000000 HALT
010116 013767 177776 170660 MOV #FPS, ANS2 :GET CPU STATUS
010122 042767 177760 170652 BIC #177760,ANS2 :SAVE CONDITION CODES
010136 170200 STEXP FPS :STORE FLOATING POINT STATUS
010134 022700 047600 CMP #047600,FPS :CHECK FLOATING POINT STATUS
010140 001401 BEQ .+4 :BRANCH IF OK
010142 104000 HLT :FPS NOT EQUAL TO 047600

010144 016767 177734 170630 MOV ANR64, ANS1 :CHECK ANS1
010152 022767 000041 170622 CMP #000041,ANS1 :BRANCH IF OK
010160 001401 BEQ .+4 :ANS1 NOT EQUAL TO 000041
010162 104001 HLT+1

010164 022767 000000 170612 CMP #00, ANS2 :CHECK ANS2
010172 001401 BEQ .+4 :BRANCH IF OK
010174 104002 HLT+2 :WRONG CONDITION CODES!

```

```

:*****
:TEST 65: TEST STEXP (STORE EXPONENT), DOUBLE MODE
: 027616,161616,034343,070707 --) 177737
: FPS = 047610, FCC = 10, AC = ACC, DST = M2-47
:*****

```

```

010176 104400 SCOPE
010200 000404 BR TST65

010202 027616 161616 034343 DAT65: 027616,161616,034343,070707
010210 070707

010212 170127 047617 TST65: LDFPS #047617 :LOAD FLOATING POINT STATUS
010216 172467 177760 LDD DAT65, ACC :LOAD 027616,161616,034343,070707 INTO ACC
010222 005067 000002 CLR ANR65
010226 175027 STEXP ACC, (PC)+ :STORE THE EXPONENT OF ACC IN .+2
010230 000000 ANR65: 0
010232 000403 BR .+10
010234 000000 HALT
010236 000000 HALT
010240 000000 HALT
010242 013767 177776 170534 MOV #FPS, ANS2 :GET CPU STATUS
010250 042767 177760 170526 BIC #177760,ANS2 :SAVE CONDITION CODES
010256 170200 STEXP FPS :STORE FLOATING POINT STATUS
010260 022700 047610 CMP #047610,FPS :CHECK FLOATING POINT STATUS
010264 001401 BEQ .+4 :BRANCH IF OK
010266 104000 HLT :FPS NOT EQUAL TO 047610

010272 016767 177734 170504 MOV ANR65, ANS1 :CHECK ANS1
010276 022767 177737 170476 CMP #177737,ANS1 :BRANCH IF OK
010304 001401 BEQ .+4

```

004

NOEC-11-DCFPK-B
DCFPK.B.F:1

TEST OF LOEXP. STEP
TEST SECTION

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010306 104001

HLT+1

:ANS1 NOT EQUAL TO 177737

010310 022767 000010 170466

OMP B10.

ANS2

:CHECK ANS2

010316 001401

BEG .+4

:BRANCH IF OK

010320 104002

HL +2

:WRONG CONDITION CODES!

E04

MANDEC-11-00FPA-8
00FPAE.F11

TEST OF LDEXP, STEXP
BELL AND SCOPE ROUTINE

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010322	104400			DONE:	SCOPE		
010324	032737	002000	177570		BIT	#SW10,0#SWR	:PING THE BELL?
010332	001005				BNE	IS	:NO!
010334	012767	000207	001242		MOV	#BELL,TYPE	:TYPE A BELL
010342	000004	011604			TYPE	..TYPE	
010346	005046			1\$:	CLR	-(6)	:CLEAR TRACE TRAP
010350	032737	010000	177570		BIT	#SW12,0#SWR	:RUN WITH TRT?
010356	001010				BNE	2\$	
010360	005157	001222			COM	TRPB	
010364	100005				BPL	2\$	
010366	052716	000020			BIS	#20,(6)	:SET TRACE TRAP
010372	012746	001062			MOV	#BEGIN,-(6)	:JUMP TO START OF TEST
010376	000412				BR	YESRT	
010400	012746	001062		2\$:	MOV	#BEGIN,-(6)	:JUMP TO START OF TEST
010404	013700	000042			MOV	0#42,R0	:GET MONITOR ADDRESS
010410	001404				BEQ	3\$:IF NONE
010412	004710				JSR	7..0)	:GO TO MONITOR
010414	000240				NOP		
010416	000240				NOP		
010420	000240				NOP		
010422	000002			3\$:	RTI		
010424	000002			YESRT:	RTI		:RETURN TO PROGRAM FROM TRAP
010426	032737	000400	177570	.EMT:	BIT	#SW08,0#SWR	:KILL LDUB OR LOOP ON SPEC. TEST
010434	001404				BEQ	1\$	
010436	123767	177570	170334		CMPB	0#SWR,ICNT	:ON RIGHT TEST? *SW7-3*
010444	001437				BEQ	OVER	
010446	113703	177570		1\$:	MOVB	0#SWR,R3	:GET UB BITS
010452	170003				LDUB		
010454	032737	040000	177570		BIT	#SW14,0#SWR	:LOOP ON TEST
010462	001026				BNE	KIT	
010464	032737	004000	177570		BIT	#SW11,0#SWR	:KILL ITERATIONS
010472	001012				BNE	SAVLAD	
010474	105767	170301			TSTB	ICNT+1	
010500	001404				BEQ	2\$:BRANCH IF FIRST
010502	126767	001106	170271		CMPB	TIMES,ICNT+1	:DONE?
010510	001013				BNE	KIT	:BRANCH IF NOT
010512	112767	000001	170261	2\$:	MOVB	#1,ICNT+1	:FIRST ITERATION
010520	105267	170254		SAVLAD:	INCB	ICNT	:COUNT TEST NUMBERS
010524	011667	001060			MOV	(6),LAD	:SAVE LOOP ADDRESS
010530	016737	170244	177570		MOV	ICNT,0#DISPLAY	:DISPLAY TEST NO. AND ITERATION COUNT
010536	000002				RTI		:RETURN
010540	105267	170235		KIT:	INCB	ICNT+1	
010544	016737	170230	177570	OVER:	MOV	ICNT,0#DISPLAY	:SET UP DISPLAY
010552	005767	001032			TST	LAD	:FIRST ONE?
010556	001760				BEQ	SAVLAD	
010560	016716	001024			MOV	LAD,(6)	:FUDGE RETURN ADDRESS
010564	000002				RTI		:FIXES PS

010566	032737	002000	177570	.TRP:	BIT	#SW10,2#SWR	:BELL ON ERROR?
010574	001405				BEO	1\$:NO - SKIP
010576	012767	000207	001000		MOV	#BELL .TYPE	:TYPE A BELL
010604	000004	011604			TYPE	.TYPE	
010610	004767	000406		1\$:	JSR	PC,ERROR	:COUNT THE NUMBER OF ERRORS
010614	010446				MOV	R4,-(6)	
010616	032737	020000	177570		BIT	#SW13,2#SWR	:SKIP TYPEOUT IF SET
010624	001072				BNE	4\$	
010626	000004	011552			TYPE	RETURN	
010632	016646	000002			MOV	2(6),-(6)	:PUT ADDRESS OF INSTRUCTION ON STACK
010636	162716	000002			SUB	2(6)	
010642	011605				MOV	(6) TTY	:TYPE (6) IN OCTAL
010644	004767	000212			JSR	%7,PRINTR	:TYPE LEADING ZERO'S
010650	000004	011560			TYPE	SPACE+3	
010654	010005				MOV	R0,TTY	:TYPE R0 IN OCTAL
010656	004767	000200			JSR	%7,PRINTR	:TYPE LEADING ZERO'S
010662	000004	011561			TYPE	SPACE+4	
010666	012703	001002			MOV	#ANS1,R3	:ADDRESS OF DATA
010672	113604				MOVB	2(6)+,R4	:AMOUNT OF DATA IN TABLE
010674	001426				BEO	3\$	
010676	100016				BPL	2\$:TYPE STACK?
010700	016667	000006	170074		MOV	6(6),ANS1	
010706	016667	000010	170070		MOV	10(6),ANS2	
010714	016667	000012	170064		MOV	12(6),ANS3	
010722	016667	000014	170060		MOV	14(6),ANS4	
010730	042704	177600			BIC	#177600,R4	:CLEAR SIGN
010734	000004	011561		2\$:	TYPE	SPACE+4	
010740	012305				MOV	(3)+,TTY	:TYPE (3)+ IN OCTAL
010742	004767	000114			JSR	%7,PRINTR	:TYPE LEADING ZERO'S
010746	005304				DEC	R4	
010750	001371				BNE	2\$	
010752	005700			3\$:	TST	FPS	
010754	100016				BPL	4\$	
010756	000004	011555			TYPE	SPACE	
010762	170367	170034			STST	FEC	
010766	016705	170030			MOV	FEC,TTY	:TYPE FEC IN OCTAL
010772	004767	000064			JSR	%7,PRINTR	:TYPE LEADING ZERO'S
010776	000004	011560			TYPE	SPACE+3	
011002	016705	170016			MOV	FEA,TTY	:TYPE FEA IN OCTAL
011006	004767	000050			JSR	%7,PRINTR	:TYPE LEADING ZERO'S
011012	012604			4\$:	MOV	(6)+,R4	
011014	005737	177570			TST	2#SWR	:HALT ON ERROR
011020	100001				BPL	+.4	:SKIP IF CONTINUE
011022	000000				HALT		:HALT ON ERROR!
011024	032737	001000	177570		BIT	#SW09,2#SWR	:CHECK FOR INHIBIT LOOP ON ERROR
011032	001001				BNE	+.4	:SKIP IF LOOP ON ERROR
011034	000002				PTI		
011036	105067	167737			CLRB	ICNT+1	
011042	032737	000400	177570		BIT	#SW08,2#SWR	:CHECK FOR LOAD MICROBREAK
011050	001233				BNE	KIT	:BRANCH IF NOT
011052	113703	177570			MOVB	2#SWR,R3	:PUT MICROBREAK ADDRESS IN R3
011056	170003				LSUB		:LOAD MICROBREAK
011060	000627				BP	KIT	:LOOP ON TEST UNTIL NO ERRORS

G04

MAINDEC-11-DCFPK-8
DCFPKB.F11

TEST OF LDEXP. STEXP
OCTAL DUMP OF A WORD

MACY11 27.732) 03-SEP-76 14:10 PAGE 45

```

011062 112767 000001 000130 PRINTR: MOVB #1,R4S ;SET ZERO FILL SWITCH
011070 000402 BR .+6
011072 005067 000122 PRINTS: CLR R4S ;SUPRESS LEADING ZERO'S
011076 112767 177772 000115 MOVB #-6,R4S+1 ;SET COUNT
011104 010446 MOV R4,-(6) ;SAVE R4
011106 012704 011210 MCV #3$,R4 ;SET POINTER TO FIRST ASCII CHAR.
011112 105014 CLRB (4) ;CLEAR FIRST BYTE
011114 000405 BR 2$ ;ROTATE FIRST BIT
011116 105014 :$: CLRB (4) ;CLEAR BYTE OF CHARACTER
011120 006105 ROL TTY ;ROTATE BIT INTO C
011122 106114 ROLB (4) ;PACK IT
011124 006105 ROL TTY ;ROTATE BIT INTO C
011126 106114 ROLB (4) ;PACK IT
011130 006105 2$: ROL TTY ;ROTATE BIT INTO C
011132 106114 ROLB (4) ;PACK IT
011134 105714 TSTB (4)
011136 001402 BEQ .+6
011140 105267 000054 INCB R4S
011144 105767 000050 TSTB R4S ;CHECK FILL SWITCH
011150 001402 BEQ .+6
011152 152724 000060 BITB #0,(4)+ ;MAKE INTO ASCII CHAR
011156 105267 000037 INCB R4S+1
011162 001355 BNE 1$ ;REPEAT
011164 022704 011210 CMP #3$,R4
011170 001002 BNE .+6
011172 112724 000060 MOVB #0,(4)+
011176 105014 CLRB (4)
011200 000004 011210 TYPE 3$ ;TYPE IT
011204 012604 MOV (6)+,R4 ;RESTORE R4
011206 000207 RTS PC

011210 000004 3$: .BLKW 4
011220 000000 A4S: 0

011222 005267 000364 ERROR: INC ERRORS ;COUNT ERRORS
011226 132737 000001 000041 BITB #1,0#41 ;AUTO MODE?
011234 001412 BEQ 1$ ;NO!
011236 022767 000010 000346 CMP #10,ERRORS ;TOO MANY?
011244 001006 BNE 1$ ;NOT YET
011246 013700 000042 MOV #42,R0 ;GET ADDRESS
011252 001403 BEQ 1$ ;FORGET IT IF ZERO
011254 005037 000042 CLR #42 ;ZAP 42
011260 004710 JSR PC,PC ;CALL THE MONITOR
011262 000207 :$: RTS PC ;RETURN

```

H04

MAINDEC-11-DCFPK-B
DCFPK8.P11

TEST OF LDEXP, STEXP MACY11 27(732) 03-SEP-76 14:10 PAGE 46
POWER DOWN AND UP ROUTINES

```

011264 012777 011460 000306 POWDOWN: MOV #ILLUP, @UPVEC :SET FOR FAST UP
011272 012777 000340 000302 MOV #340, @UPVEC+2 :PRIO:7
011300 170246 STFPS -(6) :GET THE FPS
011302 170011 SETD
011304 174046 STD ACO, -(6) :SAVE AC'S
011306 174146 STD AC1, -(6)
011310 174246 STD AC2, -(6)
011312 174346 STD AC3, -(6)
011314 172404 LDD AC4, ACO
011316 174046 STD ACO, -(6)
011320 172405 LDD ACS, ACO
011322 174046 STD ACO, -(6)
011324 010046 MOV RO, -(6) :SAVE REGISTERS
011326 010146 MOV R1, -(6)
011330 010246 MOV R2, -(6)
011332 010346 MOV R3, -(6)
011334 010446 MOV R4, -(6)
011336 010546 MOV R5, -(6)
011340 010667 000220 MOV SP, SAVE6 :SAVE SP
011344 012777 011354 000226 MOV #POWUP, @UPVEC :SET UP VECTOR
011352 000000 HALT

011354 016706 000204 POWUP: MOV SAVE6, SP :GET SP
011360 005001 CLR R1 :WAIT LOOP FOR THE TTY
011362 005201 15: INC R1
011364 001376 BNE 15
011366 012605 MOV (6)+, R5 :GET THE REGISTERS
011370 012604 MOV (6)+, R4
011372 012603 MOV (6)+, R3
011374 012602 MOV (6)+, R2
011376 012601 MOV (6)+, R1
011400 012600 MOV (6)+, R0
011402 170011 SETD
011404 172426 LDD (6)+, ACO :RESTORE THE AC'S
011406 174005 STD ACO, ACS
011410 172426 LDD (6)+, ACO
011412 174004 STD ACO, AC4
011414 172726 LDD (6)+, AC3
011416 172626 LDD (6)+, AC2
011420 172526 LDD (6)+, AC1
011422 172426 LDD (6)+, ACC
011424 170126 LDFPS (6)+ :RESTORE FPS
011426 012777 011264 000140 MOV #POWDOWN, @DOWNVEC :SET UP THE POWER DOWN VECTOR
011434 012777 000340 000134 MOV #340, @DOWNVEC+2
011442 000004 011446 TYPE ..+2 :.ASCIZ <15><12>"POWER"
011456 000002 RTI

011460 000000 ILLUP: HALT :THE POWER UP SEQUENCE WAS STARTED
011462 000776 SP .-2 : BEFORE THE POWER DOWN WAS COMPLETE

```

```

011464 010546          .IOT:  MOV      TTY, -(6)          :SAVE TTY
011466 017605 000002          .J    22(6), TTY      :GET ADDRESS TO BE TYPED
011472 105715          IS:   TSTB   (TTY)          :TERMINATOR?
011474 001406          BEQ    2$
011476 112537 177566          MOVB  (TTY)+, 2#177566 :LOAD AND TYPE THE CHARACTER
011502 105737 177564          TSTB  2#177564      :IS THE PRINTER READY?
011506 100375          BPL   -4
011510 030770          BR    1$
011512 017646 000002          2$:  MOV    22(6), -(6)  :GET THE NEXT CHARACTER
011516 062766 000002 300004          ADD   2, 4(6)        :GET ADDRESS TO BE TYPED
011524 022666 000002          CMP   (6)+, 2(6)    :ADD 2 TO THE ADDRESS
011530 001006          BNE   3$            :IS IT .+2?
011532 062705 000002          ADD   2, TTY        :NO
011536 042705 000001          BIC   2, TTY        :ADD 2 TO THE ADDRESS
011542 010566 000002          MOV   TTY, 2(6)    :BACK UP TO AN EVEN BYTE
011546 012605          3$:  MOV   (6)+, TTY    :RESTORE ADDRESS
011550 000002          RTI   :RESTORE TTY
                                :RETURN

011552 005015          000  RETURN: .ASCIZ  <15><12>  :RETURN AND LINEFEED
011555          015 020012 020040 SPACE: .ASCIZ  <15><12> " " :RETURN AND 3 SPACES
011562          000

011564 011564          .EVEN
011564 000000          SAVE6: 0
011566 172160          FPTADR: 172160      :FLOATING POINT ADDRESS ON THE 1: 20
011570 000244 000246          FPVECT: 244, 246  :FLOATING POINT VECTOR ADDRESS
011574 000024 000026          DWNVEC: 24, 26    :POWER DOWN VECTOR ADDRESS
011600 000024 000026          UPVEC:  24, 26   :POWER UP VECTOR ADDRESS
011604 000000          .TYPE: 0
011606 000000          TRPB:  0
011610 000000          LAD:   0          :LOOP ADDRESS
011612 000000          ERRORS: 0         :ERROR COUNT
011614 000377          TIMES: 377       :ITERATION COUNT
000001          .END

```


FPI11	002124	707#												
FPI12	002212	736#	747											
FPI13	002330	774#	785											
FPI14	002446	811#												
FPI15	002534	839#												
FPI16	002626	868#												
FPI17	002716	897#												
FF2	001302	501#												
FPI20	003002	925#												
FPI21	003100	954#												
FPI22	003176	983#												
FPI23	003274	1012#												
FPI24	003372	1041#												
FPI25	003470	1070#												
FPI26	003566	1099#												
FPI27	003664	1128#												
FPI3	001370	529#												
FPI32	004166	1218#												
FPI33	004306	1254#												
FPI34	004426	1290#												
FPI35	004544	1327#												
FPI36	004662	1364#												
FPI37	005000	1401#												
FPI4	001456	557#												
FPI40	005116	1438#												
FPI41	005234	1475#												
FPI42	005352	1512#												
FPI43	005470	1550#	1561											
FPI44	005636	1597#	1608											
FPI45	006004	1644#	1655											
FPI46	006152	1690#												
FPI47	006270	1727#												
FFI5	001544	585#												
FPI50	006412	1765#												
FPI51	006532	1803#												
FPI6	001632	613#												
FPI7	001720	641#												
FPS	=%000000	381#	418*	474*	475	502*	503	530*	531	558*	559	586*	587	614*
		615	642*	643	671*	673	708*	709	737*	739	775*	777	812*	813
		840*	841	869*	870	898*	899	928*	929	957*	958	996*	997	1015*
		1016	1044*	1045	1073*	1074	1102*	1103	1131*	1132	1161*	1162	1191*	1192
		1226*	1227	1262*	1263	1291*	1292	1328*	1329	1365*	1366	1402*	1403	1439*
		1440	1476*	1477	1513*	1514	1551*	1553	1598*	1600	1645*	1647	1691*	1692
		1728*	1729	1766*	1767	1804*	1805	1843*	1844	1873*	1874	1903*	1904	1933*
		1934	1963*	1964	1993*	1994	2023*	2024	2053*	2054	2084*	2085	2115*	2116
		2151*	2152	2188*	2189	2283								
		442	2421#											
FPTADR	011566	456*	457*	2422#										
FPIEC*	011570	378#	477	482	486	505	510	514	533	538	542	561	566	570
HLT	= 104000	589	594	598	617	622	626	645	650	654	675	679	682	688
		692	711	716	720	741	745	749	754	758	779	783	787	792
		796	815	820	824	843	848	852	872	877	881	901	906	910
		931	935	939	960	964	968	989	993	997	1018	1022	1026	1047
		1051	1055	1076	1080	1084	1105	1109	1113	1134	1138	1142	1154	1168
		1172	1194	1198	1202	1229	1234	1238	1265	1270	1274	1294	1299	1303
		1307	1311	1331	1336	1340	1344	1348	1368	1373	1377	1381	1395	1405

SW14	= 040000	398#	2232
SW15	= 100000	397#	
TIMES	011614	2238	2429#
TRPB	011606	2211*	2426#
TST1	001204	467	471#
TST10	001776	664	668#
TST11	002114	701	705#
TST12	002202	730	734#
TST13	002320	768	772#
TST14	002436	805	809#
TST15	002524	833	837#
TST16	002612	861	865#
TST17	002702	890	894#
TST2	001272	495	499#
TST20	002772	919	923#
TST21	003070	948	952#
TST22	003166	977	981#
TST23	003264	1006	1010#
TST24	003362	1035	1039#
TST25	003460	1064	1068#
TST26	003556	1093	1097#
TST27	003654	1122	1126#
TST3	001360	523	527#
TST30	003752	1151	1155#
TST31	004052	1181	1185#
TST32	004152	1211	1215#
TST33	004272	1247	1251#
TST34	004416	1283	1287#
TST35	004534	1320	1325#
TST36	004652	1357	1362#
TST37	004770	1394	1399#
TST4	001446	551	555#
TST40	005106	1431	1436#
TST41	005224	1468	1473#
TST42	005342	1505	1510#
TST43	005460	1543	1548#
TST44	005626	1590	1595#
TST45	005774	1637	1642#
TST46	006142	1683	1688#
TST47	006260	1720	1725#
TST5	001534	579	583#
TST50	006375	1757	1762#
TST51	006516	1795	1800#
TST52	006636	1833	1838#
TST53	006740	1863	1868#
TST54	007042	1893	1898#
TST55	007144	1923	1928#
TST56	007245	1953	1958#
TST57	007350	1983	1988#
TST6	001622	607	611#
TST60	007452	2013	2018#
TST61	007554	2043	2048#
TST62	007656	2073	2078#
TST63	007762	2104	2109#
TST64	010066	2135	2140#
TST65	010212	2172	2177#

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DCPKB.P11

TEST OF LDEXP. STEXP
REFERENCE TABLE -- MACRO NAMES

MACY1: 271732 03-SEP-76 14:10 PAGE 56

TEST	LDEXP.	STEXP	MACY1	271732	03-SEP-76	14:10	PAGE	56
2266	2279	2287	2290					
530	550	586	614	642	671	708	737	775
1044	1051	1044	1073	1102	1131	1151	1191	1226
1551	1551	1551	1590	1645	1691	1728	1766	1804
2022	2052	2084	2115	2151	2188			
1350	1381	1424	1461	1498	1535	1582	1629	1676
1891	1916	1946	1976	2006	2036			
2028	2051	2086	2115					

TEST OF LDEXP, STEXP MACY11 27(732) CROSS REFERENCE TABLE -- PERMANENT SYMBOLS

132	1326	1363	1400	1437	1474	1511	1549	1596	1643	1689	1726	1763	1801	1839
133	1364	1401	1438	1475	1512	1550	1597	1644	1690	1727	1765	1803	1841	
134	1365	1402	1439	1476	1513	1551	1598	1645	1691	1728	1766	1804	1842	
135	1366	1403	1440	1477	1514	1552	1599	1646	1692	1729	1767	1805	1843	
136	1367	1404	1441	1478	1515	1553	1600	1647	1693	1730	1768	1806	1844	
137	1368	1405	1442	1479	1516	1554	1601	1648	1694	1731	1769	1807	1845	
138	1369	1406	1443	1480	1517	1555	1602	1649	1695	1732	1770	1808	1846	
139	1370	1407	1444	1481	1518	1556	1603	1650	1696	1733	1771	1809	1847	
140	1371	1408	1445	1482	1519	1557	1604	1651	1697	1734	1772	1810	1848	
141	1372	1409	1446	1483	1520	1558	1605	1652	1698	1735	1773	1811	1849	
142	1373	1410	1447	1484	1521	1559	1606	1653	1699	1736	1774	1812	1850	
143	1374	1411	1448	1485	1522	1560	1607	1654	1700	1737	1775	1813	1851	
144	1375	1412	1449	1486	1523	1561	1608	1655	1701	1738	1776	1814	1852	
145	1376	1413	1450	1487	1524	1562	1609	1656	1702	1739	1777	1815	1853	
146	1377	1414	1451	1488	1525	1563	1610	1657	1703	1740	1778	1816	1854	
147	1378	1415	1452	1489	1526	1564	1611	1658	1704	1741	1779	1817	1855	
148	1379	1416	1453	1490	1527	1565	1612	1659	1705	1742	1780	1818	1856	
149	1380	1417	1454	1491	1528	1566	1613	1660	1706	1743	1781	1819	1857	
150	1381	1418	1455	1492	1529	1567	1614	1661	1707	1744	1782	1820	1858	
151	1382	1419	1456	1493	1530	1568	1615	1662	1708	1745	1783	1821	1859	
152	1383	1420	1457	1494	1531	1569	1616	1663	1709	1746	1784	1822	1860	
153	1384	1421	1458	1495	1532	1570	1617	1664	1710	1747	1785	1823	1861	
154	1385	1422	1459	1496	1533	1571	1618	1665	1711	1748	1786	1824	1862	
155	1386	1423	1460	1497	1534	1572	1619	1666	1712	1749	1787	1825	1863	
156	1387	1424	1461	1498	1535	1573	1620	1667	1713	1750	1788	1826	1864	
157	1388	1425	1462	1499	1536	1574	1621	1668	1714	1751	1789	1827	1865	
158	1389	1426	1463	1500	1537	1575	1622	1669	1715	1752	1790	1828	1866	
159	1390	1427	1464	1501	1538	1576	1623	1670	1716	1753	1791	1829	1867	
160	1391	1428	1465	1502	1539	1577	1624	1671	1717	1754	1792	1830	1868	
161	1392	1429	1466	1503	1540	1578	1625	1672	1718	1755	1793	1831	1869	
162	1393	1430	1467	1504	1541	1579	1626	1673	1719	1756	1794	1832	1870	
163	1394	1431	1468	1505	1542	1580	1627	1674	1720	1757	1795	1833	1871	
164	1395	1432	1469	1506	1543	1581	1628	1675	1721	1758	1796	1834	1872	
165	1396	1433	1470	1507	1544	1582	1629	1676	1722	1759	1797	1835	1873	
166	1397	1434	1471	1508	1545	1583	1630	1677	1723	1760	1798	1836	1874	
167	1398	1435	1472	1509	1546	1584	1631	1678	1724	1761	1799	1837	1875	
168	1399	1436	1473	1510	1547	1585	1632	1679	1725	1762	1800	1838	1876	
169	1400	1437	1474	1511	1548	1586	1633	1680	1726	1763	1801	1839	1877	
170	1401	1438	1475	1512	1549	1587	1634	1681	1727	1764	1802	1840	1878	
171	1402	1439	1476	1513	1550	1588	1635	1682	1728	1765	1803	1841	1879	
172	1403	1440	1477	1514	1551	1589	1636	1683	1729	1766	1804	1842	1880	
173	1404	1441	1478	1515	1552	1590	1637	1684	1730	1767	1805	1843	1881	
174	1405	1442	1479	1516	1553	1591	1638	1685	1731	1768	1806	1844	1882	
175	1406	1443	1480	1517	1554	1592	1639	1686	1732	1769	1807	1845	1883	
176	1407	1444	1481	1518	1555	1593	1640	1687	1733	1770	1808	1846	1884	
177	1408	1445	1482	1519	1556	1594	1641	1688	1734	1771	1809	1847	1885	
178	1409	1446	1483	1520	1557	1595	1642	1689	1735	1772	1810	1848	1886	
179	1410	1447	1484	1521	1558	1596	1643	1690	1736	1773	1811	1849	1887	
180	1411	1448	1485	1522	1559	1597	1644	1691	1737	1774	1812	1850	1888	
181	1412	1449	1486	1523	1560	1598	1645	1692	1738	1775	1813	1851	1889	
182	1413	1450	1487	1524	1561	1599	1646	1693	1739	1776	1814	1852	1890	
183	1414	1451	1488	1525	1562	1600	1647	1694	1740	1777	1815	1853	1891	
184	1415	1452	1489	1526	1563	1601	1648	1695	1741	1778	1816	1854	1892	
185	1416	1453	1490	1527	1564	1602	1649	1696	1742	1779	1817	1855	1893	
186	1417	1454	1491	1528	1565	1603	1650	1697	1743	1780	1818	1856	1894	
187	1418	1455	1492	1529	1566	1604	1651	1698	1744	1781	1819	1857	1895	
188	1419	1456	1493	1530	1567	1605	1652	1699	1745	1782	1820	1858	1896	
189	1420	1457	1494	1531	1568	1606	1653	1700	1746	1783	1821	1859	1897	
190	1421	1458	1495	1532	1569	1607	1654	1701	1747	1784	1822	1860	1898	
191	1422	1459	1496	1533	1570	1608	1655	1702	1748	1785	1823	1861	1899	
192	1423	1460	1497	1534	1571	1609	1656	1703	1749	1786	1824	1862	1900	
193	1424	1461	1498	1535	1572	1610	1657	1704	1750	1787	1825	1863	1901	
194	1425	1462	1499	1536	1573	1611	1658	1705	1751	1788	1826	1864	1902	
195	1426	1463	1500	1537	1574	1612	1659	1706	1752	1789	1827	1865	1903	
196	1427	1464	1501	1538	1575	1613	1660	1707	1753	1790	1828	1866	1904	
197	1428	1465	1502	1539	1576	1614	1661	1708	1754	1791	1829	1867	1905	
198	1429	1466	1503	1540	1577	1615	1662	1709	1755	1792	1830	1868	1906	
199	1430	1467	1504	1541	1578	1616	1663	1710	1756	1793	1831	1869	1907	
200	1431	1468	1505	1542	1579	1617	1664	1711	1757	1794	1832	1870	1908	

MACY11 27(732) -- PERMANENT SYMBOLS

TEST OF CROSS REFERENCE TABLE	LD EXP	ST EXP	MACY11 27(732)	03-SEP-76	14:10	PAGE 60									
1016	1045	1074	1103	1132	1162	1192	1227	1263	1280	1292	1317				
1391	1403	1428	1440	1465	1477	1502	1514	1539	1552	1586	1599				
1692	1717	1729	1754	1767	1792	1805	1844	1874	1904	1934	1964				
2085	2116	2152	2195												
562	591	619	647	677	681	713	743	747	781	785	817				
933	962	991	1020	1049	1078	1107	1136	1166	1196	1231	1267				
1407	1444	1481	1518	1557	1561	1604	1608	1651	1655	1696	1732				
1878	1908	1938	1968	1998	2028	2058	2089	2120	2156	2193					
422	460	488	516	544	572	600	628	656	694	722	760				
883	912	941	970	999	1028	1057	1086	1115	1144	1174	1204				
1313	1350	1387	1424	1461	1498	1535	1572	1609	1646	1683	1720				
1886	1916	1946	1976	2006	2036	2066	2097	2128	2165	2202	2238				
2398															
460															
372	412	488	516	544	572	600	628	656	694	722	760				
1082	1095	1120	1170	1199	1228	1257	1286	1315	1344	1374	1404				
1706	1713	1750	1791	1828	1865	1902	1939	1976	2013	2050	2087				
2096	2103	2140	2181	2218	2255	2292	2329	2366	2403	2440	2477				
2398															
412	422	460	2202	2252	2305	2349	2398								

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

*CCFPKB,DCFPKB,SEQ/SOL/CRF/DS:EPFZ/EN:ABS=DSKM:CCFPKB.P:1
TIME: 16.24 3 SECONDS
PAGE: 10: 27.46=60.6
CORE USED: 10K (20 PAGES)

