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B-TC-KDF11-B-1			FIELD MAINTENANCE PRINT SET			<table><tr><td colspan="2">UNIT VARIATIONS COVERED BY THIS PRINT SET</td></tr><tr><td>KDF11-BA</td><td></td></tr><tr><td>KDF11-BB</td><td></td></tr><tr><td>KDF11-BC</td><td></td></tr><tr><td>KDF11-BD</td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr><tr><td></td><td></td></tr></table> KDF11-B Field Maintenance Print Set Digital Equipment Corporation Print Set Part Number MP- 01236			UNIT VARIATIONS COVERED BY THIS PRINT SET		KDF11-BA		KDF11-BB		KDF11-BC		KDF11-BD																									
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D-UA-KDF11-B-0			UNIT ASSY, KDF11-B																																							
K-PL-KDF11-B-DBP			PARTS LIST, KDF11-B																																							
B-DD-M8189-0			DRAWING DIRECTORY, M8189																																							
D-UA-M8189-0-0			UNIT ASSY, M8189																																							
K-PL-M8189-0-DBP			M8189 PARTS LIST																																							
D-CS-M8189-0-K1			TIMING LOGIC & MIB DECODE																																							
D-CS-M8189-0-K2			BUS CONTROL LOGIC																																							
D-CS-M8189-0-K3			SERVICE RESET & ODT LOGIC																																							
D-CS-M8189-0-K4			F11 CHIPS & FIXED DATA																																							
D-CS-M8189-0-K5			CDAL/BDAL INTERFACE																																							
D-CS-M8189-0-K6			CDAL/IDAL INTERFACE																																							
D-CS-M8189-0-K7			IDAL ADRS DECODE																																							
D-CS-M8189-0-K8			BOOT & CLOCK LOGIC																																							
D-CS-M8189-0-K9			CONSOLE & SECOND SLU																																							
D-CS-M8189-0-K10			BAUD RATE & -12V																																							
D-CS-M8189-0-K11			CONFIGURATION GUIDE																																							
B-DD-KEF11-BB			CIS MICROCODE HYBRID																																							
REVISION HISTORY			DRN. P. BROSNIHAN			DATE 4/4/81			TITLE digital																																	
			CHK'D. G. MARINI			DATE 4/4/81			FIELD MAINTENANCE PRINT SET KDF11-B																																	
			DES. ENG. Jay Nichols			DATE 22-JUN-81																																				
			RESP. ENG. C. P. Valente			DATE 22-JUN-81			DOCUMENT NUMBER																																	
			FIELD SERVICE G. Branch			DATE 23-JUN-81			SIZE CODE NUMBER REV																																	
			NEXT HIGHER DOC. 11/23-B						B TC KDF11-B-1 B																																	
DATE 6/17/81			ECO NUMBER INITIALIZE			SHEET 1 OF 1																																				
REV. A			B																																							
12 APR 82			KDF11-B-ML 001																																							

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TABLE 1. BOOT/DIAG. SELECTION GUIDE FOR SWITCH PACK EI02

SWITCH	ON	OFF
S1	EXECUTE CPU TEST	SKIP CPU TEST
S2	EXECUTE MEMORY TEST	SKIP MEMORY TEST
S3	EXECUTE DECNET BOOT PROGRAM SELECTED BY S4-S7 PER TABLE 2.	EXECUTE STANDARD BOOT PROGRAM PER SWITCH S4
S4	EXECUTE DIALOGUE BOOT PROGRAM. OPERATOR RESPONDS WITH MNEMONIC PER TABLE 3.	EXECUTE AUTOMATIC (TURNKEY) BOOT PROGRAM SELECTED BY S5-S8 PER TABLE 3.

TABLE 2. DECNET BOOT DEVICE SELECTION (S3 ON)

S4	S5	S6	S7	DEVICE
ON	X	X	X	DUV11
OFF	ON	X	OFF	DLV11-E
OFF	ON	X	ON	DLV11-F

NOTE: X = DON'T CARE

TABLE 3. STANDARD BOOT DEVICE SELECTION (S3 OFF)

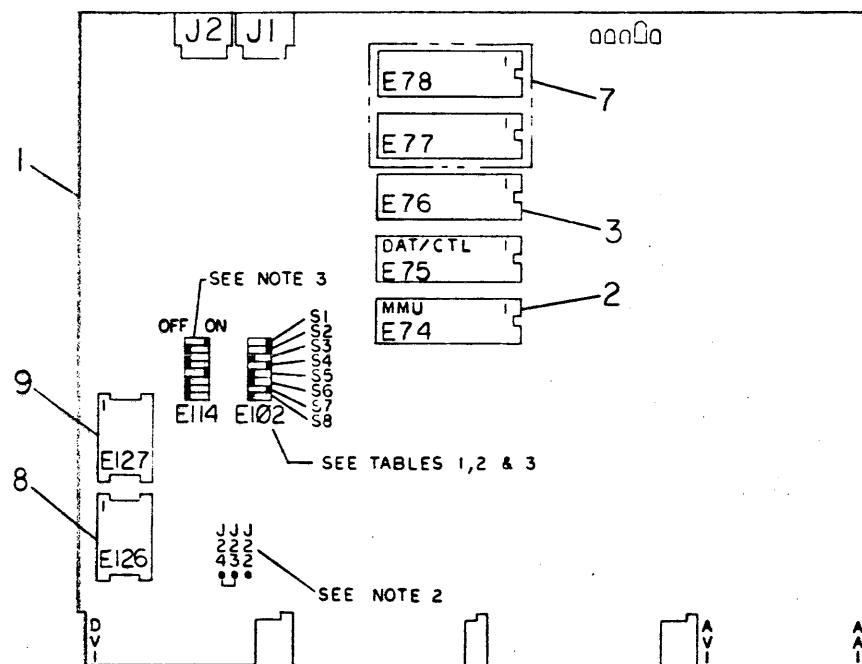
MNEMONIC	S5	S6	S7	S8	DEVICE
DKn; n<8	OFF	OFF	OFF	ON	RK05
DLn; n<4	OFF	OFF	ON	OFF	RLO1/RLO2
DDn; n<2	OFF	OFF	ON	ON	TU58
DXn; n<2	OFF	ON	OFF	OFF	RX01
DYn; n<2	OFF	ON	ON	OFF	RX02

LEGEND

NUMBER	DESCRIPTION
KDF11-BA	M8189-00 PLUS KTF11-AA & BOOT/DIAG. ROMS
KDF11-BB	M8189-00 PLUS KTF11-AA, KEF11-AA & BOOT/DIAG. ROMS
KDF11-BC	M8189-00 PLUS BOOT/DIAG. ROMS
KDF11-BD	M8189-00 PLUS KTF11-AA, KEF11-BB & BOOT/DIAG. ROMS

NOTES:

1. WHEN INSTALLING ITEMS 2,3,8&9 MAKE SURE THAT PIN 1 IS LOCATED AS SHOWN.
2. WHEN ITEMS 8 & 9 ARE MASKED ROMS CONNECT J23 TO J24 WITH J22 NOT CONNECTED. WHEN ITEMS 8 & 9 ARE EPROMS CONNECT J22 TO J23 WITH J24 NOT CONNECTED.
3. BAUD RATE SELECTION. SEE D-CS-M8189-0-K11. SETTINGS SHOWN PRODUCE 9600 BAUD EACH LINE.
4. KEF11-BB=CIS MICROCODE HYBRID. CONTAINS CIS MICROCODE TO IMPLEMENT THE COMMERCIAL INSTRUCTION SET.



CAUTION: OFF SHEET PARTS LIST EXISTS, SEE K-PL-KDF11-B-DBP

REV.	INITIAL	DATE
A		
B		
C		
D		
E		
F		
G		
H		
I		
J		
K		
L		
M		
N		
O		
P		
Q		
R		
S		
T		
U		
V		
W		
X		
Y		
Z		

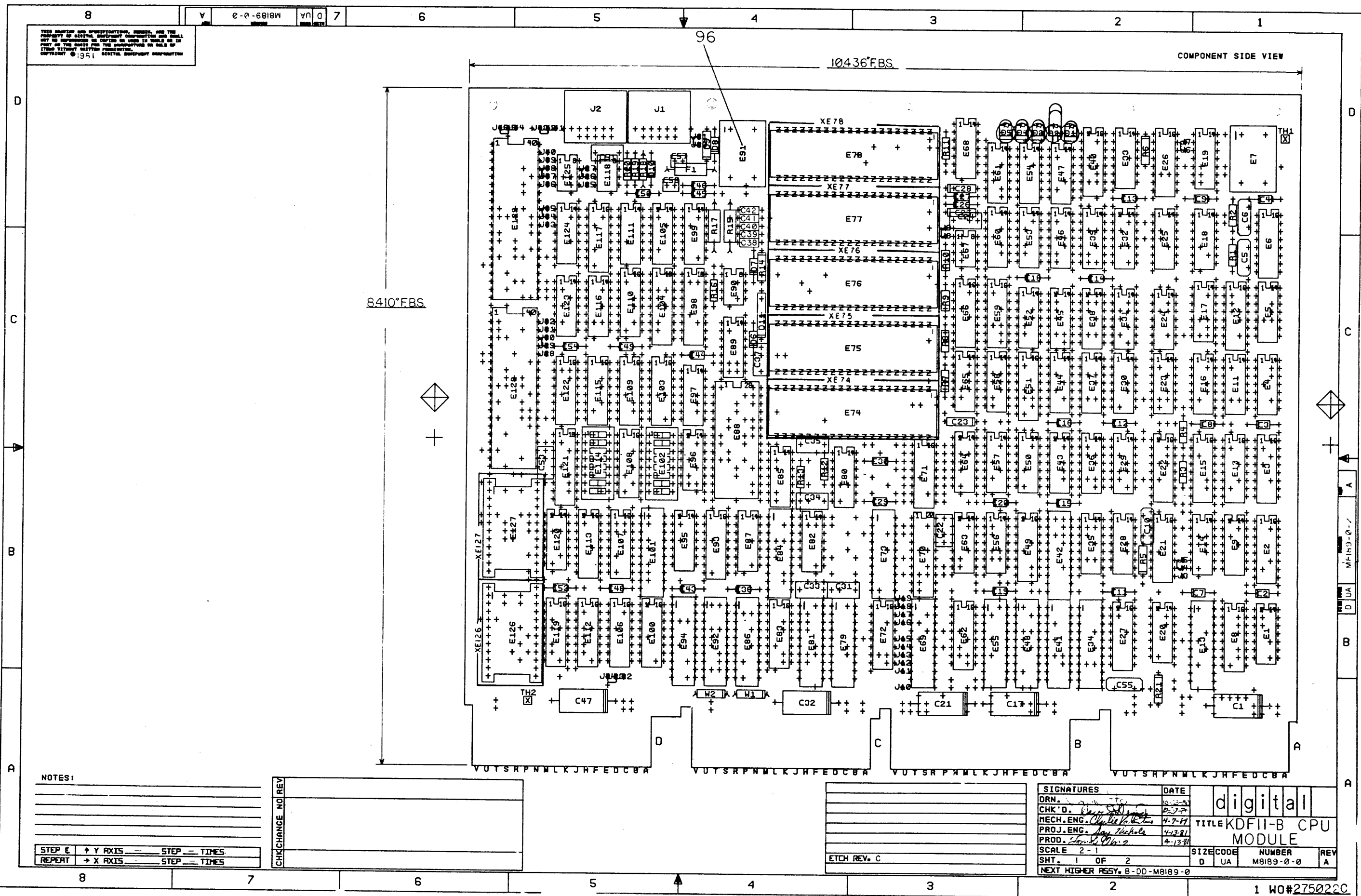
QUANTITY & VARIATION	DESCRIPTION	DWG. PART NO.	ITEM NO.
THIRD ANGLE PROJECTION	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		
REMOVE BURRS AND BREAK SHARP CORNERS	ANGLES		
DO NOT SCALE DWG	CLASS OF ACCURACY		
MATERIAL	SURFACE QUALITY		
FINISH	MICROINCHES		
	CHECK ONE		
	MEDIUM		
	PREFERRED		
	DRN		
	CHK'D		
	ENG.		
	PROJ. ENG.		
	PROD.		
	NEXT HIGHER ASSY.		
	B. D. KDF11-B		
	SCALE		
	SHEET		
	FIRST USED ON		
	11/23-B digital		
	TITLE		
	KDF11-B CPU UNIT ASSEMBLY		
	SIZE		
	CODE		
	NUMBER		
	REV		
	B		

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QUANTITY PER VARIATION		
					BA	BB	BC
1	1		M8189-00	KDF11-B QUAD HT CPU,0 AND 0-22 B	1	1	1
2	2		2115542-01	DC 304 F11 MMU,FLOAT PT REG	1	1	-
3	3		5700001-00	HYBRID ASSY FLOATING POINT	-	1	-
4	4		23244E2-00	E2-04	1	1	1
5	5		23245E2-00	E2-04	1	1	1
6	6		H3270-00	TEST CONNECTOR	1	1	1

7 NOTE: ITEM #6, THE WRAP-AROUND-TEST JUMPER FOR CONNECTOR J2, IS NOT SHOWN ON UA DWG.
8 NOTE: FOR BULK INTERPLANT SHIPMENT OF MODULES REFER TO PACKAGING INSTRUCTION A-SP-3700053-0-0
9 NOTE: FOR SHIPMENT OF INDIVIDUAL MODULES REFER TO PACKAGING INSTRUCTION A-SP-3700069-0-0

REVISION HISTORY			BASIC PART NO: KDF11		DRN: P. GROSNIHAN		DATE: 21-JAN-81		D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A		CHK'D: G. MARINI		DATE: 27-MAY-81		TITLE PARTS LIST			
INITIAL		A	SECTION. VARIATION INDEX						KDF11-B CPU UNIT ASSEMBLY			
			(A) BA,BB,BC									
			(B)		DES.ENG.: C. VALENTINE		DATE: 21-JAN-81					
			(C)		RESP.ENG.: C. VALENTINE		DATE: 21-JAN-81		DOCUMENT NUMBER			
			(D)						SIZE CODE NUMBER REV			
			(E)		MFG.ENG.: T. OLMO		DATE: 27-APR-81		K	PL	KDF11-B-DBP	A
			(F)		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
					C-UA-KDF11-B-0		#B-DD-KDF11-B		Z2423A.PLS		4	
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NOTES:

STEP E	↑ Y AXIS	STEP	TIMES
REPEAT	→ X AXIS	STEP	TIMES

CHK	CHANGE	NO	REV

ETCH REV. C

SIGNATURES	DATE
DRN.	0-1-61
CHK'D.	0-1-61
MECH. ENG.	4-7-61
PROJ. ENG.	4-12-61
PROD.	4-13-61
SCALE 2-1	SIZE CODE
SHT. 1 OF 2	NUMBER
NEXT HIGHER ASSY. B-00-M8189-0	REV

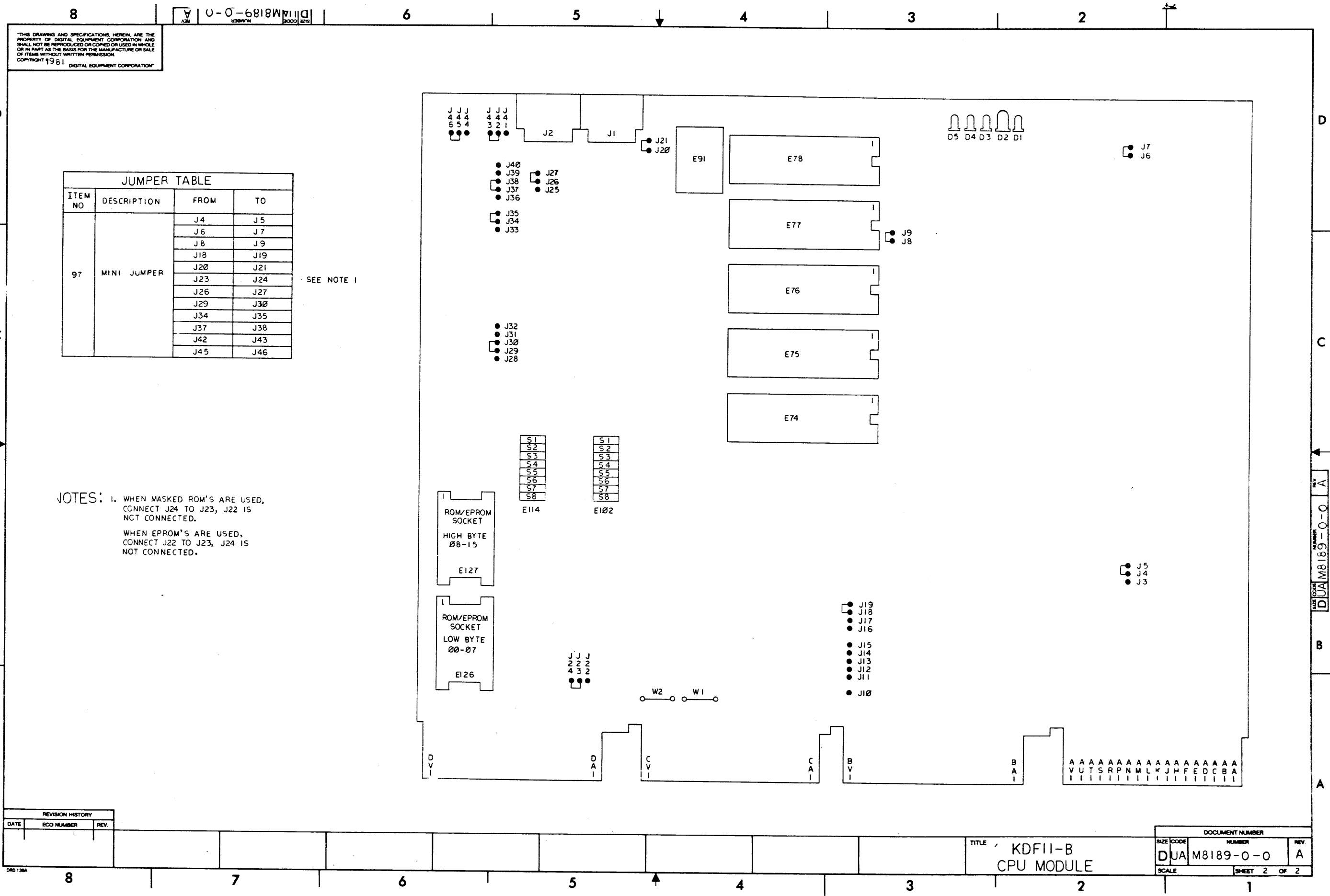
digital

TITLE KDFII-B CPU MODULE

NUMBER M8189-0-0

REV A

1 WO#275022C



LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
					00		
1	1	D-MD-5014313-0-0	5014313-00	DRILL AND ETCH BD.	1		
2	2		1000022-00	270.0 MMF 100V 5%200PPM MICA	1		C10
3	3		1000024-00	470.0 MMF 100V 5%200PPM MICA	3		C5,C6,C53
4	4		1001610-00	.01 MFD 50V +80-20% 25U CER	3		C26,C27,C30
5	5		1012784-00	.047 MFD 50V +80-20% CER	29		C2-C4,C7-C9,C11-C16,C18-C20,C23,
						CONT	C25,C28,C29,C36,C43-C46,C48-C50,
						CONT	C52,C54
6	6		1016681-00	.47 MFD 50V +80-20% CER	14		C22,C24,C31,C33-C35,C37-C42,C51,
						CONT	C53
7	7		1017472-00	10 MFD 35V +50-10% AL EL	5		C1,C17,C21,C32,C47
8	8		1011740-02	1800.0 MMF 50V 10% CER	2		C56,C57
9	9		1109977-00	1N 749A VZ= 4.3 5%	1		D9
10	10		1112689-00	LED .8MCD@16MA VF=5V	4		D1,D3-D5
11	11		1114384-00	LED 105MW 35MA GREEN	1		D2
12	12		1100114-00	D 6G4 Q5\75PCB PIV= 25V SP	3		D6-D8
13	13		1110967-00	1N 4005 PIV=600 I= 1A D041 SI	1		D10
14	14		1105508-00	1N 823 VZ= 6.2 5% .40W Y	1		D11
15	15		1213506-04	HEADER 10POS RT ANGLE W/3 SI	2		J1,J2
16	16		1215006-06	SOCKET 24PIN IC LOW PROFILE	2		XE126,XE127
17	17		1216116-00	SOCKET 40PIN	5		XE74-XE78
18	18		1210929-02	FUSE, SUB-MINI 1.000A, 125V, A	1		F1
19	19		1211164-04	SW,DIP 1P 1A 8POS	2		E102,E114
20	20		1213113-01	HANDLE,MODULE,	1		
21	21		1300229-00	100.0 .25 W 5.0 % CC	1		R7
22	22		1300316-00	470.0 .25 W 5.0 % CC	3		R6,R12,R16
23	23		1300365-00	1.0 K .25 W 5.0 % CC	4		R3,R4,R8,R11
24	24		1300488-00	12.0 K .25 W 5.0 % CC	2		R19,R20
25	25		1301322-00	180.0 .25 W 5.0 % CC	1		R13
26	26		1301808-00	22.0 K .25 W 5.0 % CC	1		R18
27	27		1302177-00	47.0 K .25 W 5.0 % CC	2		R1,R2

REVISION HISTORY		BASIC PART NO: MB189		DRN: R. BUREAU	DATE: 11-JUL-80	DIGITAL			
ENG	ECO NUMBER	REV	SECTION A OF A			TITLE		PARTS LIST	
---	INITIAL	A	SECTION VARIATION INDEX	CHK'D: BOB BARILONE	DATE: 11-JUL-80	KDF11-B CPU MODULE			
			[A] 00						
			[B]						
			[C]	DES.ENG: C.A. VALENTINE	DATE: 14-APR-81				
			[D]						
			[E]						
			[F]	RESP.ENG.: JAY NICHOLS	DATE: 14-APR-81	DOCUMENT NUMBER			
			[H]						
			[J]			SIZE	CODE	NUMBER	REV
			[K]	MFG.ENG.: TOMAS OLMO	DATE: 14-APR-81	K	PL	MB189-0-DBP	A
			[L]						
			[M]	ASSEMBLY NUMBER:	TOP DOCUMENT NUMBER:	FILE NAME:		EDIT #	
			[N]	D-UA-MB189-0-0	B-DD-MB189-0-0	Z1037A.PLS		17	
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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
					00		
28	28		1302602-00	56.0 .25 W 5.0 % CC	1		R5
29	29		1309422-00	5.10 .25 W 5.0 % CC	2		R9,R10
30	30		1311003-01	R NETWORK 14-180 14-390 16PIN	3		E8,E27,E62
31	31		1301320-00	1.20 K .25 W 5.0 % CC	1		R21
32	32		1300005-03	R NETWORK 13-10K 5.0 % 14PIN	1		E123
33	33		1300005-07	R NETWORK 15-4.7K 5.0 % 16PIN	3		E72,E108,E122
34	34		1300228-00	100.0 .50 W 5.0 % CC	2		R15,R17
35	35		1303178-00	620.0 .25 W 5.0 % CC	1		R14
36	36		1811660-02	OSCILLATOR, XTAL 5.0688 MHZ	1		E91
37	37		1811660-20	OSCILLATOR, XTAL 26.666 MHZ	1		E7
38	38		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	8		E17,E19,E29,E30,E36,E38,E43,E50
39	39		1910548-00	74S157 MUX 1 OF 2 (QUAD)	1		E15
40	40		1910550-00	74S174 FF-D HEX	5		E3,E10,E21,E26,E51
41	41		1910957-00	74S175 FF-D QUAD COMMON CLO	4		E12,E22,E40,E59
42	42		1911117-00	DEC 8838 TRANSCEIVER,BUS,QUA	1		E2
43	43		1911469-00	DEC 8640 RECEIVER,BUS,QUAD,U	3		E1,E20,E63
44	44		1909705-00	DEC 8881 NAND GATE-QUAD 2IN 0	1		E9
45	45		1911675-00	74S138 DECODER/DEMUX 3-8 LIN	1		E66
46	46		1911712-00	74S51 AND-OR GATE-INVERT D	1		E45
47	47		1912098-00	0026 DRIVER,MOS CLOCK,2	1		E90
48	48		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	3		E37,E52,E56
49	49		1912389-00	74S08 AND GATE-QUAD 2IN,PO	4		E11,E28,E39,E96
50	50		1912647-00	LS257 MUX 1 OF 2 (QUAD)	6		E83,E100,E106,E110,E113,E119
51	51		1912649-00	LS75 LATCH 4BIT,BISTABLE	1		E49
52	52		1912697-00	LS174 FF-D HEX W/CLEAR	3		E93,E107,E112
53	53		1912730-00	DC 003 INTERRUPT,2 CIRCUIT	2		E98,E104
54	54		1912746-00	DEC 74S37 NAND GATE-QUAD 2IN	1		E61
55	55		1912801-00	LS02 NOR-GATE-QUAD 2IN	1		E85
56	56		1912803-00	74LS04 INVERTER GATE,HEX	1		E89
57	57		1912804-00	LS05 INVERTER GATE-HEX 1I	2		E82,E87
58	58		1912805-00	LS08 AND GATE-QUAD 2IN,PO	1		E65
59	59		1912807-00	LS10 NAND GATE-TRIPLE 3IN	2		E25,E95
60	60		1912808-00	LS11 AND GATE-TRIPLE 3IN	1		E14
61	61		1912810-00	LS20 NAND GATE-DUAL 4IN	2		E53,E60
62	62		1912816-00	LS32 OR GATE-QUAD 2IN,POS	2		E120,E124
63	63		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	4		E5,E80,E111,E64
64	64		1912846-00	LS155 DECODER,2 OF 4 (DUAL)	3		E103,E109,E115
65	65		1912853-00	LS175 FF-D QUAD	1		E117
66	66		1912857-00	LS197 COUNTER,BINARY,PRESET	1		E105
67	67		1912858-00	LS221 ONE SHOT-DUAL,SCHMIT	1		E6
68	68		1912863-00	LS273 FF-D OCTAL W/CLEAR	1		E86
69	69		1912868-00	LS299 SHIFT REG. 8-BIT UNI	2		E92,E101
70	70		1913340-00	74S32 OR GATE-QUAD 2IN	4		E4,E44,E97,E116
71	71		1913462-00	74S240 OCTAL BUFFER,INVERTI	3		E42,E73,E79
72	72		1913671-00	74S374 FF-D OCTAL TRISTATE	1		E70
73	73		1913777-00	LS240 DRIVER,LINE,OCTAL,T	1		E94
74	74		1914085-00	74S260 NOR GATE-DUAL,POS	1		E23
75	75		1915218-00	LS245 TRANSCEIVER,BUS,OCT	2		E81,E84

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							KDF11-B CPU MODULE				M8189-0-DBP	A

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
76	76		1915305-00	AM 2908 TRANSCEIVER,BUS,LATCH	6	E13,E34,E41,E48,E55,E69
77	77		1915415-00	9636 DRIVER,DUAL,EIA RS-	1	E118
78	78		1915416-00	9637 RECEIVER,DUAL,RS-42	1	E125
79	79		1916028-01	9643 DRIVER,TTL TO MOS,D	1	E67
80	80		1910532-00	74S00 NAND GATE-QUAD 2IN	6	E16,E18,E24,E31,E35,E54
81	81		1910534-00	74S04 INVERTER GATE-HEX 1I	4	E33,E58,E68,E99
82	82		1910535-00	74S05 INVERTER GATE-HEX 1	1	E57
83	83		1910536-00	74S10 NAND GATE-TRIPLE 3IN	1	E32
84	84		1910537-00	74S11 AND GATE-TRIPLE 3INP	1	E46
85	85		1910542-00	74S64 A-O-I GATE 4-2-3-2	1	E47
86	87		2112623-00	DUAL BAUD RATE GEN/PROG DIVIDER,	1	E121
87	88		2113937-01	6402-1 UART,UNIV.,ASYNCHRO	2	E128,E129
88	91		23061C6-00	C6-01	1	E88
89	92		5700000-01	DAT/CTL HYBRID ASSY W/FP,DCF11-A	1	E75
90	93		9009149-00	PIN, STAKING, P.C. BOARD, .025 X	44	J3-J46
91	94		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	2	W1,W2
92	95		9000024-01	EYELET,ROLL FLANGE .1210DX .192	6	
93	96		9010213-01	SPACER,14 PIN BLK/NYL .060THK.	1	
94	97		1218783-00	CONN .100 2POS JUMPER	12	

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							KDF11-B CPU MODULE		K	PL	M8189-0-DBP	A

MIB DECODE LOGIC

DECODER 74S138 E66

0F 15
1F 14
2F 13
3F 12
4F 11
5F 10
6F 9
7F 8

15
14
13
12
11
10
9
8

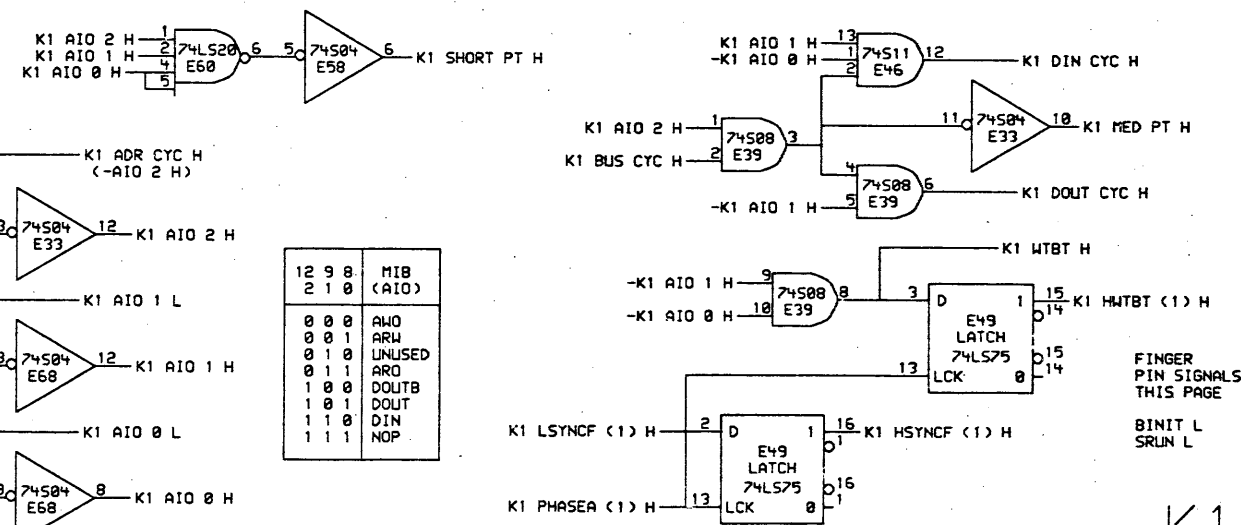
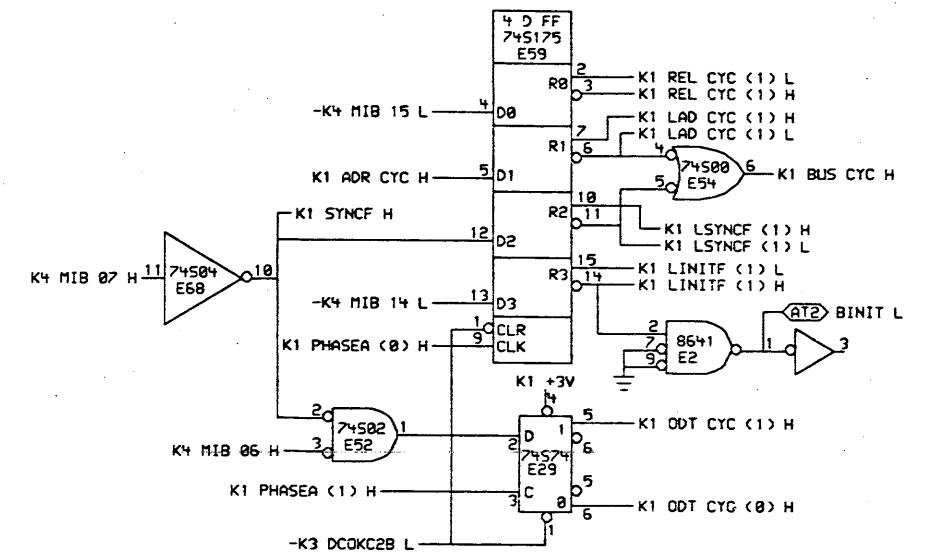
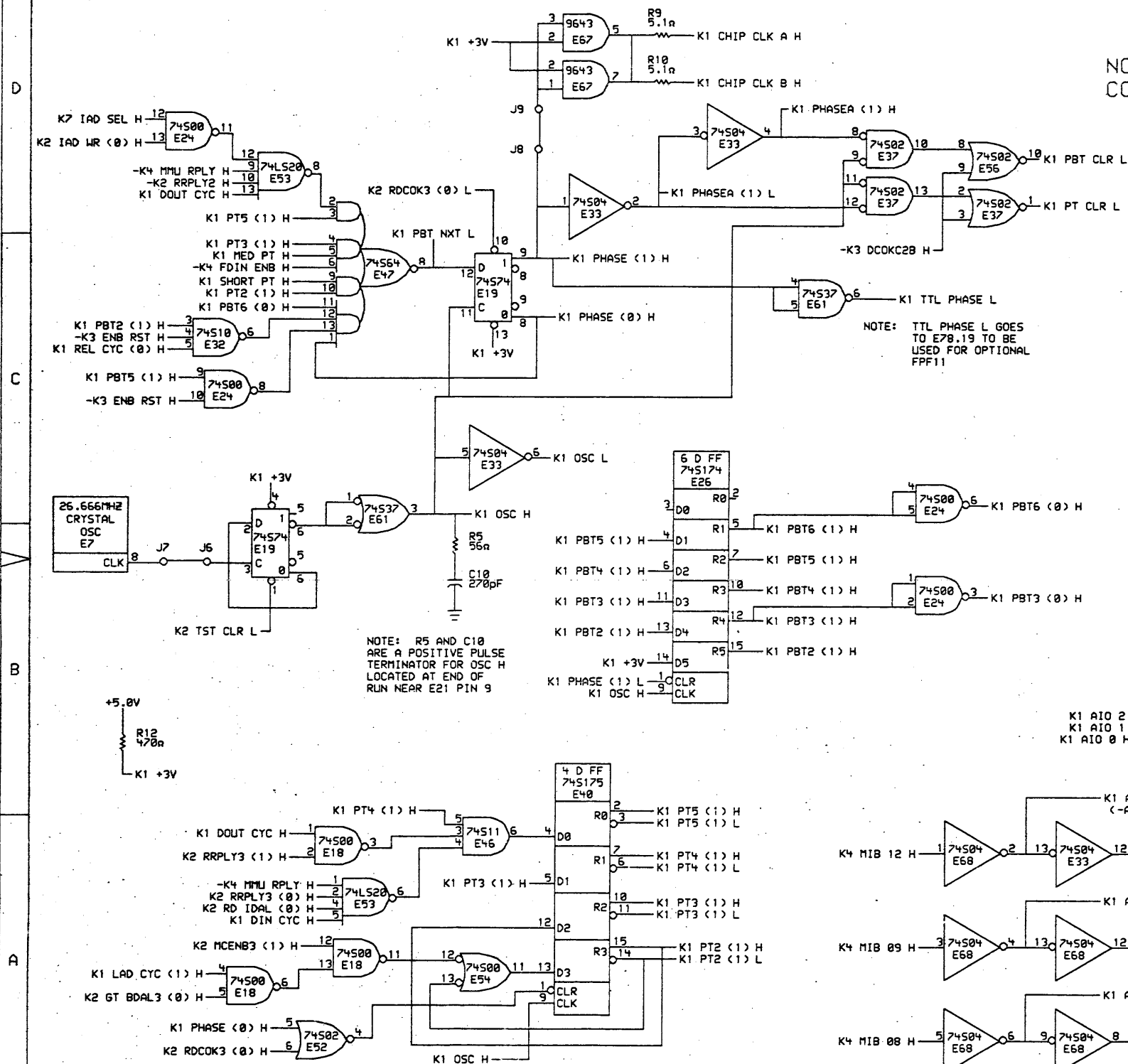
SRUN L
SRUN L

K1 DGPO 5 L
K1 DGPO 6 L
K1 DGPO 7 L

(CLR EVENT)
(CLR PWR LOW)
(WR ODTA)

K4 MIB 02 H
K4 MIB 01 H
K4 MIB 00 H
K4 MIB 03 L
K1 PASE (1) L
K1 PT3 (1) H

SEL
EN



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REVISIONS		
CHK	CHANGE NO.	REV

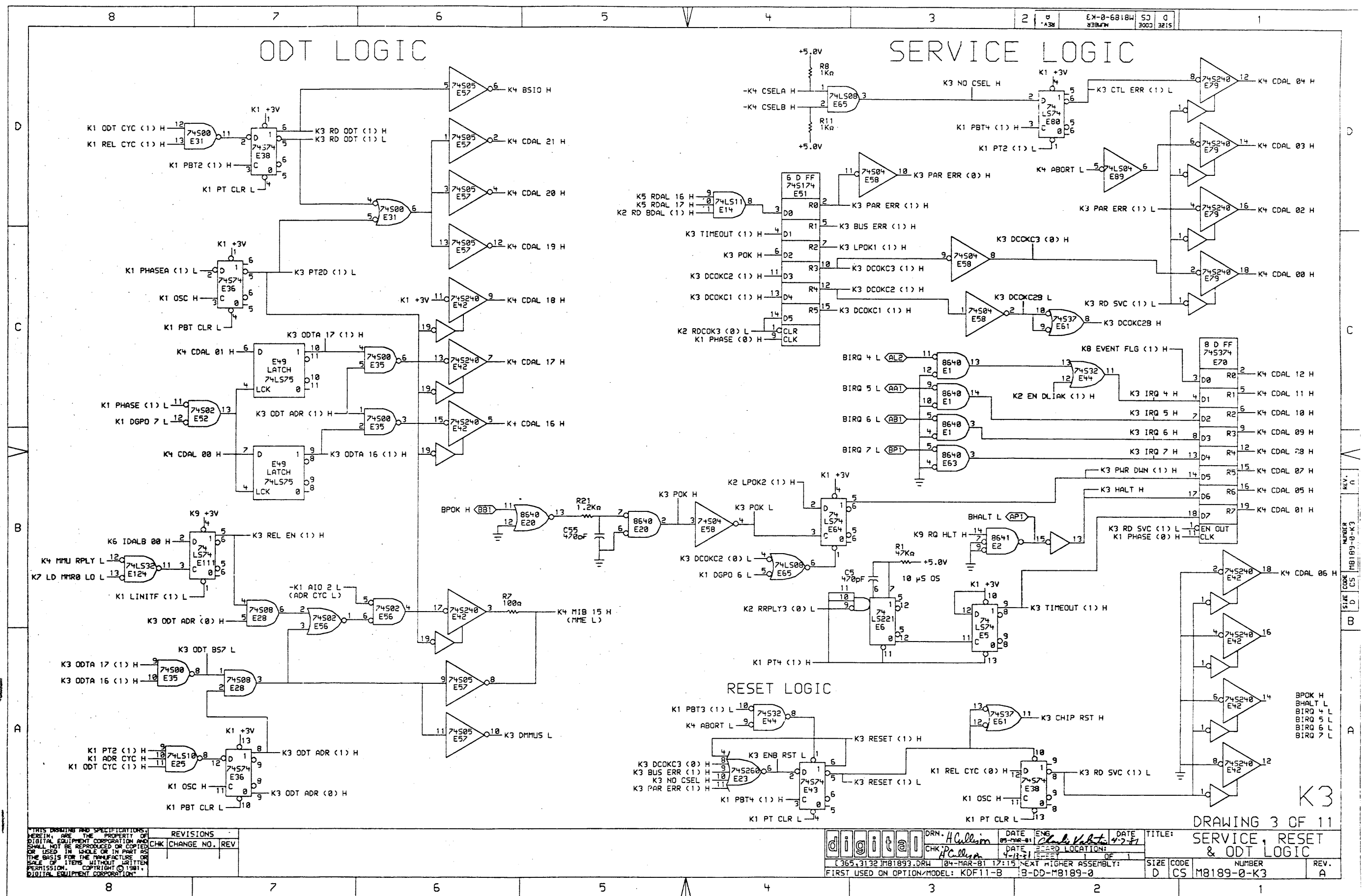
d	i	g	i	t	a
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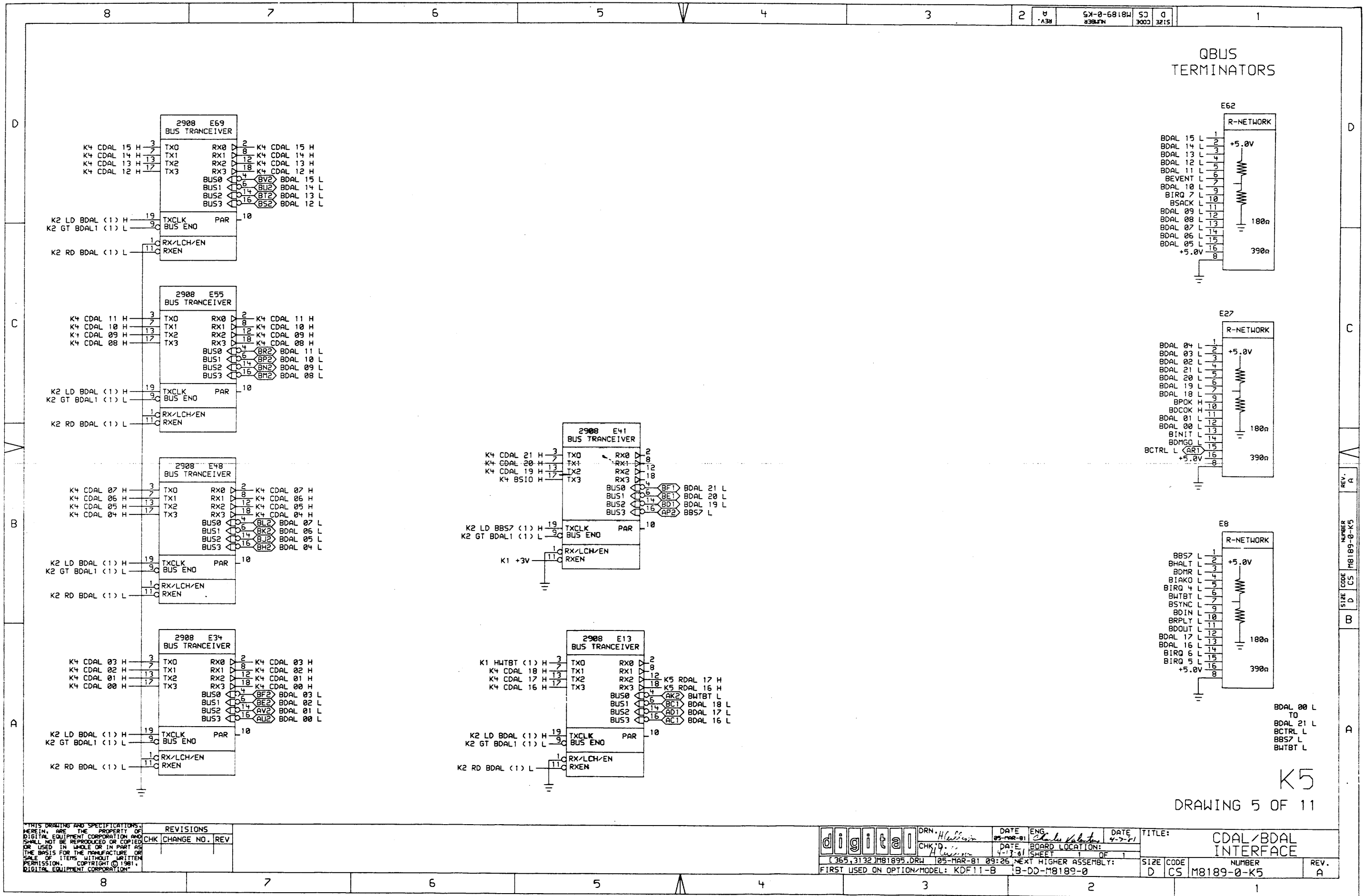
I	DRN	H. Callison	DATE	05-MAR-81	EXT	1
	CHK	H. Callison	DATE	4-13-81	BOARD LOCATION	5-EEET 1
.DRW			04-MAR-81	16:55	NEXT HIGHER ASSEMBLY	
ON/MODEL:			KDF11-B	B-DD-M8189-A		

DRAWING 1 OF 11

TIMING LOGIC
& MIB DECODE

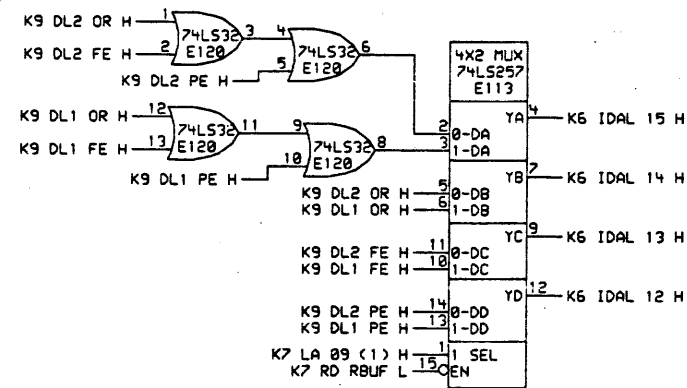
SIZE	CODE	NUMBER	REV.
D	CS	M8189-0-K1	0



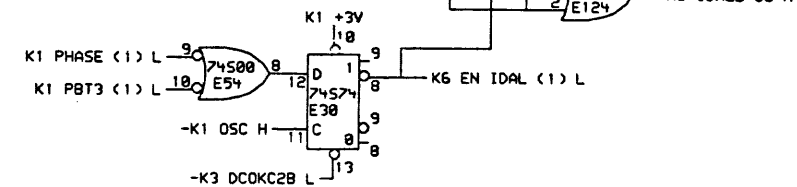
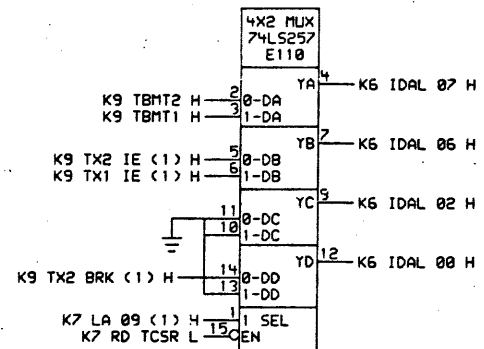
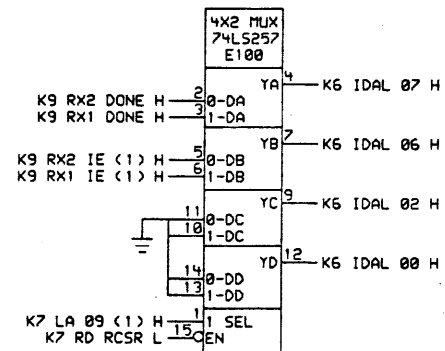
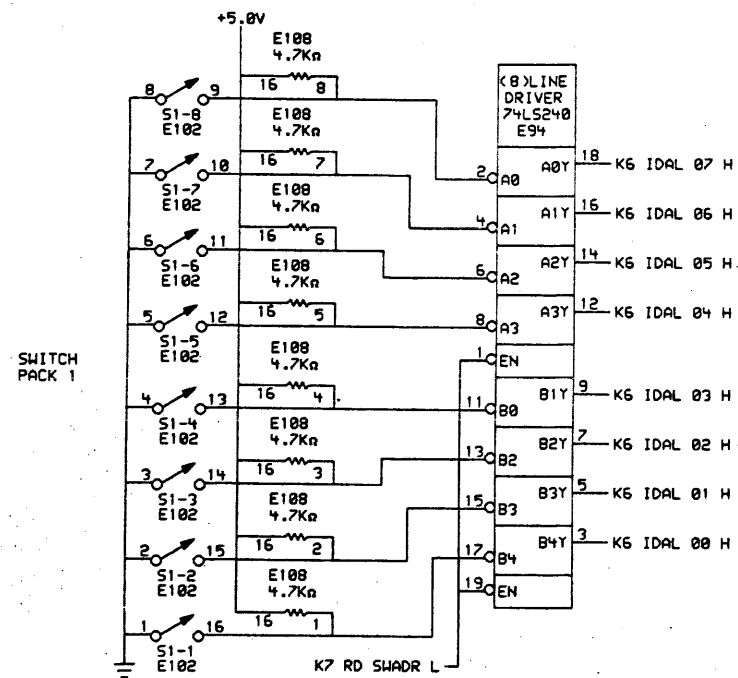


CDAL/IDAL TRANSCEIVERS

SLU's READ STATUS



BOOT SWITCHES

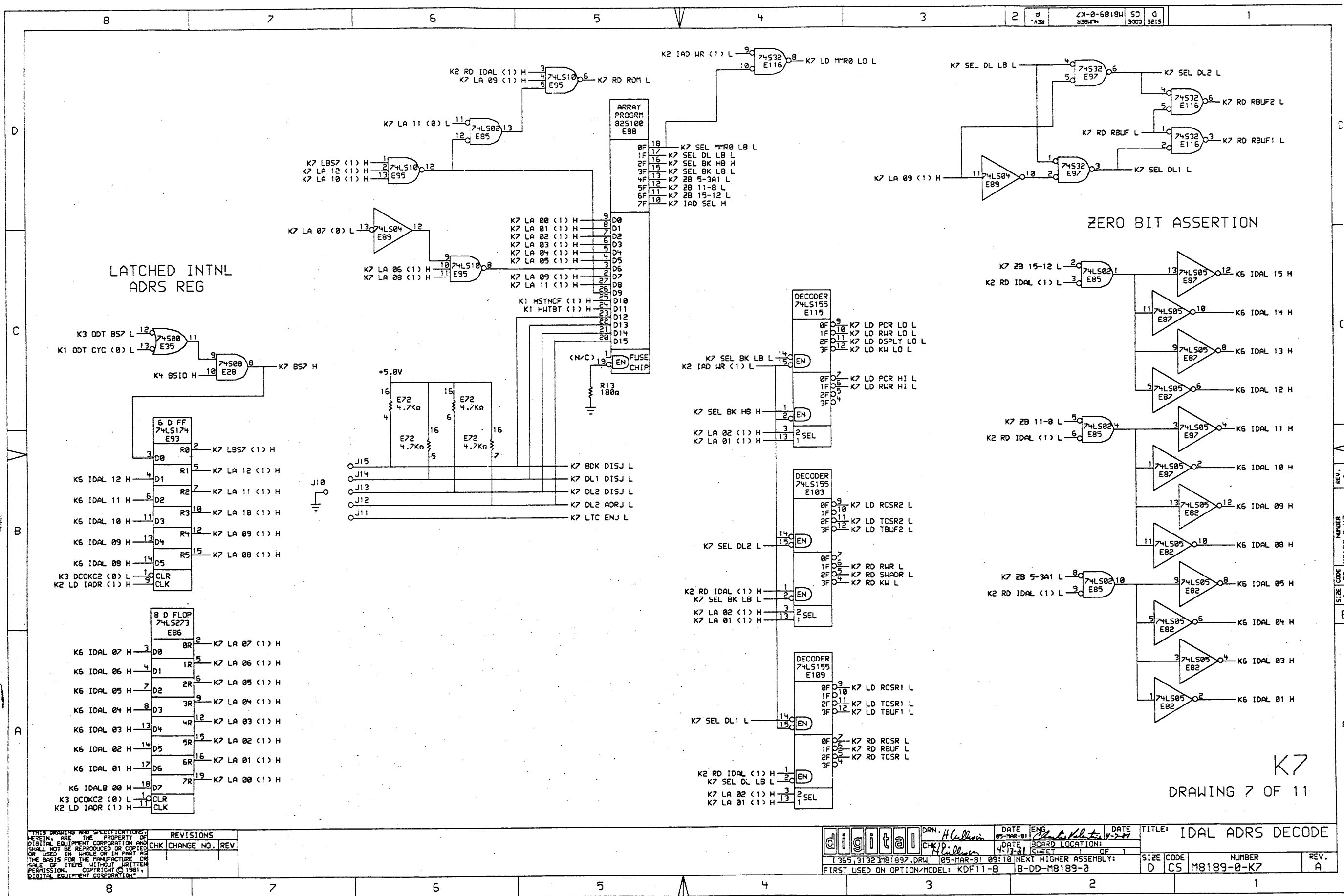


K6
DRAWING 6 OF 11

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REVISIONS
CHK CHANGE NO. REV

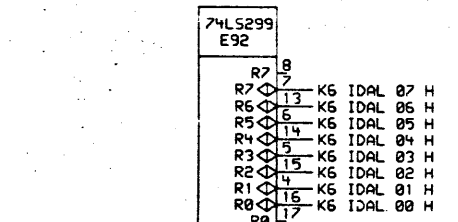
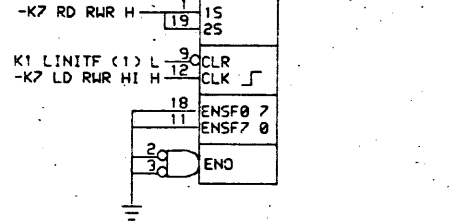
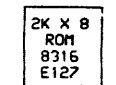
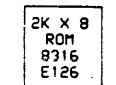
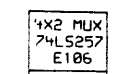
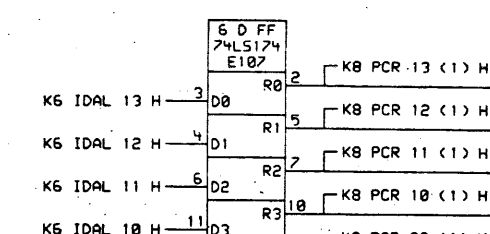
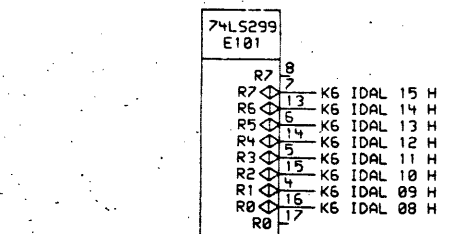
digital DRN. Haller DATE 05-MAR-81 ENG. Charles Valente DATE 4-11-81
CHK'D R. Callahan DATE 4-11-81 SHEET 1 OF 1
TITLE: CDAL/IDAL INTERFACE
FIRST USED ON OPTION/MODEL: KDF11-B 8-DD-M8189-0
SIZE CODE NUMBER REV.
D C5 M8189-0-K6 A



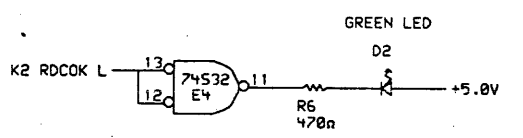
RDWR REG

PAGE CTL REG & MUX

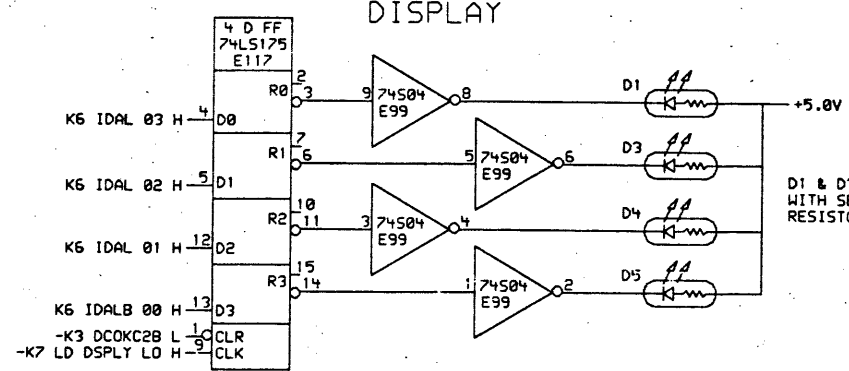
BOOT ROMS



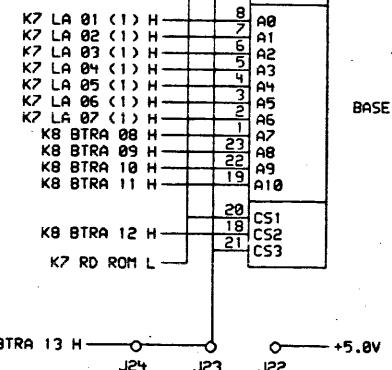
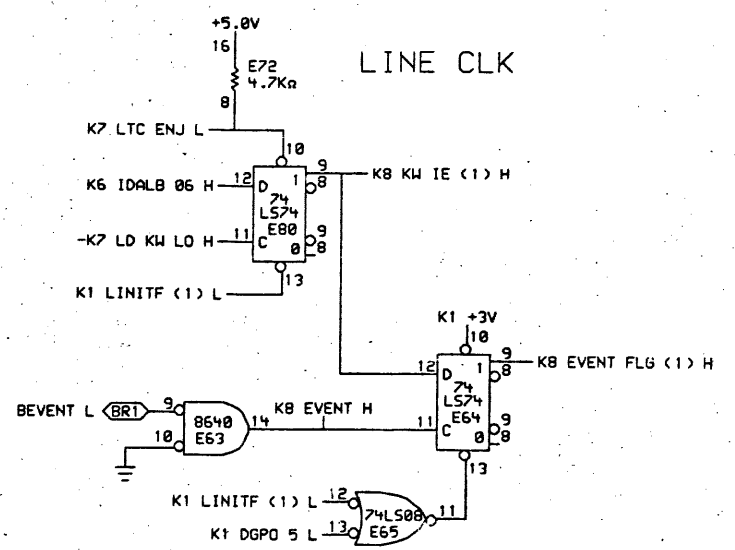
POWER OK LED



DISPLAY



LINE CLK



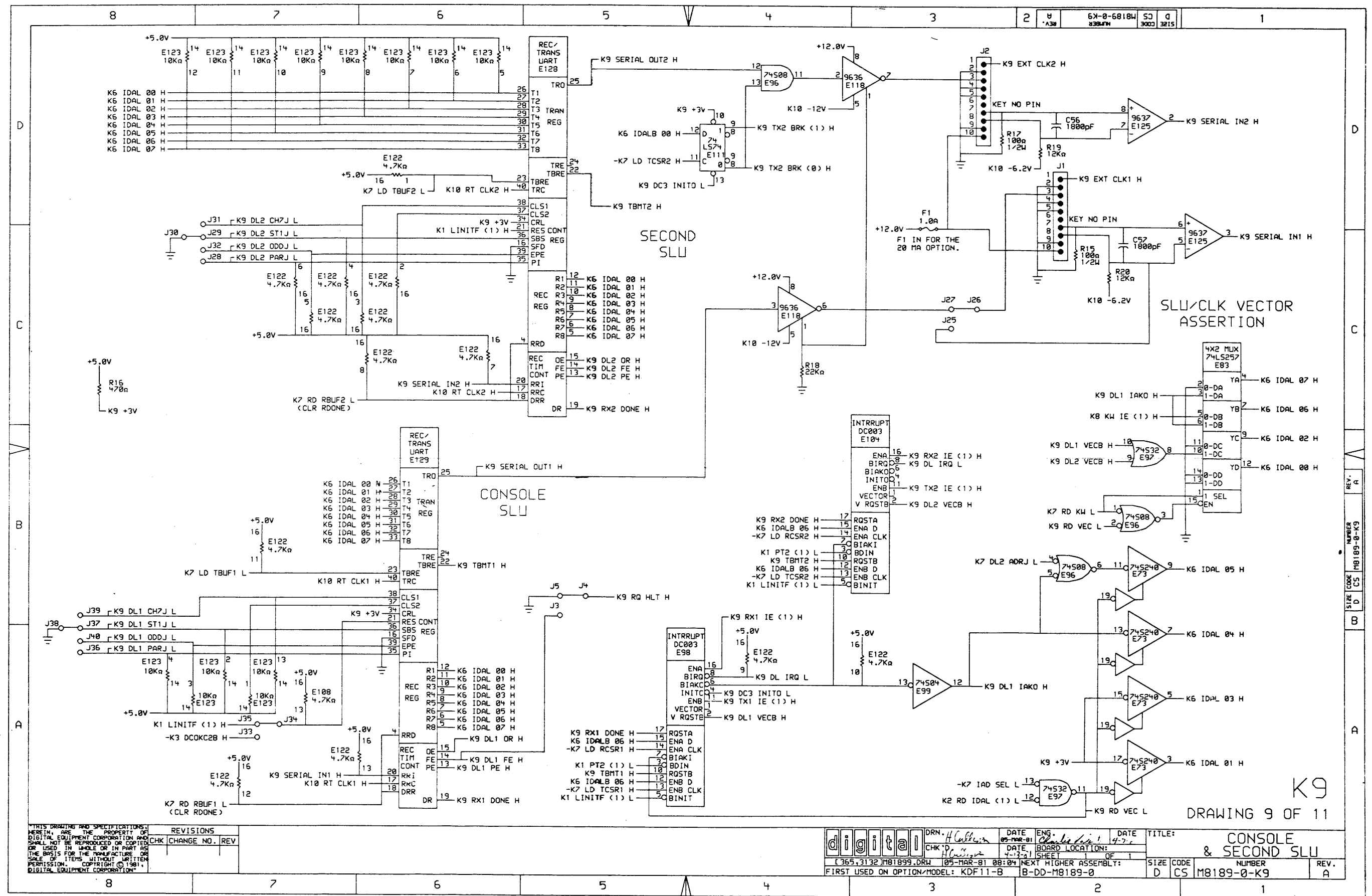
BEVENT L

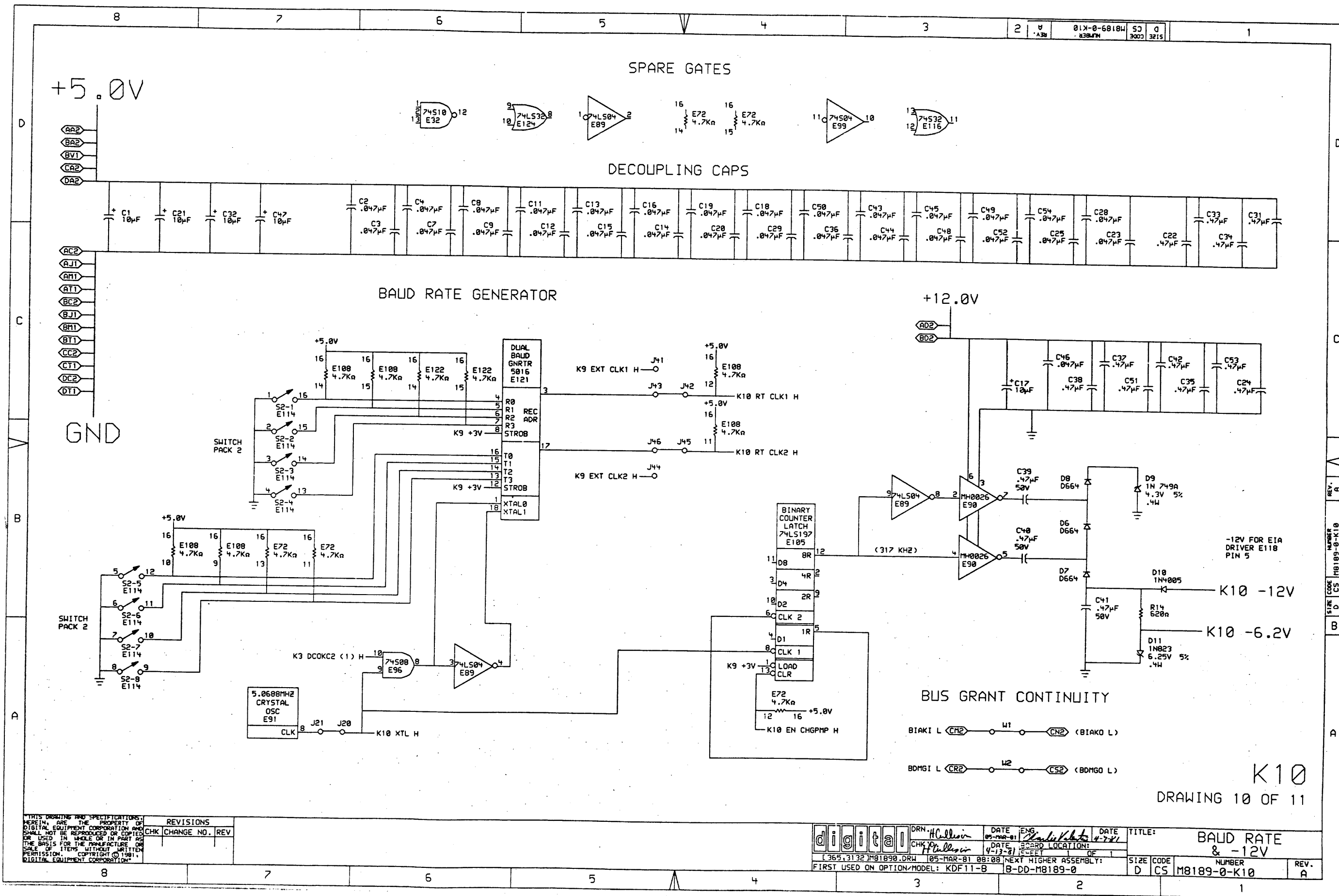
K8

DRAWING 8 OF 11

REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. H. Cullison	DATE 07-APR-81	ENG. H. Cullison	DATE 07-APR-81	TITLE: BOOT & CLOCK LOGIC
	CHK'D H. Cullison	DATE 07-APR-81	DATE 07-APR-81	OF 1	NUMBER M8189-0-K8
FIRST USED ON OPTION/MODEL: KDF11-B					
B-DD-M8189-0					
SIZE CODE	D	CS	M8189-0-K8	REV.	A

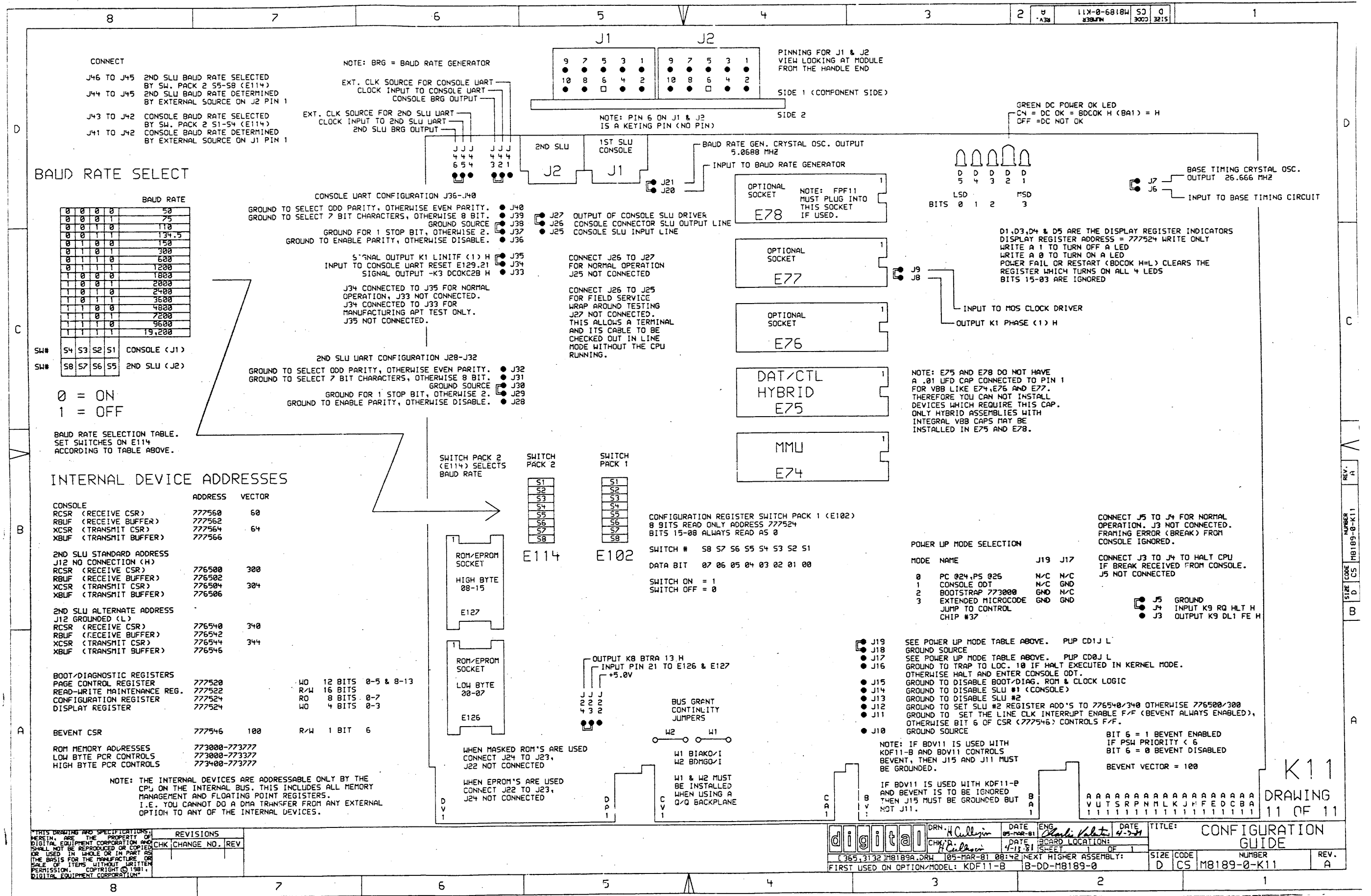




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REVISIONS		
CHK	CHANGE NO.	REV.

digital	DRN: H. Collins	DATE: 05-MAR-81	ENG: H. Collins	DATE: 4-13-81	TITLE: BAUD RATE & -12V
	CHK: H. Collins	DATE: 4-13-81	SCHEMATIC LOCATION: 1 OF 1	SIZE: D	CODE: CS
FIRST USED ON OPTION/MODEL: KDF11-B					NUMBER: M8189-0-K10
NEXT HIGHER ASSEMBLY: B-DD-M8189-0					REV. A



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NOTE :

FOR FIELD MAINTENANCE PRINT SET REFER TO B-TC-KEF11-BB-1

UNIT VARIATIONS

[illegible]

REVISIONS		CHANGE NO.	REV.
CHK	INITIAL		A

USED ON OPTION/MODEL	DRN.	DATE	TITLE																
KEF11-BB	A. AMICO	21JAN81	DRAWING DIRECTORY KEF11-BB																
	CHK'D.	DATE																	
	ROBICHAUD	08APR81																	
	PROJ. ENG. <i>MA.</i>	DATE	SIZE	CODE	NUMBER					REV									
	K. AMUNDSEN	<i>8 May 81</i>	B	DD	KEF11-BB					A									
	PROD.	DATE	DIST.																
SHEET 1 OF 2	<i>Greg. Mylech</i>	<i>9 Jan 81</i>																	

digital

