

# M9312

BOOTSTRAP TERMINATOR 8K  
CZM9BA0

AH-E058A-MC

COPYRIGHT © 1978  
FICHE 1 OF 1

APR 1978

**digital**

MADE IN USA



BO1

NOB:PM988550  
CZM9BA.F11

PAGE 1  
13-MAR-78 08:58

00010000

780330

PDP10 411

CZM9BA0 M9312 BOOT TERM P 8K

MAC/11 30A(1352, 13-  
SEQ 0001

.REM !

#### IDENTIFICATION

PRODUCT CODE: AC-E057A-MC

PRODUCT NAME: CZM9BA0 M9312.BOOT.TERM.8K

DATE: MARCH, 1978

MAINTAINER: DIAGNOSTIC GROUP

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS DOCUMENT. THE SOFTWARE DESCRIBED IN THIS DOCUMENT IS FURNISHED UNDER A LICENSE AND MAY ONLY BE SUED OR COPIED IN ACCORDANCE WITH THE TERMS OF SUCH LICENSE.

DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL.

COPYRIGHT (C) 1978 BY DIGITAL EQUIPEMNT CORPORATION

## 1.0 ABSTRACT

THIS PROGRAM VERIFIES THE ROM INFORMATION FOR THE M9312 BOOTSTRAP TERMINATOR. IT HAS TWO MODES OF OPERATION; STAND-ALONE MODE WHICH REQUIRES OPERATOR INTERVENTION AND APT-MODE.

## 2.0 REQUIREMENTS

## 2.1 HARDWARE

ANY PDP-11 UNIBUS PROCESSOR WITH CONSOLE TERMINAL AND/OR HARDWARE  
SWITCH REGISTER  
M9312 BOOT STRAP TERMINATOR  
4K MEMORY

## 2.2 SOFTWARE

THIS PROGRAM REQUIRES THAT THE CORRECT OPERATION OF THE PROCESSOR, MEMORY AND CONSOLE TERMINAL HAVE BEEN VERIFIED BY THE APPROPRIATE DIAGNOSTICS.

## 3.0 LOADING AND STARTING PROCEDURES

## 3.1 LOAD THE PROGRAM BY ANY OF THE STANDARD PROCEDURES FOR ABSOLUTE PROGRAM FORMATS.

## 3.2 STARTING ADDRESS

200- DO CRC VERIFICATION AND SIZING

## 3.3 RESTART ADDRESS

204- RESTART WITHOUT SIZING

## 3.4 SPECIAL ENVIRONMENTS

THIS PROGRAM IS APT COMPATIBLE SEE SECTION FOR ETABLE SET-UP.  
FOLLOW STANDARD APT PROCEDURES FOR LOADING AND STARTING.

## 4.0 OPERATING PROCEDURES

## 4.1 OPERATIONAL SWITCH SETTINGS

THE PROGRAM IS DESIGNED TO USE THE HARDWARE SWITCH REGISTER, HOWEVER IF THIS REGISTER IS NOT AVAILABLE THE PROGRAM WILL USE LOCATION 176 AS THE SWITCH REGISTER.

SW 15=1 OR UP-- HALT ON ERROR  
SW 13=1 OR UP-- INHIBIT ERROR TYPEOUTS  
SW 10=1 OR UP-- BELL ON ERROR

#### 4.2 EXECUTION TIMES

EXECUTION TIME IS DEPENDENT ON THE CONSOLE TERMINAL DURING THE FIRST PASS. ALL OTHER PASSES TAKE LESS THEN 1 SECOND.

#### 4.3 APT PROCEDURES

THERE ARE TWO CHOICES WHEN RUNNING UNDER APT. IF THE SIZE BIT (BIT 7-\$ENVM) IS CLEAR THE PROGRAM WILL OPERATE IN NORMAL STAND-ALONE MODE. IF THE SIZE BIT IS SET THE PROGRAM WILL COMPARE PARAMETERS FROM THE BOARD TO THE CONTENTS OF THE ETABLE. TO USE THE APT COMPARSION FEATURE THE ETABLE MUST BE SET UP IN THIS ORDER;

\$ENV:	DON'T CARE
\$ENVM:	200 OR 240
\$SWREG:	DON'T CARE
\$USWR:	NOT USED
\$CPUOP:	NOT USED
\$MAMS1:	NOT USED
\$MTYP1:	NOT USED
\$MADR1:	NOT USED
\$MAMS2:	NOT USED
\$MTYP2:	NOT USED
\$MADR2:	NOT USED
\$MAMS3:	NOT USED
\$MTYP3:	NOT USED
\$MADR3:	NOT USED
\$MAMS4:	NOT USED
\$MAMS4:	NOT USED
\$MTYP4:	NOT USED
\$MADR4:	NOT USED
\$VECT1:	NOT USED
\$VECT2:	NOT USED
\$BASE	PSEUDO POWER-FAIL VECTOR ADDRESS
\$DEVN:	NOT USED
\$CDW1:	CONTENTS OF ADDRESS IN \$BASE
\$CDW2:	NOT USED
\$DDW0:	DEVICE CODES EXPECTED
	FIRST LETTER/SECOND LETTER
DDW15:	

## 5.0 SUBROUTINE ABSTRACTS

## 5.1 NOROMS

THIS ROUTINE IS CALLED ONLY IF NO ROMS WERE FOUND DURING SIZING. IT DOES A READ OF ALL BOOTSTRAP ROM ADDRESSES AND COMPARES THE CONTENTS TO A KNOWN EXPECTED VALUE.

## 5.2 CHECKS

THIS ROUTINE SETS UP THE FIRST, LAST AND EXCEPTION ADDRESSES FOR THE "CALSUM" SUBROUTINE. IT RECEIVES THE CALCULATED CHECKSUM FROM "CALSUM" AND COMPARES IT AGAINST THE GOOD CHECKSUM TO DETERMINE CRC ERRORS.

## 5.3 CALSUM

THIS ROUTINE CALCULATES THE CRC16 CHECKSUM OF EACH ROM. IT RECEIVES THE FIRST ADDRESS TO BE CHECKED (FIRSTA), THE LAST ADDRESS TO BE CHECKED (LASTA) AND THE EXCEPTION ADDRESS (EXCADD) FROM THE "CHECKS" MODULE AND RETURNS TO IT THE CHECKSUM IN R4.

## 5.4 PROMP

THIS ROUTINE PROCESSES THE ROM PARAMETERS. IT CHECKS THE SIZE/DON'T SIZE BIT IN THE APT ETABLE AND EITHER FORMATS THE SIZING MESSAGES OR COMPARES THE SIZING INFORMATION TO THE APT ETABLE DATA.

## 5.5 DEVCOD

THIS ROUTINE LOCATES EACH DEVICE CODE AND PASSES IT AND THE ADDRESS IN WHICH IT WAS FOUND BACK TO THE "PROMP" MODULE.

## 5.6 PPFVAR

THIS ROUTINE DETERMINES THE PSEUDO POWER-FAIL VECTOR ADDRESS AND PASSES IT AND ITS CONTENTS TO THE "PROMP" MODULE.

## 5.7 PUTMES

THIS ROUTINE FORMATS THE PARAMETER AND POWER-FAIL ADDRESS MESSAGES.

#### 5.8 ERRHAN

THIS ROUTINE FORMATS ALL ERROR MESSAGES AND CALLS THE TYPE ROUTINE TO OUTPUT THEM. IT ALSO USES THE OPERATIONAL SWITCHES TO DETERMINE WHETHER TO OUTPUT THE MESSAGE, OR HALT.

#### 5.9 FILBUF

THIS ROUTINE FILLS THE MESSAGE BUFFER WITH ASCII CHARACTERS. IT RECIEVES THE ADDRESS OF THE ASCII IN R5.

#### 5.10 OCASC

THIS ROUTINE TAKES A SIXTEEN BIT BINARY NUMBER AND CONVERTS IT TO 6 ASCII CHARACTERS.

#### 5.11 OCADD

THIS ROUTINE IS USED BY THE "PUTMES" MODULE WHEN THE DATA IN R3 IS NOT IN THE RIGHT MODE TO BE HANDLED BY THE "OCASC" MODULE. IT MOVES THE ADDRESSING MODE OF R3 UP ONE LEVEL OF DEFERMENT.

#### 6.0 RELIABILITY//AVAILABILITY/SERVICEABILITY

WHEN RUNNING IN ANY ENVIRONMENT BUT APT THERE IS ONLY ONE ERROR DETECTED BY THE PROGRAM. THIS ERROR IS A CHECKSUM ERROR. THE MESSAGE WILL BE:

(CRC ERROR IN ROM EXX

WHEN RUNNING IN THE APT ENVIRONMENT THREE OTHER ERRORS MAY OCCUR.

1. AN ERROR WILL OCCUR WHEN THE DEVICE CODES IN THE ETABLE DO NOT MATCH THE DEVICE CODES FOUND IN THE ROMS. THE ERROR MESSAGE WILL BE EITHER:

1. COULD NOT FIND DEVICE CODE XY.
2. FOUND UNEXPECTED DEVICE CODE XX.

GO1

CZM9BA0 M9312 BOOT TERM 8K MACY11 30A(1052) 13-MAR-78 09:59 PAGE 6  
CZM9BA.P11 13-MAR-78 08:58

SEG 0006

THE FIRST MESSAGE WILL BE PASSED TO APT IF A DEVICE CODE LISTED IN THE ETABLE CANNOT BE FOUND IN THE EXISTING ROMS. THE SECOND MESSAGE WILL BE SENT IF A DEVICE CODE NOT LISTED IN THE ETABLE IS FOUND IN A ROM.

2. THE SECOND ERROR WILL BE IF THE PSEUDO POWER-FAIL VECTOR ADDRESS IN THE ETABLE DOES NOT MATCH THE ADDRESS DETERMINED BY THE PROGRAM. TO BE IN THE BOARDS' SWITCHES. THE MESSAGE WILL BE:

POWER-FAIL VECTOR	
EXPECTED	RECEIVED
-----	-----

WHERE EXPECTED IS THE CONTENTS OF THE ETABLE AND RECEIVED IS THE VALUE FOUND BY THE PROGRAM.

3. THE THIRD ERROR WILL BE IF THE CONTENTS OF BOARD'S PSEUDO POWER-FAIL VECTOR ADDRESS DOES NOT MATCH THE VALUE EXPECTED FROM THE ETABLE. THE MESSAGE WILL BE:

POWER-FAIL DATA ERROR	
EXPECTED	RECEIVED
-----	-----

!

000001  
160000

001100

000011  
000012  
000015  
000200  
177776177774  
177772  
177570  
177570000000  
000001  
000002  
000003  
000004  
000005  
000006  
000007  
000006  
000007

000000

```

.ENABLE ABS
.LIST ME
.NLIST MC,MD,CND
.TITLE CZM98AD M9312 BOOT TERM R
.*COPYRIGHT (C) 1978
.*DIGITAL EQUIPMENT CORP.
.*MAYNARD, MASS. 01754
.*
.*PROGRAM BY BARRY G. IRRANG
.*
.*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
.*PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.
.*
*TN=1
$SWR=160000 ;;HALT ON ERROR, LOOP ON TEST, INHIBIT ERROR TYPEOUT
.SBTTL OPERATIONAL SWITCH SETTINGS
.*
.*      SWITCH      USE
.*      -----
.*      15          HALT ON ERROR
.*      14          LOOP ON TEST
.*      13          INHIBIT ERROR TYPEOUTS
.SBTTL BASIC DEFINITIONS
.*
.*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
STACK= 1100
.EQUIV EMT,ERROR      ;;BASIC DEFINITION OF ERROR CALL
.EQUIV IOT,SCOPE      ;;BASIC DEFINITION OF SCOPE CALL
.*
.*MISCELLANEOUS DEFINITIONS
MT= 11                ;;CODE FOR HORIZONTAL TAB
LF= 12                ;;CODE FOR LINE FEED
CR= 15                ;;CODE FOR CARRIAGE RETURN
CRLF= 200             ;;CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776            ;;PROCESSOR STATUS WORD
.EQUIV PS,PSW
STKLMT= 177774        ;;STACK LIMIT REGISTER
PIRQ= 177772          ;;PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570          ;;HARDWARE SWITCH REGISTER
DDISP= 177570         ;;HARDWARE DISPLAY REGISTER
.*
.*GENERAL PURPOSE REGISTER DEFINITIONS
R0= %0                ;;GENERAL REGISTER
R1= %1                ;;GENERAL REGISTER
R2= %2                ;;GENERAL REGISTER
R3= %3                ;;GENERAL REGISTER
R4= %4                ;;GENERAL REGISTER
R5= %5                ;;GENERAL REGISTER
R6= %6                ;;GENERAL REGISTER
R7= %7                ;;GENERAL REGISTER
SP= %6                ;;STACK POINTER
PC= %7                ;;PROGRAM COUNTER
.*
.*PRIORITY LEVEL DEFINITIONS
PRO= 0                ;;PRIORITY LEVEL 0

```



# IO1

CZM98A0 M9312 BOOT TERM 8+  
CZM98A.P11 13-MAR-78 08:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 8  
BASIC DEFINITIONS

356 0008

354 000040  
355 000100  
356 000140  
357 000200  
358 000240  
359 000300  
360 000340

PR1= 40 :: PRIORITY LEVEL 1  
PR2= 100 :: PRIORITY LEVEL 2  
PR3= 140 :: PRIORITY LEVEL 3  
PR4= 200 :: PRIORITY LEVEL 4  
PR5= 240 :: PRIORITY LEVEL 5  
PR6= 300 :: PRIORITY LEVEL 6  
PR7= 340 :: PRIORITY LEVEL 7

## .\*"SWITCH REGISTER" SWITCH DEFINITIONS

362 100000  
363 040000  
364 020000  
365 010000  
366 004000  
367 002000  
368 001000  
369 000400  
370 000200  
371 000100  
372 000040  
373 000020  
374 000010  
375 000004  
376 000002  
377 000001

SW15= 100000  
SW14= 40000  
SW13= 20000  
SW12= 10000  
SW11= 4000  
SW10= 2000  
SW09= 1000  
SW08= 400  
SW07= 200  
SW06= 100  
SW05= 40  
SW04= 20  
SW03= 10  
SW02= 4  
SW01= 2  
SW00= 1  
.EQUIV SW09,SW9  
.EQUIV SW08,SW8  
.EQUIV SW07,SW7  
.EQUIV SW06,SW6  
.EQUIV SW05,SW5  
.EQUIV SW04,SW4  
.EQUIV SW03,SW3  
.EQUIV SW02,SW2  
.EQUIV SW01,SW1  
.EQUIV SW00,SW0

## .\*DATA BIT DEFINITIONS (BIT00 TO BIT15)

390 100000  
391 040000  
392 020000  
393 010000  
394 004000  
395 002000  
396 001000  
397 000400  
398 000200  
399 000100  
400 000040  
401 000020  
402 000010  
403 000004  
404 000002  
405 000001

BIT15= 100000  
BIT14= 40000  
BIT13= 20000  
BIT12= 10000  
BIT11= 4000  
BIT10= 2000  
BIT09= 1000  
BIT08= 400  
BIT07= 200  
BIT06= 100  
BIT05= 40  
BIT04= 20  
BIT03= 10  
BIT02= 4  
BIT01= 2  
BIT00= 1  
.EQUIV BIT09,BIT9  
.EQUIV BIT08,BIT8  
.EQUIV BIT07,BIT7

406  
407  
408  
409

```

410      .EQUIV BIT06,BIT6
411      .EQUIV BIT05,BIT5
412      .EQUIV BIT04,BIT4
413      .EQUIV BIT03,BIT3
414      .EQUIV BIT02,BIT2
415      .EQUIV BIT01,BIT1
416      .EQUIV BIT00,BIT0

418      ;*BASIC "CPU" TRAP VECTOR ADDRESSES
419      ERRVEC= 4      ; TIME OUT AND OTHER ERRORS
420      RESVEC= 10     ; RESERVED AND ILLEGAL INSTRUCTIONS
421      TBITVEC=14     ; "T" BIT
422      TRTVEC= 14     ; TRACE TRAP
423      BPTVEC= 14     ; BREAKPOINT TRAP (BPT)
424      IOTVEC= 20     ; INPUT/OUTPUT TRAP (IOT) **SCOPE**
425      PWRVEC= 24     ; POWER FAIL
426      EMTVEC= 30     ; EMULATOR TRAP (EMT) **ERROR**
427      TRAPVEC=34     ; "TRAP" TRAP
428      TKVEC= 60      ; TTY KEYBOARD VECTOR
429      TPVEC= 64      ; TTY PRINTER VECTOR
430      PIRQVEC=240    ; PROGRAM INTERRUPT REQUEST VECTOR
431      ;*****
432      ;* PROGRAM EQUATES
433      ;*****
434      APTER1 = 1
435      APTER2 = 2
436      APTER3 = 4
437      APTER4 = 10
438      CRCERR = 20
439      PFERR = 40
440      NOROME =100
441      SEQERR =200

442      .=0
443      .SBTTL TRAP CATCHER
444      .=0
445      ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
446      ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
447      ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
448      .=174
449      DISPREG: .WORD 0      ; SOFTWARE DISPLAY REGISTER
450      SWREG:   .WORD 0      ; SOFTWARE SWITCH REGISTER
451      .SBTTL STARTING ADDRESS(ES)
452      JMP      @START      ; JUMP TO STARTING ADDRESS OF PROGRAM
453      .=204
454      JMP      RSTART
455      .SBTTL ACT11 HOOKS
456      ;*****
457      ;HOOKS REQUIRED BY ACT11
458      $SVPC=.              ;SAVE PC
459      .=46
460      $ENDAD              ;:1)SET LOC.46 TO ADDRESS OF $ENDAD IN .SEOP
461      .=52
462
463      000004
464      000010
465      000014
466      000014
467      000014
468      000020
469      000024
470      000030
471      000034
472      000060
473      000064
474      000240
475
476      000001
477      000002
478      000004
479      000010
480      000020
481      000040
482      000100
483      000200
484
485      000000
486
487      000000
488
489      000174 000000
490      000176 000000
491
492      000200 000137 001400
493      000204 000204
494      000204 000167 001634
495
496
497
498
499      000210
500      000046
501      002152
502      000052

```

K01

CZM9BA0 M9312 BOOT TERM 8+  
CZM9BA.P11 13-MAR-78 08:58MACY11 30A(1052) 13-MAR-78 09:59 PAGE 10  
ACT11 HOOKS

SEG 0010

```

466 000052 000000 .WORD 0 ;;2 SET LOC.52 TO ZEPG
467 000210 .=$SVPC ;; RESTORE PC
468
469 001100 .=$1100
470 001100 000 $AUTOB: .BYTE 0
471 001101 000 $INTAG: .BYTE 0
472 001102 000000 .WORD 0
473 001104 177570 SWR: .WORD 0SWR
474 001106 177570 DISPLAY: .WORD 0DISP
475 001110 000000 ROMERR: 0
476 001112 000000 MESSAG: 0
477 001114 173000 FIRSTB: 173000
478 001116 000000 ROMFIN: 0
479 001120 000000 ERRCNT: 0
480
481 .SBTTL APT PARAMETER BLOCK
482
483 *****
484 ;SET LOCATIONS 24 AND 44 AS REQUIRED FOR APT
485 *****
486 001122 .SX= . ;SAVE CURRENT LOCATION
487 000024 .=$24 ;SET POWER FAIL TO POINT TO START OF PROGRAM
488 000024 200 ;FOR APT START UP
489 000044 .=$44 ;POINT TO APT INDIRECT ADDRESS PNTR.
490 000044 $APTHDR ;POINT TO APT HEADER BLOCK
491 001122 .=$X ;RESET LOCATION COUNTER
492 *****
493 ;SETUP APT PARAMETER BLOCK AS DEFINED IN THE APT-PDP11 DIAGNOSTIC
494 ;INTERFACE SPEC.
495
496 001122 $APTHD:
497 001122 000000 $HIBTS: .WORD 0 ;;TWO HIGH BITS OF 18 BIT MAILBOX ADDR.
498 001124 001136 $MBAOR: .WORD $MAIL ;;ADDRESS OF APT MAILBOX (BITS 0-15)
499 001126 000002 $STMT: .WORD 2. ;;RUN TIM OF LONGEST TEST
500 001130 000002 $PASTM: .WORD 2. ;;RUN TIME IN SECS. OF 1ST PASS ON 1 UNIT (QUICK VERIFY)
501 001132 000000 $UNITM: .WORD 0 ;;ADDITIONAL RUN TIME (SECS) OF A PASS FOR EACH ADDITIONAL UNIT
502 001134 000052 .WORD $ETEND-$MAIL/2 ;;LENGTH MAILBOX-ETABLE(WORDS)
503
504 .SBTTL APT MAILBOX-ETABLE
505 *****
506 .EVEN
507 001136 $MAIL:
508 001136 000000 $MSGTY: .WORD AMSGTY ;;APT MAILBOX
509 001140 000000 $FATAL: .WORD AFATAL ;;MESSAGE TYPE CODE
510 001142 000000 $TESTN: .WORD ATESTN ;;FATAL ERROR NUMBER
511 001144 000000 $PASS: .WORD APASS ;;TEST NUMBER
512 001146 000000 $DEVCT: .WORD ADEVCT ;;PASS COUNT
513 001150 000000 $UNIT: .WORD AUNIT ;;DEVICE COUNT
514 001152 000000 $MSGAD: .WORD AMSGAD ;;I/O UNIT NUMBER
515 001154 000000 $MSGLG: .WORD AMSGLG ;;MESSAGE ADDRESS
516 001156 000 $ETABLE: .WORD AENV ;;MESSAGE LENGTH
517 001156 000 $ENV: .BYTE AENV ;;APT ENVIRONMENT TABLE
518 001157 000 $ENVM: .BYTE AENVM ;;ENVIRONMENT BYTE
519 001160 000000 $SWREG: .WORD ASWREG ;;ENVIRONMENT MODE BITS
520 001162 000000 $USWR: .WORD AUSWR ;;APT SWITCH REGISTER
521 001164 000000 $CPUOP: .WORD ACPUOP ;;USER SWITCHES
;;CPU TYPE,OPTIONS

```

# L01

CZM9BA0 M9312 BOOT TERM 8K  
CZM9BA.P11 13-MAR-78 08:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 11  
APT MAILBOX-ETABLE

SEQ 0011

```

S22          *
S23          *
S24          *
S25          *
S26          *
S27          *
S28          *
S29          *
S30          *
S31          *
S32          *
S33          *
S34          *
S35          *
S36          *
S37          *
S38          *
S39          *
S40          *
S41          *
S42          *
S43          *
S44          *
S45          *
S46          *
S47          *
S48          *
S49          *
S50          *
S51          *
S52          *
S53          *
S54          *
S55          *
S56          *
S57          *
S58          *
S59          *
S60          *
S61          *
S62          *
S63          *
S64          *
S65          *
S66          *
S67          *
S68          *
S69          *
S70          *
S71          *
S72          *
S73          *
S74          *
S75          *
S76          *
S77          *

```

001166	000	\$MAMS1: .BYTE	AMAMS1	;; HIGH ADDRESS M.S. BYTE
001167	000	\$MTYP1: .BYTE	AMTYP1	;; MEM. TYPE, BLK#1
				MEM. TYPE, BYTE -- (HIGH BYTE
				900 NSEC CORE=001
				300 NSEC BIPOLAR=002
				500 NSEC MOS=003
001170	000000	\$MADR1: .WORD	AMADR1	;; HIGH ADDRESS, BLK#1
				MEM. LAST ADDR.=3 BYTES, THIS WORD AND LOW OF "TYPE" ABOVE
001172	000	\$MAMS2: .BYTE	AMAMS2	;; HIGH ADDRESS M.S. BYTE
001173	000	\$MTYP2: .BYTE	AMTYP2	;; MEM. TYPE, BLK#2
001174	000000	\$MADR2: .WORD	AMADR2	;; MEM. LAST ADDRESS, BLK#2
001176	000	\$MAMS3: .BYTE	AMAMS3	;; HIGH ADDRESS M.S. BYTE
001177	000	\$MTYP3: .BYTE	AMTYP3	;; MEM. TYPE, BLK#3
001200	000000	\$MADR3: .WORD	AMADR3	;; MEM. LAST ADDRESS, BLK#3
001202	000	\$MAMS4: .BYTE	AMAMS4	;; HIGH ADDRESS M.S. BYTE
001203	000	\$MTYP4: .BYTE	AMTYP4	;; MEM. TYPE, BLK#4
001204	000000	\$MADR4: .WORD	AMADR4	;; MEM. LAST ADDRESS, BLK#4
001206	000000	\$VECT1: .WORD	AECT1	;; INTERRUPT VECTOR#1, BUS PRIORITY#1
001210	000000	\$VECT2: .WORD	AECT2	;; INTERRUPT VECTOR#2, BUS PRIORITY#2
001212	000000	\$BASE: .WORD	ABASE	;; BASE ADDRESS OF EQUIPMENT UNDER TEST
001214	000000	\$DEVN: .WORD	ADEVN	;; DEVICE MAP
001216	000000	\$CDW1: .WORD	ACDW1	;; CONTROLLER DESCRIPTION WORD#1
001220	000000	\$CDW2: .WORD	ACDW2	;; CONTROLLER DESCRIPTION WORD#2
001222	000000	\$DDW0: .WORD	ADW0	;; DEVICE DESCRIPTOR WORD#0
001224	000000	\$DDW1: .WORD	ADW1	;; DEVICE DESCRIPTOR WORD#1
001226	000000	\$DDW2: .WORD	ADW2	;; DEVICE DESCRIPTOR WORD#2
001230	000000	\$DDW3: .WORD	ADW3	;; DEVICE DESCRIPTOR WORD#3
001232	000000	\$DDW4: .WORD	ADW4	;; DEVICE DESCRIPTOR WORD#4
001234	000000	\$DDW5: .WORD	ADW5	;; DEVICE DESCRIPTOR WORD#5
001236	000000	\$DDW6: .WORD	ADW6	;; DEVICE DESCRIPTOR WORD#6
001240	000000	\$DDW7: .WORD	ADW7	;; DEVICE DESCRIPTOR WORD#7
001242	000000	\$DDW8: .WORD	ADW8	;; DEVICE DESCRIPTOR WORD#8
001244	000000	\$DDW9: .WORD	ADW9	;; DEVICE DESCRIPTOR WORD#9
001246	000000	\$DDW10: .WORD	ADW10	;; DEVICE DESCRIPTOR WORD#10
001250	000000	\$DDW11: .WORD	ADW11	;; DEVICE DESCRIPTOR WORD#11
001252	000000	\$DDW12: .WORD	ADW12	;; DEVICE DESCRIPTOR WORD#12
001254	000000	\$DDW13: .WORD	ADW13	;; DEVICE DESCRIPTOR WORD#13
001256	000000	\$DDW14: .WORD	ADW14	;; DEVICE DESCRIPTOR WORD#14
001260	000000	\$DDW15: .WORD	ADW15	;; DEVICE DESCRIPTOR WORD#15

```

S67          *
S68          *
S69          *
S70          *
S71          *
S72          *
S73          *
S74          *
S75          *
S76          *
S77          *

```

001262		\$ETEND:		
001400	001400			
001400	012706	001100		
001404	012737	006430	000034	
001412	012737	000340	000036	

```

START:
.SBTTL INITIALIZE THE COMMON TAGS
MOV     #STACK, SP      ;; SETUP THE STACK POINTER
;; INITIALIZE A FEW VECTORS
MOV     #TRAP, #TRAPVEC ;; TRAP VECTOR FOR TRAP CALLS
MOV     #340, #TRAPVEC+2; LEVEL -

```



CZM9BA0 M9312 BOOT TERM BK MACY11 30A(1052) 13-MAR-78 09:59 PAGE 12  
 CZM9BA.P11 13-MAR-78 08:58 INITIALIZE THE COMMON TAGS

SEG 0012

```

578 001420 005067 177520          CLR $PASS          ;; CLEAR THE PASS COUNT
579 001424 016767 000476 000466  MOV SENDCT,SEOPCT  ;; SETUP END-OF-PROGRAM COUNTER
580                                     ;; SIZE FOR A HARDWARE SWITCH REGISTER, IF NOT FOUND OR IT IS
581                                     ;; EQUAL TO A -1, SETUP FOR A SOFTWARE SWITCH REGISTER.
582 001432 013746 000004          MOV @ERRVEC, -(SP)  ;; SAVE ERROR VECTOR
583 001436 012737 001472 000004  MOV @64$, @ERRVEC  ;; SET UP ERROR VECTOR
584 001444 012767 177570 177432  MOV @SWR, SWR      ;; SETUP FOR A HARDWARE SWITCH REGISTER
585 001452 012767 177570 177426  MOV @DISP, DISPLAY  ;; AND A HARDWARE DISPLAY REGISTER
586 001460 022777 177777 177416  CMP #-1, @SWR      ;; TRY TO REFERENCE HARDWARE SWR
587 001466 001012          BNE 66$          ;; BRANCH IF NO TIMEOUT TRAP OCCURRED
588                                     ;; AND THE HARDWARE SWR IS NOT = -1
589 001470 000403          BR 65$          ;; BRANCH IF NO TIMEOUT
590 001472 012716 001500 64$: MOV @65$, (SP)  ;; SET UP FOR TRAP RETURN
591 001476 000002          RTI
592 001500 012767 000176 177376 65$: MOV @SWREG, SWR  ;; POINT TO SOFTWARE SWR
593 001506 012767 000174 177372  MOV @DISPREG, DISPLAY
594 001514 012637 000004 66$: MOV (SP)+, @ERRVEC  ;; RESTORE ERROR VECTOR
595
596 001520 005067 177420          CLR $PASS          ;; CLEAR PASS COUNT
597 001524 132767 000200 177425  BITB @APTSIZE, $ENVM  ;; TEST USER SIZE UNDER APT
598 001532 001403          BEQ 67$          ;; YES, USE NON-APT SWITCH
599 001534 012767 001160 177342  MOV @SSWREG, SWR  ;; NO, USE APT SWITCH REGISTER
600 001542
601 .SBTTL TYPE PROGRAM NAME
602 ;; TYPE THE NAME OF THE PROGRAM IF FIRST PASS
603 001542 005227 177777          INC #-1          ;; FIRST TIME?
604 001546 001046          BNE 68$          ;; BRANCH IF NO
605 001550 022737 002152 000042  CMP @SENDAD, @#42  ;; ACT-11?
606 001556 001442          BEQ 68$          ;; BRANCH IF YES
607 001560 104401 001626          TYPE 69$          ;; TYPE ASCIZ STRING
608 .SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
609 001564 005737 000042          TST @#42          ;; ARE WE RUNNING UNDER XXDP/ACT?
610 001570 001012          BNE 70$          ;; BRANCH IF YES
611 001572 126727 177360 000001  CMPB $ENV, #1      ;; ARE WE RUNNING UNDER APT?
612 001600 001406          BEQ 70$          ;; BRANCH IF YES
613 001602 026727 177276 000176  CMP SWR, @SWREG  ;; SOFTWARE SWITCH REG SELECTED?
614 001610 001005          BNE 71$          ;; BRANCH IF NO
615 001612 104405          GTSWR          ;; GET SOFT-SWR SETTINGS
616 001614 000403          BR 71$
617 001616 112767 000001 177254 70$: MOV @1, $AUTOB  ;; SET AUTO-MODE INDICATOR
618 001624          71$:
619 001624 000417          BR 68$          ;; GET OVER THE ASCIZ
620 ;; 69$: .ASCIZ <CRLF>*CZM9BA0 M9312 BOOT TERM BK*<CRLF>
621 001664
622 001664 012700 007420          MOV @BUF1, RO      ;; CLR SIZING BUFFERS
623 001670 005020          CLR (RO)+
624 001672 020027 007504          CMP RO, @OCTBUF  ;; HAVE WE CLEARED THE WHOLE
625 001676 002774          BLT 8$          ;; BUFFER, NO, GO BACK
626 001700 005037 001116          CLR @ROMFIN      ;; INITIALIZE ROM FOUND INDICATORS
627 001704 005037 003644          CLR @TIMES      ;; INITIALIZE ENTRY COUNTER FOR PIJMES SUB
628 001710 013746 000004          MOV @ERRVEC, -(R6)  ;; SAVE CONTENTS OF LOCATION 4
629 001714 016737 000036 000004  MOV 2$, @ERRVEC  ;; SET UP FOR POSSIBLE TRAP
630 001722 122737 177777 165000  CMPB #-1, @#165000  ;; IF CONTENTS = -1, OR TRAP
631 001730 001414          BEQ 3$          ;; THEN CPU ROM NOT PLUGGED IN
632 001732 012700          MOV @165000, RO  ;; GET FIRST ADDRESS OF ROM SPACE
633 001736 005720          1$: TST (RO)+          ;; IF THIS INSTRUCTION TRAPS CPU ROM

```

## NO1

CZM9BAD M9312 BOOT TERM 8K  
CZM9BA.P11 13-MAR-78 08:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 13  
GET VALUE FOR SOFTWARE SWITCH REGISTER

SEQ 0013

```

634      001740 020027 166000      CMP      R0,      #166000      ; NOT PLUGGED IN
635      001744 001374      BNE      R1,      0      ; HAVE WE CHECKED ALL ADDRESSES
636      001746 012737 000001 001116  MOV      R1,      0      ; IF NO THEN CONTINUE LOOPING
637      001748 000402      BR      35      ; IF NO TRAPS OR LOW BYTE OF FIRST
638      001750 062716 000004      25:  ADD      R4,      (R6)      ; ADDRESS NOT EQUAL -1 ASSUME ROM PRESENT
639      001752 012637 000004      35:  MOV      (R6)+, 0      ; NO TRAP JUST RESTORE ERRUEL
640      001754 012700 000002      MOV      R0,      0      ; IF TRAP CLEAN OFF PC, PSW
641      001756 013737 001114 003250  MOV      0#FIRSTB, 0      ; RESTORE ERRVEC
642      001758 132777 000200 001242 55:  BITB     R200, 0      ; INITIALIZE ROM FOUND INDICATOR
643      001760 001404      BEQ      75      ; GET FIRST BOOT
644      001762 022777 177776 001232      CMP      R-2, 0      ; IF LOW BYTE HAS BIT 7 SET
645      001764 050037 001116      BNE      45      ; DO NOT SET ROM FOUND INDICATOR
646      001766 062737 000200 003250 45:  ADD      R200, 0      ; UNLESS CONTENTS OF ADDRESS
647      001768 006100      ROL      R0,      0      ; EQUAL TO -2. (CONTINUATION ROM)
648      001770 022737 174000 003250  CMP      R174000, 0      ; SET ROM FOUND INDICATOR
649      001772 001356      BNE      55      ; UPDATE 1ST ADDRESS TO NEXT ROM
650      001774 005737 001116      TST      0#ROMFIN      ; UPDATE ROM FOUND INDICATOR
651      001776 001003      BNE      65      ; HAVE CHECKED ALL THE ROMS
652      001778 004767 000126      JSR      PC,      0      ; IF NO CONTINUE CHECKING
653      001780 000411      BR      $EOP      ; ARE THERE ANY ROMS AVAILABLE
654      001782 004767 000160      65:  JSR      PC,      0      ; IF YES GO TO ROM TEST
655      001784 005737 001144      TST      0#SPASS      ; IF NO GO TEST NO ROMS
656      001786 001004      BNE      $EOP      ; GO TO END OF PASS
657      001788 004767 000462      JSR      PC,      0      ; GO CALCULATE CHECKSUMS
658      001790 004767 001544      JSR      PC,      0      ; ARE WE ON 1ST PASS
659      001792 000462      JSR      PC,      0      ; IF NO SKIP PROCESS OF PARAMETERS
660      001794 001544      JSR      PC,      0      ; GO PROCESS ROM PARAMETERS
661      001796 000462      JSR      PC,      0      ; GO CHECK SEQUENCE OF DEVICE CODES
662      001798 000462      JSR      PC,      0
663      001800 000462      JSR      PC,      0
664      001802 000462      JSR      PC,      0
665      001804 000462      JSR      PC,      0
666      001806 000462      JSR      PC,      0
667      001808 000462      JSR      PC,      0
668      001810 000462      JSR      PC,      0
669      001812 000462      JSR      PC,      0
670      001814 000462      JSR      PC,      0
671      001816 000462      JSR      PC,      0
672      001818 000462      JSR      PC,      0
673      001820 000462      JSR      PC,      0
674      001822 000462      JSR      PC,      0
675      001824 000462      JSR      PC,      0
676      001826 000462      JSR      PC,      0
677      001828 000462      JSR      PC,      0
678      001830 000462      JSR      PC,      0
679      001832 000462      JSR      PC,      0
680      001834 000462      JSR      PC,      0
681      001836 000462      JSR      PC,      0
682      001838 000462      JSR      PC,      0
683      001840 000462      JSR      PC,      0
684      001842 000462      JSR      PC,      0
685      001844 000462      JSR      PC,      0
686      001846 000462      JSR      PC,      0
687      001848 000462      JSR      PC,      0
688      001850 000462      JSR      PC,      0
689      001852 000462      JSR      PC,      0

```

; INCREMENT THE PASS NUMBER (\$PASS)  
 ; TYPE "END PASS"  
 ; IF THERES A MONITOR GO TO IT  
 ; IF THERE ISN'T JUMP TO RSTART  
 ; IF IT IS DESIRED TO HAVE A BELL INDICATE THE "END OF PASS" LOCATION  
 ; \$SENDMG CAN BE CHANGED TO 7.

```

$EOP:      NOP
           INC      $PASS      ; INCREMENT THE PASS NUMBER
           BIC      #100000,$PASS ; DON'T ALLOW A NEG. NUMBER
           DEC      (PC)+      ; LOOP?
$EOPCT:    .WORD    1
           BGT      $DOAGN      ; YES
           MOV      (PC)+,2(PC)+ ; RESTORE COUNTER
$ENDCT:    .WORD    1
           $EOPCT
           TYPE     $SENDMG      ; TYPE "END PASS"
           TYPE     $ENULL      ; TYPE A NULL CHARACTER
$GET42:    MOV      0#42,R0      ; GET MONITOR ADDRESS
           BEQ      $DOAGN      ; BRANCH IF NO MONITOR
           RESET    ; CLEAR THE WORLD
$ENDAD:    JSR      PC,(R0)      ; GO TO MONITOR
           NOP
           NOP
           NOP

```

CZM9BA0 M9312 BOOT TERM 8K  
CZM93A.P11 13-MAR-78 08:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 14  
END OF PASS ROUTINE

SEQ 0014

```

690 002160 000240      NOP                      ;:ACT11
691 002162             $DOAGN:                   ;:RETURN
692 002162 000137      JMP      2(PC)+          ;:RETURN
693 002164 002044      $RTNAD: .WORD RSTART
694 002166      377      000      $ENULL: .BYTE -1,-1,0 ;:NULL CHARACTER STRING
695 002171 015 042412 042116 $ENDMG: .ASCIZ <15><12> /END PASS/
696 002176 050040 051501 000123

```

```

697
698
699
700      ;NO ROMS TEST
701      ;THIS ROUTINE IS CALLED ONLY IF NO ROMS WERE FOUND
702      ;DURING SIZING. IT DOES A READ OF ALL BOOTSTRAP ROM
703      ;ADDRESSES AND COMPARES THE CONTENTS TO A KNOWN
704      ;EXPECTED VALUE.
705

```

```

706 002204 012703 173000      NOROMS: MOV      #173000,      R3      ;GET FIRST ADDRESS TO BE READ
707 002210 013704 002242      MOV      2#NODATA,      R4      ;GET ADDRESS OF EXPECTED VALUE
708 002214 022304      1$:      CMP      (R3)+,      R4      ;DOES RECIEVED VALUE EQUAL EXPECTED
709 002216 001405      BEQ      2$      ;IF YES CONTINUE TESTING
710 002220 052737 000100 001110      BIS      #NOROME,      2#ROMERR      ;IF NO SET ERROR INDICATER
711 002226 004767 001630      JSR      PC,      ERRHAN      ;REPORT ERROR
712 002232 022703 174000      2$:      CMP      #174000,      R3      ;HAVE ALL ADDRESSES BEEN TESTED
713 002236 003366      BGT      1$      ;IF NO GO TEST THIS ADDRESS
714 002240 000207      RTS      PC
715
716 002242 161777      NODATA: .WORD 161777
717
718
719
720

```

```

721      ;CHECKSUM ROMS MODULE
722      ;THIS ROUTINE DOES A CHECKSUM OF ALL ROMS PRESENT
723

```

```

724 002244 012737 0000C1 002460      CHECKS: MOV      #1,      2#ROMCNT      ;INITIALIZE ROM COUNTER
725 002252 033737 002460 001116      BIT      2#ROMCNT,      2#ROMFIN      ;IS DIAGNOSTIC ROM PRESENT
726 002260 001424      BEQ      1$      ;IF NO GO CHECKSUM BOOT ROMS
727 002262 012737 165775 002556      MOV      #165775,      2#LASTAD      ;SET UP LAST ADDRESS TO BE SUMMED
728 002270 012737 000000 002554      MOV      #0,      2#EXCADD      ;SET UP EXCEPTION ADDRESS
729 002276 012737 165000 002552      MOV      #165000,      2#FIRSTA      ;SET UP FIRST ADDRESS TO BE SUMMED
730 002304 004767 000152      JSR      PC,      CALSUM      ;GO CALCULATE CHECKSUM
731 002310 013703 165776      MOV      2#165775,      R3      ;GET EXPECTED CHECKSUM
732 002314 020304      CMP      R3,      R4      ;COMPARE CHECKSUMS
733 002316 001405      BEQ      1$      ;IF CHECKSUMS COMPARE CONTINUE
734 002320 052737 000020 001110      BIS      #CRCERR,      2#ROMERR      ;IF ERROR SET INDICATER
735 002326 004767 001530      JSR      PC,      ERRHAN      ;REPORT ERROR
736 002332 012737 173000 002552      1$:      MOV      #173000,      2#FIRSTA      ;SET UP FIRST ADDRESS TO BE SUMMED
737 002340 012737 173024 002554      MOV      #173024,      2#EXCADD      ;SET UP EXCEPTION ADDRESS
738 002346 012737 173175 002556      MOV      #173175,      2#LASTAD      ;SET UP LAST ADDRESS TO BE SUMMED
739 002354 012702 173176      MOV      #173176,      R2      ;GET ADDRESS OF GOOD DATA
740 002360 006137 002460      2$:      ROL      2#ROMCNT,      2#ROMFIN      ;UPDATE ROM COUNTER
741 002364 033737 002460 001116      BIT      2#ROMCNT,      2#ROMFIN      ;IS ROM PRESENT
742 002372 001412      BEQ      3$      ;IF NO, GO UPDATE TO NEXT ROM
743 002374 004767 000062      JSR      PC,      CALSUM      ;IF YES GO CALCULATE CHECKSUM
744 002400 011203      MOV      (R2),      R3      ;GET EXPECTED CHECKSUM
745 002402 020304      CMP      R3,      R4      ;COMPARE CHECKSUMS

```

```

746 002404 001405          BEQ      3$
747 002406 052737 000020 001110    BIS      #CRCERR,      @#ROMERR      ; IF CHECKSUMS EQUAL CONTINUE
748 002414 004767 001442          JSR      PC              ERRHAN      ; IF ERROR SET INDICATOR
749 002420 062737 000200 002552 3$:  ADD      #200,          @#FIRSTA    ; REPORT ERROR
750 002426 062737 000200 002554    ADD      #200,          @#EXCADD    ; UPDATE FIRST ADDRESS
751 002434 062737 000200 002556    ADD      #200,          @#LASTAD    ; UPDATE EXCEPTION ADDRESS
752 002442 062702 000200          ADD      #200,          R2          ; UPDATE LAST ADDRESS
753 002446 022737 174000 002552    CMP      #174000,        @#FIRSTA    ; UPDATE ADDRESS OF GOOD DATA
754 002454 001341          BNE      2$
755 002456 000207          RTS      PC              ; HAVE ALL ROMS BEEN CHECKED
756                                     ; IF NO GO CHECKSUM NEXT ONE
757 002460 000000          ROMCNT: 0      ; IF YES RETURN
758
759
760                                     ; CALCULATE CHECKSUMS MODULE
761                                     ; THIS ROUTINE CALCULATES THE CRC16 CHECKSUM OF THE CONTEI S
762                                     ; OF EVERY LOCATION EXCEPT THE EXCEPTION ADDRESS AND LAST ADDRESS
763                                     ; OF EVERY ROM
764
765
766 002462 010246          CALSUM: MOV      R2              -(SP)      ; SAVE R2
767 002464 013700 002552    MOV      @#FIRSTA,          R0          ; GET STARTING ADDRESS
768 002470 005004          CLR      R4              ; INITIALIZE CRC WORD
769 002472 012005          LOOP:  MOV      (R0)+,          R5          ; GET A BYTE
770 002474 012702 000020    MOV      #16.,          R2          ; SET BYTE COUNT
771 002500 000241          CRCLOP: CLC              ; THE NEXT NINE LINES
772 002502 006004          ROR      R4              ; DO THE MATH CALCULATIONS
773 002504 006005          ROR      R5
774 002506 102006          BVC      1$
775 002510 012701 120001    MOV      #120001,          R1
776 002514 040401          BIC      R4              R1
777 002516 042704 120001    BIC      #120001,          R4
778 002522 050104          BIS      R1              R4
779 002524 005302          1$:  DEC      R2              R4
780 002526 003364          BGT      CRCLOP
781 002530 020037 002554    CMP      R0,          @#EXCADD    ; IS NEXT ADDRESS AN EXCEPTION ADDRESS
782 002534 001001          BNE      2$              ; IF NO TEST IT
783 002536 005720          TST      (R0)+          ; IF YES SKIP ADDRESS
784 002540 020037 002556    2$:  CMP      R0,          @#LASTAD    ; HAVE ALL LOCATIONS BEEN SUMMED
785 002544 101752          BLOS     LOOP          ; IF NO CONTINUE
786 002546 012602          MOV      (SP)+,          R2          ; RESTORE R2
787 002550 000207          RTS      PC              ; IF YES RETURN
788
789 002552 000000          FIRSTA: 0
790 002554 000000          EXCADD: 0
791 002556 000000          LASTAD: 0
792
793
794
795                                     ; PROCESS ROM PARAMETERS MODULE
796                                     ; THIS ROUTINE DETERMINES IF SIZING IS TO BE DONE. IF IT IS THE
797                                     ; MODULE WILL GET THE PARAMETERS FROM THE DEVCOD AND PPFVAR
798                                     ; MODULES AND ASSEMBLE MESSAGES TO BE OUTPUT. IF SIZING IS
799                                     ; NOT TO BE DONE THIS MODULE WILL TAKE THE PARAMETERS RECEIVED
800                                     ; AND COMPARE THEM TO THE VALUES SUPPLIED IN THE ETABLE.
801
802

```



CZM98A0 M9312 BOOT TERM 8A  
CZM98A.P11 13-MAR-78 08:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 16  
END OF PASS ROUTINE

SEQ 0016

802	002560	105737	001157	PROMP:	TSTB	2#SENV	;	IF BIT IS CLEAR SIZE
803	002564	100424			BMI	1\$		; IF BIT IS SET DON'T SIZE.
804	002566	004767	000256		JSR	PC,	DEVCOD	; CALL DEVICE CODE MODULE
805	002572	004767	000542		JSR	PC,	PUTMES	; FORMAT PARAMETER MESSAGE
806	002576	007514			MES1			
807	002600	004767	000450		JSR	PC,	PPFVAR	; GO GET PSEUDO POWER-FAIL VECTOR ADDRESS
808	002604	032737	000040	001110	BIT	2#PFERR.	2#ROMERR	; WAS THERE AN ERR
809	002612	001403			BEQ	10\$		; IF NO GO FORMAT MESSAGE
810	002614	004767	001242		JSR	PC,	ERRHAN	; IF YES GO REPORT MESSAGE
811	002620	000512			BR	9\$		
812	002622	004767	000512	10\$:	JSR	PC,	PUTMES	; FORMAT PSEUDO POWER-FAIL VECTOR
813	002626	010114			MES2			; ADDRESS MESSAGE
814	002630	005237	001112		INC	2#MESSAG		; SET MESSAGE INDICATOR
815	002634	000504			BR	9\$		; GO TO EXIT
816	002636	004767	000206	1\$:	JSR	PC,	DEVCOD	; CALL GET DEVICE CODE MODULE
817	002642	012703	007420		MOV	2#BUF1	R3	; GET BOARD DEVICE CODES
818	002646	012704	001222	2\$:	MOV	2#DDW0.	R4	; GET ETABLE DEVICE CODE PARAMETERS
819	002652	062703	000002		ADD	2#	R3	; GET TO BOARD DEVICE CODE
820	002656	012402		3\$:	MOV	(R4)+,	R2	; GET ETABLE DEVICE CODE
821	002660	000302			SWAB	R2		; REFORMAT ASCII
822	002662	020213			CMP	R2,	(R3)	; DO THE TWO DEVICE CODES COMPARE
823	002664	001407			BEQ	4\$		; IF YES GET NEXT BOARD DEVICE CODE
824	002666	005714			TST	(R4)		; IF NO HAVE WE CHECKED THE WHOLE ETABLE
825	002670	001372			BNE	3\$		; IF WE HAVEN'T CHECK NEXT ETABLE ENTRY
826	002672	052737	000001	001110	BIS	2#APTER1,	2#ROMERR	; IF WE HAVE THEN DEVICE CODE ON BOARD
827	002700	004767	001156		JSR	PC,	ERRHAN	; DOES NOT EXIST IN ETABLE
828	002704	062703	000002	4\$:	ADD	2#	R3	; UPDATE TO NEXT ADDRESS
829	002710	005713			TST	(R3)		; IF CONTENTS EQUALS ZERO WE'RE DONE
830	002712	001355			BNE	2\$		; IF CONTENTS NO EQUAL ZERO CONTINUE
831	002714	012703	001222		MOV	2#DDW0.	R3	; GET BOARD DEVICE CODES
832	002720	012704	007416	5\$:	MOV	2#BUF1-2,	R4	; GET ETABLE DEVICE CODES
833	002724	000313			SWAB	(R3)		; REFORMAT ASCII
834	002726	062704	000004	6\$:	ADD	2#	R4	; GET BOARD DEVICE CODE
835	002732	021314			CMP	(R3),	(R4)	; DO THE TWO DEVICE CODES COMPARE
836	002734	001410			BEQ	7\$		; IF YES GET NEXT ETABLE DEVICE CODE
837	002736	005714			TST	(R4)		; IF NO HAVE WE CHECKED ALL THE BOARD
838	002740	001372			BNE	6\$		; DEVICE CODES, IF NO CONTINUE
839	002742	000313			SWAB	(R3)		; PUT ASCII IN RIGHT ORDER FOR OUTPUT
840	002744	052737	000002	001110	BIS	2#APTER2,	2#ROMERR	; IF YES ETABLE DEVICE CODE NOT
841	002752	004767	001104		JSR	PC,	ERRHAN	; NOT ON BOARD
842								
843	002756	062703	000002	7\$:	ADD	2#	R3	; IF CONTENTS EQUAL ZERO-DONE
844	002762	005713			TST	(R3)		; IF CONTENTS NO EQUAL ZERO-CONTINUE
845	002764	001355			BNE	5\$		
846	002766	004767	000262		JSR	PC,	PPFVAR	; GO GET PSEUDO POWER-FAIL VECTOR ADDRESS
847	002772	013704	001212		MOV	2#BASE,	R4	; GET ETABLE PPFVA
848	002776	013703	007500		MOV	2#BUF2,	R3	; GET BOARD PPFVA
849	003002	020403			CMP	R4,	R3	; DO THE TWO PARAMETERS COMPARE
850	003004	001405			BEQ	8\$		; IF YES CONTINUE
851	003006	052737	000004	001110	BIS	2#APTER3,	2#ROMERR	; SET APT ERROR INDICATOR
852	003014	004767	001042		JSR	PC,	ERRHAN	; GO TO ERROR ROUTINE
853	003020	013704	001216	8\$:	MOV	2#CDW1,	R4	; GET ETABLE DATA
854	003024	013703	007502		MOV	2#BUF2+2,	R3	; GET BOARD DATA
855	003030	020403			CMP	R4,	R3	; DO THE TWO PARAMETER COMPARE
856	003032	001405			BEQ	9\$		; IF YES THEN DONE
857	003034	052737	000010	001110	BIS	2#APTER4,	2#ROMERR	; SET APT ERROR INDICATOR

## E02

CZM98A0 M9312 BOOT TERM 8K  
CZM98A.P11 13-MAR-78 08:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 17  
END OF PASS ROUTINE

SEG 0017

```

858 003042 004767 001014          JSR    PC,          ERRHAN          ;GO TO ERROR ROUTINE
859 003046 000207          9$:    RTS      PC
860
861          ;GET DEVICE CODES MODULE
862          ;THIS SUBROUTINE LOCATES EACH DEVICE CODE AND PASSES IT AND THE
863          ;ADDRESS IN WHICH IT WAS FOUND BACK TO THE CALLING ROUTINE.
864          ;DATA IS STORED IN BUFFER "BUF1" IN THIS FORMAT:
865          ;
866          ;      BUF1:  ADDRESS OF FIRST DEVICE CODE
867          ;                DEVICE CODE
868          ;                ADDRESS OF SECOND DEVICE CODE
869          ;                DEVICE CODE
870          ;                .
871          ;                .
872          ;                .
873          ;                ADDRESS OF NTH DEVICE CODE
874          ;                DEVICE CODE .
875
876          ;IF DIAGNOSTIC ROM PRESENT IT WILL BE THE FIRST DEVICE CODE.
877          ;THERE IS ROOM FOR UP TO 12 DEVICE CODES IN TEMP
878
879 003050 012700 007420          DEVCOD: MOV    #BUF1,          R0
880 003054 012701 000001          MOV    #1,          R1
881 003060 030137 001116          BIT     R1,          @#ROMFIN          ; INITIALIZE ROM POINTER
882 003064 001411          BEQ     1$,          ; IS DIAGNOSTIC ROM PRESENT
883 003066 012737 165774 003250          MOV    #165774,          @#TESTAD          ; IF NO GO TEST BOOT ROMS
884 003074 013720 003250          MOV    @#TESTAD,          (R0)+          ; GET ADDRESS OF DIAG. ROM DEVICE CODE.
885 003100 017720 000144          MOV    @#TESTAD,          (R0)+          ; STORE ADDRESS OF DEVICE CODE
886 003104 005037 003252          CLR     @#DAFLAG          ; STORE DEVICE CODE
887 003110 013737 001114 003250 1$:    MOV    @#FIRSTB,          @#TESTAD          ; CLR DATA TYPE FLAG
888 003116 006101          ROL     R1,          ; GET ADDRESS OF FIRST BOOT ROM
889 003120 030137 001116          3$:    BIT     R1,          @#ROMFIN          ; UPDATE POINTER TO NEXT ROM
890 003124 001005          BNE     4$,          ; IS ROM PRESENT
891 003126 062737 000200 003250          ADD    #200,          @#TESTAD          ; IF YES GO GET PARAMETERS
892 003134 006101          ROL     R1,          ; IF NO UPDATE TEST ADDRESS
893 003136 000437          BR      9$,          ; UPDATE POINTER TO NEXT ROM
894 003140 005737 003252          4$:    TST     @#DAFLAG          ; GO SEE IF ALL ROM CHECKED
895 003144 001010          BNE     5$,          ; IF DATAFLAG=0 THEN DATA IS DEVICE CODE
896 003146 013720 003250          MOV    @#TESTAD,          (R0)+          ; IF DATAFLAG=1 THE DATA IS OFFSET TO NEX
897 003152 017720 000072          MOV    @#TESTAD,          (R0)+          ; STORE ADDRESS OF DEVICE CODE
898 003156 062737 000002 003250          ADD    #2,          @#TESTAD          ; STORE DEVICE CODE
899 003164 000422          BR      8$,          ; UPDATE TEST ADDRESS
900 003166 013702 003250          5$:    MOV    @#TESTAD,          R2          ; GET OVER SOME CODE
901 003172 067737 000052 003250          ADD    @#TESTAD,          @#TESTAD          ; SAVE OLD TEST ADDRESS
902 003200 013703 003250          MOV    @#TESTAD,          R3          ; UPDATE TO NEW TEST ADDRESS
903 003204 042702 177177          BIC     #177177,          R2          ; GET THE NEW ADDRESS
904 003210 042703 177177          BIC     #177177,          R3          ; SAVE ONLY BITS 7,8
905 003214 160203          SUB     R2,          R3          ; IN R3 ALSO
906 003216 005703          6$:    TST     R3,          ; CALCULATE DISTANCE BETWEEN ADD
907 003220 001404          BEQ     8$,          ; IS R3 EQUAL TO 0
908 003222 162703 000200          SUB     #200,          R3          ; IF YES THEN DONE
909 003226 006101          ROL     R1,          ; IF NO THEN MOVE POINTER
910 003230 000772          BR      6$,          ; ONE BIT FOR EVERY 200
911 003232 005137 003252          8$:    COM     @#DAFLAG          ; CHANGE DATA FLAG TO RIGHT DATA TYPE
912 003236 023727 003250 174000 9$:    CMP     @#TESTAD,          #174000          ; HAVE WE CHECKED ALL THE ROM
913 003244 002725          BLT     3$,          ; IF NO CONTINUE

```

F02

CZM98AD M9312 BOOT TERM 8A  
CZM98A.F11 13-MAR-78 09:58MACY11 30A(1052) 13-MAR-78 09:59 PAGE 18  
END OF PASS ROUTINE

SEQ 0018

```

914 003246 000207          RTS      PC          ; IF YES RETURN
915
916 003250 000000          TESTAD: 0
917 003252 000000          DAFLAG: 0
918
919          ; GET PSEUDO POWER-FAIL VECTOR ADDRESS ROUTINE
920          ; THIS SUBROUTINE TESTS LOCATIONS 173024 AND 173224 TO DETERMINE
921          ; WHICH VECTOR WILL BE USED IF POWER-FAIL OPTION ENABLED ON THE
922          ; BOARD. THE OPTION MUST BE ENABLED AND AT LEAST ONE ADDRESS
923          ; SWITCH MUST BE "ON" OR AN ERROR WILL BE DETECTED.
924          ; THE DATA WILL BE RETURNED IN "BUF2" IN THE FORMAT.
925          :
926          :          BUF2:  PSEUDO POWER-FAIL VECTOR ADDRESS
927          :          :      CONTENTS OF VECTOR ADDRESS
928          :
929 003254 032737 000777 173024 PPFVAR: BIT      #777,          @#173024      ; TEST IF LOCATION 173024 SELECTED
930 003262 001407          BEQ      1$          ; IF NOT THEN GO TEST LOCATION 173224
931 003264 012737 173024 007500      MOV      #173024,        @#BUF2      ; IF IT IS THEN STORE ADDRESS
932 003272 013737 173024 007502      MOV      @#173024,        @#BUF2+2    ; STORE CONTENTS OF LOCATION 173024
933 003300 000416          BR       3$          ; GO TO RETURN
934 003302 032737 000777 173224 1$: BIT      #777,          @#173224    ; TEST IF LOCATION 173224 SELECTED
935 003310 001407          BEQ      2$          ; IF NOT THEN SET ERROR INDICATOR
936 003312 012737 173224 007500      MOV      #173224,        @#BUF2      ; IF IT IS THEN STORE ADDRESS
937 003320 013737 173224 007502      MOV      @#173224,        @#BUF2+2    ; STORE CONTENTS OF VECTOR
938 003326 000403          BR       3$          ; GET OVER ERROR
939 003330 052737 000040 001110 2$: BIS      #PFERR,        @#ROMERR    ; SET ERROR INDICATOR
940 003336 000207          3$:      RTS      PC
941
942
943
944
945          ; PUT MESSAGE IN BUFFER ROUTINE
946          ; THIS SUBROUTINE FORMATS THE PARAMETER AND POWER-FAIL MESSAGES.
947
948 003340 017604 000000          PUTMES: MOV      @ (R6),          R4          ; GET MESSAGE BUFFER ADDRESS
949 003344 005737 003644          TST      @#TIMES          ; IS THIS FIRST TIME THROUGH
950 003350 001076          BNE      3$          ; IF NOT FIRST TIME THEN FORMAT POWER-
951          ; FAIL MESSAGE
952 003352 005237 003644          INC      @#TIMES          ; TOGGLE WATCHDOG
953 003356 012703 007420          MOV      #BUF1,          R3          ; GET DATA BUFFER ADDRESS
954 003362 022713 165774          CMP      #165774,        (R3)          ; IS DIAGNOSTIC ROM PRESENT
955 003366 001012          BNE      1$          ; IF NOT DON'T FORMAT DIAG. ROM MESSAGE
956 003370 012705 006530          MOV      #DRHEAD,        R5          ; IF IT IS GET DIAG. ROM MESSAGE HEADER
957 003374 004767 001000          JSR      PC,          FILBUF      ; GO PUT HEADER IN MESSAGE BUFFER
958 003400 000015          CR
959 003402 062703 000002          ADD      #2,          R3          ; SKIP OVER ADDRESS OF ASCII
960 003406 000313          SWAB      (R3),          (R4)+          ; FORMAT ASCII FOR MESSAGE
961 003410 112324          MOV      (R3)+,        (R4)+          ; PUT ASCII IN MESSAGE BUFFER
962 003412 112324          MOV      (R3)+,        (R4)+
963 003414 012705 006543          1$: MOV      #BRHEAD,        R5          ; GO PUT BOOT ROM HEADER IN MESS. BUF.
964 003420 004767 000754          JSR      PC,          FILBUF
965 003424 000015          CR
966 003426 112724 000015          2$: MOV      #CR,          (R4)+          ; PUT A CR LF HERE
967 003432 112724 000012          MOV      #LF,          (R4)+
968 003436 005713          TST      (R3)          ; IS DATA EQUAL TO ZERO
969 003440 001464          BEQ      5$

```

CZM98A0 M9312 BOOT TERM 8K  
CZM98A.P11 13-MAR-78 08:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 19  
END OF PASS ROUTINE

SEG 0019

970	003442	062713	000004	ADD	#4,	(R3)	:GET FIRST ENTRY POINT
971	003446	004767	001074	JSR	PC,	OCADD	:GO CONVERT OCTAL TO ASCII
972	003452	004767	000722	JSR	PC,	FILBUF	:GO PUT ENTRY POINT IN MESSAGE BUF
973	003456	000011		HT			
974	003460	112724	000040	MOVB	#40,	(R4)+	
975	003464	112724	000040	MOVB	#40,	(R4)+	
976	003470	112724	000040	MOVB	#40,	(R4)+	
977	003474	062713	000002	ADD	#2,	(R3)	:GET SECOND ENTRY POINT
978	003500	004767	001042	JSR	PC,	OCADD	:GO CONVERT OCTAL TO ASCII
979	003504	004767	000670	JSR	PC,	FILBUF	:GO PUT ENTRY POINT IN MESSAGE BUF.
980	003510	000011		HT			
981	003512	112724	000040	MOVB	#40,	(R4)+	
982	003516	112724	000040	MOVB	#40,	(R4)+	
983	003522	112724	000040	MOVB	#40,	(R4)+	
984	003526	112724	000011	MOVB	#HT,	(R4)+	:PUT TAB IN HERE
985	003532	062703	000002	ADD	#2,	R3	:UPDATE DATA BUFFER ADDRESS
986	003536	000313		SWAB	(R3)		:FORMAT ASCII FOR MESSAGE
987	003540	112324		MOVB	(R3)+,	(R4)+	:MOV ASCII TO MES1
988	003542	112324		MOVB	(R3)+,	(R4)+	
989	003544	000730		BR	2\$		:GO BACK TO START OF LOOP
990	003546	012703	007500	3\$: MOV	#BUF2,	R3	:GET DATA BUFFER ADDRESS
991	003552	012705	006660	MOV	#PFHEAD,	R5	:GET POWER-FAIL HEADER ADDRESS
992	003556	004767	000616	JSR	PC,	FILBUF	:GO PUT POWER-FAIL HEADER IN MESSAGE BUF
993	003562	000015		CR			
994	003564	022703	007504	4\$: CMP	#BUF2+4,	R3	:ARE WE DONE
995	003570	001410		BEQ	5\$		:IF YES THEN GO RETURN
996	003572	004767	000750	JSR	PC,	OCADD	:GO CONVERT OCTAL TO ASCII
997	003576	004767	000576	JSR	PC,	FILBUF	
998	003602	000011		HT			
999	003604	062703	000002	ADD	#2,	R3	:UPDATE DATA BUFFER ADDRESS
1000	003610	000765		BR	4\$		:GO BACK TO START OF LOOP
1001	003612	112724	000015	5\$: MOVB	#CR,	(R4)+	:PUT A CR/LF AT END OF MESSAGE
1002	003616	112724	000012	MOVB	#LF,	(R4)+	
1003	003622	105024		CLRB	(R4)+		:PUT ZERO TERMINATOR AT END OF MESSAGE
1004	003624	017667	000000 000002	MOV	2(R6),	6\$	:GET MESSAGE BUFFER ADDRESS
1005	003632	104401		TYPE			
1006	003634	000000		.WORD	0		
1007	003636	062716	000002	6\$: ADD	#2,	(R6)	:GET OVER MESSAGE BUFFER ADDRESS
1008	003642	000207		RTS	PC		
1009	003644	000000		TIMES:	0		
1010							
1011							
1012							
1013							
1014							
1015							
1016							
1017							
1018							
1019							
1020							
1021							
1022	003646	013700	001116	SEQTST: MOV	2#ROMFIN,	R0	:GET ROM LIST
1023	003652	006000		ROR	R0		:GET RID OF DIAG ROM INDICATOR
1024	003654	000241		CLC			:OK START, WE MUST FIND MORE THAN
1025	003656	005037	003252	CLR	2#DAFLAG		:ONE BOOT ROM TO DO THIS TEST

:SEQUENCE TEST  
:THIS TEST VERIFIES THE ORDER OF THE ROMS ADHERES TO THAT SPECIFIED  
:IN THE INSTALLATION PROCEDURE. THAT IS THE THEY HAVE BEEN INSTALLED  
:IN ALPHABETICAL ORDER ACCORDING TO THE FIRST DEVICE CODE OF THE ROM.  
:THE TYPEOUT FROM THIS SECTION DOES NOT INDICATE AN ERROR BUT IS JUST  
:A WARNING TO THE OPERATOR THAT THE SPECIFICATION HAS NOT BEEN FULLY  
:ADHERED TO.



H02

CZM9BA0 M9312 BOOT TERM R BK  
CZM9BA.P11 13-MAR-78 08:58MACY11 30A(1052) 13-MAR-78 09:59 PAGE 20  
END OF PASS ROUTINE

SEQ 0020

```

1026 003662 006000 1$: ROR RD ; IF ROM PRESENT C-BIT SETS
1027 003664 103406 BCS 2$ ; WHEN C-BITS SETS THATS "ONE"
1028 003666 005237 003252 INC 3$DAFLAG ; INCREMENT COUNTER-IF WE DO THIS
1029 003672 022737 000004 003252 CMP 4$ 2$DAFLAG ; LOOP 4 TIMES, NO BOOT ROM PRESENT AT AL
1030 003700 003033 BGT SEQEX ; SO EXIT TEST
1031 003702 005700 2$: TST RD ; WE FOUND ONE, BUT IF RD IS EQUAL TO
1032 003704 001431 BEQ SEQEX ; ZERO THERE ONLY THE ONE SO EXIT
1033 003706 005037 003252 CLR 3$DAFLAG
1034 003712 012700 007420 MOV 4$BUF1, RD ; AT LEAST TWO START TEST, GET ROM DATA
1035 003716 032737 000001 001116 BIT 1, 3$ROMFIN ; IS DIAG ROM PRESENT
1036 003724 001402 BEQ 3$ ; IF NO DON'T UPDATE DATA TABLE ADDRESS
1037 003726 062700 000004 000004 ADD 4$, RD ; IF YES THAN UPDATE TABLE ADDRESS
1038 003732 004767 000040 3$: JSR PC, GETCOD ; GO GET A DEVICE CODE
1039 003736 005704 TST R4 ; UPON RETURN FROM GET COD R4 INDICATES
1040 003740 001013 BNE SEQEX ; IF VALID DEVICE CODE WAS FOUND
1041 003742 004767 000030 JSR PC, GETCOD ; CODES SC EXIT, IF NOT THEN GET NEXT COD
1042 003746 026767 000020 000020 CMP FRSCOD, LASCOD ; IS 1ST CODE ALPHABETICALLY LESS THE 2ND
1043 003754 003003 BGT 4$ ; IF NO SEQUENCE ERROR
1044 003756 005704 TST R4 ; IF R4 NOT EQUAL TO ZERO GET CON
1045 003760 001003 BNE SEQEX ; COULD NOT FIND VALID DEVICE CODE, SO EXIT
1046 003762 000763 BR 3$ ; IF NOT THEN GO GET NEXT DEVICE CODE
1047 003764 104401 4$: TYPE ; TYPE OUT WARNING MESSAGE
1048 003766 007317 SEQMSG
1049 003770 000207 SEQEX: RTS PC ; RETURN
1050
1051 003772 000000 FRSCOD: .WORD 0
1052 003774 000000 LASCOD: .WORD 0
1053
1054
1055
1056
1057
1058
1059
1060
1061

```

```

;GET DEVICE CODES SUBROUTINE
;THIS SUBROUTINE IS CALLED BY THE SEQUENCE TEST ROUTINE TO GET THE
;DEVICE CODES FROM THE SIZING BUFFER. THE ROUTINE VALIDATES EACH DEVICE
;CODE THAT IS IT MAKES SURE THE CODE IS THE FIRST CODE IN A ROM. IF
;IT CAN NOT FIND A VALID DEVICE CODE IT MAKES R4 NONZERO BEFORE RETURNING.

```

```

1062 003776 005004 GETCOD: CLR R4
1063 004000 005737 003252 TST 3$DAFLAG ; IF DAFLAG IS ZERO PUT DEVICE CODE
1064 004004 001405 BEQ 1$ ; IN ERSCOD
1065 004006 012703 003774 MOV 4$LASCOD, R3 ; IF DAFLAG IS ONE PUT DEVICE CODE
1066 004012 005037 003252 CLR 3$DAFLAG ; IN LASCOD AND CLEAR DAFLAG
1067 004016 000404 BR 2$ ; GET OVER DAFLAG=ZERO SETUP
1068 004020 012703 003772 1$: MOV 4$FRSCOD, R3 ; PUT DEVICE CODE IN FR5 COD
1069 004024 005237 003252 INC 3$DAFLAG ; MAKE DAFLAG NOT ZERO
1070 004030 005710 2$: TST (RD) ; DOES DEVICE CODE EXIST
1071 004032 001002 BNE 3$ ; IF YES CHECK IF VALID
1072 004034 005104 COM R4 ; IF NO MAKE R4 NON-ZERO
1073 004036 000410 BR GETEND ; AND EXIT
1074 004040 012001 3$: MOV (RD)+, R1 ; GET ADDRESS OF DEVICE CODE
1075 004042 032701 000170 BIT 170, R1 ; IF BIT 3-6 CLEAR THIS IS 1ST CODE
1076 004046 001403 BEQ 4$ ; OF A ROM SO GET IT
1077 004050 062700 000002 ADD 2$, RD ; IF BITS SET UPDATE TO NEXT CODE
1078 004054 000765 BR 2$ ; AND VALIDATE IT
1079 004056 012013 4$: MOV (RD)+, (R3) ; GET DEVICE CODE AND STORE IT
1080 004060 000207 GETEND: RTS PC ; RETURN
1081

```

Address	Instruction	Comment
1082		
1083		
1084		
1085		
1086		
1087		
1088		
1089	004062 104411	ERRHAN: SAVREG
1090	004064 012704 010214	MOV #ERRMSG, R4
1091	004070 005237 001120	INC #ERRCNT
1092	004074 032777 002000 175002	BIT #BIT10, R4
1093	004102 001402	BEQ 15
1094	004104 104401	TYPE
1095	004106 006524	BELL
1096	004110 032777 020000 174766 15:	BIT #BIT13, R4
1097	004116 001110	BNE 105
1098	004120 013700 001110	MOV #ROMERR, R0
1099	004124 013767 001110 000174	MOV #ROMERR, R5
1100	004132 012701 004362	MOV #EHEADT, R1
1101	004136 000241	CLC
1102	004140 006000 25:	ROR R0
1103	004142 103403	BCS 35
1104	004144 062701 000002	ADD #2, R1
1105	004150 000773	BR 25
1106	004152 011105 35:	MOV (R1), R5
1107	004154 004767 000220	JSR PC, FILBUF
1108	004160 000015	CR
1109	004162 032737 000023 001110	BIT #23, #ROMERR
1110	004170 001424	BEQ 75
1111	004172 032737 000020 001110	BIT #20, #ROMERR
1112	004200 001415	BEQ 65
1113	004202 013700 002460	MOV #ROMCNT, R0
1114	004206 012701 006512	MOV #ROMNUM, R1
1115	004212 000241	CLC
1116	004214 006000 45:	ROR R0
1117	004216 103403	BCS 55
1118	004220 062701 000002	ADD #2, R1
1119	004224 000773	BR 45
1120	004226 112124 55:	MOVB (R1)+, (R4)+
1121	004230 112124	MOVB (R1)+, (R4)+
1122	004232 000421	BR 85
1123	004234 112324 65:	MOVB (R3)+, (R4)+
1124	004236 112324	MOVB (R3)+, (R4)+
1125	004240 000416	BR 85
1126	004242 016603 000006 75:	MOV 6(R6), R3
1127	004246 004767 000170	JSR PC, OCASC
1128	004252 004767 000122	JSR PC, FILBUF
1129	004256 000011	HT
1130	004260 016603 000010	MOV 10(R6), R3
1131	004264 004767 000152	JSR PC, OCASC
1132	004270 004767 000104	JSR PC, FILBUF
1133	004274 000011	HT
1134	004276 112724 000015 85:	MOVB #CR, (R4)+
1135	004302 112724 000012	MOVB #LF, (R4)+
1136	004306 112724 000000	MOVB #0, (R4)+
1137	004312 005767 174640	TST \$ENV

```

1138 004316 001405          BEQ      11$          ; IF NO BRANCH
1139 004320 004767 000534    JSR      PC,          SATY1    ; GO REPORT ERROR
1140 004324 010214          ERRMSG
1141 004326 000000          9$:      .WORD      0
1142 004330 000000          HALT
1143 004332 004767 000530    11$:      JSR      PC,          SATY3    ; THEN HALT
1144 004336 010214          ERRMSG    ; IF NOT ON APT JUST REPORT ERROR
1145 004340 032777 100000 174536 10$:      BIT      #BIT15,    QSWR    ; IS HALT ON ERROR SET
1146 004346 001401          BEQ      12$          ; IF NO SKIP OVER HALT
1147 004350 000000          HALT
1148 004352 005037 00111.    12$:      CLR      Q#ROMERR    ; CLEAR ERROR FLAGS
1149 004356 104412          RESREG
1150 004360 000207          RTS      PC
1151
1152 004362 006756          EHEADT: ER1MSG
1153 004364 006722          ER2MSG
1154 004366 007014          ER3MSG
1155 004370 007072          ER4MSG
1156 004372 007146          CRCMSG
1157 004374 007172          PFMSG
1158 004376 007250          NOROMM
1159
1160
1161
1162          ;FILL BUFFER ROUTINE
1163          ;THIS SUBROUTINE FILLS THE MESSAGE BUFFER WILL ASCII
1164          ;CHARACTERS.
1165
1166 004400 022776 000011 000000 FILBUF: CMP      #HT,          Q(R6)    ; IS FIRST CHARACTER A TAB OR CR
1167 004406 001003          BNE      1$          ; IF CR THEN GO PUT CR/LF IN BUFFER
1168 004410 112724 000011          MOVB     #HT,          (R4)+    ; IF TAB THEN PUT TAB IN BUFFER
1169 004414 000404          BR       2$          ; GET OVER NEXT LINE
1170 004416 112724 000015          1$:      MOVB     #CR,          (R4)+    ; MOV CR/LF TO BUFFER
1171 004422 112724 000012          MOVB     #LF,          (R4)+
1172 004426 112524          2$:      MOVB     (R5)+,        (R4)+    ; PUT A CHARACTER IN MESSAGE BUFFER
1173 004430 105715          TSTB     (R5)          ; IS NEXT CHARACTER ZERO
1174 004432 001375          BNE      2$          ; IF NOT PUT IT IN MESSAGE BUFFER AND GET
1175 004434 062716 000002          ADD      #2,          (R6)    ; UPDATE RETURN POINTER TO GET OVER CHARA
1176 004440 000207          RTS      PC          ; THEN RETURN
1177
1178
1179
1180
1181          ;OCTAL TO ASCII CONVERSION ROUTINE
1182          ;THIS SUBROUTINE TAKES A SIXTEEN BIT OCTAL NUMBER AND
1183          ;CONVERTS IT TO 6 ASCII CHARACTERS
1184
1185 004442 012700 007504          OCASC:  MOV      #OCTBUF,    R0          ; GET BUFFER ADDRESS
1186 004446 005020          CLR      (R0)+        ; CLEAR BUFFER
1187 004450 005020          CLR      (R0)+
1188 004452 005020          CLR      (R0)+
1189 004454 005020          CLR      (R0)+
1190 004456 012700 007504          MOV      #OCTBUF,    R0          ; GET BUFFER ADDRESS
1191 004462 010337 004544          MOV      R3,          Q#TEMP    ; GET OCTAL NUMBER
1192 004466 000241          CLC
1193 004470 006137 004544          ROL      Q#TEMP    ; ROTATE BIT INTO CARRY BIT

```

K02

CZM9BA0 M9312 BOOT TERM 8K  
CZM9BA.P11 13-MAR-78 08:58MACY11 30A(1052) 13-MAR-78 08:59 PAGE 23  
END OF PASS ROUTINE

SEQ 0023

```

1194 004474 006110
1195 004476 152710 000060
1196 004502 005200
1197 004504 020027 007512
1198 004510 001003
1199 004512 012705 007504
1200 004516 000207
1201 004520 006137 004544
1202 004524 106110
1203 004526 006137 004544
1204 004532 106110
1205 004534 006137 004544
1206 004540 106110
1207 004542 000755
1208 004544 000000
1209
1210
1211
1212
1213
1214
1215 004546 010346
1216 004550 011303
1217 004552 004767 177664
1218 004556 012603
1219 004560 000207
1220
1221
1222
1223
1224
1225
1226
1227
1228
1229
1230
1231
1232
1233
1234
1235
1236
1237
1238
1239
1240
1241
1242 004562 105767 000265
1243 004566 100002
1244 004570 000000
1245 004572 000430
1246 004574 010046
1247 004576 017600 000002
1248 004602 122767 000001 174346
1249 004610 001011

```

```

1$: ROL (R0)
    BISB #60, (R0)
    INC R0
    CMP R0, #OCTBUF+6
    BNE 2$
    MOV #OCTBUF, R5
    RTS PC
2$: ROL 2$TEMP
    ROLB (R0)
    ROL 2$TEMP
    ROLB (R0)
    ROL 2$TEMP
    ROLB (R0)
    BR 1$
TEMP: 0

```

```

; ROTATE CARRY BIT INTO BUFFER
; MAKE IT ASCII
; UPDATE BUFFER ADDRESS
; HAVE WE CONVERTED ALL THE NUMBER
; IF NO CONTINUE
; IF YES PUT BUFFER ADDRESS IN REGISTER
; RETURN
; ROTATE BIT INTO CARRY BIT
; ROTATE CARRY BIT INTO BUFFER
;
; GO TO START OF LOOP

```

```

; THIS ROUTINE IS CALLED BY THE PUT MESSAGE ROUTINE TO GET THE RIGHT
; VALUE IN R3 SO THE OCTAL TO ASCII ROUTINE GETS THE RIGHT NUMBER.

```

```

OCADD: MOV R3, -(R6) ; SAVE THE VALUE OF R3
      MOV (R3), R3 ; PUT THE DATA TO BE CONVERTED IN R3
      JSR PC, OCASC ; GO CONVERT OCTAL TO ASCII
      MOV (R6)+, R3 ; RESTORE R3
      RTS PC ; RETURN

```

## .SBTTL TYPE ROUTINE

```

; *****
; ROUTINE TO TYPE ASCII MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
; THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
; NOTE1: $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
; NOTE2: $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
; NOTE3: $FILLC CONTAINS THE CHARACTER TO FILL AFTER.
;
; CALL:
; 1) USING A TRAP INSTRUCTION
; TYPE ,MESADR ; MESADR IS FIRST ADDRESS OF AN ASCII STRING
; OR
; TYPE
; MESADR
;

```

```

$TYPE: TSTB $TPFLG ; IS THERE A TERMINAL?
      BPL 1$ ; BR IF YES
      HALT ; HALT HERE IF NO TERMINAL
      BR 3$ ; LEAVE
1$: MOV R0, -(SP) ; SAVE R0
    MOV 22(SP), R0 ; GET ADDRESS OF ASCII STRING
    CMPB #APTENV, $ENV ; RUNNING IN APT MODE
    BNE 62$ ; NO, GO CHECK FOR APT CONSOLE

```



```

1250 004612 132767 000100 174337      BITB      #APTSP00L,$ENVN      ;; SPOOL MESSAGE TO APT
1251 004620 001405                      BEQ        62$              ;; NO GO CHECK FOR CONSOLE
1252 004622 010067 000004                      MOV      RO,61$          ;; SETUP MESSAGE ADDRESS FOR APT
1253 004626 004767 000234                      JSR      PC,$ATY3          ;; SPOOL MESSAGE TO APT
1254 004632 000000                      .WORD      0              ;; MESSAGE ADDRESS
1255 004634 132767 000040 174315      61$:      BITB      #APTCSUP,$ENVN      ;; APT CONSOLE SUPPRESSED
1256 004642 001003                      BNE      60$              ;; YES, SKIP TYPE OUT
1257 004644 112046                      2$:      MOV      (RO)+,-(SP)      ;; PUSH CHARACTER TO BE TYPED ONTO STACK
1258 004646 001005                      BNE      4$              ;; BR IF IT ISN'T THE TERMINATOR
1259 004650 005726                      TST      (SP)+            ;; IF TERMINATOR POP IT OFF THE STACK
1260 004652 012600                      60$:      MOV      (SP)+,RO      ;; RESTORE RO
1261 004654 062716 000002                      3$:      ADD      #2,(SP)        ;; ADJUST RETURN PC
1262 004660 000002                      RTI                      ;; RETURN
1263 004662 122716 000011                      4$:      CMP      #HT,(SP)        ;; BRANCH IF <HT>
1264 004666 001430                      BEQ      8$              ;;
1265 004670 122716 000200                      CMP      #CRLF,(SP)        ;; BRANCH IF NOT <CRLF>
1266 004674 001006                      BNE      5$              ;;
1267 004676 005726                      TST      (SP)+            ;; POP <CR><LF> EQUIV
1268 004700 104401                      TYPE                      ;; TYPE A CR AND LF
1269 004702 005055                      $CRLF                    ;;
1270 004704 105067 000130                      CLRB     $CHARCNT        ;; CLEAR CHARACTER COUNT
1271 004710 000755                      BR        2$              ;; GET NEXT CHARACTER
1272 004712 004767 000056                      5$:      JSR      PC,$TYPEC      ;; GO TYPE THIS CHARACTER
1273 004716 126726 000130                      6$:      CMP      $FILLC,(SP)+  ;; IS IT TIME FOR FILLER CHARS.?
1274 004722 001350                      BNE      2$              ;; IF NO GO GET NEXT CHAR.
1275 004724 016746 000120                      MOV      $NULL,-(SP)      ;; GET # OF FILLER CHARS. NEEDED
1276                                AND THE NULL CHAR.
1277 004730 105366 000001                      7$:      DECB     1(SP)        ;; DOES A NULL NEED TO BE TYPED?
1278 004734 002770                      BLT      6$              ;; BR IF NO--GO POP THE NULL OFF OF STACK
1279 004736 004767 000032                      JSR      PC,$TYPEC      ;; GO TYPE A NULL
1280 004742 105367 000072                      DECB     $CHARCNT        ;; DO NOT COUNT AS A COUNT
1281 004746 000770                      BR        7$              ;; LOOP
1282
1283                                ;HORIZONTAL TAB PROCESSOR
1284
1285 004750 112716 000040                      8$:      MOV      #' (SP)      ;; REPLACE TAB WITH SPACE
1286 004754 004767 000014                      9$:      JSR      PC,$TYPEC      ;; TYPE A SPACE
1287 004760 132767 000007 000052      BITB     #7,$CHARCNT      ;; BRANCH IF NOT AT
1288 004766 001372                      BNE      9$              ;; TAB STOP
1289 004770 005726                      TST      (SP)+            ;; POP SPACE OFF STACK
1290 004772 000724                      BR        2$              ;; GET NEXT CHARACTER
1291 004774 105777 000044                      $TYPEC:  TST      $STPS      ;; WAIT UNTIL PRINTER IS READY
1292 005000 100375                      BPL      $TYPEC          ;;
1293 005002 116677 000002 000036      MOV      2(SP),$STPB      ;; LOAD CHAR TO BE TYPED INTO DATA REG.
1294 005010 122766 000015 000002      CMP      #CR,2(SP)        ;; IS CHARACTER A CARRIAGE RETURN?
1295 005016 001003                      BNE      1$              ;; BRANCH IF NO
1296 005020 105067 000014                      CLRB     $CHARCNT        ;; YES--CLEAR CHARACTER COUNT
1297 005024 000406                      BR        $TYPEX          ;; EXIT
1298 005026 122766 000012 000002      1$:      CMP      #LF,2(SP)    ;; IS CHARACTER A LINE FEED?
1299 005034 001402                      BEQ      $TYPEX          ;; BRANCH IF YES
1300 005036 105227                      INCB     (PC)+            ;; COUNT THE CHARACTER
1301 005040 000000                      $CHARCNT: .WORD      0    ;; CHARACTER COUNT STORAGE
1302 005042 000207                      $TYPEX:  RTS      PC
1303
1304 005044 177564                      $TPS:     .WORD      177564  ;; TTY PRINTER STATUS REG. ADDRESS
1305 005046 177566                      $TPB:     .WORD      177566  ;; TTY PRINTER BUFFER REG. ADDRESS

```

CZM98A0 M9312 BOOT TERM R BK  
CZM98A.P11 13-MAR-78 08:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 25  
TYPE ROUTINE

SEQ 0025

1306	005050	000			\$NULL:	.BYTE	0		::CONTAINS NULL CHARACTER FOR FILLS
1307	005051	002			\$FILLS:	.BYTE	2		::CONTAINS # OF FILLER CHARACTERS REQUIRED
1308	005052	012			\$FILLC:	.BYTE	12		::INSERT FILL CHARS. AFTER A "LINE FEED"
1309	005053	000			\$TPFLG:	.BYTE	0		::"TERMINAL AVAILABLE" FLAG (BIT'07'=0=YES)
1310	005054	077			\$QUES:	.ASCII	"?"		::QUESTION MARK
1311	005055	015			\$CRLF:	.ASCII	<15>		::CARRIAGE RETURN
1312	005056	000012			\$LF:	.ASCII	<12>		::LINEFEED
1313					.SBTTL	APT COMMUNICATIONS ROUTINE			
1314					*****				
1315					*****				
1316	005060	112767	000001	000236	\$ATY1:	MOVB	#1,\$FFLG		::TO REPORT FATAL ERROR
1317	005066	112767	000001	000226	\$ATY3:	MOVB	#1,\$MFLG		::TO TYPE A MESSAGE
1318	005074	000403				BR	\$ATYC		
1319	005076	112767	000001	000220	\$ATY4:	MOVB	#1,\$FFLG		::TO ONLY REPORT FATAL ERROR
1320	005104				\$ATYC:				
1321	005104	010046				MOV	RO,-(SP)		::PUSH RO ON STACK
1322	005106	010146				MOV	R1,-(SP)		::PUSH R1 ON STACK
1323	005110	105767	000206			TSTB	\$MFLG		::SHOULD TYPE A MESSAGE?
1324	005114	001450				BEQ	\$S		::IF NOT: BR
1325	005116	122767	000001	174032		CMPB	\$APTENV,\$ENV		::OPERATING UNDER APT?
1326	005124	001031				BNE	\$S		::IF NOT: BR
1327	005126	132767	000100	174023		BITB	\$APTPOOL,\$ENV		::SHOULD SPOOL MESSAGES?
1328	005134	001425				BEQ	\$S		::IF NOT: BR
1329	005136	017600	000004			MOV	\$4(SP),RO		::GET MESSAGE ADDR.
1330	005142	062766	000002	000004		ADD	#2,4(SP)		::BUMP RETURN ADDR.
1331	005150	005767	173762		1\$:	TST	\$MSGTYPE		::SEE IF DONE W/ LAST XMISSION?
1332	005154	001375				BNE	\$S		::IF NOT: WAIT
1333	005156	010067	173770			MOV	RO,\$MSGAD		::PUT ADDR IN MAILBOX
1334	005162	105720			2\$:	TSTB	(RO)+		::FIND END OF MESSAGE
1335	005164	001376				BNE	\$S		
1336	005166	166700	173760			SUB	\$MSGAD,RO		::SUB START OF MESSAGE
1337	005172	006200				ASR	RO		::GET MESSAGE LGTH IN WORDS
1338	005174	010067	173754			MOV	RO,\$MSGLG		::PUT LENGTH IN MAILBOX
1339	005200	012767	000004	173730		MOV	#4,\$MSGTYPE		::TELL APT TO TAKE MSG.
1340	005206	000413				BR	\$S		
1341	005210	017667	000004	000016	3\$:	MOV	\$4(SP),4\$		::PUT MSG ADDR IN JSR LINKAGE
1342	005216	062766	000002	000004		ADD	#2,4(SP)		::BUMP RETURN ADDRESS
1343	005224	016746	172546			MOV	177776,-(SP)		::PUSH 177776 ON STACK
1344	005230	004767	177326			JSR	PC,\$TYPE		::CALL TYPE MACRO
1345	005234	000000			4\$:	.WORD	0		
1346	005236				5\$:				
1347	005236	105767	000062		10\$:	TSTB	\$FFLG		::SHOULD REPORT FATAL ERROR?
1348	005242	001416				BEQ	\$S		::IF NOT: BR
1349	005244	005767	173706			TST	\$ENV		::RUNNING UNDER APT?
1350	005250	001413				BEQ	\$S		::IF NOT: BR
1351	005252	005767	173660		11\$:	TST	\$MSGTYPE		::FINISHED LAST MESSAGE?
1352	005256	001375				BNE	\$S		::IF NOT: WAIT
1353	005260	017667	000004	173652		MOV	\$4(SP),\$FATAL		::GET ERROR #
1354	005266	062766	000002	000004		ADD	#2,4(SP)		::BUMP RETURN ADDR.
1355	005274	005267	173636			INC	\$MSGTYPE		::TELL APT TO TAKE ERROR
1356	005300	105067	000020		12\$:	CLRB	\$FFLG		::CLEAR FATAL FLAG
1357	005304	105067	000013			CLRB	\$LFLG		::CLEAR LOG FLAG
1358	005310	105067	000006			CLRB	\$MFLG		::CLEAR MESSAGE FLAG
1359	005314	012601				MOV	(SP)+,R1		::POP STACK INTO R1
1360	005316	012600				MOV	(SP)+,RO		::POP STACK INTO RO
1361	005320	000207				RTS	PC		::RETURN

```

1362 005322 000 SMFLG: .BYTE 0 ;; MESSG. FLAG
1363 005323 000 SLFLG: .BYTE 0 ;; LOG FLAG
1364 005324 000 SFFLG: .BYTE 0 ;; FATAL FLAG
1365 005326 .EVEN
1366 000200 APTSIZE=200
1367 000001 APTENV=001
1368 000100 APTSPOOL=100
1369 000040 APTCSUP=040
1370 .SBTTL TTY INPUT ROUTINE
1371
1372 *****
1373 005326 177560 $TKS: .WORD 177560 ;; TTY KBD STATUS
1374 005330 177562 $TKB: .WORD 177562 ;; TTY KBD BUFFER
1375 .ENABL LSB
1376
1377 *****
1378 *SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
1379 *ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
1380 *SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
1381 *WHEN OPERATING IN TTY FLAG MODE.
1382 005332 022767 000176 173544 $CKSWR: CMP $SWREG,SWR ;; IS THE SOFT-SWR SELECTED?
1383 005340 0C1074 BNE 15$ ;; BRANCH IF NO
1384 005342 105777 177760 TSTB $TKS ;; CHAR THERE?
1385 005346 100071 BPL 15$ ;; IF NO, DON'T WAIT AROUND
1386 005350 117746 177754 MOVB $TKB,-(SP) ;; SAVE THE CHAR
1387 005354 042716 177600 BIC #1C177,(SP) ;; STRIP-OFF THE ASCII
1388 005360 022726 000007 CMP #7,(SP)+ ;; IS IT A CONTROL G?
1389 005364 001062 BNE 15$ ;; NO, RETURN TO USER
1390 005366 126727 173506 000001 CMPB $AUTOB,#1 ;; ARE WE RUNNING IN AUTO-MODE?
1391 005374 001456 BEQ 15$ ;; BRANCH IF YES
1392
1393 005376 104401 006057 $GTSWR: TYPE , $CNTLG ;; ECHO THE CONTROL-G (↑G)
1394 005402 104401 006064 TYPE , $MSWR ;; TYPE CURRENT CONTENTS
1395 005406 016746 172564 MOV $WREG,-(SP) ;; SAVE SWREG FOR TYPEOUT
1396 005412 104402 TYPEOC ;; GO TYPE--OCTAL ASCII(ALL DIGITS)
1397 005414 104401 006075 TYPE , $MNEW ;; PROMPT FOR NEW SWR
1398 005420 005046 19$: CLR -(SP) ;; CLEAR COUNTER
1399 005422 005046 7$: CLR -(SP) ;; THE NEW SWR
1400 005424 105777 177676 TSTB $TKS ;; CHAR THERE?
1401 005430 100375 BPL 7$ ;; IF NOT TRY AGAIN
1402
1403 005432 117746 177672 MOVB $TKB,-(SP) ;; PICK UP CHAR
1404 005436 042716 177600 BIC #1C177,(SP) ;; MAKE IT 7-BIT ASCII
1405
1406
1407
1408 005442 021627 000025 9$: CMP (SP),#25 ;; IS IT A CONTROL-U?
1409 005446 001005 BNE 10$ ;; BRANCH IF NOT
1410 005450 104401 006052 TYPE , $CNTLU ;; YES, ECHO CONTROL-U (↑U)
1411 005454 062706 000006 20$: ADD #6,SP ;; IGNORE PREVIOUS INPUT
1412 005460 000757 BR 19$ ;; LET'S TRY IT AGAIN
1413
1414
1415 005462 021627 000015 10$: CMP (SP),#15 ;; IS IT A <CR>?
1416 005466 0C1022 BNE 16$ ;; BRANCH IF NO
1417 005470 005766 000004 TST 4(SP) ;; YES, IS IT THE FIRST CHAR?

```

```

1418 005474 001403 BEQ 11$ :: BRANCH IF YES
1419 005476 016677 000002 173400 MOV 2(SP), 2SWR :: SAVE NEW SWR
1420 005504 062706 000006 11$: ADD 26, SP :: CLEAR UP STACK
1421 005510 104401 005055 14$: TYPE $CRLF :: ECHO <CR> AND <LF>
1422 005514 126727 173361 000001 CMPB $INTAG, #1 :: RE-ENABLE TTY KBD INTERRUPTS?
1423 005522 001003 BNE 15$ :: BRANCH IF NOT
1424 005524 012777 000100 177574 MOV #100, 2STKS :: RE-ENABLE TTY KBD INTERRUPTS
1425 005532 000002 15$: RTI :: RETURN
1426 005534 004767 177234 16$: JSR PC, $TYPEC :: ECHO CHAR
1427 005540 021627 000060 CMP (SP), #60 :: CHAR < 0?
1428 005544 002420 BLT 18$ :: BRANCH IF YES
1429 005546 021627 000067 CMP (SP), #67 :: CHAR > 7?
1430 005552 003015 BGT 18$ :: BRANCH IF YES
1431 005554 042726 000060 BIC #60, (SP)+ :: STRIP-OFF ASCII
1432 005560 005766 000002 TST 2(SP) :: IS THIS THE FIRST CHAR
1433 005564 001403 BEQ 17$ :: BRANCH IF YES
1434 005566 006316 ASL (SP) :: NO, SHIFT PRESENT
1435 005570 006316 ASL (SP) :: CHAR OVER TO MAKE
1436 005572 006316 ASL (SP) :: ROOM FOR NEW ONE.
1437 005574 005266 000002 17$: INC 2(SP) :: KEEP COUNT OF CHAR
1438 005600 056616 177776 BIS -2(SP), (SP) :: SET IN NEW CHAR
1439 005604 000707 BR 7$ :: GET THE NEXT ONE
1440 005606 104401 005054 18$: TYPE $QUES :: TYPE ?<CR><LF>
1441 005612 000720 BR 20$ :: SIMULATE CONTROL-U
1442 .DSABL LSB
1443
1444
1445 *****
1446 *THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
1447 *CALL:
1448 * RDCHR :: INPUT A SINGLE CHARACTER FROM THE TTY
1449 * RETURN HERE :: CHARACTER IS ON THE STACK
1450 * :: WITH PARITY BIT STRIPPED OFF
1451
1452
1453 005614 011646 $RDCHR: MOV (SP), -(SP) :: PUSH DOWN THE PC
1454 005616 016666 000004 000002 MOV 4(SP), 2(SP) :: SAVE THE PS
1455 005624 105777 177476 1$: TSTB 2STKS :: WAIT FOR
1456 005630 100375 BPL 1$ :: A CHARACTER
1457 005632 117766 177472 000004 MOVB 2STKB, 4(SP) :: READ THE TTY
1458 005640 042766 177600 000004 BIC #1C(177), 4(SP) :: GET RID OF JUNK IF ANY
1459 005646 026627 000004 000023 CMP 4(SP), #23 :: IS IT A CONTROL-S?
1460 005654 001013 BNE 3$ :: BRANCH IF NO
1461 005656 105777 177444 2$: TSTB 2STKS :: WAIT FOR A CHARACTER
1462 005662 100375 BPL 2$ :: LOOP UNTIL ITS THERE
1463 005664 117746 177440 MOVB 2STKB, -(SP) :: GET CHARACTER
1464 005670 042716 177600 BIC #1C177, (SP) :: MAKE IT 7-BIT ASCII
1465 005674 022627 000021 CMP (SP)+, #21 :: IS IT A CONTROL-Q?
1466 005700 001366 BNE 2$ :: IF NOT DISCARD IT
1467 005702 000750 BR 1$ :: YES, RESUME
1468 005704 026627 000004 000140 3$: CMP 4(SP), #140 :: IS IT UPPER CASE?
1469 005712 002407 BLT 4$ :: BRANCH IF YES
1470 005714 026627 000004 000175 CMP 4(SP), #175 :: IS IT A SPECIAL CHAR?
1471 005722 003003 BGT 4$ :: BRANCH IF YES
1472 005724 042766 BIC #40, 4(SP) :: MAKE IT UPPER CASE
1473 005732 000002 4$: RTI :: GO BACK TO USER

```

```

1474      ;*****
1475      ;THIS ROUTINE WILL INPUT A STRING FROM THE TTY
1476      ;CALL:
1477      ;      RDLIN          ;: INPUT A STRING FROM THE TTY
1478      ;      RETURN HERE   ;: ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
1479      ;                    ;: TERMINATOR WILL BE A BYTE OF ALL 0'S
1480
1481 005734 010346 $RDLIN: MOV R3, -(SP) ;: SAVE R3
1482 005736 012703 1$: MOV #STTYIN, R3 ;: GET ADDRESS
1483 005742 022703 2$: CMP #STTYIN+8., R3 ;: BUFFER FULL?
1484 005746 101405 BLOS 4$ ;: BR IF YES
1485 005750 104407 RDOCHR ;: GO READ ONE CHARACTER FROM THE TTY
1486 005752 112613 MOV (SP)+, (R3) ;: GET CHARACTER
1487 005754 122713 10$: CMPB #177, (R3) ;: IS IT A RUBOUT
1488 005760 001003 BNE 3$ ;: SKIP IF NOT
1489 005762 104401 4$: TYPE $QUES ;: TYPE A '?'
1490 005766 000763 BR 1$ ;: CLEAR THE BUFFER AND LOOP
1491 005770 111367 3$: MOV (R3), 9$ ;: ECHO THE CHARACTER
1492 005774 104401 TYPE 9$
1493 006000 122723 CMPB #15, (R3)+ ;: CHECK FOR RETURN
1494 006004 001356 BNE 2$ ;: LOOP IF NOT RETURN
1495 006006 105063 CLR B -1(R3) ;: CLEAR RETURN (THE 15)
1496 006012 104401 TYPE $LF ;: TYPE A LINE FEED
1497 006016 012603 MOV (SP)+, R3 ;: RESTORE R3
1498 006020 011646 MOV (SP), -(SP) ;: ADJUST THE STACK AND PUT ADDRESS OF THE
1499 006022 016666 000004 000002 MOV 4(SP), 2(SP) ;: FIRST ASCII CHARACTER ON IT
1500 006030 012766 006042 000004 MOV #STTYIN, 4(SP)
1501 006036 000002 RTI ;: RETURN
1502 006040 C00 9$: .BYTE 0 ;: STORAGE FOR ASCII CHAR. TO TYPE
1503 006041 000 .BYTE 0 ;: TERMINATOR
1504 006042 000010 $TTYIN: .BLKB 8 ;: RESERVE 8 BYTES FOR TTY INPUT
1505 006052 052536 005015 000 $CNTLU: .ASCIZ /U<15><12> ;: CONTROL "U"
1506 006057 136 006507 000012 $CNTLG: .ASCIZ /G<15><12> ;: CONTROL "G"
1507 006064 005015 053523 020122 $MSWR: .ASCIZ <15><12>/SWR = /
1508 006072 020075 000
1509 006075 040 047040 053505 $MNEW: .ASCIZ / NEW = /
1510 006102 036440 000040
1511      .SBTTL BINARY TO OCTAL (ASCII) AND TYPE
1512
1513      ;*****
1514      ;THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
1515      ;OCTAL (ASCII) NUMBER AND TYPE IT.
1516      ;$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
1517      ;CALL:
1518      ;      MOV NUM, -(SP) ;: NUMBER TO BE TYPED
1519      ;      TYPOS ;: CALL FOR TYPEOUT
1520      ;      .BYTE N ;: N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
1521      ;      .BYTE M ;: M=1 OR 0
1522      ;      ;: 1=TYPE LEADING ZEROS
1523      ;      ;: 0=SUPPRESS LEADING ZEROS
1524      ;
1525      ;$STYPON---ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
1526      ;$TYPOS OR $TYPOC
1527      ;CALL:
1528      ;      MOV NUM, -(SP) ;: NUMBER TO BE TYPED
1529      ;      TYPON ;: CALL FOR TYPEOUT

```

```

1530      ;*STYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
1531      ;*CALL:
1532      ;*      MOV      NUM,-(SP)      ;:NUMBER TO BE TYPED
1533      ;*      STYPOC      ;:CALL FOR TYPEOUT
1534
1535
1536 006106 017646 000000      STYPOC: MOV      2(SP),-(SP)      ;:PICKUP THE MODE
1537 006112 116667 000001 000211      MOV      1(SP),SOFILL      ;:LOAD ZERO FILL SWITCH
1538 006120 112667 000207      MOV      (SP)+,SOMODE+1      ;:NUMBER OF DIGITS TO TYPE
1539 006124 062716 000002      ADD      #2,(SP)      ;:ADJUST RETURN ADDRESS
1540 006130 000406      BR      STYPON
1541 006132 112767 000001 000171      STYPOC: MOV      #1,SOFILL      ;:SET THE ZERO FILL SWITCH
1542 006140 112767 000006 000165      MOV      #6,SOMODE+1      ;:SET FOR SIX(6) DIGITS
1543 006146 112767 000005 000154      STYPON: MOV      #5,SOCNT      ;:SET THE ITERATION COUNT
1544 006154 010346      MOV      R3,-(SP)      ;:SAVE R3
1545 006156 010446      MOV      R4,-(SP)      ;:SAVE R4
1546 006160 010546      MOV      R5,-(SP)      ;:SAVE R5
1547 006162 116704 000145      MOV      SOMODE+1,R4      ;:GET THE NUMBER OF DIGITS TO TYPE
1548 006166 005404      NEG      R4
1549 006170 062704 000006      ADD      #6,R4      ;:SUBTRACT IT FOR MAX. ALLOWED
1550 006174 110467 000132      MOV      R4,SOMODE      ;:SAVE IT FOR USE
1551 006200 116704 000125      MOV      SOFILL,R4      ;:GET THE ZERO FILL SWITCH
1552 006204 016605 000012      MOV      12(SP),R5      ;:PICKUP THE INPUT NUMBER
1553 006210 005003      CLR      R3      ;:CLEAR THE OUTPUT WORD
1554 006212 006105      1$: ROL      R5      ;:ROTATE MSB INTO "C"
1555 006214 000404      BR      3$      ;:GO DO MSB
1556 006216 006105      2$: ROL      R5      ;:FORM THIS DIGIT
1557 006220 006105      ROL      R5
1558 006222 006105      ROL      R5
1559 006224 010503      MOV      R5,R3
1560 006226 006103      3$: ROL      R3      ;:GET LSB OF THIS DIGIT
1561 006230 105367 000076      DEC      SOMODE      ;:TYPE THIS DIGIT?
1562 006234 100016      BPL      7$      ;:BR IF NO
1563 006236 042703 177770      BIC      #177770,R3      ;:GET RID OF JUNK
1564 006242 001002      BNE      4$      ;:TEST FOR 0
1565 006244 005704      TST      R4      ;:SUPPRESS THIS 0?
1566 006246 001403      BEQ      5$      ;:BR IF YES
1567 006250 005204      4$: INC      R4      ;:DON'T SUPPRESS ANYMORE 0'S
1568 006252 052703 000060      BIS      #'0,R3      ;:MAKE THIS DIGIT ASCII
1569 006256 052703 000040      5$: BIS      #' ,R3      ;:MAKE ASCII IF NOT ALREADY
1570 006262 110367 000040      MOV      R3,#$      ;:SAVE FOR TYPING
1571 006266 104401 006326      TYPE      8$      ;:GO TYPE THIS DIGIT
1572 006272 105367 000032      7$: DEC      SOCNT      ;:COUNT BY 1
1573 006276 003347      BGT      2$      ;:BR IF MORE TO DO
1574 006300 002402      BLT      6$      ;:BR IF DONE
1575 006302 005204      INC      R4      ;:INSURE LAST DIGIT ISN'T A BLANK
1576 006304 000744      BR      2$      ;:GO DO THE LAST DIGIT
1577 006306 012605      6$: MOV      (SP)+,R5      ;:RESTORE R5
1578 006310 012604      MOV      (SP)+,R4      ;:RESTORE R4
1579 006312 012603      MOV      (SP)+,R3      ;:RESTORE R3
1580 006314 016666 000002 000004      MOV      2(SP),4(SP)      ;:SET THE STACK FOR RETURNING
1581 006322 012616      MOV      (SP)+,(SP)
1582 006324 000002      RTI      ;:RETURN
1583 006326      8$: .BYTE      0      ;:STORAGE FOR ASCII DIGIT
1584 006327      .BYTE      0      ;:TERMINATOR FOR TYPE ROUTINE
1585 006330      SOCNT: .BYTE      0      ;:OCTAL DIGIT COUNTER

```

```

1586 006331 000
1587 006332 000000
1588
1589
1590
1591
1592
1593
1594
1595
1596
1597
1598
1599
1600
1601
1602
1603
1604
1605 006334
1606 006334 010046
1607 006336 010146
1608 006340 010246
1609 006342 010346
1610 006344 010446
1611 006346 010546
1612 006350 016646 000022
1613 006354 016646 000022
1614 006360 016646 000022
1615 006364 016646 000022
1616 006370 000002
1617
1618
1619
1620
1621 006372
1622 006372 012666 000022
1623 006376 012666 000022
1624 006402 012666 000022
1625 006406 012666 000022
1626 006412 012605
1627 006414 012604
1628 006416 012603
1629 006420 012602
1630 006422 012601
1631 006424 012600
1632 006426 000002
1633
1634
1635
1636
1637
1638
1639
1640
1641 006430 010046

```

```

$OFILL: .BYTE 0          ;;ZERO FILL SWITCH
$OMODE: .WORD 0          ;;NUMBER OF DIGITS TO TYPE
.SBTTL  SAVE AND RESTORE RO-R5 ROUTINES

```

```

*****
; *SAVE RO-R5
; *CALL:
; *   SAVREG
; *UPON RETURN FROM $SAVREG THE STACK WILL LOOK LIKE.
; *
; *TOP---(+16)
; * +2---(+18)
; * +4---R5
; * +6---R4
; * +8---R3
; *+10---R2
; *+12---R1
; *+14---R0

```

```

$SAVREG:
        MOV     R0,-(SP)      ;;PUSH R0 ON STACK
        MOV     R1,-(SP)      ;;PUSH R1 ON STACK
        MOV     R2,-(SP)      ;;PUSH R2 ON STACK
        MOV     R3,-(SP)      ;;PUSH R3 ON STACK
        MOV     R4,-(SP)      ;;PUSH R4 ON STACK
        MOV     R5,-(SP)      ;;PUSH R5 ON STACK
        MOV     22(SP),-(SP)   ;;SAVE PS OF MAIN FLOW
        MOV     22(SP),-(SP)   ;;SAVE PC OF MAIN FLOW
        MOV     22(SP),-(SP)   ;;SAVE PS OF CALL
        MOV     22(SP),-(SP)   ;;SAVE PC OF CALL
        RTI

```

```

; *RESTORE RO-R5
; *CALL:
; *   RESREG

```

```

$RESREG:
        MOV     (SP)+,22(SP)   ;;RESTORE PC OF CALL
        MOV     (SP)+,22(SP)   ;;RESTORE PS OF CALL
        MOV     (SP)+,22(SP)   ;;RESTORE PC OF MAIN FLOW
        MOV     (SP)+,22(SP)   ;;RESTORE PS OF MAIN FLOW
        MOV     (SP)+,R5       ;;POP STACK INTO R5
        MOV     (SP)+,R4       ;;POP STACK INTO R4
        MOV     (SP)+,R3       ;;POP STACK INTO R3
        MOV     (SP)+,R2       ;;POP STACK INTO R2
        MOV     (SP)+,R1       ;;POP STACK INTO R1
        MOV     (SP)+,R0       ;;POP STACK INTO R0
        RTI

```

```

.SBTTL  TRAP DECODER

```

```

*****
; *THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
; *AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
; *OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
; *GO TO THAT ROUTINE.

```

```

$TRAP:  MOV     R0,-(SP)      ;;SAVE R0

```



```

1642 006432 016600 000002      MOV      2(SP),RO      ;;GET TRAP ADDRESS
1643 006436 005740             TST      -(RO)          ;;BACKUP BY 2
1644 006440 111000             MOV      (RO),RO        ;;GET RIGHT BYTE OF TRAP
1645 006442 006300             ASL      RO              ;;POSITION FOR INDEXING
1646 006444 016000 006464      MOV      $TRPAD(RO),RO    ;;INDEX TO TABLE
1647 006450 000200             RTS      RO              ;;GO TO ROUTINE
1648
1649
1650                               ;;THIS IS USE TO HANDLE THE "GETPRI" MACRO
1651
1652 006452 011646 000004 000002 $TRAP2: MOV      (SP),-(SP)    ;;MOVE THE PC DOWN
1653 006454 016666             MOV      4(SP),2(SP)      ;;MOVE THE PSW DOWN
1654 006462 000002             RTI                      ;;RESTORE THE PSW
1655
1656                               .SBTTL  TRAP TABLE
1657
1658                               ;*THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
1659                               ;*BY THE "TRAP" INSTRUCTION.
1660
1661                               ; ROUTINE
1662                               ;-----
1663 006464 006452 $TRPAD: .WORD  $TRAP2
1664 006466 004562      $TYPE      ;;CALL=TYPE      TRAP+1(104401) TTY TYPEOUT ROUTINE
1665 006470 006132      $TYPOC     ;;CALL=TYPOC     TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
1666 006472 006106      $TYPOS     ;;CALL=TYPOS     TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
1667 006474 006146      $TYPON     ;;CALL=TYPON     TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
1668
1669 006476 005402      $GTSWR      ;;CALL=GTSWR     TRAP+5(104405) GET SOFT-SWR SETTING
1670
1671 006500 005332      $CKSWR      ;;CALL=CKSWR     TRAP+6(104406) TEST FOR CHANGE IN SOFT-SWP
1672 006502 005614      $RDCHR      ;;CALL=RDCHR     TRAP+7(104407) TTY TYPEIN CHARACTER ROUTINE
1673 006504 005734      $RDLIN      ;;CALL=RDLIN     TRAP+10(104410) TTY TYPEIN STRING ROUTINE
1674 006506 006334      $SAVREG     ;;CALL=SAVREG    TRAP+11(104411) SAVE RO-R5 ROUTINE
1675 006510 006372      $RESREG     ;;CALL=RESREG    TRAP+12(104412) RESTORE RO-R5 ROUTINE
1676
1677
1678                               ;MESSAGES
1679
1680
1681 006512 030062      ROMNUM: .ASCII /20/
1682 006514 032463      .ASCII /35/
1683 006516 031463      .ASCII /33/
1684 006520 032063      .ASCII /34/
1685 006522 031063      .ASCII /32/
1686 006524 177607 000377      BELL: .ASCIIZ <20><377><377>
1687
1688
1689 006530 044504 043501 020056      DRHEAD: .ASCIIZ /DIAG. ROM /
1690 006536 047522 020115 000          BRHEAD: .ASCII /BOOTSTRAP ROM ENTRY POINTS AND DEVICE CODES 15 12
1691 006543 102 047517 051524
1692 006550 051124 050101 051040
1693 006556 046517 042440 052116
1694 006564 054522 050040 044517
1695 006572 052116 020123 047101
1696 006600 020104 042504 044526
1697 006606 042503 041440 042117

```

CZM9BA0 M9312 BOOT TERM 8A MACY11 30A(1052) 13-MAR-78 09:59 PAGE 32  
CZM9BA.P11 13-MAR-78 08:58 TRAP TABLE

CZM98AO M9312 BOOT TERM 8A  
CZM98A.P11 13-MAR-78 08:58

1698	006614	051505	005015				
1699	006620	047011	020117	044504	.ASCIZ	NO DIAG.	RUN DIAG.
1700	006626	043501	004456	052522			DEVICE CODE
1701	006634	020116	044504	043501			
1702	006642	004456	042504	044526			
1703	006650	042503	041440	042117			
1704	006656	000105					
1705	006660	051520	052505	047504	PFHEAD: .ASCIZ	/PSEUDO POWER-FAIL VECTOR ADDRESS	/
1706	006666	050040	053517	051105			
1707	006674	043055	044501	020114			
1708	006702	042526	052103	051117			
1709	006710	040440	042104	042522			
1710	006716	051523	000040				
1711	006722	047503	046125	020104	ER2MSG: .ASCIZ	COULD NOT FIND DEVICE CODE	/
1712	006730	047516	020124	044506			
1713	006736	042116	042040	053105			
1714	006744	041511	020105	047503			
1715	006752	042504	000040				
1716	006756	047506	047125	020104	ER1MSG: .ASCIZ	/FOUND UNEXPECTED DEVICE CODE	
1717	006764	047125	054105	042520			
1718	006772	052103	042105	042040			
1719	007000	053105	041511	020105			
1720	007006	047503	042504	000040			
1721	007014	047520	042527	026522	ER3MSG: .ASCII	/POWER-FAIL VECTOR ERROR/	(15)(12)
1722	007022	040506	046111	053040			
1723	007030	041505	047524	020122			
1724	007036	051105	047522	006522			
1725	007044	012					
1726	007045	011	054105	042520	.ASCIZ	/	EXPECTED RECIEVED/
1727	007052	052103	042105	051011			(15)(12)
1728	007060	041505	042511	042526			
1729	007066	006504	000012				
1730	007072	047520	042527	026522	ER4MSG: .ASCII	/POWER-FAIL DATA ERROR/	(15)(12)
1731	007100	040506	046111	042040			
1732	007106	052101	020101	051105			
1733	007114	047522	006522	012			
1734	007121	011	054105	042520	.ASCIZ	/	EXPECTED RECIEVED/
1735	007126	052103	042105	051011			(15)(12)
1736	007134	041505	042511	042526			
1737	007142	006504	000012				
1738	007146	051103	020103	051105	CRCMSG: .ASCIZ	/CRC ERROR IN ROM E-	/
1739	007154	047522	020122	047111			
1740	007162	051040	046517	042440			
1741	007170	000055					
1742	007172	047503	046125	020104	PFMSG: .ASCIZ	/COULD NOT DETERMINE POWER-FAIL VECTOR ADDRESS	
1743	007200	047516	020124	042504			
1744	007206	042524	046522	047111			
1745	007214	020105	047520	042527			
1746	007222	026522	040506	046111			
1747	007230	053040	041505	047524			
1748	007236	020122	042101	051104			
1749	007244	051505	000123				
1750	007250	047516	051040	046517	NOPOMM: .ASCII	/NO ROMS TEST ERROR/	(15)(12)
1751	007256	020123	042524	052123			
1752	007264	042440	051122	051117			
1753	007272	005015					

H03

CZM98AC M9312 BOOT TERM 8A  
CZM98A.F11 13-MAR-78 08:58MACY11 30A(1052) 13-MAR-78 09:59 PAGE 33  
TRAP TABLE

SEQ 0033

					EXPECTED	RECIEVED	
1754	007274	042411	050130	041505	.ASCII		
1755	007302	042524	004504	042522			
1756	007310	044503	053105	042105			
1757	007316	000					
1758	007317	015	042012	053105	SEQMSG: .ASCII	(15)(12) DEVICE CODES NOT IN ORDER SPECIFIED B	
1759	007324	041511	020105	047503			
1760	007332	042504	020123	047516			
1761	007340	020124	047111	047440			
1762	007346	042122	051105	051440			
1763	007354	042520	044503	044506			
1764	007362	042105	041040	020131			
1765	007370	047111	052123	046101	.ASCII	INSTALLATION PROCEDURE.	
1766	007376	040514	044524	047117			
1767	007404	050040	047522	042503			
1768	007412	052504	042522	000056			
1769					.EVEN		
1770							
1771							
1772					:BUFFERS		
1773							
1774	007420	000030			BUF1:		
1775	007420	000000			.WORD	0	
1776	007422	000000			.WORD	00	
1777	007424	000000			.WORD	0000	
1778	007426	000000			.WORD	000000	
1779	007430	000000			.WORD	00000000	
1780	007432	000000			.WORD	0000000000	
1781	007434	000000			.WORD	000000000000	
1782	007436	000000			.WORD	00000000000000	
1783	007440	000000			.WORD	0000000000000000	
1784	007442	000000			.WORD	000000000000000000	
1785	007444	000000			.WORD	00000000000000000000	
1786	007446	000000			.WORD	0000000000000000000000	
1787	007450	000000			.WORD	000000000000000000000000	
1788	007452	000000			.WORD	00000000000000000000000000	
1789	007454	000000			.WORD	0000000000000000000000000000	
1790	007456	000000			.WORD	000000000000000000000000000000	
1791	007460	000000			.WORD	00000000000000000000000000000000	
1792	007462	000000			.WORD	0000000000000000000000000000000000	
1793	007464	000000			.WORD	000000000000000000000000000000000000	
1794	007466	000000			.WORD	00000000000000000000000000000000000000	
1795	007470	000000			.WORD	00	
1796	007472	000000			.WORD	00	
1797	007474	000000			.WORD	00	
1798	007476	000000			.WORD	00	
1799	007500	000000			BUF2:	.WORD	0
1800	007502	000000				.WORD	0
1801	007504	000010			OCTBUF:	.BLKB	10
1802	007514	000400			MES1:	.BLKB	400
1803	010114	000100			MES2:	.BLKB	100
1804	010214	000100			ERRMSG:	.BLKB	100
1805							
1806							
1807		000001			.END		

CZM9BA0 M9312 BOOT TERM 8K  
CZM9BA.P11 13-MAR-78 09:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 35  
CROSS REFERENCE TABLE -- USER SYMBOLS

SEG 0034

ABASE = 000000	506	547	
ACDW1 = 000000	506	549	
ACDW2 = 000000	506	550	
ACPUOP= 000000	506	521	
ADDW0 = 000000	506	551	
ADDW1 = 00'000	506	552	
ADDW10= 000000	506	561	
ADDW11= 000000	506	562	
ADDW12= 000000	506	563	
ADDW13= 000000	506	564	
ADDW14= 000000	506	565	
ADDW15= 000000	506	566	
ADDW2 = 000000	506	553	
ADDW3 = 000000	506	554	
ADDW4 = 000000	506	555	
ADDW5 = 000000	506	556	
ADDW6 = 000000	506	557	
ADDW7 = 000000	506	558	
ADDW8 = 000000	506	559	
ADDW9 = 000000	506	560	
ADEVCT= 000000	506	512	
ADEVM = 000000	506	548	
AENV = 000000	506	517	
ARENVM = 000000	506	518	
AFATAL= 000000	506	509	
AMADR1= 000000	506	534	
AMADR2= 000000	506	538	
AMADR3= 000000	506	541	
AMADR4= 000000	506	544	
AMAMS1= 000000	506	528	
AMAMS2= 000000	506	536	
AMAMS3= 000000	506	539	
AMAMS4= 000000	506	542	
AMSGAO= 000000	506	514	
AMSGLG= 000000	506	515	
AMSGTY= 000J00	506	508	
AMTYP1= 000000	506	529	
AMTYP2= 000000	506	537	
AMTYP3= 000000	506	540	
AMTYP4= 000000	506	543	
APASS = 000000	506	511	
APRIOR= 000000	506		
APTCU= 000040	1255	1369*	
APTEV= 000001	1248	1325	1367*
APTER1= 000001	434*	826	
APTER2= 000002	435*	840	
APTER3= 000004	436*	851	
APTER4= 000010	437*	857	
APTSIZ= 000200	597	1366*	
APTSPO= 000100	1250	1327	1368*
ASWREG= 000000	506	519	
ATESTN= 000000	506	510	
AUNIT = 000000	506	513	
AUSWR = 000J00	506	520	
A/ECT1= 000000	506	545	
A/ECT2= 000000	506	546	

CZM98AD M9312 BOOT TERMR 8h  
CZM98A.P11 13-MAR-78 09:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 36  
CROSS REFERENCE TABLE -- USER SYMBOLS

EEG 0035

[illegible]

CZM9BA0 M9312 BOOT TERM 8K MACY11 30A(1052) 13-MAR-78 09:59 PAGE 37  
CZM9BA.P11 13-MAR-78 08:58 CROSS REFERENCE TABLE -- USER SYMBOLS

[illegible]

CZM9BA0 M9312 BOOT TERM 8A MACY11 30A(1052) 13-MAR-78 09:59 PAGE 38  
CZM9BA.P11 13-MAR-78 09:58 CROSS REFERENCE TABLE -- USER SYMBOLS

[illegible]



M03

CZM9BA0 M9312 BOOT TERM 8K  
CZM9BA.P11 13-MAR-78 08:58MACY11 30A(1052) 13-MAR-78 09:59 PAGE 39  
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0038

SCD11	001216	549#	853				
SCD12	001220	550#					
SCDARC	005040	1270*	1280*	1287	1296*	1301*	
SCKSWR	005332	1382#	1671				
SCMTAG=	***** U	574	578				
SCNTLG	006057	1393	1506#				
SCNTLU	006052	1410	1505#				
SCPUOP	001164	521#					
SCRLF	005055	1269	1311#	1421	1505		
SDDW0	001222	551#	818	831			
SDDW1	001224	552#					
SDDW10	001246	561#					
SDDW11	001250	562#					
SDDW12	001252	563#					
SDDW13	001254	564#					
SDDW14	001256	565#					
SDDW15	001260	566#					
SDDW2	001226	553#					
SDDW3	001230	554#					
SDDW4	001232	555#					
SDDW5	001234	556#					
SDDW6	001236	557#					
SDDW7	001240	558#					
SDDW8	001242	559#					
SDDW9	001244	560#					
SDEVCT	001146	512#					
SDEVN	001214	548#					
SDDAGN	002162	678	685	691#			
SENDAD	002152	464	605	687#			
SENDOCT	002126	579	680#				
SENDMG	002171	682	695#				
SENULL	002166	683	694#				
SENV	001156	517#	611	1137	1248	1325	1349
SENVN	001157	518#	597	802	1250	1255	1327
SEOP	002102	656	659	672#			
SEOPCT	002120	579*	677#	681			
SETABL	001156	516#					
SETENO	001262	502	569#				
SFATAL	001140	509#	1353#				
SFFLG	005324	1316*	1319*	1347	1356*	1364#	
SFILLC	005052	1273	1308#				
SFILLS	005051	1307#					
SGET42	002142	684#					
SGTSMR	005402	1394#	1669				
SHD =	000003	312	313				
SHIBTS	001122	497#					
SINTAG	001101	471#	1422	1511			
SLF	005056	1312#	1496	1505			
SLFLG	005323	1357*	1363#				
SMADR1	001170	534#					
SMADR2	001174	538#					
SMADR3	001200	541#					
SMADR4	001204	544#					
SMAIL	001136	498	502	507#	596	611	1248
SMAMS1	001166	528#					
SMAMS2	001172	536#					

CZM9BA0 M9312 BOOT TERM 8K      MACY11 30A(1052) 13-MAR-78 09:59 PAGE 40  
CZM9BA.P11      13-MAR-78 08:58      CROSS REFERENCE TABLE -- USER SYMBOLS

[illegible]

CZM9BA0 M9312 BOOT TERM BK MACY11 30A(1052) 13-MAR-78 09:59 PAGE 41  
CZM9BA.P11 13-MAR-78 08:58 CROSS REFERENCE TABLE -- USER SYMBOLS

```

$USWR      001162
$VECT1     001206
$VECT2     001210
$$GET4=    000000
$DFILL     006331
.          = 010314

```

[illegible]

.SASTA= \*\*\*\*\* U  
.S\ = 0C1122

CZM9BA0 M9312 BOOT TERM 84  
CZM9BA.P11 13-MAR-78 08:58

MACY11 30A(1052) 13-MAR-78 09:59 PAGE 43  
CROSS REFERENCE TABLE -- MACRO NAMES

EEC 0041

[illegible]

D04

CZM98A0 M9312 BOOT TERM B+ MAC11 30A(1052) 13-MAR-78 09:59 PAGE 44  
CZM98A.P11 13-MAR-78 09:58 CROSS REFERENCE TABLE -- MACRO NAMES

SEQ 0042

. ABS. 010314 000

ERRORS DETECTED: 0

CZM98A,CZM98A/SOL/CRF/NL:TOC=CZM12A.P11

RUN-TIME: 20 5 .7 SECONDS

RUN-TIME RATIO: 97/27=3.5

CORE USED: 20k (39 PAGES)

E04