

DR11W

DR11-W INTRPROC EXER
CZDRKA0

AH-E783A-MC

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JAN 1980

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MADE IN USA

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IDENTIFICATION

PRODUCT CODE: AC-E782A-MC
PRODUCT NAME: CZDRKA0 DR11-W INTRPROC EXER
DATE: AUG 1979
MAINTAINER: DIAGNOSTIC ENGINEERING

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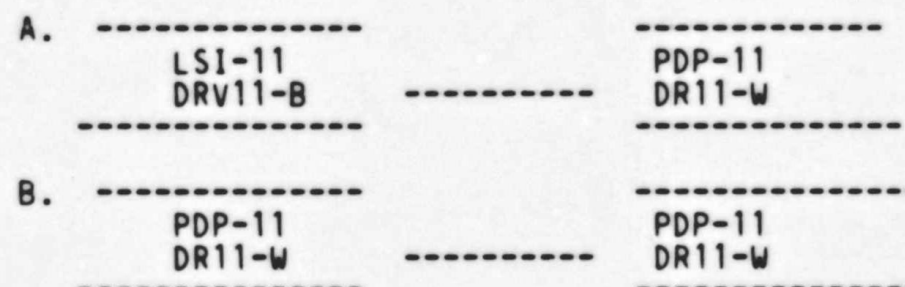
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1.0 ABSTRACT

THE DR11W INTERPROCESSOR EXERCISER WAS DESIGNED TO PASS DATA BETWEEN TWO COMPUTERS. ONE COMPUTER CONTAINS A DR11W DMA INTERFACE AND THE OTHER CONTAINS EITHER A DR11W OR A DRV11B DMA INTERFACE. THE FOLLOWING CONFIGURATIONS ARE VALID



EACH CONFIGURATION USES THE DR11W EXERCISER LOADED INTO EACH OF THE COMPUTERS INVOLVED.

2.0 REQUIREMENTS

2.1 EQUIPMENT

1. ONE PDP11 COMPUTER EQUIPPED WITH A DR11-W.
2. ONE LSI-11 EQUIPPED WITH A DRV11-B OR ANOTHER PDP11 WITH A DR11-W.
3. EACH SYSTEM EQUIPPED WITH AN I/O TERMINAL
4. CABLES (TWO) TO INTER CONNECT THE TWO DMA INTERFACES

2.2 STORAGE

THE PROGRAM USES THE LOWER 3400 WORDS OF MEMORY

3.0 LOADING AND START PROCEDURE

1. CHOOSE ONE OF THE INTERPROCESSOR CONFIGURATIONS SPECIFIED AND LOAD THE DR11-W INTERPROCESSOR EXERCISER INTO EACH SYSTEM.
1. MAKE SURE EACH DMA INTERFACE OF BOTH COMPUTER SYSTEMS ARE CABLED TOGETHER THRU THE I/O CONNECTIONS

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2. MAKE SURE THE DEVICE & VECTOR ADDRESSES AGREE WITH THE
 DEFAULT VALUES DEFINED IN SECTION 6.1. IF NOT, CHANGE
 LOCATION(S) AS DESIRED VIA A 'PATCH' METHOD CONSISTENT WITH
 WITH THE SYSTEM USED.
3. INSURE THAT THE HALT SWITCH IS DISABLED (IF ANY).
4. DEFINE WHAT COMPUTER IS TO BE THE INITIAL SLAVE - THE
 OTHER WILL BE THE INITIAL MASTER (THE ONE STARTED AS
 MASTER WILL REPORT THE 'END OF PASS' MESSAGE).
5. START THE SLAVE **FIRST** AT ADDRESS 204. AFTER THE SLAVE
 HAS BEEN STARTED THE FOLLOWING PRINTOUT WILL OCCUR:

DR11W INTERPROCESSOR EXERCISER

IS THIS CPU TESTING A DRV11B OR DR11W?
TYPE V OR W:

THE OPERATOR TYPES V FOR DRV11B OR W FOR DR11W. IF THE USER
WERE TO TYPE 'V' THEN THE FOLLOWING WOULD BE PRINTED:

DRV11B BOARD
INTERPROCESSOR LINK NOW IN PROGRESS....

AT THIS POINT THE SLAVE IS WAITING FOR THE MASTER TO BE STARTED.

6. NOW START THE OTHER COMPUTER(MASTER) AT SA 200. SIMILARLY THE
 PROGRAM WILL REQUEST THE BOARD TYPE. WHEN USER ANSWERS
 (LET'S SAY WITH A W) THE PRINTOUT WILL SHOW:

DR11W BOARD
INTERPROCESSOR LINK NOW IN PROGRESS....

THE TWO SYSTEMS SHOULD NOW BE COMMUNICATING WITH THE TESTS
BEING PERFORMED.

THE END OF PASS MESSAGE WILL SUSEQUENTLY OCCUR AS FOLLOWS:

END PASS # 1
END PASS # 2
.
ETC.

THE FIRST END PASS IS WITH NO ITERATIONS; ALL OTHERS ARE
WITH ITERATIONS.

4.0 ERRORS

4.1 ERROR COMMENT

ALL ERRORS ARE ACCOMPAINED WITH AN ENGLISH LANGUAGE DESCRIPTIVE COMMENT AS TO THE TYPE OF FAILURE. FURTHER QUALIFICATION OF THE ERROR CAN BE OBTAINED FROM THE COMMENT AT THE ERROR PC OF FROM THE TEST ITSELF.

4.2 ERROR DATA

ERRPC	LISTING ADDRESS WHERE THE ERROR WAS DETECTED
BUSADR	DRV11B BUS REG ADDRESS OF CONCERNED OPERATION
EXPCT	DATA THAT WAS EXPECTED
RCVD	DATA THAT WAS RECEIVED
MEMADR	MEMORY ADDRESS OF DATA ERROR

4.3 ERROR RECOVERY

4.3.1 RECOVERABLE ERRORS

BECAUSE OF THE SYNCHRONIZATION ESTABLISHED WITH THE OTHER COMPUTER, MOST ERROR CONDITIONS WILL CAUSE BOTH COMPUTERS TO RE-SYNCHRONIZE AT THE START OF THE PROGRAM. WHEN AN ERROR OCCURS, AN ERROR MESSAGE WILL BE PRINTED. FOLLOWING THIS, A DELAY OF 5-10 SEC WILL OCCUR AND THE COMPUTER STARTED AS SLAVE WILL PRINT:

*RESYNC...

THIS INDICATES THAT THIS CPU HAS SUCCESSFULLY STARTED ITS PROGRAM AGAIN AND IS WAITING FOR MASTER CPU.
APPROX. 5-10 SEC LATER THE CPU STARTED AS MASTER WILL TYPE:

*RESYNC...

AT THIS POINT BOTH CPU'S SHOULD BE AGAIN COMMUNICATING AND TESTING WILL HAVE BEEN RESUMED FROM THE START OF THE PROGRAM.

4.3.2 NON-RECOVERABLE ERRORS

SOME ERRORS CAN OCCUR DUE TO DMA INTERFACE MALFUNCTIONS, INTERMITTENT CABLE CONNECTIONS, ETC.

1. AN ERROR OF THIS TYPE MAY RESULT IN ONE OR BOTH SYSTEMS STUCK IN ONE OF THE 'WAIT LOOPS' FOUND IN THE PROGRAM. THE WAIT LOOP COULD BE A SOFTWARE LOOP INVOKED BY A SYSTEM WHILE WAITING FOR ITS COMPANION TO SET A BIT IN IT'S REGISTER. IF THIS NEVER COMES THEN THE SYSTEM WAITS INDEFINITELY AND SYNCHRONIZATION IS LOST.
2. A TEST SUCH AS 'BURST DATA LATE' MAY FAIL AND HANG THE BUS OF ONE SYSTEM. IN THIS CASE, THE CPU FAILS TO EXECUTE ANY FURTHER INSTRUCTIONS.

AS STATED IN 4.3.1 MOST ERROR CONDITIONS CAN BE HANDLED BY RESYNCHRONIZATION. HOWEVER, THE PROGRAMS WILL NOT BE ABLE TO RECOVER

THEMSELVES FROM THE ABOVE ERROR TYPES. THEREFORE IT BEHOOVES THE OPERATOR TO PERIODICALLY CHECK FOR 'END PASS' PRINTOUTS WHICH WILL OCCUR APPROX. EVERY 2 MINUTES. THIS WILL VERIFY THAT BOTH SYSTEMS ARE STILL IN SYNC. IF SYNCHRONIZATION IS LOST, BOTH SYSTEMS MUST BE HALTED WHEREBY THE ADDRESS LOCATION OF EACH PROGRAM CAN BE DETECTED THRU 'ADDRESS' REGISTER OR 'ODT' PRINTOUT. BOTH PROGRAMS MUST BE RE-STARTED AS SPECIFIED IN SECT. 3.0.

5.0 SOFTWARE SWITCH REGISTER

5.1 OPTIONS

SWITCH	OCTAL	FUNCTION
BIT15=1	100000	HALT ON ERROR
BIT13=1	020000	INHIBIT ERROR TYPEOUTS
BIT10=1	002000	BELL ON ERROR

5.2 IMPLEMENTATION

IF THE DIAGNOSTIC IS RUN ON A CPU WITHOUT A SWITCH REGISTER THEN A SOFTWARE SWITCH REGISTER IS USED WHICH ALLOWS THE USER THE SAME SWITCH OPTIONS AS THE HARDWARE SWITCH REGISTER. IF THE HARDWARE SWITCH REGISTER DOES NOT EXIST OR IF ONE DOES AND IT CONTAINS ALL ONES (177777) THEN THE SOFTWARE SWITCH REGISTER (LOC. 176) IS USED. UNDER THESE CONDITIONS THE PROGRAM WILL REQUEST THAT THE SOFTWARE SWITCH REGISTER BE LOADED UPON 1ST PASS OF THE PROGRAM. IT WILL PRINT:

SWR:=XXXXXX NEW= (USER ENTERS IN FOLLOWING NEW=)

5.3 CONTROL

1. THE SOFTWARE SWITCH REGISTER 'SWREG' (LOC. 176) CAN BE CHANGED BY USING ODT FACILITIES.
2. THE SOFTWARE SWITCH REGISTER CAN BE CHANGED UNDER PROGRAM CONTROL BY TYPING THE 'CONTROL & G' KEYS. THIS KEYBOARD OPERATION WILL PRINT OUT THE CURRENT CONTENTS AND ACCEPT NEW OCTAL SWITCH REGISTER DATA TERMINATED WITH A CARRIAGE RETURN.

WHEN USING THE ODT MODE AND ONCE IT HAS BEEN ENTERED DUE TO AN ERROR CONDITION WITH BIT15 SET (HALT ON ERROR), STEP #2 ABOVE IS OF NO VALUE, SO RESORT TO STEP #1 TO ALTER THE CONTENTS.

6.0 MISCELLANEOUS

6.1 DRV11B AND/OR DR11W BUS & VECTOR ADDRESS MODIFICATION

MODIFY LOCATION 'INTADR' IF BASE BUS ADDRESS IS NOT 172410
MODIFY LOCATION 'DRVECT' IF VECTOR ADDRESS IS NOT 124

NOTE: USE APPROPRIATE PATCH FACILITIES TO MODIFY THESE LOCATIONS
AFTER PROGRAM LOAD.

6.2 POWER FAIL

THE PROGRAM OFFERS NO PROVISIONS FOR RESTART PROCEDURES
DUE TO POWER FAIL. UPON POWER UP, AND ASSUMING NON
VOLATILE MEMORY, THE PROGRAM OF EACH SYSTEM MUST BE RESTARTED
ACCORDING TO SECT. 3.0.

7.0 EXECUTION TIME

EXECUTION TIME IS ABOUT 3 SEC FOR NO ITERATIONS
EXECUTION TIME W/ITERATIONS IS DETERMINED BY THE SYSTEM CONFIGURATION
ACTUAL RUN TIMES ARE AS FOLLOWS:

11/34 - 11/05: 3MIN
11/70 - 11/45: 45SEC.
11/70 - 11/03: 1MIN 15SEC.

8.0 PROGRAM DESCRIPTION

8.1 GENERAL

THIS INTERPROCESSOR EXERCISER WAS DESIGNED TO TEST THE I/O
ABILITY OF THE DR11W GENERAL PURPOSE INTERFACE TO COMMUNICATE TO
ANOTHER DR11W OR DRV11B LOCATED IN ANOTHER SYSTEM. THE TWO
COMPUTERS ARE STARTED AT DIFFERENT ADDRESSES TO ESTABLISH
INITIAL SYNCHRONIZATION. THE SLAVE COMPUTER IS STARTED 1ST
(START 204) AND THE MASTER COMPUTER IS STARTED 2ND (START 200).
THE TERMS 'MASTER' AND 'SLAVE' SHOULD BE USED LOOSELY AS THE
MASTER WILL BECOME THE SLAVE AND THE SLAVE WILL BECOME THE
MASTER AS THE PROGRAM ADVANCES. THE COMPUTER STARTED AT
ADDRESS 200 WILL ALWAYS REPORT THE 'END OF PASS' MESSAGE.

8.2 PROGRAM SEGMENTS

1A. MTST1 - MASTER SENDS OUT PROGRAM CONTROLLED SINGLE WORDS
THRU THE DATA BUFFER REGISTER AND EXPECTS THE SLAVE TO ECHO

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EACH WORD BACK TO THE MASTER VIA THE DATA BUFFER REGISTER.

1B. STST1 - SLAVE ECHOS ALL CHANGES IN THE DATA BUFFER REGISTER

2A. TEST THAT SLAVE CAN INTERRUPT MASTER: F2(SLAVE) TO ATTN(MASTER)

2B. TEST THAT SLAVE INTERRUPTS MASTER:F2 TO ATTN

3A. MTST2 - MASTER SENDS OUT A 'FNCT' BIT CODE IN THE COMMAND STATUS REGISTER AND EXPECTS THE SLAVE TO ECHO EACH CODE IN ITS 'FNCT' BITS. THE MASTER WILL READ THE 'STAT' BIT CODE FROM THE COMMAND/STATUS REGISTER AND COMPARE IT TO THE CODE WRITTEN.

3B. STST2 - SLAVE READS THE 'STAT' BIT CODE FROM IT'S COMMAND/STATUS REGISTER, CONVERTS THIS CODE AND WRITES IT INTO IT'S 'FNCT' BITS IN THE COMMAND/STATUS REGISTER.

4A. MTST3-BLOCK MODE XFER-MASTER SENDS OUT A 32 WORD DATA BLOCK TO THE SLAVE AND CHECKS FOR PROPER INTERRUPT STATUS, WORD COUNT, AND BUFFER ADDRESS AT THE COMPLETION OF THE TRANSFER.

4B. STST3-BLOCK MODE XFER-SLAVE RECEIVES A 32 WORD DATA BLOCK FROM THE MASTER AND CHECKS FOR PROPER INTERRUPT STATUS, WORD COUNT, BUFFER ADDRESS, AND DATA CONTENT.

5A. MTST4 BURST MODE WORD XFER-MASTER XMITS 32 WORDS TO SLAVE (DR11-W TO (DR11-W ONLY) SAME AS 4A EXCEPT BURST MODE:BOARD DOES NOT RELEASE BUS UNTIL WORD COUNT OVERFLOW

5B. STST4 BURST MODE WORD XFER TEST-SLAVE RECEIVES 32 WORDS FROM MASTER(DR11W TO DR11W ONLY) SAME AS 4B EXCEPT BURST MODE

6A. MTST5 BURST DATA LATE TEST-MASTER XMITS 5 WORDS TO SLAVE WHILE SLAVE RECEIVES 5 AND TIMES OUT (DR11-W TO DR11-W ONLY)

PROCEDURE: MASTER XMITS 5 WORDS TO SLAVE IN BURST MODE
SLAVE IS SETUP TO DO 10 XFERS. CHECK THAT MASTER INTERRUPTS BY WCOF. CHECK CSR,WC,AND BA. AFTER 5 XFERS ARE DONE IN SLAVE,TIMEOUT OCCURS(50US), SINCE SLAVE WAITS FOR GO-AHEAD FROM MASTER TO CONTINUE,BUT MASTER NEVER RESPONDS. IN SLAVE: CHECK THAT 5 XFERS HAVE BEEN COMPLETED.
NOTE: 50US IS MAX TIMEOUT THAT REPRESENTS LENGTH OF TIME BOARD HOLDS THE BUS WHILE WAITING FOR COMPANION. IT IS BEYOND THE CAPABILITY OF THIS DIAGNOSTIC TO VERIFY A 50US TIMEOUT. IN FACT, IF THE TIMEOUT FEATURE MALFUNCTIONS SO AS TO HOLD THE BUS INDEFINITELY,THE PROGRAM WILL 'HANG UP' THE CPU WITH NO RECOVERY PROVIDED FOR.

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THEREFORE, OPERATOR SCRUTINY IS ADVISABLE TO VERIFY
THAT THE PROGRAM IS STILL RUNNING AND THAT THE DR11-W HAS
AT LEAST RELEASED THE BUS IN TEST 5.

- 6B. STS5 BURST DATA LATE TEST-SLAVE RECEIVES 5 WORDS
 FROM MASTER AND TIMES OUT WHILE EXPECTING MORE
 (DR11-W TO DR11-W ONLY)
 SETS UP TO RECEIVE 10 WORDS IN BURST MODE FROM MASTER, BUT MASTER WILL ONLY SEND
 TIMEOUT OCCURS AND THE BUS IS RELEASED
 THEN CHECKS FOR PROPER INTERRUPT STATUS, WC, BA & DATA

%

441
470

```
.TITLE CZDRKAO DR11-W INTRPROC EXER
;*COPYRIGHT (C) 1979
;*DIGITAL EQUIPMENT CORP.
;*MAYNARD, MASS. 01754
;*
;*PROGRAM BY JOHN W. CIUKAJ
;*
;*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
;*PACKAGE (MAINDEC-11-DZQAC-C3), JAN 19, 1977.
;*
```

471
472
473000001
160000
122000
000001

```
$TN=1
$SWR=160000 ;;HALT ON ERROR, LOOP ON TEST, INHIBIT ERROR TYP0UT
$SWR=122000
$TN=1
.SBTTL OPERATIONAL SWITCH SETTINGS
;*
;*      SWITCH      USE
;*      -----
;*      15          HALT ON ERROR
;*      13          INHIBIT ERROR TYPEOUTS
;*      10          BELL ON ERROR
```

474

001100
104000
000004

```
.SBTTL BASIC DEFINITIONS
;*INITIAL ADDRESS OF THE STACK POINTER *** 1100 ***
```

STACK= 1100

ERROR=EMT

SCOPE=IOT

```
;*MISCELLANEOUS DEFINITIONS
```

000011
000012
000015
000200
177776
177776
177774
177772
177570
177570

```
HT= 11 ;;CODE FOR HORIZONTAL TAB
LF= 12 ;;CODE FOR LINE FEED
CR= 15 ;;CODE FOR CARRIAGE RETURN
CRLF= 200 ;;CODE FOR CARRIAGE RETURN-LINE FEED
PS= 177776 ;;PROCESSOR STATUS WORD
PSW=PS
STKLMT= 177774 ;;STACK LIMIT REGISTER
PIRQ= 177772 ;;PROGRAM INTERRUPT REQUEST REGISTER
DSWR= 177570 ;;HARDWARE SWITCH REGISTER
DDISP= 177570 ;;HARDWARE DISPLAY REGISTER ~
```

```
;*GENERAL PURPOSE REGISTER DEFINITIONS
```

000000
000001
000002
000003
000004
000005
000006
000007
000006
000007

```
R0= %0 ;;GENERAL REGISTER
R1= %1 ;;GENERAL REGISTER
R2= %2 ;;GENERAL REGISTER
R3= %3 ;;GENERAL REGISTER
R4= %4 ;;GENERAL REGISTER
R5= %5 ;;GENERAL REGISTER
R6= %6 ;;GENERAL REGISTER
R7= %7 ;;GENERAL REGISTER
SP= %6 ;;STACK POINTER
PC= %7 ;;PROGRAM COUNTER
```

```
;*PRIORITY LEVEL DEFINITIONS
```

000000
000040
000100
000140
000200
000240
000300

```
PR0= 0 ;;PRIORITY LEVEL 0
PR1= 40 ;;PRIORITY LEVEL 1
PR2= 100 ;;PRIORITY LEVEL 2
PR3= 140 ;;PRIORITY LEVEL 3
PR4= 200 ;;PRIORITY LEVEL 4
PR5= 240 ;;PRIORITY LEVEL 5
PR6= 300 ;;PRIORITY LEVEL 6
```



```
000340 PR7= 340 ;:PRIORITY LEVEL 7
100000 ;*"SWITCH REGISTER" SWITCH DEFINITIONS
040000 SW15= 100000
020000 SW14= 40000
010000 SW13= 20000
004000 SW12= 10000
002000 SW11= 4000
001000 SW10= 2000
000400 SW09= 1000
000200 SW08= 400
000100 SW07= 200
000040 SW06= 100
000020 SW05= 40
000010 SW04= 20
000004 SW03= 10
000002 SW02= 4
000001 SW01= 2
001000 SW00= 1
000400 SW9=SW09
000200 SW8=SW08
000100 SW7=SW07
000040 SW6=SW06
000020 SW5=SW05
000010 SW4=SW04
000004 SW3=SW03
000002 SW2=SW02
000001 SW1=SW01
000000 SW0=SW00
100000 ;*DATA BIT DEFINITIONS (BIT00 TO BIT15)
040000 BIT15= 100000
020000 BIT14= 40000
010000 BIT13= 20000
004000 BIT12= 10000
002000 BIT11= 4000
001000 BIT10= 2000
000400 BIT09= 1000
000200 BIT08= 400
000100 BIT07= 200
000040 BIT06= 100
000020 BIT05= 40
000010 BIT04= 20
000004 BIT03= 10
000002 BIT02= 4
000001 BIT01= 2
001000 BIT00= 1
000400 BIT9=BIT09
000200 BIT8=BIT08
000100 BIT7=BIT07
000040 BIT6=BIT06
000020 BIT5=BIT05
000010 BIT4=BIT04
000004 BIT3=BIT03
000002 BIT2=BIT02
000001 BIT1=BIT01
000000 BIT0=BIT00
000004 ;*BASIC "CPU" TRAP VECTOR ADDRESSES
ERRVEC= 4 ;:TIME OUT AND OTHER ERRORS
```



```
000010 RESVEC= 10          ;;RESERVED AND ILLEGAL INSTRUCTIONS
000014 TBITVEC=14        ;;"T" BIT
000014 TRTVEC= 14        ;;TRACE TRAP
000014 BPTVEC= 14        ;;BREAKPOINT TRAP (BPT)
000020 IOTVEC= 20        ;;INPUT/OUTPUT TRAP (IOT) **SCOPE**
000024 PWRVEC= 24        ;;POWER FAIL
000030 EMTVEC= 30        ;;EMULATOR TRAP (EMT) **ERROR**
000034 TRAPVEC=34        ;;"TRAP" TRAP
000060 TKVEC= 60          ;;TTY KEYBOARD VECTOR
000064 TPVEC= 64          ;;TTY PRINTER VECTOR
000240 PIRQVEC=240       ;;PROGRAM INTERRUPT REQUEST VECTOR
475 106427 MTPS=106427   ;;INSTR EQUATE THAT MOVES BYTE TO PSW
476
000000 .SBTTL TRAP CATCHER
      .=0
      ;*ALL UNUSED LOCATIONS FROM 4 - 776 CONTAIN A ".+2,HALT"
      ;*SEQUENCE TO CATCH ILLEGAL TRAPS AND INTERRUPTS
      ;*LOCATION 0 CONTAINS 0 TO CATCH IMPROPERLY LOADED VECTORS
000174 .=174
000174 000000 DISPREG: .WORD 0          ;;SOFTWARE DISPLAY REGISTER
000176 000000 SWREG:  .WORD 0          ;;SOFTWARE SWITCH REGISTER
000200 000137 001506 .SBTTL STARTING ADDRESS(ES)
477 000204 000137 001514 JMP @#START1      ;;JUMP TO STARTING ADDRESS OF PROGRAM
478 000100 000100 JMP @#START2      ;;GO START AS SLAVE COMPUTER
479 000100 000104 000200 000002 .=100
480 .WORD 104,200,2      ;IF 'B EVENT' ON Q-BUS IS CONNECTED
                        ;JUST DO A RTI (IGNORE IT)
```

481

001100 001100
001100 000000
001102 000
001103 000
001104 000000
001106 000000
001110 000000
001112 000000
001114 000
001115 001
001116 000000
001120 000000
001122 000000
001124 000000
001126 000000
001130 000000
001132 000000
001134 000
001135 000
001136 000000
001140 177570
001142 177570
001144 177560
001146 177562
001150 177564
001152 177566
001154 000
001155 002
001156 012
001157 000
001160 207
001163 000
001164 077
001165 015
001166 012

377

377

000

.SBTTL COMMON TAGS

: THIS TABLE CONTAINS VARIOUS COMMON STORAGE LOCATIONS
: USED IN THE PROGRAM.

.=1100

\$CMTAG:			:: START OF COMMON TAGS
\$PASS:	.WORD	0	:: CONTAINS PASS COUNT
\$STNM:	.BYTE	0	:: CONTAINS THE TEST NUMBER
\$ERFLG:	.BYTE	0	:: CONTAINS ERROR FLAG
\$ICNT:	.WORD	0	:: CONTAINS SUBTEST ITERATION COUNT
\$LPADR:	.WORD	0	:: CONTAINS SCOPE LOOP ADDRESS
\$LPERR:	.WORD	0	:: CONTAINS SCOPE RETURN FOR ERRORS
\$ERTTL:	.WORD	0	:: CONTAINS TOTAL ERRORS DETECTED
\$ITEMB:	.BYTE	0	:: CONTAINS ITEM CONTROL BYTE
\$ERMAX:	.BYTE	1	:: CONTAINS MAX. ERRORS PER TEST
\$ERRPC:	.WORD	0	:: CONTAINS PC OF LAST ERROR INSTRUCTION
\$GDADR:	.WORD	0	:: CONTAINS ADDRESS OF 'GOOD' DATA
\$BDADR:	.WORD	0	:: CONTAINS ADDRESS OF 'BAD' DATA
\$GDDAT:	.WORD	0	:: CONTAINS 'GOOD' DATA
\$BDDAT:	.WORD	0	:: CONTAINS 'BAD' DATA
	.WORD	0	:: RESERVED--NOT TO BE USED
	.WORD	0	
\$AUTOB:	.BYTE	0	:: AUTOMATIC MODE INDICATOR
\$INTAG:	.BYTE	0	:: INTERRUPT MODE INDICATOR
	.WORD	0	
\$SWR:	.WORD	DSWR	:: ADDRESS OF SWITCH REGISTER
\$DISPLAY:	.WORD	DDISP	:: ADDRESS OF DISPLAY REGISTER
\$TKS:	177560		:: TTY KBD STATUS
\$TKB:	177562		:: TTY KBD BUFFER
\$TPS:	177564		:: TTY PRINTER STATUS REG. ADDRESS
\$TPB:	177566		:: TTY PRINTER BUFFER REG. ADDRESS
\$NULL:	.BYTE	0	:: CONTAINS NULL CHARACTER FOR FILLS
\$FILLS:	.BYTE	2	:: CONTAINS # OF FILLER CHARACTERS REQUIRED
\$FILLC:	.BYTE	12	:: INSERT FILL CHARS. AFTER A 'LINE FEED'
\$TPELG:	.BYTE	0	:: 'TERMINAL AVAILABLE' FLAG (BIT<07>=0=YES)
\$BELL:	.ASCIIZ	<207><377><377>	:: CODE FOR BELL
\$QUES:	.ASCII	/??	:: QUESTION MARK
\$CRLF:	.ASCII	<15>	:: CARRIAGE RETURN
\$LF:	.ASCIIZ	<12>	:: LINE FEED

.SBTTL ERROR POINTER TABLE
;*THIS TABLE CONTAINS THE INFORMATION FOR EACH ERROR THAT CAN OCCUR.*
;*THE INFORMATION IS OBTAINED BY USING THE INDEX NUMBER FOUND IN
;*LOCATION \$ITEMB. THIS NUMBER INDICATES WHICH ITEM IN THE TABLE IS PERTINENT.
;*NOTE1: IF \$ITEMB IS 0 THE ONLY PERTINENT DATA IS (\$ERRPC).
;*NOTE2: EACH ITEM IN THE TABLE CONTAINS 4 POINTERS EXPLAINED AS FOLLOWS:

;* EM ;;POINTS TO THE ERROR MESSAGE
;* DH ;;POINTS TO THE DATA HEADER
;* DT ;;POINTS TO THE DATA
;* DF ;;POINTS TO THE DATA FORMAT

\$ERRTB:

;ERROR 1

EM1

;SLAVE FAILED TO ECHO DBR CONTENTS

DH1

;ERRPC BUSADR EXPCT RCVD

DT1

;\$ERRPC \$BDADR \$GDDAT \$BDDAT

0

;ERROR 2

EM2

;SLAVE FAILED TO ECHO 'STAT' BITS

DH1

;ERRPC BUSADR EXPCT RCVD

DT1

;\$ERRPC \$BDADR \$GDDAT \$BDDAT

0

;ERROR 3

EM3

;FAILED TO INTR ON A 'DATI'

DH1

;ERRPC BUSADR EXPCT RCVD

DT1

;\$ERRPC \$BDADR \$GDDAT \$BDDAT

0

;ERROR 4

EM4

;STATUS ER ON 'DATI'

DH1

;ERRPC BUSADR EXPCT RCVD

DT1

;\$ERRPC \$BDADR \$GDDAT \$BDDAT

0

001170
482
483 001170 013754
484 001172 015633
485 001174 015734
486 001176 000000
487
488
489 001200 014047
490 001202 015633
491 001204 015734
492 001206 000000
493
494
495 001210 014130
496 001212 015633
497 001214 015734
498 001216 000000
499
500
501 001220 014203
502 001222 015633
503 001224 015734
504 001226 000000

506			;ERROR 5	
507	001230	014247	EM5	;WORD COUNT ER ON 'DAT1'
508	001232	015633	DH1	;ERRPC BUSADR EXPCT RCVD
509	001234	015734	DT1	;SERRPC \$BDADR \$GDDAT \$BDDAT
510	001236	000000	0	
511				
512			;ERROR 6	
513	001240	014317	EM6	;BUFFER ADRS ER ON 'DAT1'
514	001242	015633	DH1	;ERRPC BUSADR EXPCT RCVD
515	001244	015734	DT1	;SERRPC \$BDADR \$GDDAT \$BDDAT
516	001246	000000	0	
517				
518			;ERROR 7	
519	001250	014370	EM7	;FAILED TO INTR ON A 'DATO'
520	001252	015633	DH1	;ERRPC BUSADR EXPCT RCVD
521	001254	015734	DT1	;SERRPC \$BDADR \$GDDAT \$BDDAT
522	001256	000000	0	
523			;ERROR 10	
524	001260	014442	EM10	;STATUS ER ON 'DATO'
525	001262	015633	DH1	;ERRPC BUSADR EXPCT RCVD
526	001264	015734	DT1	;SERRPC \$BDADR \$GDDAT \$BDDAT
527	001266	000000	0	
528				
529			;ERROR 11	
530	001270	014505	EM11	;WORD COUNT ER ON 'DATO'
531	001272	015633	DH1	;ERRPC BUSADR EXPCT RCVD
532	001274	015734	DT1	;SERRPC \$BDADR \$GDDAT \$BDDAT
533	001276	000000	0	
534				
535			;ERROR 12	
536	001300	014554	EM12	;BUFFER ADRS ER ON 'DATO'
537	001302	015633	DH1	;ERRPC BUSADR EXPCT RCVD
538	001304	015734	DT1	;SERRPC \$BDADR \$GDDAT \$BDDAT
539	001306	000000	0	
540				
541			;ERROR 13	
542	001310	014624	EM13	;DATA ER ON 'DATO'
543	001312	015670	DH2	;ERRPC MEMADR EXPCT RCVD
544	001314	015734	DT1	;SERRPC \$BDADR \$GDDAT \$BDDAT
545	001316	000000	0	
546				
547			;ERROR 14	
548	001320	014665	EM14	;CSR READY BIT WAS SET - SHOULD BE CLEAR
549	001322	015633	DH1	
550	001324	015734	DT1	
551	001326	000000	0	
552				
553			;ERROR 15	
554			EM15	;PROTOCOL INITIALIZATION ERROR -MASTER AND SLAVE
555	001330	014753	DH3	
556	001332	015725	DT2	
557	001334	015746	0	
558	001336	000000		
559				
560			;ERROR 16	
561	001340	015075	EM16	;STUCK FOREVER WAITING FOR COMPANION
562	001342	015725	DH3	

563	001344	015752	DT3	
564	001346	000000	0	
565				
566			;ERROR 17	
567	001350	015147	EM17	;TIMEOUT ERROR BURST MODE XFER-INCURRED INTERRUPT
568	001352	015725	DH3	
569	001354	015746	DT2	
570	001356	000000	0	
571				
572			;ERROR 20	;SLAVE DID NOT INTERRUPT MASTER
573	001360	015241	EM20	
574	001362	015725	DH3	
575	001364	015746	DT2	
576	001366	000000	0	
577				
578			;ERROR 21	
579	001370	015354	EM21	;TEST 2-ERROR BIT SET IN MASTER
580	001372	015725	DH3	
581	001374	015752	DT3	
582	001376	000000	0	
583				
584			;ERROR 22	
585	001400	015446	EM22	;BURST DATA LATE BIT IN EIR NOT SET
586	001402	015633	DH1	
587	001404	015734	DT1	
588	001406	000000	0	
589				

```
594 ; BASE REGISTER ADDRESS ASSIGNMENT
595
596 001410 172410 INTADR: 172410 ;MODIFY THIS LOC IF DIFFERENT
597
598 ; VECTOR ADDRESS ASSIGNMENT
599
600 001412 000124 DRVECT: 124 ;MODIFY THIS LOC IF DIFFERENT
601
602 ; BUS REGISTER ADDRESS POINTERS
603
604 001414 172410 WCR: 172410 ;WORD COUNT
605 001416 172412 BAR: 172412 ;BUFFER ADDRESS
606 001420 172414 CSR: 172414 ;COMMAND/STATUS
607 001422 172416 BDR: 172416 ;BUFFER DATA REGISTER
608
609 ; VECTOR ADDRESS POINTERS
610
611 001424 000124 DRVCT0: 124 ;READY & NEX VECTOR
612 001426 000126 DRVCT2: 126 ;NEW PSW ON INTR
613
614 ;COMMON PROGRAM LOCATION(S)
615
616 001430 000000 CNT: 0
617 001432 000000 NUM: 0
618 001434 000000 BOARD: 0 ;DESCRIBES BOARD TYPE:DR11W OR DRV11B
619 001436 000000 ABORT: 0 ;TIMER FOR ABORTING PROGRAM
620 001440 000000 TESTPC: 0
621 001442 000000 TOTAL: 0 ;USED TO DETERMINE BOARD TYPE
622 001444 000000 TIME: 0 ;GENERAL PURPOSE COUNTER
623 001446 000000 TEMP: 0
624 001450 000000 SAVE: 0 ;REG DATA SAVED HERE
625 001452 000000 MSTER: 0 ;0=MASTER START - NON-ZERO=SLAVE START
626 001454 000001 ICOUNT: 1 ;# OF TIMES TO REPEAT ALL TESTS BEFORE END PASS MSG
627 001456 177740 XPRAM: -32. ;XMIT WORD COUNT
628 001460 015756 DBUF ;XMIT BUFFER ADRS
629 001462 000511 511 ;XMIT STATUS/CONTROL
630 001464 177740 RPRAM: -32. ;RCV WORD COUNT
631 001466 015756 DBUF ;RCV BUFFER ADRS
632 001470 000113 113 ;RCV STATUS/CONTROL
633
634 001472 177773 XMITMO:-5 ;XMIT BURST DATA LATE TEST.
635 001474 015756 DBUF
636 001476 000501 501
637
638 001500 177766 RCVTMO:-10. ;RCV BURST DATA LATE TEST
639 001502 015756 DBUF
640 001504 000103 103
641
```



```
644 .SBTTL PROGRAM START
645 001506 005037 001452 START1: CLR MSTER ;THIS IT MASTER START
646 001512 000403 BR START ;SKIP NEXT
647 001514 012737 177777 001452 START2: MOV #-1,MSTER ;SLAVE START
648 001522 START:
.SBTTL INITIALIZE THE COMMON TAGS
;;CLEAR THE COMMON TAGS ($CMTAG) AREA
001522 012706 001100 MOV #SCMTAG,R6 ;:FIRST LOCATION TO BE CLEARED
001526 005026 CLR (R6)+ ;:CLEAR MEMORY LOCATION
001530 022706 001140 CMP #SWR,R6 ;:DONE?
001534 001374 BNE -6 ;:LOOP BACK IF NO
001536 012706 001100 MOV #STACK,SP ;:SETUP THE STACK POINTER
;;INITIALIZE A FEW VECTORS
001542 012737 012452 000020 MOV #SCOPE,@IOTVEC ;:IOT VECTOR FOR SCOPE ROUTINE
001550 012737 000340 000022 MOV #340,@IOTVEC+2 ;:LEVEL 7
001556 012737 012172 000030 MOV #ERROR,@EMTVEC ;:EMT VECTOR FOR ERROR ROUTINE
001564 012737 000340 000032 MOV #340,@EMTVEC+2 ;:LEVEL 7
001572 012737 013320 000034 MOV #STRAP,@TRAPVEC ;:TRAP VECTOR FOR TRAP CALLS
001600 012737 000340 000036 MOV #340,@TRAPVEC+2;LEVEL 7
;;SIZE FOR A HARDWARE SWITCH REGISTER. IF NOT FOUND OR IT IS
;;EQUAL TO A "-1", SETUP FOR A SOFTWARE SWITCH REGISTER.
001606 013746 000004 MOV @ERRVEC,-(SP) ;:SAVE ERROR VECTOR
001612 012737 001646 000004 MOV #64$,@ERRVEC ;:SET UP ERROR VECTOR
001620 012737 177570 001140 MOV #DSWR,SWR ;:SETUP FOR A HARDWARE SWICH REGISTER
001626 012737 177570 001142 MOV #DDISP,DISPLAY ;:AND A HARDWARE DISPLAY REGISTER
001634 022777 177777 177276 CMP #-1,@SWR ;:TRY TO REFERENCE HARDWARE SWR
001642 001012 BNE 66$ ;:BRANCH IF NO TIMEOUT TRAP OCCURRED
;;AND THE HARDWARE SWR IS NOT = -1
001644 000403 BR 65$ ;:BRANCH IF NO TIMEOUT
001646 012716 001654 64$: MOV #65$,(SP) ;:SET UP FOR TRAP RETURN
001652 000002 RTI
001654 012737 000176 001140 65$: MOV #SWREG,SWR ;:POINT TO SOFTWARE SWR
001662 012737 000174 001142 MOV #DISPREG,DISPLAY
001670 012637 000004 66$: MOV (SP)+,@ERRVEC ;:RESTORE ERROR VECTOR
649 001674 012700 001414 MOV #WCR,R0 ;:SET UP REG ADRS POINTERS
650 001700 013701 001410 MOV INTADR,R1 ;:GET BASE ADRS
651 001704 010120 SETUP2: MOV R1,(R0)+ ;:LOAD EM
652 001706 062701 000002 ADD #2,R1 ;:
653 001712 022700 001424 CMP #BDR+2,R0 ;:ALL DONE?
654 001716 001372 BNE SETUP2 ;:BR IF NOT
655 001720 012700 001424 MOV #DRVCT0,R0 ;:SET UP VECTOR ADRS POINTER
656 001724 013701 001412 MOV DRVCT,R1 ;:GET BASE VECTOR ADRS
657 001730 010120 SETUP3: MOV R1,(R0)+ ;:
658 001732 062701 000002 ADD #2,R1 ;:
659 001736 022700 001430 CMP #DRVCT2+2,R0 ;:ALL DONE?
660 001742 001372 BNE SETUP3 ;:BR IF NOT
661 .SBTTL TYPE PROGRAM NAME
;;TYPE THE NAME OF THE PROGRAM IF FIRST PASS
001744 005227 177777 INC #-1 ;:FIRST TIME?
001750 001041 BNE 64$ ;:BRANCH IF NO
001752 104401 002010 TYPE ,65$ ;:TYPE ASCII STRING
.SBTTL GET VALUE FOR SOFTWARE SWITCH REGISTER
001756 005737 000042 TST @#42 ;:ARE WE RUNNING UNDER XXDP/ACT?
001762 001006 BNE 66$ ;:BRANCH IF YES
001764 023727 001140 000176 CMP SWR,#SWREG ;:SOFTWARE SWITCH REG SELECTED?
001772 001005 BNE 67$ ;:BRANCH IF NO
001774 104406 GTSWR ;:GET SOFT-SWR SETTINGS
```

```
001776 000403          BR      67$  
002000 112737 000001 001134 66$:  MOVB   #1,$AUTOB      ;;SET AUTO-MODE INDICATOR  
002006          67$:  
002006 000422          BR      64$      ;;GET OVER THE ASCIIZ  
          ;;65$: .ASCIIZ <CRLF>#DR11W INTERPROCESSOR EXERCISER #<CRLF>  
002054          64$:  
662 002054 000240          NOP
```



```
664 .SBTTL BOARD TYPE DIALOGUE
665 002056 BRDTYP:
666 002056 012706 001100 MOV #STACK,SP ;ALWAYS RESET STACK PTR
667 002062 000005 RESET ;INITIALIZE BEFORE TESTING
668 002064 012737 000001 001454 MOV #1,ICOUNT ;1ST TIME DO ALL TEST ONCE - THEN 100(10)
669 002072 005227 177777 INC #-1 ;DETERMINE BOARD TYPE ON FIRST PASS ONLY
670 002076 001402 BEQ 5$
671 002100 000137 002524 JMP MORS
672 002104 5$:
002104 104401 002112 TYPE ,65$ ;;TYPE ASCIZ STRING
002110 000425 BR ,64$ ;;GET OVER THE ASCIZ
;;65$: .ASCIZ <CRLF>/IS THIS CPU TESTING A DRV11B OR DR11W?/<CRLF>
673 002164 104401 002172 64$:
002170 000411 TYPE ,67$ ;;TYPE ASCIZ STRING
BR ,66$ ;;GET OVER THE ASCIZ
;;67$: .ASCIZ /TYPE V OR W: /
674 002214 104410 66$:
675 002216 021627 000126 2$: RDCHR
676 002222 001051 CMP (SP),#126
677 002224 012737 000126 001434 BNE 3$
678 002232 104401 002240 MOV #126,BOARD
002236 000411 TYPE ,69$ ;;TYPE ASCIZ STRING
BR ,68$ ;;GET OVER THE ASCIZ
;;69$: .ASCIZ /V/<CRLF><CRLF>/DRV11B BOARD/<CRLF>
679 002262 104401 002270 68$:
002266 000426 TYPE ,71$ ;;TYPE ASCIZ STRING
BR ,70$ ;;GET OVER THE ASCIZ
;;71$: .ASCIZ /INTERPROCESSOR LINK NOW IN PROGRESS...../<CRLF><CRLF>
680 002344 000576 70$:
681 002346 021627 000127 3$: BR SYNCST
682 002352 001414 CMP (SP),#127
683 002354 104401 002362 BEQ 6$
002360 000410 TYPE ,73$ ;;TYPE ASCIZ STRING
BR ,72$ ;;GET OVER THE ASCIZ
;;73$: .ASCIZ /ERROR IN ENTRY/
684 002402 000640 72$:
685 002404 012737 000127 001434 6$: BR 5$
686 002412 104401 002420 MOV #127,BOARD
002416 000410 TYPE ,75$ ;;TYPE ASCIZ STRING
BR ,74$ ;;GET OVER THE ASCIZ
;;75$: .ASCIZ /W/<CRLF><CRLF>/DR11W BOARD/<CRLF>
687 002440 104401 002446 74$:
002444 000426 TYPE ,77$ ;;TYPE ASCIZ STRING
BR ,76$ ;;GET OVER THE ASCIZ
;;77$: .ASCIZ /INTERPROCESSOR LINK NOW IN PROGRESS...../<CRLF><CRLF>
688 002522 000507 76$:
689 002524 MORS: BR SYNCST
690 002524 022737 000126 001434 4$: CMP #126,BOARD
691 002532 001403 BEQ 16$
692 002534 104401 002645 TYPE ,7$
693 002540 000500 BR 15$
694 002542 104401 002550 16$: TYPE ,8$
695 002546 000475 BR 15$
696 002550 200 200 040 8$: .ASCII <CRLF><CRLF>/ DRV11B BOARD/<CRLF>
002553 104 122 126
002556 061 061 102
```

	002561	040	102	117	
	002564	101	122	104	
	002567	200			
697	002570	111	116	124	.ASCII /INTERPROCESSOR LINK NOW IN PROGRESS...../<CRLF><CRLF><CRLF>
	002573	105	122	120	
	002576	122	117	103	
	002601	105	123	123	
	002604	117	122	040	
	002607	114	111	116	
	002612	113	040	116	
	002615	117	127	040	
	002620	111	116	040	
	002623	120	122	117	
	002626	107	122	105	
	002631	123	123	056	
	002634	056	056	056	
	002637	056	200	200	
	002642	200			
698	002643	040	000		.ASCIIZ / /
699	002645	200	200	040	7\$: .ASCII <CRLF><CRLF>/ DR11W BOARD /<CRLF>
	002650	104	122	061	
	002653	061	127	040	
	002656	102	117	101	
	002661	122	104	040	
	002664	200			
700	002665	111	116	124	.ASCII /INTERPROCESSOR LINK NOW IN PROGRESS...../<CRLF><CRLF><CRLF>
	002670	105	122	120	
	002673	122	117	103	
	002676	105	123	123	
	002701	117	122	040	
	002704	114	111	116	
	002707	113	040	116	
	002712	117	127	040	
	002715	111	116	040	
	002720	120	122	117	
	002723	107	122	105	
	002726	123	123	056	
	002731	056	056	056	
	002734	056	200	200	
	002737	200			
701	002740	040	000		.ASCIIZ / /
702					.EVEN

704
705
706.SBTTL
.SBTTL
.SBTTL

MASTER TESTS

707 002742

15\$:

708 002742 004737 013576

SYNCST:

JSR

PC,CPUHI

709 002746 005737 001452

TST

MSTER

710 002752 001402

BEQ

MINIT1

711 002754 000137 006072

JMP

SINIT1

;START AS MASTER?

;YES - GO SEND TO SLAVE & CHECK ECHO

;NO - GO ECHO MASTER'S DATA

```
713                                     ;MASTER PROTOCOL INIT-TEST 1
714                                     ;:*****
715
716 002760 032777 020000 176432 MINIT1: BIT    #BIT13,@CSR
717 002766 001425                      BEQ     1$
718 002770 104401 002776                      TYPE  ,65$           ;;TYPE ASCII STRING
                                BR      64$           ;;GET OVER THE ASCIIZ
                                64$: .ASCIIZ <CRLF>/CABLE IS NOT INSERTED - HALTING /
719 003040 000000
720 003042 004737 013576                      1$: JSR     PC,CPUHI
721 003046 012706 001100                      MOV     #STACK,SP
722 003052 005077 176342                      CLR     @CSR
723 003056 012737 001000 001444                MOV     #1000,TIME
724 003064 032777 001000 176326 2$: BIT     #1000,@CSR           ;HAS SLAVE SET MASTER DSTATC
725 003072 001005                      BNE     MTST1
726 003074 005337 001444                      DEC     TIME
727 003100 001371                      BNE     2$
728 003102 104015                      104000+15
729 003104 104412                      RSYNC
```



```

731 .SBTTL * MTST1 TEST THAT SLAVE CAN ECHO THE DBR CORRECTLY
732 :*****
733 :*****
734 :MASTER COMPUTER STARTS HERE FROM PROGRAM START
735 :IT SENDS OUT SINGLE WORDS (FLOATING I/O PTRN) THRU THE
736 :DBR TO THE SLAVE COMPUTER
737 :IT LOOKS FOR THE SLAVE TO ECHO THE DBR WORD CORRECTLY
738 :WITHIN A CERTIAN AMOUNT OF TIME
739 :IF IT FAILS TO RETURN THE WORD AN ERROR IS REPORTED
740 :THIS TEST IS NOT EXITED UNTIL ALL DATA PATTERNS HAVE
741 :BEEN SENT AND RECEIVED CORRECTLY
742 :*****
743 :*****
744 MTST1:
745 003106 005001 1$: CLR R1 ;R1 SAYS FLOAT 0 LEFT WHEN ZERO
746 003106 012700 177776 MOV #-2,R0 ;START WITH #177776 IN R0
747 003114 010077 176302 2$: MOV R0,@BDR ;SEND PATTERN OUT
748 003120 004737 013502 JSR PC,GOCMPN ;TELL SLAVE TO PROCEED
749 003124 010037 001124 MOV R0,$GDDAT ;SAVE IN EXPECTED
750 003130 013737 001422 001122 MOV BDR,$BDDADR ;SET UP DBR ADRS
751 003136 004737 013414 JSR PC,WTCPN ;WAIT FOR SLAVE TO ECHO DATA
752 003142 017737 176254 001126 MOV @BDR,$BDDAT ;READ IT BACK
753 003150 012737 001000 001444 MOV #1000,TIME ;DELAY
754 003156 005337 001444 3$: DEC TIME
755 003162 001375 BNE 3$
756 003164 023737 001124 001126 CMP $GDDAT,$BDDAT ;CORRECT?
757 003172 001402 BEQ 4$ ;BR IF SO
758 003174 104001 104000+1 RSYNC ;RSYNC ON ERROR
759 003176 104412 4$: COM R0 ;CONVERT PTRN TO FLOATING 1
760 003200 005100 COM R1 ;TIME TO FLOAT LEFT?
761 003202 005101 BNE 2$ ;BR IF NOT
762 003204 001343 ASL R0 ;YES - FLOAT LEFT
763 003206 006300 INC R0 ;KEEP LSB SET
764 003210 005200 BCS 2$ ;BR IF ZERO NOT TO CARRY YET
765 003212 103740 MOV #177001,@BDR ;TELL SLAVE TO GO TO NEXT TEST
766 003214 012777 177001 176200 JSR PC,GOCMPN ;TELL SLAVE TO READ TEST TERMINATOR
767 003222 004737 013502
768

```

```

770                                     ;MASTER PROTOCOL INIT-TEST2
771                                     .SBTTL * TEST THAT SLAVE CAN INTERRUPT MASTER: F2(SLAVE) TO ATTN(MASTER)
772                                     ;*****
773 003226 012737 001000 001444 MINIT2: MOV #1000,TIME ;WAIT FOR SLAVE TO CLEAR
774 003234 005337 001444 1$: DEC TIME
775 003240 001375 BNE 1$
776 003242 017737 176152 001446 MOV @CSR,TEMP
777 003250 042737 177775 001446 BIC #177775,TEMP ;CLEAR ALL BUT F1
778 003256 013777 001446 176134 MOV TEMP,@CSR
779 003264 032777 100000 176126 BIT #100000,@CSR ;IS ERROR BIT CLEAR
780 003272 001402 BEQ .+6
781 003274 104021 104000+21
782 003276 104412 RSYNC ;RSYNC ON ERROR
783 003300 012777 003462 176116 MOV #RTRN,@DRVCT0 ;DEFINE RETURN
784 003306 012777 000340 176112 MOV #340,@DRVCT2
785 003314 004737 013624 JSR PC,CPULO ;ALLOW CPU INTERRUPT
786 003320 017737 176074 001446 MOV @CSR,TEMP
787 003326 042737 100000 001446 BIC #BIT15,TEMP
788 003334 052737 000101 001446 BIS #101,TEMP ;IE,GO
789 003342 013777 001446 176050 MOV TEMP,@CSR
790 003350 032777 000002 176042 BIT #2,@CSR ;TELL SLAVE TO INTERRUPT MASTER
791 003356 001415 BEQ 4$
792 003360 017737 176034 001446 MOV @CSR,TEMP
793 003366 042737 000002 001446 BIC #2,TEMP
794 003374 042737 100000 001446 BIC #BIT15,TEMP
795 003402 013777 001446 176010 MOV TEMP,@CSR
796 003410 000414 BR 5$
797 003412 017737 176002 001446 4$: MOV @CSR,TEMP
798 003420 052737 000002 001446 BIS #2,TEMP
799 003426 042737 100000 001446 BIC #BIT15,TEMP
800 003434 013777 001446 175756 MOV TEMP,@CSR
801 003442 012737 010000 001444 5$: MOV #10000,TIME ;DELAY;WAIT FOR SLAVE TO INTERRUPT
802 003450 005337 001444 3$: DEC TIME
803 003454 001375 BNE 3$
804 003456 104020 104000+20
805 003460 104412 RSYNC ;RESYNC ON ERROR
806 003462 RTRN:
807 003462 004737 011200 1$: JSR PC,RSTVEC
808 003466 032777 002000 175724 BIT #2000,@CSR ;IS ATTN CLR?
809 003474 001372 BNE 1$
810 003476 012777 000000 175714 MOV #0,@CSR ;MAKE SURE DSTATC OF SLAVE IS CLR
811 003504 022626 CMP (SP)+,(SP)+ ;READJUST STACK
812 003506 012737 001000 001444 MOV #1000,TIME ;DELAY
813 003514 005337 001444 2$: DEC TIME
814 003520 001375 BNE 2$
815
816 003522 005077 175674 CLR @BDR
  
```



```

818 .SBTTL * MTST2 TEST THAT SLAVE CAN ECHO THE 'STAT' BITS CORRECTLY
819 :*****
820 :*****
821 :MASTER SENDS OUT A 'FNCT' CODE (1-7) TO THE SLAVE COMPUTER
822 :THE MASTER THEN LOOKS FOR THE SLAVE TO ECHO THE CODE VIA THE
823 : 'STAT' BITS WITHIN A CERTIAN AMOUNT OF TIME
824 :IF IT FAILS TO RETURN THE CORRECT CODE AN ERROR IS REPORTED
825 :THIS TEST IS NOT EXITED UNTIL ALL 'FNCT' CODES HAVE BEEN
826 :SENT AND RECEIVED CORRECTLY
827 :*****
828 :*****
829 003526 MTST2:
830 003526 013737 001420 001122 MOV CSR,$BADDR ;SET UP CSR ADRS
831 003534 012700 000002 MOV #2,R0 ;SET UP INITIAL FNCT BIT COUNT
832 003540 012737 001202 001124 MOV #1202,$GDDAT ;LD EXPECTED
833 003546 010077 175646 1$: MOV R0,@CSR ;LOAD FNCT BITS
834 003552 004737 013414 JSR PC,WTCPN ;LOOK FOR SLAVE TO ECHO VIA 'STAT' BITS
835 003556 012737 001000 001444 MOV #1000,TIME ;DELAY
836 003564 005337 001444 2$: DEC TIME
837 003570 001375 BNE 2$
838 003572 017737 175622 001126 MOV @CSR,$BDDAT ;READ CSR
839 003600 033737 001124 001126 BIT $GDDAT,$BDDAT ;CORRECT?
840 003606 001002 BNE 3$ ;BR IF SO
841 003610 104002 104000+2
842 003612 104412 RSYNC ;RSYNC ON ERROR
843 003614 062737 001002 001124 3$: ADD #1002,$GDDAT ;ADVANCE EXPECTED
844 003622 062700 000002 ADD #2,R0 ;ADVANCE PTRN
845 003626 032700 000020 BIT #20,R0 ;ALL BEEN DONE?
846 003632 001745 BEQ 1$ ;BR IF NOT
847 003634 012777 177002 175560 MOV #177002,@BDR ;TELL SLAVE TO GO TO NEXT TEST
848 003642 004737 013502 JSR PC,GOCMPN ;GO TO COMPANION
849 003646 004737 013414 JSR PC,WTCPN ;WAIT FOR COMPANION
850

```

```

852 .SBTTL * MTST3 BLOCK MODE WORD XFER-MASTER XMIT 32 WORDS TO SLAVE
853 :*****
854 :*****
855 :MASTER XMIT A 32 WORD BLOCK OF DATA TO SLAVE
856 :THEN CHECKS FOR PROPER INTERRUPT STATUS, WC & BA
857 :THE SLAVE CHECKS THE SAME PLUS THE DATA RECEIVED
858 :*****
859 :*****
860 MTST3:
861 003652 004737 013576 1$: JSR PC, CPUHI ;NO INTR ALLOWED YET
862 003656 005077 175536 41$: CLR @CSR
863 003662 022777 000200 175530 CMP #200, @CSR ;IS ONLY READY SET
864 003670 001372 BNE 41$
865 003672 017737 175522 001446 38$: MOV @CSR, TEMP
866 003700 042737 100000 001446 BIC #BIT15, TEMP
867 003706 052737 000002 001446 BIS #2, TEMP ;SET DSTATC IN SLAVE
868 ;TELLS SLAVE TO SETUP FOR XFERS
869 003714 013777 001446 175476 MOV TEMP, @CSR
870 003722 032777 001000 175470 40$: BIT #1000, @CSR ;DSTATC HERE SAYS SLAVE
871 ;IS READY TO DO XFERS
872 003730 001774 BEQ 40$
873 003732 004537 011160 37$: JSR R5, SETVEC ;SET UP INTR RETURN ADRS
874 003736 004172 3$ ;RETURN TO 3$ ON INTR
875 003740 004537 011236 JSR R5, LDBUF ;GO LOAD 'DBUF' WITH DATA PTRN
876 003744 177740 -32. ;DO 32. LOCATIONS
877 003746 012737 010000 001444 MOV #10000, TIME ;SET UP A TIMER VALUE
878 003754 013777 001456 175432 MOV XPRAM, @WCR ;SET UP WORD COUNT
879 003762 013777 001460 175426 MOV XPRAM+2, @BAR ;SET UP BUFFER ADRS
880 003770 004737 013624 JSR PC, CPULO ;ALLOW THE RDY INTR
881 003774 017737 175420 001446 MOV @CSR, TEMP
882 004002 042737 100000 001446 BIC #BIT15, TEMP
883 004010 052737 000010 001446 BIS #10, TEMP ;SINGLE CYCLE
884 004016 013777 001446 175374 MOV TEMP, @CSR
885 004024 017737 175370 001446 MOV @CSR, TEMP
886 004032 042737 100000 001446 BIC #BIT15, TEMP
887 004040 042737 000002 001446 BIC #2, TEMP
888 004046 013777 001446 175344 MOV TEMP, @CSR
889 004054 013777 001462 175336 MOV XPRAM+4, @CSR ;SET UP CONTROL - IE, FNCT 3 & GO, AND CYCLE
890 004062 005337 001444 2$: DEC TIME ;WAIT FOR INTR
891 004066 001375 BNE 2$ ;UNTIL 0
892 004070 017737 175324 001126 MOV @CSR, $BDDAT ;READ CSR - SHOULD NEVER GET HERE
893 004076 017737 175316 001446 MOV @CSR, TEMP
894 004104 042737 100000 001446 BIC #BIT15, TEMP
895 004112 042737 000500 001446 BIC #500, TEMP ;DISABLE IE & CYCLE (IF THIS IS
896 ;A DRV11B: CYCLE HIGH WILL
897 ;PREVENT BDR FROM BEING READ)
898 004120 013777 001446 175272 MOV TEMP, @CSR
899 004126 022737 000127 001434 CMP #127, BOARD ;IF DRV11W: CYCLE WILL BE CLEAR
900 004134 001404 BEQ 45$
901 004136 012737 005710 001124 MOV #5710, $GDDAT ;LD EXPECTED - STAT A & C, CYCLE, RDY, IE, FNCT 3
902 ;MUST BE DRV11B
903 004144 000403 BR 46$
904 004146 012737 005310 001124 45$: MOV #5310, $GDDAT ;STAT A&C, RDY, IE, FNCT 3
905 004154 013737 001420 001122 46$: MOV CSR, $BDADR ;SET UP CSR ADRS
906 004162 104401 015516 TYPE ,MSG1
907 004166 104003 104000+3
908 004170 000514 BR 6$ ;GO RESYNC ON ERROR
  
```



```

909 004172 022626 3$: CMP (SP)+,(SP)+ ;FIX STACK SINCE NO RTI
910 004174 017737 175220 001126 MOV @CSR,$BDDAT ;READ STATUS
911 004202 017737 175212 001446 MOV @CSR,TEMP
912 004210 042737 100000 001446 BIC #BIT15,TEMP
913 004216 042737 000500 001446 BIC #500,TEMP ;DISABLE IE & CYCLE(CLR CYCLE IF THIS IS A DRV11B)
914 004224 013777 001446 175166 MOV TEMP,@CSR
915 004232 022737 000127 001434 CMP #127,BOARD
916 004240 001404 BEQ 31$
917 004242 012737 005710 001124 MOV #5710,$GDDAT ;LD EXPECTED - STAT A & C, CYCLE, RDY, IE, FNCT 3
918 ;MUST BE DRV11B
919 004250 000403 BR 30$
920 004252 012737 005310 001124 31$: MOV #5310,$GDDAT
921 004260 30$:
    004260 023737 001124 001126 CMP $GDDAT,$BDDAT ;CORRECT?
    004266 001407 BEQ 4$ ;BR IF SO
922 004270 013737 001420 001122 MOV CSR,$BDADR ;SET UP CSR ADRS
923 004276 104401 015516 TYPE ,MSG1
924 004302 104004 104000+4
925 004304 000446 BR 6$ ;GO RESYNC ON ER
926 004306 005037 001124 4$: CLR $GDDAT ;LD EXPECTED WC
927 004312 017737 175076 001126 MOV @WCR,$BDDAT ;READ WORD COUNT
928 004320 001407 BEQ 5$ ;BR IF ZERO
929 004322 013737 001414 001122 MOV WCR,$BDADR ;SET UP WCR ADRS
930 004330 104401 015516 TYPE ,MSG1
931 004334 104005 104000+5
932 004336 000431 BR 6$ ;GO RESYNC ON ER
933 004340 013737 001460 001124 5$: MOV XPRAM+2,$GDDAT ;GET STARTING ADRS OF XFER
934 004346 013700 001456 MOV XPRAM,R0 ;GET WC
935 004352 005400 NEG R0 ;GET ACTUAL #
936 004354 006300 ASL R0 ;CONVERT TO WORD
937 004356 060037 001124 ADD R0,$GDDAT ;ADD TO BASE ADRS
938 004362 017737 175030 001126 MOV @BAR,$BDDAT ;READ BAR ADRS
939 004370 042737 000001 001126 BIC #BIT00,$BDDAT ;ELIMINATE LSB
940 004376 023737 001124 001126 CMP $GDDAT,$BDDAT ;CORRECT?
941 004404 001431 BEQ 8$ ;BR IF SO
942 004406 013737 001416 001122 MOV BAR,$BDADR ;SET UP BAR ADRS
943 004414 104401 015516 TYPE ,MSG1
944 004420 104006 104000+6
945
946

```

```

948                                     ;ERROR IN MASTER-WAIT ON SLAVE FOR ITS STATUS
949
950 004422 012737 010000 001444 6$: MOV    #10000,TIME    ;WAIT FOR SLAVE TO COMPLETE DATA CK
951 004430 005337 001444 7$: DEC    TIME                ;COUNT AWAY
952 004434 001375                                     BNE    7$          ;UNTIL DONE
953 004436 000005                                     RESET                                     ;TELL SLAVE WE HAVE AN ERROR
954 004440 017737 174754 001450 21$: MOV    @CSR,SAVE
955 004446 042737 170777 001450 BIC    #170777,SAVE
956 004454 001404                                     BEQ    24$
957
958 004456 022737 001000 001450 23$: CMP    #1000,SAVE
959 004464 001365                                     BNE    21$
960 004466 104412                                     24$: RSYNC                                     ;RSYNC ON ERROR
961
962                                     ;NO ERROR IN MASTER-WAIT ON SLAVE FOR ITS STATUS
963
964 004470 012737 010000 001444 8$: MOV    #10000,TIME    ;WAIT FOR SLAVE TO COMPLETE CKS
965 004476 005337 001444 20$: DEC    TIME                ;COUNT AWAY
966 004502 001375                                     BNE    20$          ;UNTIL DONE
967 004504 112777 000002 174706 MOVB   #2,@CSR        ;TELL SLAVE ALL OK
968 004512 017737 174702 001450 9$: MOV    @CSR,SAVE      ;READ CSR
969 004520 042737 170777 001450 BIC    #170777,SAVE      ;SAVE 'STAT' BITS
970 004526 001405                                     BEQ    11$          ;WAS THERE AN ERROR?
971 004530 022737 001000 001450 CMP    #1000,SAVE        ;NO ERROR?
972 004536 001402                                     BEQ    10$
973 004540 000764                                     BR     9$
974 004542 104412                                     11$: RSYNC                                     ;RSYNC ON ERROR
975 004544 004737 011200 10$: JSR     PC,RSTVEC          ;GO RESTORE VECTOR
976

```



```

978                                     ;BURST MODE TESTING DETERMINATION
979                                     ;*****
980                                     ;DETERMINE IF BURST XFERS AND BURST DATA LATE ARE
981                                     ;BE DONE. TEST WILL BE DONE ONLY IF BOTH MASTER AND SLAVE
982                                     ;ARE BOTH DR11W'S. OTHERWISE SLAVE WILL BECOME MASTER
983                                     ;AND MASTER SLAVE AND TESTS 1 THRU 3 WILL BE DONE AGAIN.
984                                     ;*****
985
986
987 004550 012737 001000 001444 MSBRD: MOV #1000,TIME ;WAIT FOR SLAVE
988 004556 005337 001444 1$: DEC TIME
989 004562 001375 BNE 1$
990 004564 013777 001434 174630 MOV BOARD,@BDR ;TELL SLAVE WHAT BOARD
991                                     ;TYPE MASTER IS
992 004572 004737 013502 JSR PC,GOCMPN ;TELL SLAVE TO RESPOND
993 004576 004737 013414 JSR PC,WTCMPN ;WAIT FOR SLAVE TO EVALUATE
994 004602 005777 174614 TST @BDR ;SLAVE RESPONDS AND MASTER CAN
995                                     ;FIND OUT IF TEST 4 SHOULD BE DONE
996 004606 001402 BEQ 2$ ;0 SAYS NO
997 004610 000137 004620 JMP MTST4 ;YES;DO TEST 4
998 004614 000137 006072 2$: JMP SINI1 ;
999
1000

```

```

1002 .SBTTL * MTST4 BURST MODE WORD XFER-MASTER XMTS 32 WORDS TO SLAVE
1003 .SBTTL (DR11-W TO DR11-W ONLY)
1004 :*****
1005 :*****
1006 :SAME AS TEST 3 EXCEPT BURST MODE:BOARD DOES NOT RELEASE BUS
1007 :UNTIL WORD COUNT OVERFLOW
1008 :*****
1009 :*****
1010 MTST4:
1011 004620 004737 013576 1$: JSR PC,CPUHI ;NO INTR ALLOWED YET
1012 004624 005077 174570 41$: CLR @CSR
1013 004630 022777 000200 174562 CMP #200,@CSR ;IS ONLY READY SET
1014 004636 001372 BNE 41$
1015 004640 017737 174554 001446 38$: MOV @CSR,TEMP
1016 004646 042737 100000 001446 BIC #BIT15,TEMP
1017 004654 052737 000002 001446 BIS #2,TEMP ;SET DSTATC IN SLAVE
1018 ;TELLS SLAVE TO SETUP FOR XFERS
1019 004662 013777 001446 174530 MOV TEMP,@CSR
1020 004670 032777 001000 174522 40$: BIT #1000,@CSR ;DSTATC HERE SAYS SLAVE
1021 ;IS READY TO DO XFERS
1022 004676 001774 BEQ 40$
1023 004700 004537 011160 37$: JSR R5,SETVEC ;SET UP INTR RETURN ADRS
1024 004704 005070 3$ ;RETURN TO 3$ ON INTR
1025 004706 004537 011236 JSR R5,LDBUF ;GO LOAD 'DBUF' WITH DATA PTRN
1026 004712 177740 -32. ;DO 32. LOCATIONS
1027 004714 012737 010000 001444 MOV #10000,TIME ;SET UP A TIMER VALUE
1028 004722 013777 001456 174464 MOV XPRAM,@WCR ;SET UP WORD COUNT
1029 004730 013777 001460 174460 MOV XPRAM+2,@BAR ;SET UP BUFFER ADRS
1030 004736 004737 013624 JSR PC,CPULO ;ALLOW THE RDY INTR
1031 004742 017737 174452 001446 MOV @CSR,TEMP
1032 004750 042737 100000 001446 BIC #BIT15,TEMP
1033 004756 042737 000002 001446 BIC #2,TEMP
1034 004764 013777 001446 174426 MOV TEMP,@CSR
1035 004772 012777 000501 174420 MOV #501,@CSR ;SET UP CONTROL - IE, GO,AND CYCLE
1036 005000 005337 001444 2$: DEC TIME ;WAIT FOR INTR
1037 005004 001375 BNE 2$ ;UNTIL 0
1038 005006 017737 174406 001126 MOV @CSR,$BDDAT ;READ CSR - SHOULD NEVER GET HERE
1039 005014 017737 174400 001446 MOV @CSR,TEMP
1040 005022 042737 100000 001446 BIC #BIT15,TEMP
1041 005030 042737 000100 001446 BIC #100,TEMP ;DISABLE IE
1042 005036 013777 001446 174354 MOV TEMP,@CSR
1043 005044 012737 001300 001124 45$: MOV #1300,$GDDAT ;STAT C,RDY,IE
1044 005052 013737 001420 001122 46$: MOV CSR,$BDADR ;SET UP CSR ADRS
1045 005060 104401 015550 TYPE ,MSG2
1046 005064 104003 104000+3
1047 005066 000504 BR 6$ ;GO RESYNC ON ERROR
1048 005070 022626 3$: CMP (SP)+,(SP)+ ;FIX STACK SINCE NO RTI
1049 005072 017737 174322 001126 MOV @CSR,$BDDAT ;READ STATUS
1050 005100 017737 174314 001446 MOV @CSR,TEMP
1051 005106 042737 100000 001446 BIC #BIT15,TEMP
1052 005114 042737 000100 001446 BIC #100,TEMP ;DISABLE IE
1053 005122 013777 001446 174270 MOV TEMP,@CSR
1054 005130 012737 001300 001124 31$: MOV #1300,$GDDAT
1055 005136 30$:
1056 005136 023737 001124 001126 CMP $GDDAT,$BDDAT ;CORRECT?
1057 005144 001407 BEQ 4$ ;BR IF SO
1057 005146 013737 001420 001122 MOV CSR,$BDADR ;SET UP CSR ADRS

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1058 005154 104401 015550
1059 005160 104004
1060 005162 000446
1061 005164 005037 001124
1062 005170 017737 174220 001126
1063 005176 001407
1064 005200 013737 001414 001122
1065 005206 104401 015550
1066 005212 104005
1067 005214 000431
1068 005216 013737 001460 001124
1069 005224 013700 001456
1070 005230 005400
1071 005232 006300
1072 005234 060037 001124
1073 005240 017737 174152 001126
1074 005246 042737 000001 001126
1075 005254 023737 001124 001126
1076 005262 001437
1077 005264 013737 001416 001122
1078 005272 104401 015550
1079 005276 104006
1080
1081
1082
1083
1084
1085 005300 012737 010000 001444
1086 005306 005337 001444
1087 005312 001375
1088 005314 000005
1089 005316 012777 000010 174074
1090 005324 017737 174070 001450
1091 005332 042737 170777 001450
1092 005340 022737 005000 001450
1093 005346 001404
1094
1095 005350 022737 004000 001450
1096 005356 001362
1097 005360 104412
1098
1099
1100
1101
1102
1103 005362 012737 010000 001444
1104 005370 005337 001444
1105 005374 001375
1106 005376 012777 000012 174014
1107 005404 017737 174010 001450
1108 005412 042737 170777 001450
1109 005420 022737 005000 001450
1110 005426 001405
1111 005430 022737 004000 001450
1112 005436 001362
1113 005440 104412
1114 005442 004737 011200

                                TYPE      ,MSG2
                                104000+4
                                BR          6$
                                4$: CLR      $GDDAT
                                MOV      @WCR,$BDDAT
                                BEQ      5$
                                MOV      WCR,$BDADR
                                TYPE      ,MSG2
                                104000+5
                                BR          6$
                                5$: MOV      XPRAM+2,$GDDAT
                                MOV      XPRAM,R0
                                NEG      R0
                                ASL      R0
                                ADD      R0,$GDDAT
                                MOV      @BAR,$BDDAT
                                BIC      #BIT00,$BDDAT
                                CMP      $GDDAT,$BDDAT
                                BEQ      8$
                                MOV      BAR,$BDADR
                                TYPE      ,MSG2
                                104000+6

                                ;ERROR IN MASTER-WAIT ON SLAVE FOR ITS STATUS
                                6$: MOV      #10000,TIME
                                7$: DEC      TIME
                                BNE      7$
                                RESET
                                MOV      #10,@CSR
                                21$: MOV      @CSR,SAVE
                                BIC      #170777,SAVE
                                CMP      #5000,SAVE
                                BEQ      24$
                                23$: CMP      #4000,SAVE
                                BNE      21$
                                24$: RSYNC
                                ;RSYNC ON ERROR

                                ;NO ERROR IN MASTER-WAIT ON SLAVE FOR ITS STATUS
                                8$: MOV      #10000,TIME
                                20$: DEC      TIME
                                BNE      20$
                                MOV      #12,@CSR
                                9$: MOV      @CSR,SAVE
                                BIC      #170777,SAVE
                                CMP      #5000,SAVE
                                BEQ      10$
                                CMP      #4000,SAVE
                                BNE      9$
                                11$: RSYNC
                                10$: JSR      PC,RSTVEC
                                ;WAIT FOR SLAVE TO COMPLETE DATA CK
                                ;COUNT AWAY
                                ;UNTIL DONE
                                ;IF ERROR THAN INIT
                                ;TELL SLAVE WE HAVE AN ERROR
                                ;WAIT FOR SLAVE TO COMPLETE CKS
                                ;COUNT AWAY
                                ;UNTIL DONE
                                ;TELL SLAVE ALL OK
                                ;READ CSR
                                ;SAVE 'STAT' BITS
                                ;IF NO ER,CONTINUE
                                ;ERROR?
                                ;RSYNC ON ERROR
                                ;GO RESTORE VECTOR

```

```

1116 .SBTTL * MTST5 BURST DATA LATE TEST-MASTER XMIT5 5 WORDS TO SLAVE
1117 .SBTTL WHILE SLAVE RECEIVES 5 AND TIMES OUT (DR11-W TO DR11-W ONLY)
1118
1119 *****
1120 *****
1121 ;PROCEDURE: MASTER XMIT5 5 WORDS TO SLAVE IN BURST MODE
1122 ;SLAVE IS SETUP TO DO 10 XFERS. CHECK THAT MASTER INTERRUPTS
1123 ;BY WCOF. AFTER 5 XFERS ARE DONE IN
1124 ;SLAVE, TIMEOUT OCCURS, SINCE SLAVE WAITS FOR GO-AHEAD
1125 ;FROM MASTER TO CONTINUE, BUT MASTER NEVER RESPONDS. IN
1126 ;SLAVE: CHECK THAT 5 XFERS HAVE BEEN COMPLETED.
1127 *****
1128 *****
1129 MTST5:
1130 005446 004737 013576 1$: JSR PC, CPUHI ;NO INTR ALLOWED YET
1131 005452 005077 173742 41$: CLR @CSR
1132 005456 022777 000200 173734 CMP #200, @CSR ;IS ONLY READY SET
1133 005464 001372 BNE 41$
1134 005466 017737 173726 001446 38$: MOV @CSR, TEMP
1135 005474 042737 100000 001446 BIC #BIT15, TEMP
1136 005502 052737 000002 001446 BIS #2, TEMP ;SET DSTATC IN SLAVE
1137 ;TELLS SLAVE TO SETUP FOR XFERS
1138 005510 013777 001446 173702 MOV TEMP, @CSR
1139 005516 032777 001000 173674 40$: BIT #1000, @CSR ;DSTATC HERE SAYS SLAVE
1140 ;IS READY TO DO XFERS
1141 005524 001774 BEQ 40$
1142 005526 004537 011160 37$: JSR R5, SETVEC ;SET UP INTR RETURN ADRS
1143 005532 005666 3$: ;RETURN TO 3$ ON INTR
1144 005534 004537 011236 JSR R5, LDBUF ;GO LOAD 'DBUF' WITH DATA PTRN
1145 005540 177740 -32. ;DO 32. LOCATIONS
1146 005542 012737 010000 001444 MOV #10000, TIME ;SET UP A TIMER VALUE
1147 005550 013777 001472 173636 MOV XMITMO, @WCR ;SET UP WORD COUNT
1148 005556 013777 001474 173632 MOV XMITMO+2, @BAR ;SET UP BUFFER ADRS
1149 005564 004737 013624 JSR PC, CPULO ;ALLOW THE RDY INTR
1150 005570 013777 001476 173622 MOV XMITMO+4, @CSR ;SET UP CONTROL - IE, GO, AND CYCLE
1151
1152 005576 005337 001444 2$: DEC TIME ;WAIT FOR INTR
1153 005602 001375 BNE 2$ ;UNTIL 0
1154 005604 017737 173610 001126 MOV @CSR, $BDDAT ;READ CSR - SHOULD NEVER GET HERE
1155 005612 017737 173602 001446 MOV @CSR, TEMP
1156 005620 042737 100000 001446 BIC #BIT15, TEMP
1157 005626 042737 000100 001446 BIC #100, TEMP ;DISABLE IE
1158 005634 013777 001446 173556 MOV TEMP, @CSR
1159 005642 012737 001300 001124 MOV #1300, $GDDAT ;LD EXPECTED STAT C , RDY, IE
1160 005650 013737 001420 001122 MOV CSR, $BDADR ;SET UP CSR ADRS
1161 005656 104401 015602 TYPE ,MSG3
1162 005662 104003 104000+3
1163 005664 000430 BR 6$ ;GO RESYNC ON ERROR
1164 005666 022626 3$: CMP (SP)+, (SP)+ ;FIX STACK SINCE NO RTI
1165 005670 017737 173524 001446 MOV @CSR, TEMP
1166 005676 042737 100000 001446 BIC #BIT15, TEMP
1167 005704 042737 000100 001446 BIC #100, TEMP ;DISABLE IE
1168 005712 013777 001446 173500 MOV TEMP, @CSR
1169 005720 017737 173470 001126 MOV @WCR, $BDDAT ;READ WORD COUNT
1170 005726 001432 BEQ 9$ ;BR IF ZERO
1171 005730 013737 001414 001122 MOV WCR, $BDADR ;SET UP WCR ADRS
1172

```



```

1173 005736 104401 015602          TYPE      ,MSG3
1174 005742 104005          104000+5
1175 005744 000400          BR          6$          ;GO RESYNC ON ER
1176
1177
1178
1179          ;ERROR IN MASTER-WAIT FOR SLAVE STATUS
1180
1181 005746 000005          6$:      RESET
1182 005750 012777 000010 173442      MOV      #10,@CSR          ;TELL SLAVE WE HAVE AN ERROR
1183 005756 017737 173436 001450      21$:      MOV      @CSR,SAVE
1184 005764 042737 170777 001450      BIC      #170777,SAVE
1185 005772 022737 005000 001450      CMP      #5000,SAVE
1186 006000 001404          BEQ      24$
1187 006002 022737 004000 001450      23$:      CMP      #4000,SAVE
1188 006010 001362          BNE      21$
1189 006012 104412          24$:      RSYNC          ;RSYNC ON ERROR
1190
1191
1192
1193          ;NO ERROR IN MASTER-WAIT FOR SLAVE STATUS
1194
1195 006014 017737 173400 001450      9$:      MOV      @CSR,SAVE          ;READ CSR
1196 006022 042737 170777 001450      BIC      #170777,SAVE          ;SAVE 'STAT' BITS
1197 006030 022737 005000 001450      CMP      #5000,SAVE
1198 006036 001410          BEQ      10$          ;BR IF SLAVE DONE WITH NO ER'S
1199 006040 022737 004000 001450      CMP      #4000,SAVE          ;WAS THERE AN ER?
1200 006046 001362          BNE      9$          ;BR IF NOT
1201 006050 012777 000010 173342      MOV      #10,@CSR
1202 006056 104412          RSYNC          ;RSYNC ON ERROR
1203 006060 012777 000012 173332      10$:      MOV      #12,@CSR          ;TELL SLAVE ALL OK
1204 006066 004737 011200          JSR      PC,RSTVEC          ;GO RESTORE VECTOR
  
```

```
1207 .SBTTL
1208 .SBTTL
1209 .SBTTL
1210
1211
1212
1213
1214 006072 032777 020000 173320 SINIT1: BIT #BIT13,@CSR
1215 006100 001425 BEQ 1$
1216 006102 104401 006110 TYPE ,65$ ;;TYPE ASCII STRING
006106 000421 BR 64$ ;;GET OVER THE ASCIIZ
;;65$: .ASCIIZ <CRLF>/CABLE IS NOT INSERTED - HALTING /
64$:
1217 006152 000000 HALT
1218 006154 004737 013576 1$: JSR PC,CPUHI
1219 006160 012706 001100 MOV #STACK,SP
1220 006164 005077 173230 CLR @CSR
1221 006170 032777 001000 173222 BIT #1000,@CSR ;WHAT IS DSTATC
1222 006176 001404 BEQ 3$ ;IF CLR-BRANCH AND WAIT FOR MASTER
1223 006200 032777 001000 173212 2$: BIT #1000,@CSR ;IF NOT, WAIT FOR CLEAR
1224 006206 001374 BNE 2$
1225 006210 004737 013502 3$: JSR PC,GOCMPN
1226 006214 032777 001000 173176 4$: BIT #1000,@CSR ;HAS MASTER SET DSTATC IN SLAVE?
1227 006222 001774 BEQ 4$ ;NOT YET
1228 006224 000137 006240 JMP SL1STR
```



```

1230 .SBTTL * STST1 RECEIVE MASTER'S DBR DATA AND SEND IT BACK VIA DBR
1231 :*****
1232 :*****
1233 :NOW THIS COMPUTER BECOMES THE SLAVE AND ECHOS MASTER'S DBR DATA
1234 :SLAVE COMPUTER STARTS HERE FROM PROGRAM START 204
1235 :*****
1236 :*****
1237 :
1238 :
1239 006230 004737 013502 STST1: JSR PC,GOCMPN ;TELL MASTER WE ARE READY
1240 006234 004737 013414 JSR PC,WTCMPN ;DATA AVAILABLE?
1241 :WAIT ON IT
1242 006240 017737 173156 001450 SL1STR: MOV @BDR,SAVE ;GET DATA
1243 006246 022737 177001 001450 CMP #177001,SAVE ;TEST TERMINATOR?
1244 006254 001412 BEQ 4$ ;BR IF SO
1245 006256 013777 001450 173136 MOV SAVE,@BDR ;SEND IT BACK
1246 006264 012737 001000 001444 MOV #1000,TIME ;DELAY BEFORE CALLING MASTER
1247 006272 005337 001444 5$: DEC TIME
1248 006276 001375 BNE 5$
1249 006300 000753 BR STST1 ;GO LOOK FOR MORE DATA
1250 006302 000240 4$: NOP ;

```

```

1252 .SBTTL * TEST THAT SLAVE INTERRUPTS MASTER:F2 TO ATTN
1253 ;SLAVE PROTOCOL INIT-TEST2
1254 ;*****
1255 006304 SINIT2:
1256
1257 006304 005077 173110 CLR @CSR ;CLR DSTATC OF MASTER
1258 006310 004737 013414 JSR PC,WTCMPN
1259 006314 017737 173100 001446 MOV @CSR,TEMP
1260 006322 042737 100000 001446 BIC #BIT15,TEMP
1261 006330 052737 000004 001446 BIS #4,TEMP ;SET INTERRUPT MASTER
1262 006336 013777 001446 173054 MOV TEMP,@CSR
1263 006344 017737 173050 001446 MOV @CSR,TEMP
1264 006352 042737 100000 001446 BIC #BIT15,TEMP
1265 006360 042737 000004 001446 BIC #4,TEMP ;CLR F2
1266 006366 013777 001446 173024 MOV TEMP,@CSR
1267 006374 012737 010000 001444 MOV #10000,TIME
1268 006402 032777 001000 173010 2$: BIT #1000,@CSR ;WAIT DSTATC SLAVE TO CLEAR
1269 006410 001404 BEQ STST2
1270 006412 005337 001444 DEC TIME
1271 006416 001371 BNE 2$
1272 006420 104412 RSYNC
  
```



```

1274 .SBTTL * STST2 RECEIVE MASTER'S 'STAT' BITS AND SEND IT BACK VIA 'FNCT' BITS
1275 :*****
1276 :*****
1277 :RECEIVE 'STAT' BITS AND CONVERT TO 'FNCT' BITS AND WRITE TO CSR
1278 :*****
1279 :*****
1280 STST2:
1281 006422 004737 013414 4$: JSR PC,WTCMPN ;WAIT FOR MASTER
1282 006426 012737 001000 001444 MOV #1000,TIME ;DELAY
1283 006434 005337 001444 5$: DEC TIME
1284 006440 001375 BNE 5$
1285 006442 022777 177002 172752 2$: CMP #177002,@BDR ;TEST TERMINATOR?
1286 006450 001412 BEQ 3$ ;BR IF SO
1287 006452 017737 172742 001450 MOV @CSR,SAVE ;READ THE CSR
1288 006460 042737 170777 001450 BIC #170777,SAVE ;SAVE ONLY THE STAT BITS
1289 006466 113777 001451 172724 MOVB SAVE+1,@CSR ;ECHO WITH FNCT BITS
1290 006474 000752 BR 4$ ;LOOK FOR NEXT 'STAT' CODE
1291 006476 004737 013502 3$: JSR PC,GOCMPN ;TELL MASTER TO CONTINUE
1292

```

```

1294 .SBTTL * STST3 BLOCK MODE WORD XFER TEST-SLAVE RECEIVES 32
1295 .SBTTL WORDS FROM MASTER
1296 ;*****
1297 ;*****
1298 ;THIS TEST SETS UP TO RECEIVE A 32 WORD BLOCK OF DATA FROM THE MASTER
1299 ;THEN CHECKS FOR PROPER INTERRUPT STATUS, WC, BA & DATA
1300 ;IF AN ERROR IS DETECTED THE SLAVE TELLS THE MASTER, REPORTS THE
1301 ;ERROR, AND RESYNCS ON PROGRAM
1302 ;*****
1303 ;*****
1304 STST3:
1305 1$: JSR PC, CPUHI ;NO INTRs ALLOWED YET
1306 MOV #1000, TIME
1307 40$: DEC TIME
1308 BNE 40$
1309 41$: CLR @CSR
1310 CMP #1200, @CSR ;ONLY DSTATC AND READY SET
1311 BNE 41$
1312 39$: JSR R5, SETVEC ;SET UP THE INTR RETURN ADRS
1313 3$ ;RETURN TO 3$ ON DATO INTR
1314 JSR R5, CLRBUF ;GO CLR 'DBUF'
1315 -32. ;DO 32. LOCATIONS
1316 MOV #10000, TIME ;SET UP A TIMER VALUE
1317 MOV RPRAM, @WCR ;SET UP WORD COUNT
1318 MOV RPRAM+2, @BAR ;SET UP BUFFER ADRS
1319 JSR PC, CPULO ;ALLOW THE INTR
1320 MOV @CSR, TEMP
1321 BIC #BIT15, TEMP
1322 BIS #12, TEMP ;FNCT 3&1
1323 MOV TEMP, @CSR
1324 MOV RPRAM+4, @CSR ;SET UP CONTROL - FNCT 3 & 1, IE & GO
1325 2$: DEC TIME ;WAIT FOR INTR
1326 BNE 2$ ;UNTIL ZERO
1327 MOV @CSR, $BDDAT ;READ CSR - SHOULD NEVER GET HERE
1328 MOV @CSR, TEMP
1329 BIC #BIT15, TEMP
1330 BIC #100, TEMP ;DISABLE IE
1331 MOV TEMP, @CSR
1332 MOV #4312, $GDDAT ;LD EXPECTED - STAT A, RDY, IE & FNCT 3 & 1
1333 MOV CSR, $BDADR ;SET UP CSR ADRS
1334
1335 TYPE ,MSG1
1336 104000+7
1337 BR 10$ ;GO RESYNC ON ER
1338 3$: CMP (SP)+, (SP)+ ;FIX STACK SINCE NO RETURN
1339 MOV @CSR, $BDDAT ;READ STATUS
1340 MOV @CSR, TEMP
1341 BIC #100, TEMP ;DISABLE IE
1342 BIC #BIT15, TEMP
1343 MOV TEMP, @CSR
1344 MOV #4312, $GDDAT ;LD EXPECTED - STAT A, RDY, IE & FNCT 3 & 1
1345 CMP $GDDAT, $BDDAT ;CORRECT?
1346 BEQ 4$ ;BR IF SO
1347 MOV CSR, $BDADR ;SET UP CSR ADRS
1348
1349 TYPE ,MSG1
1350 104000+10
  
```



```

1351 007020 000502                                BR      10$      ;GO RESYNC ON ERROR
1352 007022 005037 001124 4$: CLR      $GDDAT      ;LD EXPECTED WC
1353 007026 017737 172362 001126 MOV     @WCR,$BDDAT ;READ WCR
1354 007034 001407 BEQ      5$      ;BR IF SO
1355 007036 013737 001414 001122 MOV     WCR,$BDADR ;SET UP WCR ADRS
1356
1357 007044 104401 015516                                TYPE    ,MSG1
1358 007050 104011 104000+11
1359 007052 000465                                BR      10$      ;GO RESYNC ON ERROR
1360 007054 013737 001466 001124 5$: MOV     RPRAM+2,$GDDAT ;GET STARTING ADRS OF XFER
1361 007062 013700 001464 MOV     RPRAM,R0      ;GET WC
1362 007066 005400 NEG      R0      ;GET ACTUAL #
1363 007070 006300 ASL      R0      ;CONVERT TO WORD
1364 007072 060037 001124 ADD     R0,$GDDAT ;ADD TO BASE ADRS
1365 007076 017737 172314 001126 MOV     @BAR,$BDDAT ;READ BAR ADRS
1366 007104 042737 000001 001126 BIC     #BIT00,$BDDAT ;ELIMINATE LSB
1367 007112 023737 001124 001126 CMP     $GDDAT,$BDDAT ;CORRECT?
1368 007120 001407 BEQ      6$      ;BR IF SO
1369 007122 013737 001416 001122 MOV     BAR,$BDADR ;SET UP BAR ADRS
1370
1371 007130 104401 015516                                TYPE    ,MSG1
1372 007134 104012 104000+12
1373 007136 000433                                BR      10$      ;GO RESYNC ON ERROR
1374 007140 013700 001466 6$: MOV     RPRAM+2,R0      ;GET BUFFER ADRS
1375 007144 013701 001464 MOV     RPRAM,R1      ;GET WORD COUNT
1376 007150 012702 177776 7$: MOV     #177776,R2      ;GET 1ST DATA PTRN (FLOATING 0)
1377 007154 005003 CLR      R3      ;R3 SAYS WHEN TO SHIFT PTRN
1378 007156 020220 8$: CMP     R2,(R0)+      ;COMPARE DATA IN DBUF TO EXPECTED
1379 007160 001011 BNE      9$      ;BR IF DATA ERROR
1380 007162 005201 INC      R1      ;COUNT THE WORD COUNT
1381 007164 001435 BEQ      13$     ;BR IF DATA CHECKS DONE
1382 007166 005102 COM      R2      ;CONVERT PTRN TO FLOATING 1
1383 007170 005103 COM      R3      ;TIME TO SHIFT?
1384 007172 001371 BNE      8$      ;BR IF NOT - GO CK NEXT
1385 007174 006302 ASL      R2      ;FLOAT PTRN LEFT
1386 007176 005202 INC      R2      ;KEEP LSB SET
1387 007200 103363 BCC      7$      ;GC RESET FLOATING PTRN
1388 007202 000765 BR       8$      ;GO CHECK NEXT
1389 007204 014037 001126 9$: MOV     -(R0),$BDDAT ;GET BAD DATA
1390 007210 010037 001122 MOV     R0,$BDADR ;GET MEM ADRS OF DATA ER
1391 007214 010237 001124 MOV     R2,$GDDAT ;LD EXPECTED DATA
1392
1393 007220 104401 015516                                TYPE    ,MSG1
1394 007224 104013 104000+13

```

```
1396                                     ;ERROR IN SLAVE-WAIT FOR MASTER STATUS
1397
1398 007226 000005                      10$: RESET                      ;TELL MASTER WE HAVE ERROR
1399
1400 007230 017737 172164 001450 15$: MOV @CSR,SAVE ;LOOP ON ERROR
1401 007236 042737 170777 001450 BIC #170777,SAVE ;BUT WAIT FOR MASTER
1402 007244 001404 BEQ 24$
1403 007246 022737 001000 001450 16$: CMP #1000,SAVE
1404 007254 001365 BNE 15$
1405 007256 104412                      24$: RSYNC                      ;RSYNC ON ERROR
1406
1407
1408
1409                                     ;NO ERROR IN SLAVE-WAIT FOR MASTER STATUS
1410
1411 007260 112777 000002 172132 13$: MOVB #2,@CSR ;TELL MASTER ALL OK
1412 007266 017737 172126 001450 14$: MOV @CSR,SAVE ;READ CSR
1413 007274 042737 170777 001450 BIC #170777,SAVE ;SAVE 'STAT' BITS ONLY
1414 007302 001405 BEQ 31$ ;IS ER IN MASTER?
1415 007304 022737 001000 001450 CMP #1000,SAVE ;MASTER OK?
1416 007312 001402 BEQ 30$
1417 007314 000764 BR 14$
1418 007316 104412                      31$: RSYNC                      ;RSYNC ON ERROR
1419 007320 004737 011200                      30$: JSR PC,RSTVEC ;GO RESTORE VECTOR
1420
1421
1422
```



```
1424                                     ;BURST MODE WORD XFER TEST DETERMINATION
1425 ;:*****
1426                                     ;DETERMINE IF BURST XFERS AND BURST DATA LATE ARE TO BE
1427                                     ;DONE
1428 ;:*****
1429
1430 007324 004737 013414                JSR      PC,WTCMPN      ;WAIT FOR MASTER TO INDICATE
1431                                     ;WHAT BOARD TYPE IT IS
1432 007330 012737 001000 001444 SLBRD: MOV      #1000,TIME      ;WAIT ON MASTER
1433 007336 005337 001444 1$:          DEC      TIME
1434 007342 001375                      BNE      1$
1435 007344 013737 001434 001442          MOV      BOARD,TOTAL      ;DETERMINE IF BOTH MASTER
1436                                     ;AND SLAVE ARE DR11W'S
1437 007352 067737 172044 001442          ADD      @BDR,TOTAL
1438 007360 023727 001442 000256          CMP      TOTAL,#256
1439 007366 001406                      BEQ      2$                ;DO BURST MODE
1440
1441
1442 007370 005077 172026                CLR      @BDR
1443 007374 004737 013502                JSR      PC,GOCMPN
1444 007400 000137 010776                JMP      SLVFIN          ;NO BURST MODE
1445 007404 012777 000001 172010 2$:    MOV      #1,@BDR
1446 007412 004737 013502                JSR      PC,GOCMPN
1447
```

```

1449 .SBTTL * STST4 BURST MODE WORD XFER TEST-SLAVE RECEIVES 32
1450 .SBTTL WORDS FROM MASTER(DR11-W TO DR11-W ONLY)
1451 ;*****
1452 ;*****
1453 ;THIS TEST SETS UP TO RECEIVE A 32 WORD BLOCK OF DATA FROM THE MASTER
1454 ;SAME AS TEST 3 EXCEPT BURST MODE
1455 ;*****
1456 ;*****
1457 STST4:
1458 007416 004737 013576 1$: JSR PC,CPUHI ;NO INTRs ALLOWED YET
1459 007422 012737 001000 001444 MOV #1000,TIME
1460 007430 005337 001444 40$: DEC TIME
1461 007434 001375 BNE 40$
1462 007436 005077 171756 41$: CLR @CSR
1463 007442 022777 001200 171750 CMP #1200,@CSR ;ONLY DSTATC AND READY SET
1464 007450 001372 BNE 41$
1465 007452 004537 011160 39$: JSR R5,SETVEC ;SET UP THE INTR RETURN ADRS
1466 007456 007642 3$: ;RETURN TO 3$ ON DATO INTR
1467 007460 004537 011220 JSR R5,CLRBUF ;GO CLR 'DBUF'
1468 007464 177740 -32. ;DO 32. LOCATIONS
1469 007466 012737 010000 001444 MOV #10000,TIME ;SET UP A TIMER VALUE
1470 007474 013777 001464 171712 MOV RPRAM,@WCR ;SET UP WORD COUNT
1471 007502 013777 001466 171706 MOV RPRAM+2,@BAR ;SET UP BUFFER ADRS
1472 007510 004737 013624 JSR PC,CPULO ;ALLOW THE INTR
1473 007514 017737 171700 001446 MOV @CSR,TEMP
1474 007522 042737 100000 001446 BIC #BIT15,TEMP
1475 007530 052737 000002 001446 BIS #2,TEMP ;FNCT 1
1476 007536 013777 001446 171654 MOV TEMP,@CSR
1477 007544 012777 000103 171646 MOV #103,@CSR ;SET UP CONTROL - FNCT 1, IE & GO
1478 007552 005337 001444 2$: DEC TIME ;WAIT FOR INTR
1479 007556 001375 BNE 2$ ;UNTIL ZERO
1480 007560 017737 171634 001126 MOV @CSR,$BDDAT ;READ CSR - SHOULD NEVER GET HERE
1481 007566 017737 171626 001446 MOV @CSR,TEMP
1482 007574 042737 100000 001446 BIC #BIT15,TEMP
1483 007602 042737 000100 001446 BIC #100,TEMP ;DISABLE IE
1484 007610 013777 001446 171602 MOV TEMP,@CSR
1485 007616 012737 000302 001124 MOV #302,$GDDAT ;LD EXPECTED - RDY, IE & FNCT 1
1486 007624 013737 001420 001122 MOV CSR,$BDADR ;SET UP CSR ADRS
1487
1488 007632 104401 015550 TYPE ,MSG2
1489 007636 104007 104000+7 BR 10$ ;GO RESYNC ON ER
1490 007640 000540 3$: CMP (SP)+,(SP)+ ;FIX STACK SINCE NO RETURN
1491 007642 022626 MOV @CSR,$BDDAT ;READ STATUS
1492 007644 017737 171550 001126 MOV @CSR,TEMP
1493 007652 017737 171542 001446 BIC #BIT15,TEMP
1494 007660 042737 100000 001446 BIC #100,TEMP ;DISABLE IE
1495 007666 042737 000100 001446 MOV TEMP,@CSR
1496 007674 013777 001446 171516 MOV #302,$GDDAT ;LD EXPECTED - RDY, IE & FNCT 1
1497 007702 012737 000302 001124 CMP $GDDAT,$BDDAT ;CORRECT?
1498 007710 023737 001124 001126 BEQ 4$ ;BR IF SO
1499 007716 001407 MOV CSR,$BDADR ;SET UP CSR ADRS
1500 007720 013737 001420 001122
1501
1502 007726 104401 015550 TYPE ,MSG2
1503 007732 104010 104000+10 BR 10$ ;GO RESYNC ON ERROR
1504 007734 000502 4$: CLR $GDDAT ;LD EXPECTED WC
1505 007736 005037 001124

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1506	007742	017737	171446	001126	MOV	@WCR,\$BDDAT	:READ WCR
1507	007750	001407			BEQ	5\$	:BR IF SO
1508	007752	013737	001414	001122	MOV	WCR,\$BDADR	:SET UP WCR ADRS
1509							
1510	007760	104401	015550		TYPE	,MSG2	
1511	007764	104011					
1512	007766	000465			BR	10\$	:GO RESYNC ON ERROR
1513	007770	013737	001466	001124	5\$: MOV	RPRAM+2,\$GDDAT	:GET STARTING ADRS OF XFER
1514	007776	013700	001464		MOV	RPRAM,R0	:GET WC
1515	010002	005400			NEG	R0	:GET ACTUAL #
1516	010004	006300			ASL	R0	:CONVERT TO WORD
1517	010006	060037	001124		ADD	R0,\$GDDAT	:ADD TO BASE ADRS
1518	010012	017737	171400	001126	MOV	@BAR,\$BDDAT	:READ BAR ADRS
1519	010020	042737	000001	001126	BIC	#BIT00,\$BDDAT	:ELIMINATE LSB
1520	010026	023737	001124	001126	CMF	\$GDDAT,\$BDDAT	:CORRECT?
1521	010034	001407			BEQ	6\$	:BR IF SO
1522	010036	013737	001416	001122	MOV	BAR,\$BDADR	:SET UP BAR ADRS
1523							
1524	010044	104401	015550		TYPE	,MSG2	
1525	010050	104012					
1526	010052	000433			BR	10\$	:GO RESYNC ON ERROR
1527	010054	013700	001466		6\$: MOV	RPRAM+2,R0	:GET BUFFER ADRS
1528	010060	013701	001464		MOV	RPRAM,R1	:GET WORD COUNT
1529	010064	012702	177776		7\$: MOV	#177776,R2	:GET 1ST DATA PTRN (FLOATING 0)
1530	010070	005003			CLR	R3	:R3 SAYS WHEN TO SHIFT PTRN
1531	010072	020220			8\$: CMP	R2,(R0)+	:COMPARE DATA IN DBUF TO EXPECTED
1532	010074	001011			BNE	9\$	:BR IF DATA ERROR
1533	010076	005201			INC	R1	:COUNT THE WORD COUNT
1534	010100	001443			BEQ	13\$	:BR IF DATA CHECKS DONE
1535	010102	005102			COM	R2	:CONVERT PTRN TO FLOATING 1
1536	010104	005103			COM	R3	:TIME TO SHIFT?
1537	010106	001371			BNE	8\$	:BR IF NOT - GO CK NEXT
1538	010110	006302			ASL	R2	:FLOAT PTRN LEFT
1539	010112	005202			INC	R2	:KEEP LSB SET
1540	010114	103363			BCC	7\$	:GO RESET FLOATING PTRN
1541	010116	000765			BR	8\$	:GO CHECK NEXT
1542	010120	014037	001126		9\$: MOV	-(R0),\$BDDAT	:GET BAD DATA
1543	010124	010037	001122		MOV	R0,\$BDADR	:GET MEM ADRS OF DATA ER
1544	010130	010237	001124		MOV	R2,\$GDDAT	:LD EXPECTED DATA
1545							
1546	010134	104401	015550		TYPE	,MSG2	
1547	010140	104013					

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1549                                     ;ERROR IN SLAVE-WAIT FOR MASTER STATUS
1550
1551 010142 000005                                     10$: RESET
1552 010144 012777 000010 171246 MOV #10,@CSR ;TELL MASTER WE HAVE AN ERROR
1553 010152 017737 171242 001450 15$: MOV @CSR,SAVE ;LOOP ON ERROR
1554 010160 042737 170777 001450 BIC #170777,SAVE ;BUT WAIT FOR MASTER
1555 010166 022737 005000 001450 CMP #5000,SAVE
1556 010174 001404 BEQ 24$
1557 010176 022737 004000 001450 16$: CMP #4000,SAVE
1558 010204 001362 BNE 15$
1559 010206 104412 24$: RSYNC ;RSYNC ON ERROR
1560
1561
1562
1563                                     ;NO ERROR IN SLAVE-WAIT FOR MASTER STATUS
1564
1565 010210 012777 000012 171202 13$: MOV #12,@CSR ;TELL MASTER ALL OK
1566 010216 017737 171176 001450 14$: MOV @CSR,SAVE ;READ CSR
1567 010224 042737 170777 001450 BIC #170777,SAVE ;SAVE 'STAT' BITS ONLY
1568 010232 022737 005000 001450 CMP #5000,SAVE
1569 010240 001405 BEQ 30$ ;IF NO ER,CONTINUE
1570 010242 022737 004000 001450 CMP #4000,SAVE ;IF ERROR,RESYNC
1571 010250 001362 BNE 14$
1572 010252 104412 31$: RSYNC ;RSYNC ON ERROR
1573 010254 004737 011200 30$: JSR PC,RSTVEC ;GO RESTORE VECTOR
  
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1575 .SBTTL * STST5 BURST DATA LATE TEST-SLAVE RECEIVES 5 WORDS
1576 .SBTTL FROM MASTER AND TIMES OUT WHILE EXPECTING MORE
1577 .SBTTL (DR11-W TO DR11-W ONLY)
1578 *****
1579 *****
1580 ;SETS UP TO RECEIVE 10 WORDS IN BURST MODE FROM MASTER,BUT MASTER WILL ONLY SEND 5 WORDS.
1581 ; TIMEOUT OCCURS AND THE BUS IS RELEASED
1582 ;THEN CHECKS FOR PROPER INTERRUPT STATUS, WC, BA & DATA
1583 ;IF AN ERROR IS DETECTED THE SLAVE TELLS THE MASTER, REPORTS THE
1584 ;ERROR, AND RESYNCS ON PROGRAM
1585 ;IF ALL OK THEN THIS COMPUTER BECOMES MASTER AND GOES TO MTST1
1586 *****
1587 *****
1588 STST5:
1589 010260 004737 013576 1$: JSR PC,CPUHI ;NO INTRs ALLOWED YET
1590 010260 012737 001000 001444 MOV #1000,TIME ;DELAY ON MASTER
1591 010272 005337 001444 40$: DEC TIME
1592 010276 001375 BNE 40$
1593 010300 005077 171114 41$: CLR @CSR
1594 010304 022777 001200 171106 CMP #1200,@CSR ;ONLY DSTATC AND READY
1595 010312 001372 BNE 41$
1596 010314 004537 011160 39$: JSR R5,SETVEC ;SET UP THE INTR RETURN ADRS
1597 010320 010424 2$: ;RETURN TO 2$ ON DATO INTR
1598 010322 004537 011220 JSR R5,CLRBURF ;GO CLR 'DBUF'
1599 010326 177740 -32. ;DO 32. LOCATIONS
1600 010330 012737 040000 001444 MOV #40000,TIME ;SET UP A TIMER VALUE
1601 010336 013777 001500 171050 MOV RCVTMO,@WCR ;SET UP WORD COUNT
1602 010344 013777 001502 171044 MOV RCVTMO+2,@BAR ;SET UP BUFFER ADRS
1603 010352 004737 013624 JSR PC,CPULO ;ALLOW THE INTR
1604 010356 017737 171036 001446 MOV @CSR,TEMP
1605 010364 042737 100000 001446 BIC #BIT15,TEMP
1606 010372 052737 000002 001446 BIS #2,TEMP ;FNCT1
1607 010400 013777 001446 171012 MOV TEMP,@CSR
1608 010406 013777 001504 171004 MOV RCVTMO+4,@CSR ;SET UP CONTROL:IE,FNCT1 & GO
1609 ;TIMEOUT
1610 010414 005337 001444 25$: DEC TIME ;WAIT FOR MASTER TO SETUP;
1611 010420 001375 BNE 25$ ;THEN SLAVE BUS WILL BE HELD FOR DURATION
1612 ;OF BURST XFERS;TIMEOUT WILL OCCUR WITH SUBSEQUENT
1613 ;RELEASE OF BUS AND PROGRAM CONTINUATION
1614 010422 000416 BR 3$ ;GO TO CHECKS
1615 010424 017737 170770 001446 2$: MOV @CSR,TEMP
1616 010432 042737 100000 001446 BIC #BIT15,TEMP
1617 010440 042737 000100 001446 BIC #100,TEMP ;DISABLE IE
1618 010446 013777 001446 170744 MOV TEMP,@CSR
1619 010454 104017 104000+17
1620 010456 000471 BR 10$
1621 010460 017737 170734 001126 3$: MOV @CSR,$BDDAT ;READ STATUS
1622 010466 032737 000200 001126 BIT #BIT17,$BDDAT ;TEST READY BIT
1623 010474 001412 BEQ 5$
1624 010476 013737 001446 001124 MOV TEMP,$GDDAT
1625 010504 013737 001420 001122 MOV CSR,$BDADR
1626 010512 104401 015602 TYPE ,MSG3
1627 010516 104014 104000+14
1628 010520 000450 BR 10$
1629 010522 012777 100000 170670 5$: MOV #BIT15,@CSR ;GO TO EIR
1630 010530 017737 170664 001126 MOV @CSR,$BDDAT ;SAVE EIR
1631 010536 032737 001000 001126 BIT #BIT9,$BDDAT ;TEST BURST DATA LATE BIT

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```

1632 010544 001015
1633 010546 042737 000400 001126 BNE 4$
1634 010554 012737 101000 001124 BIC #BIT8,$BDDAT
1635 010562 013737 001420 001122 MOV #101000,$GDDAT
1636 010570 104401 015602 MOV CSR,$BDADR
1637 010574 104022 TYPE ,MSG3
1638 010576 000421 104000+22 BR 10$ ;GO RESYNC ON ERROR
1639 010600 012737 177773 001124 4$: MOV #177773,$GDDAT ;LD EXPECTED WC
1640 010606 017737 170602 001126 MOV @WCR,$BDDAT ;READ WCR
1641 010614 023737 001124 001126 CMP $GDDAT,$BDDAT ;CORRECT?
1642 010622 001432 BEQ 13$ ;BR IF SO
1643 010624 013737 001414 001122 MOV WCR,$BDADR ;SET UP WCR ADRS
1644 010632 104401 015602 TYPE ,MSG3
1645 010636 104011 104000+11 BR 10$ ;GO RESYNC ON ERROR
1646 010640 000400
1647
1648
1649
1650 ;ERROR IN SLAVE-WAIT FOR MASTER STATUS
1651
1652 010642 000005 10$: RESET ;IF ERROR,INIT
1653 010644 012777 000010 170546 MOV #10,@CSR
1654 010652 017737 170542 001450 15$: MOV @CSR,SAVE ;LOOP ON ERROR
1655 010660 042737 170777 001450 BIC #170777,SAVE ;BUT WAIT FOR MASTER
1656 010666 022737 005000 001450 CMP #5000,SAVE
1657 010674 001404 BEQ 24$
1658 010676 022737 004000 001450 16$: CMP #4000,SAVE
1659 010704 001362 BNE 15$
1660 010706 104412 24$: RSYNC ;RSYNC ON ERROR
1661
1662
1663 ;NO ERROR IN SLAVE-WAIT FOR MASTER STATUS
1664
1665
1666 010710 000005 13$: RESET ;SLAVE HAS TIMED OUT LEAVING
1667 ;READY CLEAR. FNCT BITS TO DSTAT
1668 ;BITS COMMUNICATION CAN BE
1669 ;RESUMED BY SETTING READY.RESET
1670 ;WILL SET READY.
1671 010712 012737 010000 001444 MOV #10000,TIME
1672 010720 005337 001444 11$: DEC TIME
1673 010724 001375 BNE 11$
1674 010726 012777 000012 170464 MOV #12,@CSR ;TELL MASTER ALL OK
1675 010734 017737 170460 001450 14$: MOV @CSR,SAVE ;READ CSR
1676 010742 042737 170777 001450 BIC #170777,SAVE ;SAVE 'STAT' BITS ONLY
1677 010750 022737 005000 001450 CMP #5000,SAVE
1678 010756 001405 BEQ EOPT ;BR IF MASTER DONE WITH NO ER'S
1679 010760 022737 004000 001450 CMP #4000,SAVE ;DOES MASTER HAVE AN ERROR?
1680 010766 001362 BNE 14$ ;BR IF NOT
1681 010770 104412 RSYNC ;RSYNC ON ERROR
1682 010772 004737 011200 EOPT: JSR PC,RSTVEC ;GO RESTORE VECTOR
1683 010776 104407 SLVFIN: CKSWR ;GO CHECK SWR
1684 011000 005737 001452 TST MSTER ;WERE WE STARTED AS MASTER?
1685 011004 001055 BNE EOPTA ;BR IF NOT
1686 011006 005337 001454 DEC ICOUNT ;COUNT TEST PASS
1687 011012 001052 BNE EOPTA ;BR IF NOT DUE FOR 'END PASS' MSG
1688 011014 012737 000144 001454 MOV #144,ICOUNT ;RESET PASS COUNT TO 100 ITERATIONS

```


1690

.SBTTL END OF PASS ROUTINE

:*****

:*INCREMENT THE PASS NUMBER (\$PASS)

:*TYPE "END PASS #XXXXX" (WHERE XXXXX IS A DECIMAL NUMBER)

:*IF THERES A MONITOR GO TO IT

:*IF THERE ISN'T JUMP TO EOPTA

\$EOP:

011022	000240			NOP			
011022	005037	001102		CLR	\$TSTNM	::ZERO THE TEST NUMBER	
011024	005237	001100		INC	\$PASS	::INCREMENT THE PASS NUMBER	
011030	042737	100000	001100	BIC	#100000,\$PASS	::DON'T ALLOW A NEG. NUMBER	
011042	005327			DEC	(PC)+	::LOOP?	
011044	000001			\$EOPCT: .WORD	1		
011046	003022			BGT	\$DOAGN	::YES	
011050	012737			MOV	(PC)+,@(PC)+	::RESTORE COUNTER	
011052	000001			\$ENDCT: .WORD	1		
011054	011044			\$EOPCT			
011056	104401	011123		TYPE	,\$ENDMG	::TYPE 'END PASS #'	
011062	013746	001100		MOV	\$PASS,-(SP)	::SAVE \$PASS FOR TYPEOUT	
011066	104405			TYPDS		::GO TYPE--DECIMAL ASCII WITH SIGN	
011070	104401	011120		TYPE	,\$ENULL	::TYPE A NULL CHARACTER	
011074	013700	000042		\$GET42: MOV	@#42,R0	::GET MONITOR ADDRESS	
011100	001405			BEQ	\$DOAGN	::BRANCH IF NO MONITOR	
011102	000005			RESET		::CLEAR THE WORLD	
011104	004710			\$ENDAD: JSR	PC,(R0)	::GO TO MONITOR	
011106	000240			NOP		::SAVE ROOM	
011110	000240			NOP		::FOR	
011112	000240			NOP		::ACT11	
011114				\$DOAGN:			
011114	000137			JMP	@(PC)+	::RETURN	
011116	011140			\$RTNAD: .WORD	EOPTA		
011120	377	377	000	\$ENULL: .BYTE	-1,-1,0	::NULL CHARACTER STRING	
011123	015	012	105	\$ENDMG: .ASCIZ	<15><12>/END PASS #/		
011126	116	104	040				
011131	120	101	123				
011134	123	040	043				
011137	000						
1691	011140	012737	001000	001444	EOPTA: MOV	#1000,TIME	:SET UP A COUNT
1692	011146	005337	001444		EOPTB: DEC	TIME	:STALL FOR OTHER CPU TO BECOME SLAVE
1693	011152	001375			BNE	EOPTB	:UNTIL TIME=0
1694	011154	000137	002760		JMP	MINI11	:NOW BECOME MASTER

```
1696      .SBTTL
1697      .SBTTL  PROGRAM SUBROUTINES
1698
1699      ;*****
1700      ;THIS ROUTINE SETS THE PRIORITY LEVEL FOR NO INTERRUPT -
1701      ;SETS UP THE INTERRUPT TO RETURN ON INTERRUPT
1702      ;TO THE ADDRESS INDICATED ((R5)) BY THE CALL +2
1703      ;*****
1704 011160 004737 013576      SETVEC: JSR      PC,CPUHI      ;SET UP FOR NO INTERRUPT
1705 011164 012577 170234      MOV      (R5)+,@DRVCT0    ;SET UP INTR RETURN ADRS
1706 011170 012777 000200 170230      MOV      #200,@DRVCT2    ;KEEP PRIORITY LEVEL AT TOP ON INTR
1707 011176 000205      RTS      R5      ;EXIT
1708
1709      ;*****
1710      ;THIS ROUTINE RESTORES THE INTERRUPT VECTOR TO A HALT
1711      ;AND RAISES THE PRIORITY LEVEL
1712      ;*****
1713 011200 013777 001426 170216      RSTVEC: MOV      DRVCT2,@DRVCT0    ;POINT VECTOR TO HALT
1714 011206 005077 170214      CLR      @DRVCT2      ;SET UP HALT
1715 011212 004737 013576      JSR      PC,CPUHI      ;RAISE PRIORITY LEVEL
1716 011216 000207      RTS      PC      ;EXIT
1717
1718      ;*****
1719      ;THIS ROUTINE CLEARS THE 'DBUF' BEFORE A 'DATI' XFER
1720      ;THE # OF LOCATIONS IN 'DBUF' TO BE CLEARED IS SPECIFIED BY
1721      ;THE VALUE IN THE CALL +2 - WHEN ALL CLEARED THE RETURN IS TO
1722      ;THE CALL +4
1723      ;*****
1724 011220 012500      CLRBUF: MOV      (R5)+,R0      ;GET THE LOC COUNT
1725 011222 012701 015756      MOV      #DBUF,R1      ;GET 1ST ADRS
1726 011226 005021      1$:      CLR      (R1)+      ;CLR MEM LOC
1727 011230 005200      INC      R0      ;COUNT LOC
1728 011232 001375      BNE      1$      ;UNTIL ALL DONE
1729 011234 000205      RTS      R5      ;EXIT
1730
```



```
1732  
1733  
1734  
1735  
1736  
1737 011236 012500  
1738 011240 012701 015756  
1739 011244 012702 177776  
1740 011250 005003  
1741 011252 010221  
1742 011254 005200  
1743 011256 001001  
1744 011260 000205  
1745 011262 005102  
1746 011264 005103  
1747 011266 001371  
1748 011270 006302  
1749 011272 005202  
1750 011274 103363  
1751 011276 000765
```

```
*****  
:THIS ROUTINE LOADS 'DBUF' WITH A FLOATING ZERO/ONE PATTERN  
:THE # OF LOCATIONS IN 'DBUF' TO BE LOADED IS SPECIFIED BY  
:THE VALUE IN THE CALL +2 - WHEN ALL LOADED THE RETURN IS TO CALL +4  
*****  
LDBUF: MOV      (R5)+,R0      ;GET LOC COUNT  
        MOV      #DBUF,R1    ;GET 1ST ADRS  
1$:     MOV      #177776,R2   ;SET UP FLOATING ZERO PTRN  
        CLR      R3          ;R3 SAYS WHEN TO SHIFT PTRN  
2$:     MOV      R2,(R1)+     ;LOAD MEM WITH PTRN  
        INC      R0          ;COUNT LOC  
        BNE      3$          ;BR IF MORE  
        RTS      R5          ;EXIT  
3$:     COM      R2          ;CONVERT PTRN TO FLOATING 1  
        COM      R3          ;SHOULD WE SHIFT?  
        BNE      2$          ;BR IF NOT - THIS WILL BE A FLOATING 1  
        ASL      R2          ;FLOAT ZERO LEFT  
        INC      R2          ;KEEP LSB SET  
        BCC      1$          ;GO RESET FLOATING PATRN  
        BR       2$          ;GO LOAD NEXT PATRN
```

```
.SBTTL  TYPE ROUTINE
```

```

;*****
;ROUTINE TO TYPE ASCII MESSAGE. MESSAGE MUST TERMINATE WITH A 0 BYTE.
;THE ROUTINE WILL INSERT A NUMBER OF NULL CHARACTERS AFTER A LINE FEED.
;NOTE1:          $NULL CONTAINS THE CHARACTER TO BE USED AS THE FILLER CHARACTER.
;NOTE2:          $FILLS CONTAINS THE NUMBER OF FILLER CHARACTERS REQUIRED.
;NOTE3:          $FILLC CONTAINS THE CHARACTER TO FILL AFTER.

```

```

;*CALL:
;*1) USING A TRAP INSTRUCTION
;*      TYPE      ,MESADR      ;;MESADR IS FIRST ADDRESS OF AN ASCIZ STRING

```

: *OR
:
:
:
:

TYPE
TYPE
MESADR

Address	Hex	Dec	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op41
---------	-----	-----	-------	----	-----	-----	-----	-----	-----	-----	-----	-----	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	------

011456	116677	000002	167466	MOVB	2(SP),@STPB	::LOAD CHAR TO BE TYPED INTO DATA REG.
011464	122766	000015	000002	CMPB	#CR,2(SP)	::IS CHARACTER A CARRIAGE RETURN?
011472	001003			BNE	1\$	::BRANCH IF NO
011474	105037	011514		CLRB	\$CHARCNT	::YES--CLEAR CHARACTER COUNT
011500	000406			BR	\$TYPEX	::EXIT
011502	122766	000012	000002	1\$: CMPB	#LF,2(SP)	::IS CHARACTER A LINE FEED?
011510	001402			BEQ	\$TYPEX	::BRANCH IF YES
011512	105227			INCB	(PC)+	::COUNT THE CHARACTER
011514	000000			\$CHARCNT: .WORD	0	::CHARACTER COUNT STORAGE
011516	000207			\$TYPEX: RTS	PC	

1757

```
.SBTTL BINARY TO OCTAL (ASCII) AND TYPE
*****
*THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 6-DIGIT
*OCTAL (ASCII) NUMBER AND TYPE IT.
*$TYPOS---ENTER HERE TO SETUP SUPPRESS ZEROS AND NUMBER OF DIGITS TO TYPE
*CALL:
*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
*      TYPOS     ;;CALL FOR TYPEOUT
*      .BYTE    N              ;;N=1 TO 6 FOR NUMBER OF DIGITS TO TYPE
*      .BYTE    M              ;;M=1 OR 0
*                               ;;1=TYPE LEADING ZEROS
*                               ;;0=SUPPRESS LEADING ZEROS
*$TYPON----ENTER HERE TO TYPE OUT WITH THE SAME PARAMETERS AS THE LAST
*$TYPOS OR $TYPOC
*CALL:
*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
*      TYPON     ;;CALL FOR TYPEOUT
*$TYPOC---ENTER HERE FOR TYPEOUT OF A 16 BIT NUMBER
*CALL:
*      MOV      NUM,-(SP)      ;;NUMBER TO BE TYPED
*      TYPOC     ;;CALL FOR TYPEOUT
011520 017646 000000      011743 $TYPOS: MOV      @ (SP),-(SP)      ;;PICKUP THE MODE
011524 116637 000001      MOV      1(SP), $OFILL      ;;LOAD ZERO FILL SWITCH
011532 112637 011745      MOV      (SP)+, $OMODE+1      ;;NUMBER OF DIGITS TO TYPE
011536 062716 000002      ADD      #2, (SP)            ;;ADJUST RETURN ADDRESS
011542 000406      BR          $TYPON
011544 112737 000001      011743 $TYPOC: MOV      #1, $OFILL      ;;SET THE ZERO FILL SWITCH
011552 112737 000006      011745      MOV      #6, $OMODE+1      ;;SET FOR SIX(6) DIGITS
011560 112737 000005      011742 $TYPON: MOV      #5, $OCNT      ;;SET THE ITERATION COUNT
011566 010346      MCV      R3, -(SP)                ;;SAVE R3
011570 010446      MOV      R4, -(SP)                ;;SAVE R4
011572 010546      MOV      R5, -(SP)                ;;SAVE R5
011574 113704 011745      MOV      $OMODE+1, R4        ;;GET THE NUMBER OF DIGITS TO TYPE
011600 005404      NEG      R4
011602 062704 000006      ADD      #6, R4              ;;SUBTRACT IT FOR MAX. ALLOWED
011606 110437 011744      MOV      R4, $OMODE          ;;SAVE IT FOR USE
011612 113704 011743      MOV      $OFILL, R4          ;;GET THE ZERO FILL SWITCH
011616 016605 000012      MOV      12(SP), R5          ;;PICKUP THE INPUT NUMBER
011622 005003      CLR      R3                        ;;CLEAR THE OUTPUT WORD
011624 006105      1$:      ROL      R5                ;;ROTATE MSB INTO 'C'
011626 000404      BR          3$                      ;;GO DO MSB
011630 006105      2$:      ROL      R5                ;;FORM THIS DIGIT
011632 006105      ROL      R5
011634 006105      ROL      R5
011636 010503      MOV      R5, R3
011640 006103      3$:      ROL      R3                ;;GET LSB OF THIS DIGIT
011642 105337 011744      DEC      $OMODE              ;;TYPE THIS DIGIT?
011646 100016      BPL      7$                          ;;BR IF NO
011650 042703 177770      BIC      #177770, R3          ;;GET RID OF JUNK
011654 001002      BNE      4$                          ;;TEST FOR 0
011656 005704      TST      R4                        ;;SUPPRESS THIS 0?
011660 001403      BEQ      5$                          ;;BR IF YES
011662 005204      4$:      INC      R4                ;;DON'T SUPPRESS ANYMORE 0'S
011664 052703 000060      BIS      #'0, R3              ;;MAKE THIS DIGIT ASCII
011670 052703 000040      5$:      BIS      #' , R3        ;;MAKE ASCII IF NOT ALREADY
```


011674	110337	011740		MOVB	R3,8\$	::SAVE FOR TYPING
011700	104401	011740		TYPE	,8\$	::GO TYPE THIS DIGIT
011704	105337	011742	7\$:	DECB	\$OCNT	::COUNT BY 1
011710	003347			BGT	2\$	::BR IF MORE TO DO
011712	002402			BLT	6\$	::BR IF DONE
011714	005204			INC	R4	::INSURE LAST DIGIT ISN'T A BLANK
011716	000744			BR	2\$	::GO DO THE LAST DIGIT
011720	012605		6\$:	MOV	(SP)+,R5	::RESTORE R5
011722	012604			MOV	(SP)+,R4	::RESTORE R4
011724	012603			MOV	(SP)+,R3	::RESTORE R3
011726	016666	000002 000004		MOV	2(SP),4(SP)	::SET THE STACK FOR RETURNING
011734	012616			MOV	(SP)+,(SP)	
011736	000002			RTI		::RETURN
011740	000		8\$:	.BYTE	0	::STORAGE FOR ASCII DIGIT
011741	000			.BYTE	0	::TERMINATOR FOR TYPE ROUTINE
011742	000		\$OCNT:	.BYTE	0	::OCTAL DIGIT COUNTER
011743	000		\$OFILL:	.BYTE	0	::ZERO FILL SWITCH
011744	000000		\$OMODE:	.WORD	0	::NUMBER OF DIGITS TO TYPE

1759

```

.SBTTL CONVERT BINARY TO DECIMAL AND TYPE ROUTINE
;*****
;THIS ROUTINE IS USED TO CHANGE A 16-BIT BINARY NUMBER TO A 5-DIGIT
;SIGNED DECIMAL (ASCII) NUMBER AND TYPE IT. DEPENDING ON WHETHER THE
;NUMBER IS POSITIVE OR NEGATIVE A SPACE OR A MINUS SIGN WILL BE TYPED
;BEFORE THE FIRST DIGIT OF THE NUMBER. LEADING ZEROS WILL ALWAYS BE
;REPLACED WITH SPACES.
;CALL:
;*      MOV      NUM,-(SP)      ;;PUT THE BINARY NUMBER ON THE STACK
;*      TYPDS    ;;GO TO THE ROUTINE
$TYPDS:
      MOV      R0,-(SP)      ;;PUSH R0 ON STACK
      MOV      R1,-(SP)      ;;PUSH R1 ON STACK
      MOV      R2,-(SP)      ;;PUSH R2 ON STACK
      MOV      R3,-(SP)      ;;PUSH R3 ON STACK
      MOV      R5,-(SP)      ;;PUSH R5 ON STACK
      MOV      #20200,-(SP)   ;;SET BLANK SWITCH AND SIGN
      MOV      20(SP),R5      ;;GET THE INPUT NUMBER
      BPL      1$            ;;BR IF INPUT IS POS.
      NEG      R5            ;;MAKE THE BINARY NUMBER POS.
      MOVB     #'-,1(SP)     ;;MAKE THE ASCII NUMBER NEG.
      CLR      R0            ;;ZERO THE CONSTANTS INDEX
      MOV      #SDBLK,R3     ;;SETUP THE OUTPUT POINTER
      MOVB     #'',(R3)+     ;;SET THE FIRST CHARACTER TO A BLANK
      CLR      R2            ;;CLEAR THE BCD NUMBER
      MOV      $DTBL(R0),R1   ;;GET THE CONSTANT
      SUB      R1,R5         ;;FORM THIS BCD DIGIT
      BLT      4$            ;;BR IF DONE
      INC      R2            ;;INCREASE THE BCD DIGIT BY 1
      BR       3$
      ADD      R1,R5         ;;ADD BACK THE CONSTANT
      TST      R2            ;;CHECK IF BCD DIGIT=0
      BNE      5$            ;;FALL THROUGH IF 0
      TSTB     (SP)          ;;STILL DOING LEADING 0'S?
      BMI      7$            ;;BR IF YES
      ASLB     (SP)          ;;MSD?
      BCC      6$            ;;BR IF NO
      MOVB     1(SP),-1(R3)   ;;YES--SET THE SIGN
      BIS      #'0,R2        ;;MAKE THE BCD DIGIT ASCII
      BIS      #' ,R2        ;;MAKE IT A SPACE IF NOT ALREADY A DIGIT
      MOVB     R2,(R3)+      ;;PUT THIS CHARACTER IN THE OUTPUT BUFFER
      TST      (R0)+         ;;JUST INCREMENTING
      CMP      R0,#10        ;;CHECK THE TABLE INDEX
      BLT      2$            ;;GO DO THE NEXT DIGIT
      BGT      8$            ;;GO TO EXIT
      MOV      R5,R2         ;;GET THE LSD
      BR       6$            ;;GO CHANGE TO ASCII
      TSTB     (SP)+         ;;WAS THE LSD THE FIRST NON-ZERO?
      BPL      9$            ;;BR IF NO
      MOVB     -1(SP),-2(R3)  ;;YES--SET THE SIGN FOR TYPING
      CLRB     (R3)          ;;SET THE TERMINATOR
      MOV      (SP)+,R5      ;;POP STACK INTO R5
      MOV      (SP)+,R3      ;;POP STACK INTO R3
      MOV      (SP)+,R2      ;;POP STACK INTO R2
      MOV      (SP)+,R1      ;;POP STACK INTO R1
      MOV      (SP)+,R0      ;;POP STACK INTO R0
      TYPE     ,SDBLK        ;;NOW TYPE THE NUMBER
  
```

011746	010046				
011750	010146				
011752	010246				
011754	010346				
011756	010546				
011760	012746	020200			
011764	016605	000020			
011770	100004				
011772	005405				
011774	112766	000055	000001		
012002	005000			1\$:	
012004	012703	012162			
012010	112723	000040			
012014	005002			2\$:	
012016	016001	012152			
012022	160105			3\$:	
012024	002402				
012026	005202				
012030	000774				
012032	060105			4\$:	
012034	005702				
012036	001002				
012040	105716				
012042	100407				
012044	106316			5\$:	
012046	103003				
012050	116663	000001	177777		
012056	052702	000060		6\$:	
012062	052702	000040		7\$:	
012066	110223				
012070	005720				
012072	020027	000010			
012076	002746				
012100	003002				
012102	010502				
012104	000764				
012106	105726			8\$:	
012110	100003				
012112	116663	177777	177776		
012120	105013			9\$:	
012122	012605				
012124	012603				
012126	012602				
012130	012601				
012132	012600				
012134	104401	012162			

012140	016666	000002	000004	MOV	2(SP),4(SP)	::ADJUST THE STACK
012146	012616			MOV	(SP)+,(SP)	
012150	000002			RTI		::RETURN TO USER
012152	023420	\$DTBL:		10000.		
012154	001750			1000.		
012156	000144			100.		
012160	000012			10.		
012162		\$DBLK:		.BLKW	4	

1761

.SBTTL ERROR HANDLER ROUTINE

:*****

: *THIS ROUTINE WILL INCREMENT THE ERROR FLAG AND THE ERROR COUNT,

: *SAVE THE ERROR ITEM NUMBER AND THE ADDRESS OF THE ERROR CALL

: *AND GO TO SWRCK ON ERROR

: *THE SWITCH OPTIONS PROVIDED BY THIS ROUTINE ARE:

: *SW15=1 HALT ON ERROR

: *SW13=1 INHIBIT ERROR TYPEOUTS

: *SW10=1 BELL ON ERROR

: *CALL

: * ERROR N ;;ERROR=EMT AND N=ERROR ITEM NUMBER

\$ERROR:

CKSWR

;;TEST FOR CHANGE IN SOFT-SWR

CKSWR

;GO LOOK FOR SWR CHANGE

7\$:

INCB

\$ERFLG

;;SET THE ERROR FLAG

BEQ

7\$

;;DON'T LET THE FLAG GO TO ZERO

1\$:

MOV

\$TSTNM,@DISPLAY

;;DISPLAY TEST NUMBER AND ERROR FLAG

BIT

#BIT10,@SWR

;;BELL ON ERROR?

BEQ

1\$

;;NO - SKIP

TYPE

,\$BELL

;;RING BELL

INC

\$ERTTL

;;COUNT THE NUMBER OF ERRORS

MOV

(SP),\$ERRPC

;;GET ADDRESS OF ERROR INSTRUCTION

SUB

#2,\$ERRPC

MOVB

@\$ERRPC,\$ITEMB

;;STRIP AND SAVE THE ERROR ITEM CODE

BIT

#BIT13,@SWR

;;SKIP TYPEOUT IF SET

BNE

20\$

;;SKIP TYPEOUTS

JSR

PC,SWRCK

;;GO TO USER ERROR ROUTINE

TYPE

,\$CRLF

20\$:

2\$:

TST

@SWR

;;HALT ON ERROR

BPL

3\$

;;SKIP IF CONTINUE

HALT

;;HALT ON ERROR!

CKSWR

;;TEST FOR CHANGE IN SOFT-SWR

3\$:

RTI

;;RETURN

:*****

:*****

:GO TYPE ERROR

:GO UPDATE SOFTWARE SWR IF 'CNTRL/G'

:*****

:*****

SWRCK: JSR PC,\$ERRTYP

:GO TYPE ERROR

CKSWR

:GO LOOK FOR SWR CHANGE

RTS

PC

:RETURN TO ERROR HANDLER

012172
012172 104407
012174 104407
012176 105237 001103
012202 001775
012204 013777 001102 166730
012212 032777 002000 166720
012220 001402
012222 104401 001160
012226 005237 001112
012232 011637 001116
012236 102737 000002 001116
012244 117737 166646 001114
012252 032777 020000 166660
012260 001004
012262 004737 012306
012266 104401 001165
012272
012272 005777 166642
012276 100002
012300 000000
012302 104407
012304
012304 000002

1762

1763

1764

1765

1766 012306 004737 012316

1767 012312 104407

1768 012314 000207

1770

.SBTTL ERROR MESSAGE TYPEOUT ROUTINE

*THIS ROUTINE USES THE "ITEM CONTROL BYTE" (\$ITEMB) TO DETERMINE WHICH
*ERROR IS TO BE REPORTED. IT THEN OBTAINS, FROM THE "ERROR TABLE" (\$ERRTB),
*AND REPORTS THE APPROPRIATE INFORMATION CONCERNING THE ERROR.

\$ERRTYP:

012316			TYPE	,\$CRLF	:: "CARRIAGE RETURN" & "LINE FEED"
012316	104401	001165	MOV	RO,-(SP)	:: SAVE RO
012322	010046		CLR	RO	:: PICKUP THE ITEM INDEX
012324	005000		BISB	@#\$ITEMB,RO	
012326	153700	001114	BNE	1\$	:: IF ITEM NUMBER IS ZERO, JUST
012332	001004				:: TYPE THE PC OF THE ERROR
012334	013746	001116	MOV	\$ERRPC,-(SP)	:: SAVE \$ERRPC FOR TYPEOUT
					:: ERROR ADDRESS
012340	104402		TYPOC		:: GO TYPE--OCTAL ASCII(ALL DIGITS)
012342	000426		BR	6\$	:: GET OUT
012344	005300		1\$: DEC	RO	:: ADJUST THE INDEX SO THAT IT WILL
012346	006300		ASL	RO	:: WORK FOR THE ERROR TABLE
012350	006300		ASL	RO	
012352	006300		ASL	RO	
012354	062700	001170	ADD	#\$ERRTB,RO	:: FORM TABLE POINTER
012360	012037	012370	MOV	(RO)+,2\$	:: PICKUP "ERROR MESSAGE" POINTER
012364	001404		BEQ	3\$	:: SKIP TYPEOUT IF NO POINTER
012366	104401		TYPE		:: TYPE THE "ERROR MESSAGE"
012370	000000		2\$: .WORD	0	:: "ERROR MESSAGE" POINTER GOES HERE
012372	104401	001165	TYPE	,\$CRLF	:: "CARRIAGE RETURN" & "LINE FEED"
012376	012037	012406	3\$: MOV	(RO)+,4\$	:: PICKUP "DATA HEADER" POINTER
012402	001404		BEQ	5\$	:: SKIP TYPEOUT IF 0
012404	104401		TYPE		:: TYPE THE "DATA HEADER"
012406	000000		4\$: .WORD	0	:: "DATA HEADER" POINTER GOES HERE
012410	104401	001165	TYPE	,\$CRLF	:: "CARRIAGE RETURN" & "LINE FEED"
012414	011000		5\$: MOV	(RO),RO	:: PICKUP "DATA TABLE" POINTER
012416	001004		BNE	7\$	:: GO TYPE THE DATA
012420	012600		6\$: MOV	(SP)+,RO	:: RESTORE RO
012422	104401	001165	TYPE	,\$CRLF	:: "CARRIAGE RETURN" & "LINE FEED"
012426	000207		RTS	PC	:: RETURN
012430			7\$:		
012430	013046		MOV	@(RO)+,-(SP)	:: SAVE @(RO)+ FOR TYPEOUT
012432	104402		TYPOC		:: GC TYPE--OCTAL ASCII(ALL DIGITS)
012434	005710		TST	(RO)	:: IS THERE ANOTHER NUMBER?
012436	001770		BEQ	6\$	:: BR IF NO
012440	104401	012446	TYPE	,8\$	:: TYPE TWO(2) SPACES
012444	000771		BR	7\$	:: LOOP
012446	040	040	8\$: .ASCII	/ /	:: TWO(2) SPACES
			.EVEN		

1772

```
.SBTTL SCOPE HANDLER ROUTINE
;*****
;THIS ROUTINE CONTROLS THE LOOPING OF SUBTESTS. IT WILL INCREMENT
;*AND LOAD THE TEST NUMBER($TSTNM) INTO THE DISPLAY REG.(DISPLAY<7:0>)
;*AND LOAD THE ERROR FLAG ($ERFLG) INTO DISPLAY<15:08>
;*CALL
;*
;SCOPE                                ;;SCOPE=10T
$SCOPE:
        CKSWR                        ;;TEST FOR CHANGE IN SOFT-SWR
        CKSWR                        ;;GO LOOK FOR SWR CHANGE
;*****START OF CODE FOR THE XOR TESTER*****
$XTSTR: BR 6$                        ;;IF RUNNING ON THE "XOR" TESTER CHANGE
                                        ;;THIS INSTRUCTION TO A "NOP" (NOP=240)
                                        ;;SAVE THE CONTENTS OF THE ERROR VECTOR
        MOV @#ERRVEC,-(SP)           ;;SET FOR TIMEOUT
        MOV #5$,@#ERRVEC           ;;TIME OUT ON XOR?
        TST @#177060               ;;RESTORE THE ERROR VECTOR
        MOV (SP)+,@#ERRVEC          ;;GO TO THE NEXT TEST
        BR $SVLAD                  ;;CLEAR THE STACK AFTER A TIME OUT
5$:     CMP (SP)+,(SP)+             ;;RESTORE THE ERROR VECTOR
        MOV (SP)+,@#ERRVEC          ;;LOOP ON THE PRESENT TEST
        BR $OVER                   ;;*****END OF CODE FOR THE XOR TESTER*****
6$:     $SVLAD: INCB $TSTNM          ;;COUNT TEST NUMBERS
        MOV (SP),$LPADR             ;;SAVE SCOPE LOOP ADDRESS
        CLRB $ERFLG                 ;;ZERO THE ERROR FLAG
166404 $OVER: MOV $TSTNM,@DISPLAY    ;;DISPLAY TEST NUMBER
        MOV $LPADR,(SP)             ;;FUDGE RETURN ADDRESS
        RTI                         ;;FIXES PS
```


1774

```
.SBTTL  TTY INPUT ROUTINE
*****
.ENABL  LSB
*****
*SOFTWARE SWITCH REGISTER CHANGE ROUTINE.
*ROUTINE IS ENTERED FROM THE TRAP HANDLER, AND WILL
*SERVICE THE TEST FOR CHANGE IN SOFTWARE SWITCH REGISTER TRAP CALL
*WHEN OPERATING IN TTY FLAG MODE.

012544 022737 000176 001140 $CKSWR: CMP    #SWREG,SWR    ;;IS THE SOFT-SWR SELECTED?
012552 001074                BNE     15$           ;;BRANCH IF NO
012554 105777 166364        TSTB   @STKS          ;;CHAR THERE?
012560 100071                BPL     15$           ;;IF NO, DON'T WAIT AROUND
012562 117746 166360        MOVB   @STKB,-(SP)      ;;SAVE THE CHAR
012566 042716 177600        BIC     #^C177,(SP)    ;;STRIP-OFF THE ASCII
012572 022726 000007        CMP     #7,(SP)+      ;;IS IT A CONTROL G?
012576 001062                BNE     15$           ;;NO, RETURN TO USER
012600 123727 001134 000001 CMPB   $AUTOB,#1      ;;ARE WE RUNNING IN AUTO-MODE?
012606 001456                BEQ     15$           ;;BRANCH IF YES
012610 104401 013271        TYPE   , $CNTLG       ;;ECHO THE CONTROL-G (^G)
012614 104401 013276        $GTSWR: TYPE , $MSWR   ;;TYPE CURRENT CONTENTS
012620 013746 000176        MOV     SWREG,-(SP)    ;;SAVE SWREG FOR TYPEOUT
012624 104402                TYPOC              ;;GO TYPE--OCTAL ASCII(ALL DIGITS)
012626 104401 013307        TYPE   , $MNEW        ;;PROMPT FOR NEW SWR
012632 005046 19$:         CLR     -(SP)          ;;CLEAR COUNTER
012634 005046                CLR     -(SP)          ;;THE NEW SWR
012636 105777 166302 7$:   TSTB   @STKS          ;;CHAR THERE?
012642 100375                BPL     7$           ;;IF NOT TRY AGAIN
012644 117746 166276        MOVB   @STKB,-(SP)    ;;PICK UP CHAR
012650 042716 177600        BIC     #^C177,(SP)    ;;MAKE IT 7-BIT ASCII
012654 021627 000025 9$:   CMP     (SP),#25        ;;IS IT A CONTROL-U?
012660 001005                BNE     10$          ;;BRANCH IF NOT
012662 104401 013264        TYPE   , $CNTLU       ;;YES, ECHO CONTROL-U (^U)
012666 062706 000006 20$:  ADD     #6,SP          ;;IGNORE PREVIOUS INPUT
012672 000757                BR      19$          ;;LET'S TRY IT AGAIN
012674 021627 000015 10$:  CMP     (SP),#15        ;;IS IT A <CR>?
012700 001022                BNE     16$          ;;BRANCH IF NO
012702 005766 000004        TST     4(SP)         ;;YES, IS IT THE FIRST CHAR?
012706 001403                BEQ     11$          ;;BRANCH IF YES
012710 016677 000002 166222 MOV     2(SP),@SWR      ;;SAVE NEW SWR
012716 062706 000006 11$:  ADD     #6,SP          ;;CLEAR UP STACK
012722 104401 001165 14$:  TYPE   , $CRLF        ;;ECHO <CR> AND <LF>
012726 123727 001135 000001 CMPB   $INTAG,#1     ;;RE-ENABLE TTY KBD INTERRUPTS?
012734 001003                BNE     15$          ;;BRANCH IF NOT
012736 012777 000100 166200 MOV     #100,@STKS    ;;RE-ENABLE TTY KBD INTERRUPTS
012744 000002 15$:         RTI                    ;;RETURN
012746 004737 011450 16$:  JSR     PC,$TYPEPC     ;;ECHO CHAR
012752 021627 000060        CMP     (SP),#60      ;;CHAR < 0?
012756 002420                BLT     18$          ;;BRANCH IF YES
012760 021627 000067        CMP     (SP),#67      ;;CHAR > 7?
012764 003015                BGT     18$          ;;BRANCH IF YES
012766 042726 000060        BIC     #60,(SP)+     ;;STRIP-OFF ASCII
012772 005766 000002        TST     2(SP)         ;;IS THIS THE FIRST CHAR
012776 001403                BEQ     17$          ;;BRANCH IF YES
013000 006316                ASL     (SP)         ;;NO, SHIFT PRESENT
013002 006316                ASL     (SP)         ;;CHAR OVER TO MAKE
013004 006316                ASL     (SP)         ;;ROOM FOR NEW ONE.
013006 005266 000002 17$:  INC     2(SP)         ;;KEEP COUNT OF CHAR
```

```
013012 056616 177776      BIS      -2(SP),(SP)      ;;SET IN NEW CHAR
013016 000707              BR      7$              ;;GET THE NEXT ONE
013020 104401 001164      18$:  TYPE      ,SQUES      ;;TYPE ?<CR><LF>
013024 000720              BR      20$              ;;SIMULATE CONTROL-U
                        .DSABL  LSB
                        ;*****
                        ;*THIS ROUTINE WILL INPUT A SINGLE CHARACTER FROM THE TTY
                        ;*CALL:
                        ;*      RDCHR              ;;INPUT A SINGLE CHARACTER FROM THE TTY
                        ;*      RETURN HERE        ;;CHARACTER IS ON THE STACK
                        ;*                      ;;WITH PARITY BIT STRIPPED OFF
013026 011646      $RDCHR: MOV      (SP),-(SP)      ;;PUSH DOWN THE PC
013030 016666 000004 000002 MOV      4(SP),2(SP)      ;;SAVE THE PS
013036 105777 166102      1$:  TSTB      @STKS      ;;WAIT FOR
013042 100375              BPL      1$              ;;A CHARACTER
013044 117766 166076 000004 MOVB      @STKB,4(SP)      ;;READ THE TTY
013052 042766 177600 000004 BIC      #^C<177>,4(SP)      ;;GET RID OF JUNK IF ANY
013060 026627 000004 000023 CMP      4(SP),#23      ;;IS IT A CONTROL-S?
013066 001013              BNE      3$              ;;BRANCH IF NO
013070 105777 166050      2$:  TSTB      @STKS      ;;WAIT FOR A CHARACTER
013074 100375              BPL      2$              ;;LOOP UNTIL ITS THERE
013076 117746 166044      MOVB      @STKB,-(SP)      ;;GET CHARACTER
013102 042716 177600      BIC      #^C177,(SP)      ;;MAKE IT 7-BIT ASCII
013106 022627 000021      CMP      (SP)+,#21      ;;IS IT A CONTROL-Q?
013112 001366              BNE      2$              ;;IF NOT DISCARD IT
013114 000750              BR      1$              ;;YES, RESUME
013116 026627 000004 000140      3$:  CMP      4(SP),#140      ;;IS IT UPPER CASE?
013124 002407              BLT      4$              ;;BRANCH IF YES
013126 026627 000004 000175      CMP      4(SP),#175      ;;IS IT A SPECIAL CHAR?
013134 003003              BGT      4$              ;;BRANCH IF YES
013136 042766 000040 000004      BIC      #40,4(SP)      ;;MAKE IT UPPER CASE
013144 000002      4$:  RTI              ;;GO BACK TO USER
                        ;*****
                        ;*THIS ROUTINE WILL INPUT A STRING FROM THE TTY
                        ;*CALL:
                        ;*      RDLIN              ;;INPUT A STRING FROM THE TTY
                        ;*      RETURN HERE        ;;ADDRESS OF FIRST CHARACTER WILL BE ON THE STACK
                        ;*                      ;;TERMINATOR WILL BE A BYTE OF ALL 0'S
013146 010346      $RDLIN: MOV      R3,-(SP)      ;;SAVE R3
013150 012703 013254      1$:  MOV      #STYIN,R3      ;;GET ADDRESS
013154 022703 013264      2$:  CMP      #STYIN+8.,R3      ;;BUFFER FULL?
013160 101405              BLOS      4$              ;;BR IF YES
013162 104410      RDCHR      ;;GO READ ONE CHARACTER FROM THE TTY
013164 112613      MOVB      (SP)+,(R3)      ;;GET CHARACTER
013166 122713 000177      10$: CMPB      #177,(R3)      ;;IS IT A RUBOUT
013172 001003              BNE      3$              ;;SKIP IF NOT
013174 104401 001164      4$:  TYPE      ,SQUES      ;;TYPE A '?'
013200 000763              BR      1$              ;;CLEAR THE BUFFER AND LOOP
013202 111337 013252      3$:  MOVB      (R3),9$      ;;ECHO THE CHARACTER
013206 104401 013252      TYPE      ,9$
013212 122723 000015      CMPB      #15,(R3)+      ;;CHECK FOR RETURN
013216 001356              BNE      2$              ;;LOOP IF NOT RETURN
013220 105063 177777      CLRB      -1(R3)      ;;CLEAR RETURN (THE 15)
013224 104401 001166      TYPE      ,LF          ;;TYPE A LINE FEED
013230 012603      MOV      (SP)+,R3      ;;RESTORE R3
013232 011646      MOV      (SP),-(SP)      ;;ADJUST THE STACK AND PUT ADDRESS OF THE
```


013234	016666	000004	000002	MOV	4(SP),2(SP)	::	FIRST ASCII CHARACTER ON IT
013242	012766	013254	000004	MOV	#\$TTYIN,4(SP)		
013250	000002			RTI		::	RETURN
013252	000			9\$: .BYTE	0	::	STORAGE FOR ASCII CHAR. TO TYPE
013253	000			.BYTE	0	::	TERMINATOR
013254				\$TTYIN: .BLKB	8.	::	RESERVE 8 BYTES FOR TTY INPUT
013264	136	125	015	\$CNTLU: .ASCIZ	/'U/'<15><12>	::	CONTROL "U"
013267	012	000					
013271	136	107	015	\$CNTLG: .ASCIZ	/'G/'<15><12>	::	CONTROL "G"
013274	012	000					
013276	015	012	123	\$MSWR: .ASCIZ	<15><12>/SWR = /		
013301	127	122	040				
013304	075	040	000				
013307	040	040	116	\$MNEW: .ASCIZ	/ NEW = /		
013312	105	127	040				
013315	075	040	000				

1776
1777013320 010046
013322 016600 000002
013326 005740
013330 111000
013332 022700 000026
013336 003002
013340 000000
013342 000776
013344 006300
013346 016000 013366
013352 000200013354 011646
013356 016666 000004 000002
013364 000002

```
.EVEN
.SBTTL TRAP DECODER
;*****
;THIS ROUTINE WILL PICKUP THE LOWER BYTE OF THE "TRAP" INSTRUCTION
;AND USE IT TO INDEX THROUGH THE TRAP TABLE FOR THE STARTING ADDRESS
;OF THE DESIRED ROUTINE. THEN USING THE ADDRESS OBTAINED IT WILL
;GO TO THAT ROUTINE.
```

```
$TRAP:  MOV    R0,-(SP)      ;;SAVE R0
        MOV    2(SP),R0    ;;GET TRAP ADDRESS
        TST    -(R0)       ;;BACKUP BY 2
        MOVB   (R0),R0     ;;GET RIGHT BYTE OF TRAP
        CMP    # $TERM,R0  ;;CHECK FOR OUT OF BOUNDS
        BGT    .+6         ;;BR IF OK
        HALT                    ;;OUT OF BOUNDS
        BR     .-2         ;;HANGUP
        ASL    R0          ;;POSITION FOR INDEXING
        MOV    $TRPAD(R0),R0 ;;INDEX TO TABLE
        RTS    R0          ;;GO TO ROUTINE
```

```
;;THIS IS USE TO HANDLE THE "GETPRI" MACRO
$TRAP2: MOV    (SP),-(SP)   ;;MOVE THE PC DOWN
        MOV    4(SP),2(SP) ;;MOVE THE PSW DOWN
        RTI                    ;;RESTORE THE PSW
```

```
.SBTTL TRAP TABLE
;THIS TABLE CONTAINS THE STARTING ADDRESSES OF THE ROUTINES CALLED
;BY THE "TRAP" INSTRUCTION.
;ROUTINE
```

013366 013354
013370 011300
013372 011544
013374 011520
013376 011560
013400 011746
013402 012614
013404 012544
013406 013026
013410 013146
1778 013412 013652
1779 000026
1780

```
$TRPAD: .WORD  $TRAP2
        $TYPE   ;;CALL=TYPE      TRAP+1(104401) TTY TYPEOUT ROUTINE
        $TYPOC  ;;CALL=TYPOC    TRAP+2(104402) TYPE OCTAL NUMBER (WITH LEADING ZEROS)
        $TYPOS  ;;CALL=TYPOS    TRAP+3(104403) TYPE OCTAL NUMBER (NO LEADING ZEROS)
        $TYPON  ;;CALL=TYPON    TRAP+4(104404) TYPE OCTAL NUMBER (AS PER LAST CALL)
        $TYPDS  ;;CALL=TYPDS    TRAP+5(104405) TYPE DECIMAL NUMBER (WITH SIGN)
        $GTSWR  ;;CALL=GTSWR    TRAP+6(104406) GET SOFT-SWR SETTING
        $CKSWR  ;;CALL=CKSWR    TRAP+7(104407) TEST FOR CHANGE IN SOFT-SWR
        $RDCHR  ;;CALL=RDCHR    TRAP+10(104410) TTY TYPEIN CHARACTER ROUTINE
        $RDLIN  ;;CALL=RDLIN    TRAP+11(104411) TTY TYPEIN STRING ROUTINE
        RESYNC  ;;CALL=RSYNC    TRAP+12(104412) RESYNC PROGRAM

$TERM=-.$TRPAD
```



```
1782 ;:*****
1783
1784 .SBTTL INTERBOARD COMMUNICATION SUBROUTINES
1785 ;WTCMPN
1786 ;:*****
1787
1788 013414 012737 010000 001436 WTCMPN: MOV #10000,ABORT ;SETUP ABORT TIMER
1789 013422 032777 001000 165770 BIT #1000,@CSR
1790 013430 001010 BNE 2$ ;IF DSTATC IS ONE,WAIT FOR 0
1791
1792
1793 013432 032777 001000 165760 1$: BIT #1000,@CSR ;WAIT FOR DSTATC=1
1794 013440 001017 BNE 4$ ;GO AHEAD;IT HAS COME
1795 013442 005337 001436 DEC ABORT
1796 013446 001371 BNE 1$ ;IF NOT STUCK IN WAIT LOOP,
1797 ;KEEP WAITING
1798 013450 000407 BR 3$ ;WAITED TOO LONG;ABORT PROGRAM
1799 013452 032777 001000 165740 2$: BIT #1000,@CSR ;WAIT DSTATC=0
1800 013460 001407 BEQ 4$ ;GO AHEAD;IT HAS COME
1801 013462 005337 001436 DEC ABORT ;KEEP WAITING
1802 013466 001371 BNE 2$
1803
1804 013470 011637 001440 3$: MOV (SP),TESTPC ;SAVE LOCATION WHERE PROGRAM
1805 ;ORIGINATED
1806 013474 104016 104000+16
1807 013476 104412 RSYNC ;RESYNCHRONIZE BOTH PROGRAMS
1808 ;FROM START
1809 013500 000207 4$: RTS PC
1810
1811
1812 ;:*****
1813 ;GOCMPN
1814 ;:*****
1815
1816 013502 GOCMPN:
1817 013502 032777 000002 165710 BIT #2,@CSR ;IS F1 CLR?
1818 013510 001415 BEQ 1$ ;IF SO,SET F1
1819 013512 017737 165702 001446 MOV @CSR,TEMP
1820 013520 042737 100000 001446 BIC #BIT15,TEMP
1821 013526 042737 000002 001446 BIC #2,TEMP ;F1 IS SET,CLR IT
1822 013534 013777 001446 165656 MOV TEMP,@CSR
1823 013542 000207 RTS PC
1824 013544 017737 165650 001446 1$: MOV @CSR,TEMP
1825 013552 042737 100000 001446 BIC #BIT15,TEMP
1826 013560 052737 000002 001446 BIS #2,TEMP
1827 013566 013777 001446 165624 MOV TEMP,@CSR
1828 013574 000207 RTS PC
1829
1830
```

```

1832      .SBTTL CPU PRIORITY SUBROUTINES
1833      ;;*****
1834      ;CPUHI
1835      ;;*****
1836
1837 013576 CPUHI:
1838 013576 023727 001434 000126      CMP      BOARD,#126      ;IS THIS CPU LSI?
1839 013604 001003      BNE      1$      ;NO,IT IS UNIBUS
1840 013606 106427      106427
1841 013610 000200      200
1842 013612 000207      RTS      PC
1843 013614 012737 000340 177776 1$:      MOV      #340,PS      ;SET HIGH PRIOR. IN UNIBUS
1844 013622 000207      RTS      PC
1845
1846      ;;*****
1847      ;CPULO
1848      ;;*****
1849
1850 013624 CPULO:
1851 013624 023727 001434 000126      CMP      BOARD,#126
1852 013632 001003      BNE      1$
1853 013634 106427      106427
1854 013636 000000      0
1855 013640 000207      RTS      PC
1856 013642 012737 000000 177776 1$:      MOV      #0,PS
1856 013650 000207      RTS      PC
  
```



```

1858      .SBTTL  INTERPROCESSOR PROGRAM RESYNCHRONIZATION  ROUTINE
1859      ;;*****
1860      ;RSYNC
1861      ;;*****
1862
1863 013652 004737 011200      RESYNC: JSR      PC,RSTVEC
1864 013656 012706 001100      MOV      #STACK,SP
1865 013662 005737 001452      TST      MSTER      ;WAS THIS CPU STARTED AS MASTER?
1866 013666 001404      BEQ      MRDELY      ;YES
1867 013670 012737 000017 001444  SLDELY: MOV      #15.,TIME      ;SLAVE CPU;WAIT FOR MASTER TO REACH ITS
1868      ;RESYNC ROUTINE
1869 013676 000403      BR      WTSYNC
1870 013700 012737 000036 001444  MRDELY: MOV      #30.,TIME
1871 013706      WTSYNC:
1872 013706 005001      1$:      CLR      R1
1873 013710 005301      2$:      DEC      R1
1874 013712 001376      BNE      2$
1875 013714 005337 001444      DEC      TIME
1876 013720 001372      BNE      1$
1877 013722 000005      RESET
1878 013724 104401 013732      TYPE      ,65$      ;;TYPE ASCIZ STRING
1879      013730 000407      BR      64$      ;;GET OVER THE ASCIZ
1880      ;:65$: .ASCIZ  <CRLF>/*RESYNC.../<CRLF>
1881      64$:
1879 013750 000137 002742      JMP      SYNCST
1880
1881      .SBTTL  ASCII MESSAGES
1882 013754      124      105      123      EM1: .ASCII  /TEST 1      STATUS: MASTER/<CRLF>
1883      013757      124      040      061
1884      013762      040      040      040
1885      013765      040      123      124
1886      013770      101      124      125
1887      013773      123      072      040
1888      013776      115      101      123
1889      014001      124      105      122
1890      014004      200
1891      014005      123      114      101      .ASCIZ  /SLAVE FAILED TO ECHO DBR CONTENTS/
1892      014010      126      105      040
1893      014013      106      101      111
1894      014016      114      105      104
1895      014021      040      124      117
1896      014024      040      105      103
1897      014027      110      117      040
1898      014032      104      102      122
1899      014035      040      103      117
1900      014040      116      124      105
1901      014043      116      124      123
1902      014046      000
1903      014047      040      123      124      EM2: .ASCII  / STATUS: MASTER/<CRLF>
1904      014052      101      124      125
1905      014055      123      072      040
1906      014060      115      101      123
1907      014063      124      105      122
1908      014066      200
1909      014067      123      114      101      .ASCIZ  /SLAVE FAILED TO ECHO 'STAT' BITS/
1910      014072      126      105      040
1911      014075      106      101      111

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	014100	114	105	104	
	014103	040	124	117	
	014106	040	105	103	
	014111	110	117	040	
	014114	047	123	124	
	014117	101	124	047	
	014122	040	102	111	
	014125	124	123	000	
1886	014130	040	123	124	EM3: .ASCII / STATUS: MASTER/<CRLF>
	014133	101	124	125	
	014136	123	072	040	
	014141	115	101	123	
	014144	124	105	122	
	014147	200			
1887	014150	106	101	111	.ASCIIZ /FAILED TO INTR ON A 'DATI'/'
	014153	114	105	104	
	014156	040	124	117	
	014161	040	111	116	
	014164	124	122	040	
	014167	117	116	040	
	014172	101	040	047	
	014175	104	101	124	
	014200	111	047	000	
1888	014203	040	123	124	EM4: .ASCII / STATUS: MASTER/<CRLF>
	014206	101	124	125	
	014211	123	072	040	
	014214	115	101	123	
	014217	124	105	122	
	014222	200			
1889	014223	123	124	101	.ASCIIZ /STATUS ER ON 'DATI'/'
	014226	124	125	123	
	014231	040	105	122	
	014234	040	117	116	
	014237	040	047	104	
	014242	101	124	111	
	014245	047	000		
1890	014247	040	123	124	EM5: .ASCII / STATUS: MASTER/<CRLF>
	014252	101	124	125	
	014255	123	072	040	
	014260	115	101	123	
	014263	124	105	122	
	014266	200			
1891	014267	127	117	122	.ASCIIZ /WORD COUNT ER ON 'DATI'/'
	014272	104	040	103	
	014275	117	125	116	
	014300	124	040	105	
	014303	122	040	117	
	014306	116	040	047	
	014311	104	101	124	
	014314	111	047	000	
1892	014317	040	123	124	EM6: .ASCII / STATUS: MASTER/<CRLF>
	014322	101	124	125	
	014325	123	072	040	
	014330	115	101	123	
	014333	124	105	122	
	014336	200			
1893	014337	102	125	106	.ASCIIZ /BUFFER ADRS ER ON 'DATI'/'

	014342	106	105	122	
	014345	040	101	104	
	014350	122	123	040	
	014353	105	122	040	
	014356	117	116	040	
	014361	047	104	101	
	014364	124	111	047	
	014367	000			
1894	014370	040	123	124	EM7: .ASCII / STATUS: SLAVE/<CRLF>
	014373	101	124	125	
	014376	123	072	040	
	014401	123	114	101	
	014404	126	105	200	
1895	014407	106	101	111	.ASCIZ /FAILED TO INTR ON A 'DATO'/'
	014412	114	105	104	
	014415	040	124	117	
	014420	040	111	116	
	014423	124	122	040	
	014426	117	116	040	
	014431	101	040	047	
	014434	104	101	124	
	014437	117	047	000	
1896	014442	040	123	124	EM10: .ASCII / STATUS: SLAVE/<CRLF>
	014445	101	124	125	
	014450	123	072	040	
	014453	123	114	101	
	014456	126	105	200	
1897	014461	123	124	101	.ASCIZ /STATUS ER ON 'DATO'/'
	014464	124	125	123	
	014467	040	105	122	
	014472	040	117	116	
	014475	040	047	104	
	014500	101	124	117	
	014503	047	000		
1898	014505	040	123	124	EM11: .ASCII / STATUS: SLAVE/<CRLF>
	014510	101	124	125	
	014513	123	072	040	
	014516	123	114	101	
	014521	126	105	200	
1899	014524	127	117	122	.ASCIZ /WORD COUNT ER ON 'DATO'/'
	014527	104	040	103	
	014532	117	125	116	
	014535	124	040	105	
	014540	122	040	117	
	014543	116	040	047	
	014546	104	101	124	
	014551	117	047	000	
1900	014554	040	123	124	EM12: .ASCII / STATUS: SLAVE/<CRLF>
	014557	101	124	125	
	014562	123	072	040	
	014565	123	114	101	
	014570	126	105	200	
1901	014573	102	125	106	.ASCIZ /BUFFER ADRS ER ON 'DATO'/'
	014576	106	105	122	
	014601	040	101	104	
	014604	122	123	040	
	014607	105	122	040	

	014612	117	116	040	
	014615	047	104	101	
	014620	124	117	047	
	014623	000			
1902	014624	040	123	124	EM13: .ASCII / STATUS: SLAVE/<CRLF>
	014627	101	124	125	
	014632	123	072	040	
	014635	123	114	101	
	014640	126	105	200	
1903	014643	104	101	124	.ASCIIZ /DATA ER ON 'DATO' /
	014646	101	040	105	
	014651	122	040	117	
	014654	116	040	047	
	014657	104	101	124	
	014662	117	047	000	
1904	014665	123	124	101	EM14: .ASCII /STATUS: SLAVE/<CRLF>
	014670	124	125	123	
	014673	072	040	123	
	014676	114	101	126	
	014701	105	200		
1905	014703	122	105	101	.ASCIIZ /READY BIT WAS SET - IT SHOULD BE CLEAR/<CRLF>
	014706	104	131	040	
	014711	102	111	124	
	014714	040	127	101	
	014717	123	040	123	
	014722	105	124	040	
	014725	055	040	111	
	014730	124	040	123	
	014733	110	117	125	
	014736	114	104	040	
	014741	102	105	040	
	014744	103	114	105	
	014747	101	122	200	
	014752	000			
1906	014753	040	123	124	EM15: .ASCII / STATUS: MASTER/<CRLF>
	014756	101	124	125	
	014761	123	072	040	
	014764	115	101	123	
	014767	124	105	122	
	014772	200			
1907	014773	115	101	123	.ASCII /MASTER CANNOT START COMMUNICATION WITH SLAVE-/
	014776	124	105	122	
	015001	040	103	101	
	015004	116	116	117	
	015007	124	040	123	
	015012	124	101	122	
	015015	124	040	103	
	015020	117	115	115	
	015023	125	116	111	
	015026	103	101	124	
	015031	111	117	116	
	015034	040	127	111	
	015037	124	110	040	
	015042	123	114	101	
	015045	126	105	055	
1908	015050	127	101	111	.ASCIIZ /WAITING FOR DSTATC /<CRLF>
	015053	124	111	116	

	015056	107	040	106	
	015061	117	122	040	
	015064	104	123	124	
	015067	101	124	103	
	015072	040	200	000	
1909	015075	123	124	125	EM16: .ASCIZ /STUCK WAITING FOR COMPANION FOR GO-AHEAD/<CRLF>
	015100	103	113	040	
	015103	127	101	111	
	015106	124	111	116	
	015111	107	040	106	
	015114	117	122	040	
	015117	103	117	115	
	015122	120	101	116	
	015125	111	117	116	
	015130	040	106	117	
	015133	122	040	107	
	015136	117	055	101	
	015141	110	105	101	
	015144	104	200	000	
1910	015147	124	105	123	EM17: .ASCII /TEST 5 TIME OUT ERROR-/
	015152	124	040	065	
	015155	040	124	111	
	015160	115	105	040	
	015163	117	125	124	
	015166	040	105	122	
	015171	122	117	122	
	015174	055			
1911	015175	123	114	101	.ASCIZ /SLAVE SHOULD NOT HAVE INTERRUPTED/<CRLF>
	015200	126	105	040	
	015203	123	110	117	
	015206	125	114	104	
	015211	040	116	117	
	015214	124	040	110	
	015217	101	126	105	
	015222	040	040	111	
	015225	116	124	105	
	015230	122	122	125	
	015233	120	124	105	
	015236	104	200	000	
1912					
1913	015241	040	123	124	EM20: .ASCII / STATUS: MASTER/<CRLF>
	015244	101	124	125	
	015247	123	072	040	
	015252	115	101	123	
	015255	124	105	122	
	015260	200			
1914	015261	115	101	123	.ASCII /MASTER IS LEFT WAITING FOR INTERRUPT TO OCCUR/
	015264	124	105	122	
	015267	040	111	123	
	015272	040	114	105	
	015275	106	124	040	
	015300	127	101	111	
	015303	124	111	116	
	015306	107	040	106	
	015311	117	122	040	
	015314	111	116	124	
	015317	105	122	122	

	015322	125	120	124	
	015325	040	124	117	
	015330	040	117	103	
	015333	103	125	122	
1915	015336	126	111	101	.ASCIIZ /VIA ATTN SET/<CRLF>
	015341	040	101	124	
	015344	124	116	040	
	015347	123	105	124	
	015352	200	000		
1916	015354	124	105	123	EM21: .ASCII /TEST 2 STATUS: MASTER/<CRLF>
	015357	124	040	062	
	015362	040	040	040	
	015365	123	124	101	
	015370	124	125	123	
	015373	072	040	115	
	015376	101	123	124	
	015401	105	122	200	
1917	015404	105	122	122	.ASCIIZ /ERROR BIT IN MASTER CSR NOT CLEAR/
	015407	117	122	040	
	015412	102	111	124	
	015415	040	111	116	
	015420	040	115	101	
	015423	123	124	105	
	015426	122	040	103	
	015431	123	122	040	
	015434	116	117	124	
	015437	040	103	114	
	015442	105	101	122	
	015445	000			
1918	015446	102	125	122	EM22: .ASCIIZ /BURST DATA LATE BIT OF EIR WAS NOT SET/<CRLF>
	015451	123	124	040	
	015454	104	101	124	
	015457	101	040	114	
	015462	101	124	105	
	015465	040	102	111	
	015470	124	040	117	
	015473	106	040	105	
	015476	111	122	040	
	015501	127	101	123	
	015504	040	116	117	
	015507	124	040	123	
	015512	105	124	200	
	015515	000			
1919					
1920	015516	200	124	105	MSG1: .ASCIIZ <CRLF> /TEST 3-BLOCK MODE XFERS/<CRLF>
	015521	123	124	040	
	015524	063	055	102	
	015527	114	117	103	
	015532	113	040	115	
	015535	117	104	105	
	015540	040	130	106	
	015543	105	122	123	
	015546	200	000		
1921	015550	200	124	105	MSG2: .ASCIIZ <CRLF> /TEST 4-BURST MODE XFERS/<CRLF>
	015553	123	124	040	
	015556	064	055	102	
	015561	125	122	123	

	015564	124	040	115	
	015567	117	104	105	
	015572	040	130	106	
	015575	105	122	123	
	015600	200	000		
1922	015602	200	124	105	MSG3: .ASCIIZ <CRLF>/TEST 5-BURST DATA LATE/<CRLF>
	015605	123	124	040	
	015610	065	055	102	
	015613	125	122	123	
	015616	124	040	104	
	015621	101	124	101	
	015624	040	114	101	
	015627	124	105	200	
	015632	000			
1923	015633	105	122	122	DH1: .ASCIIZ /ERRPC BUSADR EXPCT RCVD/
	015636	120	103	040	
	015641	040	040	102	
	015644	125	123	101	
	015647	104	122	040	
	015652	040	105	130	
	015655	120	103	124	
	015660	040	040	040	
	015663	122	103	126	
	015666	104	000		
1924	015670	105	122	122	DH2: .ASCIIZ /ERRPC MEMADR EXPCT RCVD/
	015673	120	103	040	
	015676	040	040	115	
	015701	105	115	101	
	015704	104	122	040	
	015707	040	105	130	
	015712	120	103	124	
	015715	040	040	040	
	015720	122	103	126	
	015723	104	000		
1925	015725	105	122	122	DH3: .ASCIIZ /ERRPC/
	015730	120	103	000	
1926					.EVEN
1927	015734	001116	001122	001124	DT1: \$ERRPC,\$BDADR,\$GDDAT,\$BDDAT,0
	015742	001126	000000		
1928	015746	001116	000000		DT2: \$ERRPC,0
1929	015752	001440	000000		DT3: TESTPC,0
1930					*****
1931					;'DBUF' IS THE WORKING AREA IN MEM FOR ALL NPR OPERATIONS
1932					*****
1933	015756	000000			DBUF: 0 ;1ST ADRS OF DATA BUFFER
1934		000001			.END

ABORT	001436	EM10	014442	PS	= 177776	SW15	= 100000	\$ERTTL	001112
BAR	001416	EM11	014505	PSW	= 177776	SW2	= 000004	\$FILLC	001156
BDR	001422	EM12	014554	PWRVEC	= 000024	SW3	= 000010	\$FILLS	001155
BIT0	= 000001	EM13	014624	RCVTMO	001500	SW4	= 000020	\$GDADR	001120
BIT00	= 000001	EM14	014665	RDCHR	= 104410	SW5	= 000040	\$GDDAT	001124
BIT01	= 000002	EM15	014753	RDLIN	= 104411	SW6	= 000100	\$GET42	011074
BIT02	= 000004	EM16	015075	RESVEC	= 000010	SW7	= 000200	\$GTSWR	012614
BIT03	= 000010	EM17	015147	RESYNC	013652	SW8	= 000400	\$HD	= 000003
BIT04	= 000020	EM2	014047	RPRAM	001464	SW9	= 001000	\$ICNT	001104
BIT05	= 000040	EM20	015241	RSTVEC	011200	SYNCS	002742	\$INTAG	001135
BIT06	= 000100	EM21	015354	RSYNC	= 104412	TBITVE	= 000014	\$ITEMB	001114
BIT07	= 000200	EM22	015446	RTRN	003462	TEMP	001446	\$LF	001166
BIT08	= 000400	EM3	014130	R6	= %000006	TESTPC	001440	\$LPADR	001106
BIT09	= 001000	EM4	014203	R7	= %000007	TIME	001444	\$LPERR	001110
BIT1	= 000002	EM5	014247	SAVE	001450	TKVEC	= 000060	\$MNEW	013307
BIT10	= 002000	EM6	014317	SCOPE	= 000004	TOTAL	001442	\$MSWR	013276
BIT11	= 004000	EM7	014370	SETUP2	001704	TPVEC	= 000064	\$NULL	001154
BIT12	= 010000	EOPT	010772	SETUP3	001730	TRAPVE	= 000034	\$OCNT	011742
BIT13	= 020000	EOPTA	011140	SETVEC	011160	TRTVEC	= 000014	\$OMODE	011744
BIT14	= 040000	EOPTB	011146	SINIT1	006072	TYPDS	= 104405	\$OVER	012530
BIT15	= 100000	ERROR	= 104000	SINIT2	006304	TYPE	= 104401	\$PASS	001100
BIT2	= 000004	ERRVEC	= 000004	SLBRD	007330	TYPOC	= 104402	\$QUES	001164
BIT3	= 000010	GOCMPN	013502	SLDELY	013670	TYPON	= 104404	\$RDCHR	013026
BIT4	= 000020	GTSWR	= 104406	SLVFIN	010776	TYPOS	= 104403	\$RDLIN	013146
BIT5	= 000040	HT	= 000011	SL1STR	006240	WCR	001414	\$RDSZ	= 000010
BIT6	= 000100	ICOUNT	001454	STACK	= 001100	WTCMPN	013414	\$RTNAD	011116
BIT7	= 000200	INTADR	001410	START	001522	WTSYNC	013706	\$SCOPE	012452
BIT8	= 000400	IOTVEC	= 000020	START1	001506	XMITMO	001472	\$SETUP	= 000107
BIT9	= 001000	LDBUF	011236	START2	001514	XPRAM	001456	\$STUP	= 177777
BOARD	001434	LF	= 000012	STKLMT	= 177774	\$AUTOB	001134	\$SVLAD	012514
BPTVEC	= 000014	MINIT1	002760	STST1	006230	\$BCADR	001122	\$SWR	= 122000
BRDTYP	002056	MINIT2	003226	STST2	006422	\$BDDAT	001126	\$SWRMK	= 000000
CKSWR	= 104407	MORS	002524	STST3	006502	\$BELL	001160	\$TERM	= 000026
CLRBUF	011220	MRDELY	013700	STST4	007416	\$CHARC	011514	\$TKB	001146
CNT	001430	MSBRD	004550	STST5	010260	\$CKSWR	012544	\$TKS	001144
CPUH1	013576	MSG1	015516	SWR	001140	\$CMTAG	001100	\$TN	= 000001
CPULO	013624	MSG2	015550	SWRCK	012306	\$CM3	= 000000	\$TPB	001152
CR	= 000015	MSG3	015602	SWREG	000176	\$CNTLG	013271	\$TPFLG	001157
CRLF	= 000200	MSTER	001452	SW0	= 000001	\$CNTLU	013264	\$TPS	001150
CSR	001420	MTPS	= 106427	SW00	= 000001	\$CRLF	001165	\$TRAP	013320
DBUF	015756	MTST1	003106	SW01	= 000002	\$DBLK	012162	\$TRAP2	013354
DDISP	= 177570	MTST2	003526	SW02	= 000004	\$DOAGN	011114	\$TRP	= 000013
DH1	015633	MTST3	003652	SW03	= 000010	\$DTBL	012152	\$TRPAD	013366
DH2	015670	MTST4	004620	SW04	= 000020	\$ENDAD	011104	\$TSTNM	001102
DH3	015725	MTST5	005446	SW05	= 000040	\$ENDCT	011052	\$TTYIN	013254
DISPLA	001142	NUM	001432	SW06	= 000100	\$ENDMG	011123	\$TYPDS	011746
DISPRE	000174	PIRQ	= 177772	SW07	= 000200	\$ENULL	011120	\$TYPE	011300
DRVCT0	001424	PIRQVE	= 000240	SW08	= 000400	\$EOP	011022	\$TYPEC	011450
DRVCT2	001426	PR0	= 000000	SW09	= 001000	\$EOPCT	011044	\$TYPEX	011516
DRVECT	001412	PR1	= 000040	SW1	= 000002	\$ERFLG	001103	\$TYPOC	011544
DSWR	= 177570	PR2	= 000100	SW10	= 002000	\$ERMAX	001115	\$TYPON	011560
DT1	015734	PR3	= 000140	SW11	= 004000	\$ERROR	012172	\$TYPOS	011520
DT2	015746	PR4	= 000200	SW12	= 010000	\$ERRPC	001116	\$XTSTR	012456
DT3	015752	PR5	= 000240	SW13	= 020000	\$ERRTB	001170	\$GET4	= 000000
EMTVEC	= 000030	PR6	= 000300	SW14	= 040000	\$ERRTY	012316	\$UFILL	011743
EM1	013754	PR7	= 000340						

CZDRKAO DR11-W INTRPROC EXER MACRO M1111 24-AUG-79 14:19 PAGE ^{J 6}46-8
SYMBOL TABLE

SEQ 0074

. ABS. 015760 000
 000000 001

ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 35848 WORDS (141 PAGES)

DYNAMIC MEMORY: 20746 WORDS (79 PAGES)

ELAPSED TIME: 00:05:21

LINK, LINK=SYSMAC.MLB/ML, LINK.P11