

DHU11

DHU-11 FUNC TST PART 3
CZDHWAO

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AH-T799A-MC
FICHE 1 OF 2

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The microfiche card displays a grid of 100 frames, each containing a small, high-contrast image of a circuit board or component. The images are arranged in a 10x10 grid. The top-left frame is blank, and the rest contain various electronic components and circuitry. The images are arranged in a 10x10 grid. The top-left frame is blank, and the rest contain various electronic components and circuitry. The images are arranged in a 10x10 grid. The top-left frame is blank, and the rest contain various electronic components and circuitry.

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IDENTIFICATION

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1.0 GENERAL PROGRAM CONSIDERATIONS

1.1 PROGRAM ABSTRACT

CZDHMAO IS PART OF THE DMU-11 FUNCTIONAL VERIFICATION TEST. THIS PART OF THE TEST PERFORMS EXTENSIVE DATA TRANSMISSION AND RECEPTION TESTS. THIS PART ALSO INCLUDES A KEYBOARD ECHO AND MODEM LOOPBACK TEST.

THIS DIAGNOSTIC HAS BEEN WRITTEN FOR USE WITH THE DIAGNOSTIC RUNTIME SERVICES SOFTWARE (SUPERVISOR). THESE SERVICES PROVIDE THE INTERFACE TO THE OPERATOR AND TO THE SOFTWARE ENVIRONMENT. THIS PROGRAM CAN BE USED WITH XXDP+, ACT, APT, SLIDE AND PAPER TAPE. FOR A COMPLETE DESCRIPTION OF THE RUNTIME SERVICES, REFER TO THE XXDP+ USER'S MANUAL. THERE IS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES IN THE OPERATING INSTRUCTIONS-COMMANDS OF THIS DOCUMENT.

1.2 SYSTEM REQUIREMENTS

THE FOLLOWING HARDWARE IS REQUIRED TO RUN THE DMU FVT:

- 0 UNIBUS PROCESSOR WITH AT LEAST 32K BYTES OF MEMORY.
- 0 DMU BOARDS INSTALLED ON THE UNIBUS.
- 0 APPROPRIATE PROGRAM LOAD DEVICE SUPPORTING XXDP+ MEDIA OR A DOWN LINE LOADING SYSTEM.

1.3 RELATED DOCUMENTS AND STANDARDS

- 0 XXDP+ USER'S MANUAL - DESCRIBES THE RUNNING OF DIAGNOSTICS UNDER THE XXDP+ MONITOR.

1.4 DIAGNOSTIC HIERARCHY PREREQUISITES

THE PROCESSOR, THE UNIBUS, THE SYSTEM MEMORY, THE CONSOLE TERMINAL
AND THE LOAD MEDIA ARE ASSUMED TO HAVE BEEN TESTED AND FOUND WORKING
BEFORE THIS PROGRAM IS RUN.

1.5 ASSUMPTIONS

2.0 OPERATING INSTRUCTIONS

THIS SECTION CONTAINS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES.
FOR DETAILED INFORMATION, REFER TO THE XXDP+ USER'S MANUAL (CHQUS).

2.1 COMMANDS

THERE ARE ELEVEN LEGAL COMMANDS FOR THE DIAGNOSTIC RUNTIME SERVICES
(SUPERVISOR). THIS SECTION LISTS THE COMMANDS AND GIVES A VERY
BRIEF DESCRIPTION OF THEM. THE XXDP+ USER'S MANUAL HAS MORE DETAILS.

COMMAND	EFFECT
-----	-----
START	START THE DIAGNOSTIC FROM AN INITIAL STATE
RESTART	START THE DIAGNOSTIC WITHOUT INITIALIZING
CONTINUE	CONTINUE AT TEST THAT WAS INTERRUPTED (AFTER +C)
PROCEED	CONTINUE FROM AN ERROR HALT
EXIT	RETURN TO XXDP+ MONITOR (XXDP+ OPERATION ONLY!)
ADD	ACTIVATE A UNIT FOR TESTING (ALL UNITS ARE CONSIDERED TO BE ACTIVE AT START TIME)
DROP	DEACTIVATE A UNIT
PRINT	PRINT STATISTICAL INFORMATION (IF IMPLEMENTED BY THE DIAGNOSTIC - SEE PERFORMANCE AND PROGRESS REPORTS SECTION OF THIS DOCUMENT)
DISPLAY	TYPE A LIST OF ALL DEVICE INFORMATION
FLAGS	TYPE THE STATE OF ALL FLAGS (SEE FLAGS SECTION)
ZFLAGS	CLEAR ALL FLAGS (SEE FLAGS SECTION)

A COMMAND CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. SO
YOU MAY, FOR EXAMPLE, TYPE "STA" INSTEAD OF "START".
MORE INFORMATION CAN BE FOUND WITHIN THE SECTION LABELLED
EXTENDED COMMAND SYNTAX

2.2 SWITCHES

THERE ARE SEVERAL SWITCHES WHICH ARE USED TO MODIFY SUPERVISOR OPERATION.
THESE SWITCHES ARE APPENDED TO THE LEGAL COMMANDS. ALL OF THE LEGAL
SWITCHES ARE TABULATED BELOW WITH A BRIEF DESCRIPTION OF EACH.
IN THE DESCRIPTIONS BELOW, A DECIMAL NUMBER IS DESIGNATED BY "DDDDD".

SWITCH	EFFECT
-----	-----
/TESTS:LIST	EXECUTE ONLY THOSE TESTS SPECIFIED IN THE LIST. LIST IS A STRING OF TEST NUMBERS, FOR EXAMPLE - /TESTS:1:5:7-10. THIS LIST WILL CAUSE TESTS 1,5,7,8,9,10 TO

BE RUN. ALL OTHER TESTS WILL NOT BE RUN.
 EXECUTE DDDDD PASSES (DDDD = 1 TO 64000)
 SET SPECIFIED FLAGS. SEE THE FLAGS SECTION
 OF THIS DOCUMENT.
 REPORT END OF PASS MESSAGE AFTER EVERY
 DDDDD PASSES ONLY. (DDDD = 1 TO 64000)
 TEST/ADD/DROP ONLY THOSE UNITS SPECIFIED
 IN THE LIST. LIST EXAMPLE - /UNITS:0:5:10-12
 USE UNITS 0,5,10,11,12 (UNIT NUMBERS = 0-63)

EXAMPLE OF SWITCH USAGE:

START/TESTS:1-5/PASS:1000/EOP:100

THE EFFECT OF THIS COMMAND WILL BE: 1) TESTS 1 THROUGH 5 WILL BE
 EXECUTED, 2) ALL UNITS WILL TESTED 1000 TIMES AND 3) THE END OF
 PASS MESSAGES WILL BE PRINTED AFTER EACH 100 PASSES ONLY. A
 SWITCH CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. YOU MAY,
 FOR EXAMPLE, TYPE "/TES:1-5" INSTEAD OF "/TESTS:1-5".

BELOW IS A TABLE THAT SPECIFIES WHICH SWITCHES CAN BE USED BY EACH
 COMMAND.

	TESTS	PASS	FLAGS	EOP	UNITS
START	X	X	X	X	X
RESTART	X	X	X	X	X
CONTINUE		X	X	X	
PROCEED			X		
DROP					X
ADD					X
PRINT					
DISPLAY					X
FLAGS					
ZFLAGS					
EXIT					

2.3 FLAGS

FLAGS ARE USED TO SET UP CERTAIN OPERATIONAL PARAMETERS SUCH AS LOOPING ON ERROR. ALL FLAGS ARE CLEARED AT STARTUP AND REMAIN CLEARED UNTIL EXPLICITLY SET USING THE FLAGS SWITCH. FLAGS ARE ALSO CLEARED AFTER A START COMMAND UNLESS SET USING THE FLAG SWITCH. THE ZFLAGS COMMAND MAY ALSO BE USED TO CLEAR ALL FLAGS. WITH THE EXCEPTION OF THE START AND ZFLAGS COMMANDS, NO COMMANDS AFFECT THE STATE OF THE FLAGS; THEY REMAIN SET OR CLEARED AS SPECIFIED BY THE LAST FLAG SWITCH.

FLAG	EFFECT
----	-----
HOE	HALT ON ERROR - CONTROL IS RETURNED TO RUNTIME SERVICES COMMAND MODE
LOE	LOOP ON ERROR
IER*	INHIBIT ALL ERROR REPORTS
IBR*	INHIBIT ALL ERROR REPORTS EXCEPT FIRST LEVEL (FIRST LEVEL CONTAINS ERROR TYPE, NUMBER, PC, TEST AND UNIT)
IXR*	INHIBIT EXTENDED ERROR REPORTS (THOSE CALLED BY PRINTX MACRO'S)
PRI	DIRECT MESSAGES TO LINE PRINTER
PNT	PRINT TEST NUMBER AS TEST EXECUTES
BOE	"BELL" ON ERROR
UAM	UNATTENDED MODE (NO MANUAL INTERVENTION)
ISR	INHIBIT STATISTICAL REPORTS (DOES NOT APPLY TO DIAGNOSTICS WHICH DO NOT SUPPORT STATISTICAL REPORTING)
IDR	INHIBIT PROGRAM DROPPING OF UNITS
ADR	EXECUTE AUTODROP CODE
LOT	LOOP ON TEST
EVL	EXECUTE EVALUATION (ON DIAGNOSTICS WHICH HAVE EVALUATION SUPPORT)

*SEE THE ERROR INFORMATION SECTION OF THIS DOCUMENT.

SEE THE XXDP+ USER'S MANUAL FOR MORE DETAILS ON FLAGS. YOU MAY SPECIFY MORE THAN ONE FLAG WITH THE FLAG SWITCH. FOR EXAMPLE, TO CAUSE THE PROGRAM TO LOOP ON ERROR, INHIBIT ERROR REPORTS AND TYPE A "BELL" ON ERROR, YOU MAY USE THE FOLLOWING STRING:

/FLAGS:LOE:IER:BOE

2.4 EXTENDED COMMAND SYNTAX

2.4.1 START COMMAND -

```
*****
STA(RT)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:
<FLAG-LIST>/EOP:<INCR>
*****
```

2.4.1.1 TESTS SWITCH (/TESTS:<TEST-LIST>) -

<TEST-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (1:2 ETC.) OR RANGES OF DECIMAL NUMBERS (1-5:8-10 ETC.), SEPERATED BY COLONS, THAT SPECIFY THE TESTS TO BE EXECUTED. TESTS WILL BE EXECUTED IN NUMERICAL ORDER REGARDLESS OF THE ORDER OF SPECIFICATION. THE DEFAULT IS TO EXECUTE ALL TESTS. ON THIS AND ALL SWITCHES, THE ANGLE BRACKETS <> ARE PUNCTUATION USED IN THE DEFINITION ONLY, AND ARE NOT TO BE TYPED BY THE OPERATOR. SEE EXAMPLE AT END OF "EFFECT OF START COMMAND" SECTION.

2.4.1.2 PASS SWITCH (/PASS:<PASS-CNT>) -

<PASS-CNT> IS A DECIMAL NUMBER INDICATING THE DESIRED NUMBER OF PASSES. A PASS IS DEFINED AS THE EXECUTION OF THE FULL DIAGNOSTIC (ALL SELECTED TESTS). THE DEFAULT IS NON-ENDING EXECUTION. IN THIS CASE, EXIT FROM THE PROGRAM IS ACCOMPLISHED EITHER BY TYPING A CONTROL/C OR BY OCCURANCE OF AN ERROR WITH THE HALT ON ERROR FLAG BEING SET. THE EXIT IS A RETURN TO COMMAND MODE. SEE EXAMPLE AT END OF "EFFECT OF START COMMAND" SECTION.

2.4.1.3 FLAGS SWITCH (/FLAGS:<FLAG-LIST>) -

<FLAG-LIST> IS A SEQUENCE OF ELEMENTS OF THE FORM <FLAG>, <FLAG=1>, OR <FLAG=0>, SEPARATED BY COLONS, WHERE <FLAG> HAS ONE OF THE FOLLOWING VALUES:

HOE	HALT ON ERROR, CAUSING COMMAND MODE TO BE ENTERED WHEN AN ERROR IS ENCOUNTERED.
LOE	LOOP ON ERROR, CAUSING THE DIAGNOSTIC TO LOOP CONTINUOUSLY WITHIN THE SMALLEST DEFINED BLOCK OF CODING (SEGMENT, SUBTEST, OR TEST) CONTAINING THE ERROR.
IER	INHIBIT ERROR REPORTING.
IBE	INHIBIT BASIC ERROR REPORTS.
IXE	INHIBIT EXTENDED ERROR REPORTS.
PRI	DIRECT ALL MESSAGES TO A LINE PRINTER.
PNT	PRINT NUMBER OF TEST BEING EXECUTED.
BOE	BELL ON ERROR (NOT RELATED TO BELL PROMPTING).
UAM	RUN IN UNATTENDED MODE, BYPASSING MANUAL INTERVENTION (ILLEGAL FOR THIS DIAGNOSTIC).
ISR	INHIBIT STATISTICAL REPORTS.

IDU INHIBIT DROPPING OF UNITS BY DIAGNOSTIC.
(HAS NO EFFECT IN THIS DIAGNOSTIC.)
LOT LOOP ON TEST.
THE FLAGS NAMED OR EQUATED TO 1 ARE SET, THOSE EQUATED TO 0 ARE
CLEARED. A FLAG NOT SPECIFIED IS CLEARED. IF THE FLAGS SWITCH IS NOT
GIVEN ALL FLAGS ARE CLEARED. SEE EXAMPLE AT END OF "EFFECT OF START
COMMAND" SECTION.

2.4.1.4 END OF PASS SWITCH (/EOP:<INCR>) -

<INCR> IS A DECIMAL NUMBER INDICATING HOW OFTEN (IN TERMS OF
PASSES) IT IS DESIRED THAT THE END OF PASS MESSAGE BE PRINTED. THE
DEFAULT IS AT THE END OF EVERY PASS. SEE EXAMPLE AT END OF "EFFECT OF
START COMMAND" SECTION.

2.4.1.5 EFFECT OF START COMMAND -

THE EFFECT OF THE START COMMAND IS TO INITIATE THE HARDWARE
PARAMETER DIALOGUE, THE SOFTWARE PARAMETER DIALOGUE, THE
INITIALIZATION QUESTIONS, AND THEN THE DIAGNOSTIC COMMENCES TESTING.

THE HARDWARE PARAMETER DIALOGUE COMMENCES WITH THE QUESTION "4
UNITS (D) ?" TO WHICH THE OPERATOR SHOULD REPLY WITH THE NUMBER OF
UNITS TO BE TESTED. FOLLOWING THIS ARE THE QUESTIONS WHEREBY THE
P-TABLES THEMSELVES ARE BUILT. EACH P-TABLE IS A CORE-RESIDENT TABLE
CONTAINING ALL THE HARDWARE INFORMATION FOR ONE COMPLETE UNIT. EACH
QUESTION IS FOLLOWED BY THE RESPONSE RADIX (D FOR DECIMAL, B FOR
BINARY, O FOR OCTAL, L FOR YES/NO) IN PARENTHESES AND THE DEFAULT
VALUE AFTER THE PARENTHESES. FOR THE ACTUAL HARDWARE P TABLE
QUESTIONS SEE THE "HARDWARE PARAMETERS" SECTION.

FOLLOWING THE HARDWARE QUESTIONS ARE THE SOFTWARE QUESTIONS TO
BUILD THE SOFTWARE TABLES, WHICH DEFINE OPERATING PARAMETERS OF THE
DIAGNOSTIC PROGRAM. THESE QUESTIONS ARE DESCRIBED IN THE "SOFTWARE
PARAMETERS" SECTION.

EXAMPLE:

STA/TESTS:1:3-4:/PASS:3/FLAGS:IER:HOE=1

THIS COMMAND WILL CAUSE THREE PASSES TO BE MADE, WITH EACH PASS
CONSISTING OF TESTS 1,3, AND 4. THERE IS NO DIFFERENCE BETWEEN SAYING
<FLAG> AND SAYING <FLAG=1>. THE NOTATION <FLAG=0> IS MEANINGFUL ONLY
ON A COMMAND OTHER THAN START TO CLEAR A FLAG THAT WAS PREVIOUSLY SET.
NOTE THAT ON ALL COMMANDS ONLY THE FIRST THREE LETTERS ARE SCANNED.

2.4.2 RESTART COMMAND -

```
*****  
RES(TART)/TESTS:<TEST-LIST>/PASS:<PASS-CNT>/FLAGS:  
<FLAG-LIST>/UNITS:<UNIT-LIST>  
*****
```

2.4.2.1 TESTS, PASS, AND FLAGS SWITCHES -

<TEST-LIST>, <PASS-CNT>, AND <FLAG-LIST> ARE AS IN THE START COMMAND.

2.4.2.2 UNITS SWITCH (/UNITS:<UNIT-LIST>) - <UNIT-LIST> IS A SEQUENCE OF DECIMAL NUMBERS (0,1 ETC.) OR RANGES OF DECIMAL NUMBERS (0-5, 8-10 ETC.) THAT SPECIFY THE UNITS TO BE TESTED. THE NUMBERS ARE SEPARATED BY COLONS. THE NUMBERS MAY RANGE FROM 0 THRU N-1 (N IS THE NUMBER OF UNITS SPECIFIED IN THE PREVIOUS START COMMAND). THE NUMBER INDICATES THE POSITION OF THE P-TABLE AS THE DATA WAS ENTERED DURING THE HARDWARE DIALOGUE. THE UNITS WHICH ARE SELECTED MUST NOT HAVE BEEN DROPPED BY THE DROP COMMAND. SEE THE DISCUSSION OF ADD AND DROP COMMANDS BELOW. DEFAULT IS TO TEST ALL UNITS WHICH HAVE NOT BEEN DROPPED BY A DROP COMMAND.

2.4.2.3 EFFECT OF RESTART COMMAND -

THE RESTART COMMAND DIFFERS FROM THE START COMMAND IN THAT THE P-TABLES FROM THE PREVIOUS START COMMAND (THERE MUST HAVE BEEN ONE) ARE USED, INSTEAD OF NEW ONES BEING BUILT. THE UNITS SWITCH SHOULD NOT BE USED WITH THIS PROGRAM. THE SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED (OPERATOR WILL BE ASKED). THE COMMAND CAN BE USED AFTER COMMAND MODE HAS BEEN REENTERED IN ANY OF THE THREE NORMAL WAYS: A) THE REQUESTED NUMBER OF PASSES HAVE BEEN MADE, B) AN ERROR WAS ENCOUNTERED WITH THE HALT ON ERROR FLAG SET, OR C) A CONTROL/C WAS ENTERED BY THE OPERATOR.

2.4.3 CONTINUE COMMAND -

```
*****  
CON(TINUE)/PASS:<PASS-CNT>/FLAGS:<FLAG-LIST>  
*****
```

2.4.3.1 FLAG SWITCH (/FLAGS:<FLAG-LIST>) -

<FLAG-LIST> IS SAME AS IN THE START COMMAND, BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

2.4.3.2 EFFECT OF CONTINUE COMMAND

CONTINUE MUST FOLLOW A START OR RESTART, AND COMMAND MODE MUST HAVE BEEN ENTERED DUE TO A HALT ON ERROR OR A CONTROL/C. THE EFFECT OF THE COMMAND IS TO GO TO THE BEGINNING OF THE TEST THAT WAS BEING EXECUTED WHEN THE HALT OR CONTROL/C TOOK PLACE. SOFTWARE DIALOGUE MAY OPTIONALLY BE REEXECUTED. HARDWARE PARAMETERS MAY NOT BE CHANGED.

2.4.4 PROCEED COMMAND -

PRO(CEED)/FLAGS:<FLAG-LIST>

2.4.4.1 FLAGS SWITCH (/FLAGS:<FLAG-LIST>) -

<FLAG-LIST> IS AS IN THE START COMMAND, BUT UNSPECIFIED FLAGS RETAIN THEIR CURRENT VALUE.

2.4.4.2 EFFECT OF PROCEED COMMAND -

PROCEED MUST FOLLOW A START, RESTART, OR CONTINUE. COMMAND MODE MUST HAVE BEEN ENTERED VIA A HALT ON ERROR. THE EFFECT OF THE COMMAND IS TO BEGIN EXECUTION AT THE LOCATION FOLLOWING THE ERROR CALL. NEITHER HARDWARE NOR SOFTWARE PARAMETERS MAY BE ALTERED.

2.4.5 ADD COMMAND -

ADD/UNITS:<UNIT-LIST>

2.4.6 EFFECT OF ADD COMMAND -

THE UNITS SPECIFIED ARE ADDED TO THE TEST SEQUENCE. EACH UNIT MUST HAVE A P-TABLE IN MEMORY DUE TO AN EARLIER HARDWARE DIALOGUE. THIS COMMAND MUST BE FOLLOWED BY A RESTART OR CONTINUE. THE UNITS SWITCH MUST BE SPECIFIED. THE ADD COMMAND IS MEANINGFUL ONLY FOR UNITS THAT WERE PREVIOUSLY DROPPED.

2.4.7 DROP COMMAND -

DRO(P)/UNITS:<UNIT-LIST>

2.4.8 EFFECT OF DROP COMMAND -
THE UNITS SPECIFIED WILL BE DROPPED FROM TESTING. THE UNITS
WILL BE RESELECTED ONLY BY THE EXECUTION OF AN ADD OR START
COMMAND. THE UNITS SWITCH MUST BE ENTERED. THIS COMMAND
MUST BE FOLLOWED BY A RESTART OR A CONTINUE COMMAND.

2.4.9 PRINT COMMAND

PRI(NT)

2.4.9.1 EFFECT OF PRINT COMMAND -
THE TOTAL NUMBER OF ERRORS FOR EACH UNIT SINCE THE LAST
START OR RESTART COMMAND ARE PRINTED. THE ISR (INHIBIT
STATISTICAL REPORTING) FLAG IS CLEARED.

2.4.10 DISPLAY COMMAND -

DIS(PLAY)/UNITS:<UNIT LIST>

2.4.10.1 EFFECT OF DISPLAY COMMAND -

THE HARDWARE P-TABLE FOR THE TEST STATION IS PRINTED IN THE
FORMAT IN WHICH IT WAS ENTERED.

2.4.11 FLAGS COMMAND -

FLA(GS)

2.4.11.1 EFFECT OF FLAGS COMMAND -

THE CURRENT SETTINGS OF ALL FLAGS ARE PRINTED.

2.4.12 ZFLAGS COMMAND -

ZFL(AGS)

2.4.13 ZFLAGS COMMAND -

ALL LAGS ARE CLEARED.

2.4.14 CONTROL CHARACTERS -

- C A CONTROL/C (C) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES A RETURN TO COMMAND MODE.
- Z A CONTROL/Z (Z) ENTERED DURING ONE OF THE TWO OPERATOR DIALOGUES-- HARDWARE P-TABLE DIALOGUE OR SOFTWARE P-TABLE DIALOGUE CAUSES THE DEFAULTS TO BE TAKEN FOR THE REMAINDER OF THAT DIALOGUE.
- O A CONTROL/O (O) ENTERED DURING THE EXECUTION OF A DIAGNOSTIC CAUSES ALL TELETYPE OUTPUT TO BE SUPPRESSED FOR THE REMAINDER OF THE DIAGNOSTIC OR UNTIL ANOTHER CONTROL/O IS TYPED, WHICH RESTORES NORMAL TELETYPE OUTPUT.

2.5 HARDWARE QUESTIONS

WHEN A DIAGNOSTIC IS STARTED, THE RUNTIME SERVICES WILL PROMPT THE USER FOR HARDWARE INFORMATION BY TYPING "CHANGE HW (L) ?" YOU MUST ANSWER "Y" AFTER A START COMMAND UNLESS THE HARDWARE INFORMATION HAS BEEN "PRELOADED" USING THE SETUP UTILITY (SEE CHAPTER 6 OF THE XXDP+ USER'S MANUAL). WHEN YOU ANSWER THIS QUESTION WITH A "Y", THE RUNTIME SERVICES WILL ASK FOR THE NUMBER OF UNITS (IN DECIMAL). YOU WILL THEN BE ASKED THE FOLLOWING QUESTIONS FOR EACH UNIT.

1. CSR ADDRESS - THIS QUESTION REQUESTS THE CSR ADDRESS OF THE SPECIFIED DMU-11. THE DEFAULT ANSWER FOR THIS QUESTION IS ADDRESS 160460 (OCTAL).
2. INTERRUPT VECTOR ADDRESS - THIS QUESTION REQUESTS THE INTERRUPT VECTOR ADDRESS OF THE SPECIFIED DMU-11. THE DEFAULT ANSWER FOR THIS QUESTION IS 310 (OCTAL).
3. ACTIVE LINES BIT MAP - THIS QUESTION REQUESTS AN OCTAL BIT MAP OF THE SERIAL COMMUNICATION LINES ON THE DMU11 WHICH ARE BEING SELECTED FOR TESTING. IF THE BIT IN THE BIT MAP IS SET WHICH CORRESPONDS TO A PARTICULAR LINE (I.E. BIT 5 FOR LINE 5) THAT LINE WILL BE TESTED BY THE FVT. THE DEFAULT ANSWER FOR THIS QUESTION IS ALL LINES I.E. 177777.
4. TYPE OF LOOPBACK (1=INTERNAL, 2=H3277, 3=H325 4=MODEM 5=KEYBOARD ECHO).
THIS QUESTION REQUESTS THE TYPE OF LOOPBACK TO BE USED WHEN TESTING THE DMU-11.
THE FOLLOWING TYPES ARE SUPPORTED:
 - 0 INTERNAL - ONLY INTERNAL UART LOOPBACK IS TO BE USED IN TESTING THE DMU-11.
 - 0 H3277 - STAGGERED BERG CONNECTOR(S) ARE INSTALLED ON THE BERG CONNECTOR SOCKETS OF THE DMU-11.
 - 0 H325 - SINGLE LINE, 25 PIN LOOPBACK CONNECTORS (TYPE H325) ARE INSTALLED ON THE LINES TO BE TESTED.
 - 0 MODEM - THE OPERATOR IS ALLOWED TO SET UP A MODEM LINK AND THEN PERFORM A TRANSMISSION AND RECEPTION TEST AT A SINGLE BAUDRATE WITH THE MODEM CONTROL SIGNALS DTR AND RTS ACTIVE. THIS IS A SPECIAL TEST AND ALL OTHER TESTS IN THIS PART WILL BE PERFORMED IN INTERNAL LOOPBACK.
 - 0 KEYBOARD ECHO - THE UARTS ON THE DUT ARE PLACED IN REMOTE LOOPBACK. TERMINALS (OR OTHER COMMUNICATIONS EQUIPMENT) WILL HAVE WHATEVER THEY TRANSMIT TO THE DMU LOOPED BACK TO THEM.
5. BR LEVEL - THIS QUESTION REQUESTS THE INTERRUPT BR LEVEL OF THE SPECIFIED DMU-11. THE DEFAULT ANSWER IS BR 5.

2.6 SOFTWARE QUESTIONS

AFTER YOU HAVE ANSWERED THE HARDWARE QUESTIONS OR AFTER A RESTART OR CONTINUE COMMAND, THE RUNTIME SERVICES WILL ASK FOR SOFTWARE PARAMETERS. THESE PARAMETERS WILL GOVERN SOME DIAGNOSTIC SPECIFIC OPERATION MODES. YOU WILL BE PROMPTED BY "CHANGE SW (L) ?" IF YOU WISH TO CHANGE ANY PARAMETERS, ANSWER BY TYPING "Y". THE SOFTWARE QUESTIONS AND THE DEFAULT VALUES ARE DESCRIBED IN THE NEXT PARAGRAPH(S).

1. REPORT UNIT NUMBER AS EACH UNIT IS TESTED - THIS QUESTION ASKS WHETHER THE PROGRAM SHOULD REPORT THE NUMBER OF THE UNIT WHICH IT IS TESTING AS IT BEGINS TO TEST THAT UNIT.
2. REPORT NUMBER OF BITS TESTED IN DMA ADDR TEST - THIS QUESTION ASKS WHETHER THE OPERATOR WANTS A PRINTOUT DESCRIBING WHICH ADDRESS BITS HAVE BEEN TESTED WHEN THE DMA ADDRESSING TEST EXECUTES.
3. EXTENDED ERROR REPORTING - THIS QUESTION ASKS WHETHER EXTENDED ERROR INFORMATION IS REQUIRED OTHER THAN THE "TEST FAILED" MESSAGE, ON EACH ERROR REPORTED. THE DEFAULT IS "NO" I.E. ONLY A MESSAGE REPORTING THE FACT THAT THE TEST FAILED WILL BE PRINTED.
4. NUMBER OF INDIVIDUAL DATA ERRORS TO REPORT ON A LINE - THIS QUESTION IS ASKED ONLY IF THE PREVIOUS QUESTION WAS ANSWERED "YES". THE QUESTION ASKS FOR THE NUMBER OF DATA ERRORS WHICH SHOULD BE REPORTED INDIVIDUALLY BY THIS PROGRAM FOR EACH LINE FOR EACH TRANSMISSION TEST. ERRORS WHICH ARE NOT REPORTED INDIVIDUALLY ARE REPORTED IN SUMMARY ERROR REPORTS.

2.7 EXTENDED P-TABLE DIALOGUE

WHEN YOU ANSWER THE HARDWARE QUESTIONS, YOU ARE BUILDING ENTRIES IN A TABLE THAT DESCRIBES THE DEVICES UNDER TEST. THE SIMPLEST WAY TO BUILD THIS TABLE IS TO ANSWER ALL QUESTIONS FOR EACH UNIT TO BE TESTED. IF YOU HAVE A MULTIPLEXED DEVICE SUCH AS A MASS STORAGE CONTROLLER WITH SEVERAL DRIVES OR A COMMUNICATION DEVICE WITH SEVERAL LINES, THIS BECOMES TEDIOUS SINCE MOST OF THE ANSWERS ARE REPETITIOUS.

TO ILLUSTRATE A MORE EFFICIENT METHOD, SUPPOSE YOU ARE TESTING A FICTIONAL DEVICE, THE XY11. SUPPOSE THIS DEVICE CONSISTS OF A CONTROL MODULE WITH EIGHT UNITS (SUB-DEVICES) ATTACHED TO IT. THESE UNITS ARE DESCRIBED BY THE OCTAL NUMBERS 0 THROUGH 7. THERE IS ONE HARDWARE PARAMETER THAT CAN VARY AMONG UNITS CALLED THE Q-FACTOR. THIS Q-FACTOR MAY BE 0 OR 1. BELOW IS A SIMPLE WAY TO BUILD A TABLE FOR ONE XY11 WITH EIGHT UNITS.

UNITS (0) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0<CR>
Q-FACTOR (0) 0 ? 1<CR>

UNIT 2
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 1<CR>
Q-FACTOR (0) 1 ? 0<CR>

UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 4
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 3<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 5
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 4<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 6
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 5<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 7
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 6<CR>
Q-FACTOR (0) 0 ? 1<CR>

```
UNIT 8
CSR ADDRESS (0) 160000<CR>
SUB-DEVICE # (0) ? 7<CR>
Q-FACTOR (0) 1 ? <CR>
```

NOTICE THAT THE DEFAULT VALUE FOR THE Q-FACTOR CHANGES WHEN A NON-DEFAULT RESPONSE IS GIVEN. BE CAREFUL WHEN SPECIFYING MULTIPLE UNITS!

AS YOU CAN SEE FROM THE ABOVE EXAMPLE, THE HARDWARE PARAMETERS DO NOT VARY SIGNIFICANTLY FROM UNIT TO UNIT. THE PROCEDURE SHOWN IS NOT VERY EFFICIENT.

THE RUNTIME SERVICES CAN TAKE MULTIPLE UNIT SPECIFICATIONS HOWEVER. LET'S BUILD THE SAME TABLE USING THE MULTIPLE SPECIFICATION FEATURE.

```
# UNITS (0) ? 8<CR>
```

```
UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0,1<CR>
Q-FACTOR (0) 0 ? 1,0<CR>
```

```
UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2-5<CR>
Q-FACTOR (0) 0 ? 0<CR>
```

```
UNIT 7
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 6,7<CR>
Q-FACTOR (0) 0 ? 1<CR>
```

AS YOU CAN SEE IN THE ABOVE DIALOGUE, THE RUNTIME SERVICES WILL BUILD AS MANY ENTRIES AS IT CAN WITH THE INFORMATION GIVEN IN ANY ONE PASS THROUGH THE QUESTIONS. IN THE FIRST PASS, TWO ENTRIES ARE BUILT SINCE TWO SUB-DEVICES AND Q-FACTORS WERE SPECIFIED. THE SERVICES ASSUME THAT THE CSR ADDRESS IS 160000 FOR BOTH SINCE IT WAS SPECIFIED ONLY ONCE. IN THE SECOND PASS, FOUR ENTRIES WERE BUILT. THIS IS BECAUSE FOUR SUB-DEVICES WERE SPECIFIED. THE "-" CONSTRUCT TELLS THE RUNTIME SERVICES TO INCREMENT THE DATA FROM THE FIRST NUMBER TO THE SECOND. IN THIS CASE, SUB-DEVICES 2, 3, 4 AND 5 WERE SPECIFIED. (IF THE SUB-DEVICE WERE SPECIFIED BY ADDRESSES, THE INCREMENT WOULD BE BY 2 SINCE ADDRESSES MUST BE ON AN EVEN BOUNDARY.) THE CSR ADDRESSES AND Q-FACTORS FOR THE FOUR ENTRIES ARE ASSUMED TO BE 160000 AND 0 RESPECTIVELY SINCE THEY WERE ONLY SPECIFIED ONCE. THE LAST TWO UNITS ARE SPECIFIED IN THE THIRD PASS.

THE WHOLE PROCESS COULD HAVE BEEN ACCOMPLISHED IN ONE PASS AS SHOWN BELOW.

```
# UNITS (0) ? 8<CR>
```


UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0-7<CR>
Q FACTOR (0) 0 ? 0.1.0, ..1.1<CR>

AS YOU CAN SEE FROM THIS EXAMPLE, NULL REPLIES (COMMAS ENCLOSING
A NULL FIELD) TELL THE RUNTIME SERVICES TO REPEAT THE LAST REPLY.

2.8 QUICK START-UP PROCEDURE (XXDP+)

TO START-UP THIS PROGRAM:

1. BOOT XXDP+.
2. GIVE THE DATE AND ANSWER THE LSI AND 50HZ (IF THERE IS A CLOCK) QUESTIONS
3. TYPE "R NAME", WHERE NAME IS THE NAME OF THE BIN OR BIC FILE FOR THIS PROGRAM
4. TYPE "START"
5. ANSWER THE "CHANGE HW" QUESTION WITH "Y"
6. ANSWER ALL THE HARDWARE QUESTIONS
7. ANSWER THE "CHANGE SW" QUESTION WITH "N"

WHEN YOU FOLLOW THIS PROCEDURE YOU WILL BE USING ONLY THE DEFAULTS FOR FLAGS AND SOFTWARE PARAMETERS. FOR DEFAULT INFORMATION SEE THE SECTIONS WITHIN THIS DOCUMENT ON FLAGS, AND HARDWARE QUESTIONS.

3.0 ERROR INFORMATION

3.1 TYPES OF ERROR MESSAGES

THERE ARE THREE LEVELS OF ERROR MESSAGES THAT MAY BE ISSUED BY A DIAGNOSTIC: GENERAL, BASIC AND EXTENDED. GENERAL ERROR MESSAGES ARE ALWAYS PRINTED UNLESS THE "IER" FLAG IS SET (SEE THE FLAGS SECTION OF THIS DOCUMENT).

THE GENERAL ERROR MESSAGE IS OF THE FORM:

NAME TYPE NUMBER ON UNIT NUMBER TST NUMBER PC:XXXXXX
ERROR MESSAGE

WHERE; NAME = DIAGNOSTIC NAME
TYPE = ERROR TYPE (SYS FATAL, DEV FATAL, HARD OR SOFT)
NUMBER = ERROR NUMBER
UNIT NUMBER = 0 - N (N IS LAST UNIT IN PTABLE)
TST NUMBER = TEST AND SUBTEST WHERE ERROR OCCURRED
PC:XXXXXX = ADDRESS OF ERROR MESSAGE CALL

BASIC ERROR MESSAGES ARE MESSAGES THAT CONTAIN SOME ADDITIONAL INFORMATION ABOUT THE ERROR. THESE ARE ALWAYS PRINTED UNLESS THE "IER" OR "IBR" FLAGS ARE SET (SEE THE FLAGS SECTION OF THIS DOCUMENT).
THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL MESSAGE.

EXTENDED ERROR MESSAGES CONTAIN SUPPLEMENTARY ERROR INFORMATION SUCH AS REGISTER CONTENTS OR GOOD/BAD DATA. THESE ARE ALWAYS PRINTED UNLESS THE "IER", "IBR" OR "IXR" FLAGS ARE SET (SEE THE

FLAGS SECTION OF THIS DOCUMENT).
THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL ERROR
MESSAGE AND ANY ASSOCIATED BASIC ERROR MESSAGES.

3.2 SPECIFIC ERROR MESSAGES

THIS PROGRAM IS INTENDED TO PROVIDE A GO/NOGO INDICATION
OF THE FUNCTIONALITY OF THE DHU-11 BOARDS. TO EXECUTE THE
PROGRAM IN THIS MODE THE OPERATOR NEED ONLY ANSWER THE
"EXTENDED ERROR REPORTING" SOFTWARE QUESTION WITH "NO". THE
PROGRAM WILL THEN ONLY PRINT THE NAME OF THE FAILING TEST
THE TEST AND ERROR NUMBERS. FOR A LIST OF THE TEST NAMES
IN THIS PROGRAM SEE THE TEST SUMMARIES SECTION OF THIS
DOCUMENT. AN EXAMPLE OF SUCH A AN ERROR MESSAGE IS THE
FOLLOWING:

CZDMW DVC FTL ERR 4409 ON UNIT 00 TST 04 SUB 000 PC: XXXXXX
DMA ADDRESS TEST FAILED

THIS ERROR INDICATES THAT A FATAL ERROR WAS ENCOUNTERED
DURING THE TEST WHICH TESTS THE DMA_ABORT BIT.

IF THE OPERATOR HAD REQUESTED EXTENDED ERROR REPORTING THE
SAME ERROR WOULD BE REPORTED AS FOLLOWS:

CZDMW DVC FTL ERR 4409 ON UNIT 00 TST 04 SUB 000 PC: XXXXXX
DMA ADDRESS TEST FAILED
BAD BITS BETWEEN BITS 0 AND 15.

4.0 PERFORMANCE AND PROGRESS REPORTS

AT THE END OF EACH PASS, THE PASS COUNT IS GIVEN ALONG WITH THE TOTAL NUMBER OF ERRORS REPORTED SINCE THE DIAGNOSTIC WAS STARTED. THE "EOP" SWITCH CAN BE USED TO CONTROL HOW OFTEN THE END OF PASS MESSAGE IS PRINTED. FOR FURTHER INFORMATION SEE THE SWITCHES SECTION OF THIS DOCUMENT.

5.0 TEST SUMMARIES

THE FOLLOWING ARE INCLUDED WITHIN CZDHWA:

1. DEVICE REGISTER ACCESS TEST - VERIFIES THAT THE UUT REGISTERS WILL RESPOND WITH THE CORRECT UNIBUS HANDSHAKING SIGNALS. VERIFIES THAT THE UUT IS AT THE CORRECT ADDRESS.
2. KEYBOARD ECHO TEST - ALLOWS THE OPERATOR TO TEST TERMINAL LINKS (OR OTHER COMMUNICATIONS LINKS), WHICH ARE ATTACHED TO UUT SERIAL PORTS, FROM REMOTE ENDS OF THE LINKS.
3. MODEM LOOPBACK TEST - ALLOWS THE OPERATOR TO TEST MODEM LINKS WHICH ARE ATTACHED TO THE UUT SERIAL PORTS.
4. DMA ADDR TEST - VERIFIES THAT THE UUT CAN ACCESS THE FULL MEMORY WHICH IS ON THE MACHINE VIA DMA ACCESS.
5. FRAMING ERROR TEST - VERIFIES THAT FORCED FRAMING ERRORS ARE REPORTED CORRECTLY.
6. PARITY ERROR TEST - VERIFIES THAT FORCED PARITY ERRORS ARE REPORTED CORRECTLY.
7. DMA MODE TEST - VERIFIES THAT THE UUT WILL TX AND RX DATA CORRECTLY USING DMA TRANSMISSION.
8. SPLIT SPEED TEST - VERIFIES THAT THE UUT WILL FUNCTION CORRECTLY USING DIFFERENT TX AND RX SPEEDS ON EACH ACTIVE LINE.
9. REPORT BMP CODES TEST - THIS PSEUDO TEST REPORTS THE FIRST 32 CHARACTERS WHICH WERE DISCOVERED IN THE FIFO DURING THE EXECUTION OF THE OTHER TESTS. THIS AVOIDS INTERRUPTION OF THE OTHER TESTS BY THESE CODES IF THEY ARE NOT CRITICAL TO THE PERFORMANCE OF THE TESTS.

6.0 EXAMPLE ERROR FREE PASS

THE FOLLOWING IS AN EXAMPLE OF AN ERROR FREE PASS DIALOGUE:

.R CZDHWAO
CZDHWAO.BIN
DRS
CZDHW-A-0
DHU FUNC TST PART3
UNIT IS DHU-11
RESTRT ADDR: 147670
DR>STA/PAS:1

CHANGE HW (L) ? Y

* UNITS (D) ? 2

UNIT 0
CSR ADDRESS: (0) 160460 ? +Z

UNIT 1
CSR ADDRESS: (0) 160460 ? 160500
INTERRUPT VECTOR ADDRESS: (0) 310 ? 320
ACTIVE LINE BIT MAP: (0) 177777 ? <CR>
TYPE OF LOOPBACK (1=INTERNAL, 2=H3277, 3=H325
4=MODEM, 5=KEYBOARD ECHO): (0) 2 ? 1
INTERRUPT BR LEVEL: (0) 5 ? <CR>

CHANGE SW (L) ? Y

REPORT UNIT NUMBER AS EACH UNIT IS TESTED: (L) Y ? <CR>
REPORT NUMBER OF BITS TESTED IN DMA ADDR TEST: (L) N ? <CR>
EXTENDED ERROR REPORTING: (L) N ? <CR>

TESTING UNIT : 0

TESTING UNIT : 1

CZDHW EOP 1
0 TOTAL ERRS

DR>

&

1020
1021
1022
1023
1024
1025
1026
1027
1028
1029
1030
1031

.LIST SEQ,LOC,BIN,MEB
.NLIST CND

```
;*****
;
;          FVTA.PHD
;
;*****
```

.SBTTL PROGRAM HEADER

```

1032
1033
1034          .MCALL SVC
1035 000000          SVC          ; INITIALIZE SUPERVISOR MACROS
1036
1037          ;*****
1038          ; IF STRUCTURED MACROS ARE TO BE USED, ADD ".MCALL STRUCT" AND "STRUCT"
1039          ; TO INITIALIZE THE STRUCTURED MACROS.
1040
1041          000001          SVCINS= 1          ; LIST INSTRUCTIONS, SHIFTED RIGHT
1042          000001          SVCTST= 1          ; LIST TEST TAGS, SHIFTED RIGHT
1043          000001          SVCSUB= 1          ; LIST SUBTEST TAGS, SHIFTED RIGHT
1044          000001          SVCGBL= 1          ; LIST GLOBAL TAGS, SHIFTED RIGHT
1045          000001          SVCTAG= 1          ; LIST OTHER TAGS, SHIFTED RIGHT
1046
1047          ; CHANGE THE VALUES OF THE SVC... SYMBOLS TO BE ZERO IF YOU WISH
1048          ; TO ALIGN THE MACRO CALLS AND THEIR EXPANSIONS. CHANGE THE
1049          ; SYMBOLS TO BE MINUS-ONE TO NOT LIST THE EXPANSIONS. YOU MAY
1050          ; CHANGE THE SYMBOLS AT ANY POINT IN YOUR PROGRAM.
1051          ;*****
1052
1053 000000          .ENABL ABS
1054          .ENABL AMA
1055          002000          "          2000
1056
1057 002000          BGNMOD
1058
1059          ;**
1060          ; THE PROGRAM HEADER IS THE INTERFACE BETWEEN
1061          ; THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
1062          ;--
1063
1064 002000          POINTER BGNRPT,BGNSW,BGNSFT,BGNDU,ERRTBL
1065
1082
1083 002000          HEADER CZDHW,A,0,200,0,PRI07
1083 002000
1083 002000          103
1083 002001          132
1083 002002          104
1083 002003          110
1083 002004          127
1083 002005          000
1083 002006          000
1083 002007          000
1083 002010
1083 002010          101
1083 002011
1083 002011          060
1083 002012
1083 002012          000000
1083 002014
1083 002014          000200
1083 002016
1083 002016          036702
1083 002020
1083 002020          037246

```

```

L$NAME::
          .ASCII /C/
          .ASCII /Z/
          .ASCII /D/
          .ASCII /H/
          .ASCII /W/
          .BYTE 0
          .BYTE 0
          .BYTE 0
L$REV::
          .ASCII /A/
L$DEPO::
          .ASCII /C/
L$UNIT::
          .WORD 0
L$TIML::
          .WORD 200
L$HPCP::
          .WORD L$HARD
L$SPCP::
          .WORD L$SOFT

```

002022
 002022 002150
 002024
 002024 002162
 002026
 002026 037636
 002030
 002030 000000
 002032
 002032 000000
 002034
 002034 000000
 002036
 002036 000000
 002040
 002040 002124
 002042
 002042 000340
 002044
 002044 000000
 002046
 002046 000000
 002050
 002050 003
 002051 003
 002052
 002052 000000
 002054 000000
 002056
 002056 000000
 002060
 002060 005364
 002062
 002062 027652
 002064
 002064 000000
 002066
 002066 000000
 002070
 002070 000000
 002072
 002072 030526
 002074
 002074 000000
 002076
 002076 005374
 002100
 002100 104035
 002102
 002102 005314
 002104
 002104 027666
 002106
 002106 030510
 002110
 002110 030506
 002112

L\$HPTP::
 L\$SPTP:: .WORD L\$HW
 L\$LADP:: .WORD L\$SW
 L\$STA:: .WORD L\$LAST
 L\$CO:: .WORD 0
 L\$DTP:: .WORD 0
 L\$APT:: .WORD 0
 L\$DTP:: .WORD 0
 L\$PRIO:: .WORD L\$DISPATCH
 L\$ENVI:: .WORD PRI07
 L\$EXP1:: .WORD 0
 L\$MREV:: .WORD 0
 L\$EF:: .BYTE C\$REVISION
 .BYTE C\$EDIT
 L\$SPC:: .WORD 0
 L\$DEVP:: .WORD 0
 L\$REPP:: .WORD L\$DVTYP
 L\$EXP4:: .WORD L\$RPT
 L\$EXP5:: .WORD 0
 L\$AUT:: .WORD 0
 L\$DUT:: .WORD L\$DU
 L\$LUN:: .WORD 0
 L\$DESP:: .WORD L\$DESC
 L\$LOAD:: EMT E\$LOAD
 L\$ETP:: .WORD L\$ERRTBL
 L\$ICP:: .WORD L\$INIT
 L\$CCP:: .WORD L\$CLEAN
 L\$ACP:: .WORD L\$AUTO
 L\$PRT::

002112 027660
002114
002114 000000
002116
002116 000000
002120
002120 000000

1084

L\$TEST:: .WORD L\$PROT
L\$DLY:: .WORD 0
L\$HIME:: .WORD 0
L\$HIME:: .WORD 0

1096
1097
1098
1099
1100
1101
1102
1103

```
.SBITL  DISPATCH TABLE
```

```

; **
; THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.
; IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.
; -

```

1103	002122	
	002122	000011
	002124	
	002124	030644
	002126	031126
	002130	031370
	002132	032322
	002134	034000
	002136	034414
	002140	035072
	002142	036060
	002144	036620

DISPATCH 9

```

          .WORD      9
L$DISPATCH::
          .WORD      T1
          .WORD      T2
          .WORD      T3
          .WORD      T4
          .WORD      T5
          .WORD      T6
          .WORD      T7
          .WORD      T8
          .WORD      T9

```

1104

```

1112
1113
1114
1115
1116
1117
*****
1118
1119
1120
1121
1122
1123
1124
1125
1126
1127
1128
1129
1130 002146
      002146 000004
      002150
      002150
1131
1132 002150 160460
1133 002152 000310
1134 002154 177777
1135 002156 002
1136 002157 005
1137
1138 002160
      002160

;*****
;
;          FVTA.DHT
;
;*****

.SBTTL  DEFAULT HARDWARE P-TABLE

; **
; THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
; THE TEST-DEVICE PARAMETERS.  THE STRUCTURE OF THIS TABLE
; IS IDENTICAL TO THE STRUCTURE OF THE HARDWARE P-TABLES,
; AND IS USED AS A "TEMPLATE" FOR BUILDING THE P TABLES.
; --

      BGNHW  DFPTBL

                                .WORD  L10000-L$HW/2
                                L$HW::
                                DFPTBL::

      .WORD  160460 ;DEFAULT CSR ADDRESS
      .WORD  310   ;DEFAULT VECTOR ADDRESS
      .WORD  177777 ;DEFAULT ACTIVE LINES BIT MAP
      .BYTE  2     ;DEFAULT LOOPBACK MODE
      .BYTE  5     ;DEFAULT BR LEVEL

      ENDHW

                                L10000:

```

```

1140                                     ; *****
1141                                     ;
1142                                     ;
1143                                     ;           FVTA.SWT
1144                                     ;
1145                                     ; *****
1146
1147
1148
1149      .SBTTL  SOFTWARE P-TABLE
1150
1151      ;**
1152      ; THE SOFTWARE TABLE CONTAINS VARIOUS DATA USED BY THE
1153      ; PROGRAM AS OPERATIONAL PARAMETERS.  THESE PARAMETERS ARE
1154      ; SET UP AT ASSEMBLY TIME AND MAY BE VARIED BY THE OPERATOR
1155      ; AT RUN TIME.
1156      ;--
1157
1158      002160      BGNSW      SFPTBL
1159      002160      000002
1160                                     .WORD      L10001-L$SW/2
1161      002162                                     L$SW::
1162      002162                                     SFPTBL::
1163
1164      002162      000020      OPTION::      .WORD      20      ;BIT MAP OF PROGRAM CONTROL FLAGS
1165      002164      000000      nderpt::      .WORD      0      ;DEFAULT NUMBER OF INDIVIDUAL DATA ERRORS TO RPT.
1166
1167      002166      ENDSW
1168      002166                                     L10001:

```



```

1165
1166 ;*****
1167 ;
1168 ;           FVTA.EQU
1169 ;
1170 ;*****
1171
1172
1173 .SBTTL  GLOBAL EQUATES SECTION
1174
1175
1176
1177
1178 ;**
1179 ; THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
1180 ; ARE USED IN MORE THAN ONE TEST.
1181 ;--
1182
1183
1184
1185
1186
1187
1188
1189
1190
1191
1192         000020          NUMLNS==20      ;NUMBER OF LINES ON DHV11 IS 8.
1193         177777          MAPLNS==177777  ;BIT MAP OF LINES ON DHV11.
1194
1195 ;***** DEVICE REGISTER OFFSETS FROM THE CSR'S ADDRESS *****
1196         000000          CSRO==0         ;CSR REGISTER OFFSET FROM THE CSR ADDRESS
1197         000002          RBUFO==2        ;RECEIVE REGISTER OFFSET FROM THE CSR ADDRESS
1198         000002          RXTIMO==2       ;RECIEVE TIMER REGISTER OFFSET FROM THE CSR ADDRESS
1199         000004          LPRO==4         ;LINE PARAMETER REGISTER OFFSET FROM THE CSR ADDRESS
1200         000006          FLSO==6        ;FIFOSIZE/STATUS REGISTER OFFSET FROM THE CSR ADDRESS
1201         000006          FDATA==6       ;FIFO DATA REGISTER OFFSET FROM THE CSR ADDRESS
1202         000010          LNCTRO==10     ;LINE CONTROL REGISTER OFFSET FROM THE CSR ADDRESS
1203         000012          TXAD10==12     ;TRANSMIT ADDRESS 1 REGISTER OFFSET FROM THE CSR ADDRESS
1204         000014          TXAD20==14     ;TRANSMIT ADDRESS 2 REGISTER OFFSET FROM THE CSR ADDRESS
1205         000016          TXBFCO==16     ;TRANSMIT COUNT REGISTER OFFSET FROM THE CSR ADDRESS
1206
1207 ;***** EQUATES USED WITH RESPECT TO THE RX BUFFER *****
1208         000020          RXBETX==16.     ;LEVEL OF RX BUFFER AT WHICH TO RE-ENABLE TRANSMISSION.
1209         000030          RXBDTX==24.     ;LEVEL OF RX BUFFER AT WHICH TO DISABLE TRANSMISSION.
1210         000100          RXBFUL==64.     ;TOTAL CHARACTER CAPACITY OF THE RX BUFFER.
1211
1212
1213
1214
1215
1216
1217
1218
1219
1220
1221
1222
1223
1224
1225
1226
1227 002166

```

EQUALS

; BIT DIFINITIONS

```

100000
040000
020000
010000
004000
002000
001000
000400
000200
000100
000040
000020
000010
000004

```

```

BIT15== 100000
BIT14== 40000
BIT13== 20000
BIT12== 10000
BIT11== 4000
BIT10== 2000
BIT09== 1000
BIT08== 400
BIT07== 200
BIT06== 100
BIT05== 40
BIT04== 20
BIT03== 10
BIT02== 4

```

```

000002      BIT01== 2
000001      BIT00== 1
;
001000      BIT9==  BIT09
000400      BIT8==  BIT08
000200      BIT7==  BIT07
000100      BIT6==  BIT06
000040      BIT5==  BIT05
000020      BIT4==  BIT04
000010      BIT3==  BIT03
000004      BIT2==  BIT02
000002      BIT1==  BIT01
000001      BIT0==  BIT00
;
; EVENT FLAG DEFINITIONS
; EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION
;
000040      EF.START==      32.      ; START COMMAND WAS ISSUED
000037      EF.RESTART==    31.      ; RESTART COMMAND WAS ISSUED
000036      EF.CONTINUE==   30.      ; CONTINUE COMMAND WAS ISSUED
000035      EF.NEW==        29.      ; A NEW PASS HAS BEEN STARTED
000034      EF.PWR==        28.      ; A POWER-FAIL/POWER-UP OCCURRED
;
; PRIORITY LEVEL DEFINITIONS
;
000340      PRI07== 340
000300      PRI06== 300
000240      PRI05== 240
000200      PRI04== 200
000140      PRI03== 140
000100      PRI02== 100
000040      PRI01== 40
000000      PRI00== 0
;
; OPERATOR FLAG BITS
;
000004      EVL==      4
000010      LOT==     10
000020      ADR==     20
000040      IDU==     40
000100      ISR==    100
000200      UAM==    200
000400      BOE==    400
001000      PNT==   1000
002000      PRI==   2000
004000      IXE==   4000
010000      IBE==  10000
020000      IER==  20000
040000      LOE==  40000
100000      MOE== 100000

```

```

1230
1231
1232
1233
1234
1235
1236
1237
1238
1239
1240
1241
1242
1243
1244
1245
1246
1247
1248
1249
1250 002166 000300
1251 002170 000304
1252 002172 000377
1253 002174 000
1254 002175 004
1255 002176 000000
1256
1257
1258
1259
1260
1261 002200
1262 002200 160020
1263 002202 160022
1264 002204 160024
1265 002206 160026
1266
1267 002210 160030
1268 002212 160032
1269 002214 160034
1270 002216 160036
1271
1272
1273
1274
1275 002220 000000
1276 002222 000000
1277 002224 000000
1278 002226 000000
1279 002230 000000
1280 002232 000000
1281 002234 000000
1282 002236 031463
1283 002240 146314
1284 002242 000000
1285 002244 000000
1286 002246 000000

```

```

;*****
;
;          FVTC.GDT
;
;*****

.SBTTL  GLOBAL DATA SECTION

; **
; THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
; IN MORE THAN ONE TEST.
; --

;*****
;          UNIT VARIABLE AREA
;*****

RXVECA:: .WORD 300      ;RX VECTOR ADDRESS.
TXVECA:: .WORD 304      ;TX VECTOR ADDRESS.
ACTLNS:: .WORD 377      ;ACTIVE LINE BIT MAP.
LOPBCK:: .BYTE 0        ;LOOPBACK MODE
BRLEVL:: .BYTE 4        ;INTERRUPT BUS REQUEST LEVEL
UNITN:: .WORD 0         ;UNIT NUMBER.

;*****
;          DEVICE REGISTER ADDRESS TABLE
;*****
DRADRT::
CSRA:: .WORD 160020     ;DHU-11 CSR ADDRESS.
RXTMA:: RBUFA:: .WORD 160022 ;DHU-11 RECIEVE BUFFER/TIMER ADDRESS.
LPRA:: .WORD 160024     ;DHU-11 LINE PARAMETER REGISTER ADDRESS.
FDATA:: FLSA:: .WORD 160026 ;DHU-11 FIFO SIZE/LINE STATUS REGISTER ADDRESS,
;AND FIFO DATA REGISTER ADDRESS.
LNCTRA:: .WORD 160030   ;DHU-11 LINE CONTROL REGISTER ADDRESS.
TXAD1A:: .WORD 160032   ;DHU-11 TRANSMIT BUFFER 1 REGISTER ADDRESS
TXAD2A:: .WORD 160034   ;DHU-11 TRANSMIT BUFFER 2 REGISTER ADDRESS
TXBFCA:: .WORD 160036   ;DHU-11 TRANSMIT BUFFER COUNT REGISTER ADDRESS

;*****
;          ASSORTED GLOBAL VARIABLES:
;*****
CTRLCF:: .WORD 0        ;STORAGE FOR THE CONTROL-C FLAG.
DMTSTA:: .WORD 0        ;STO'G FOR DMA TEST ADDRESS (IN PAR FORM).
FERROR:: .WORD 0        ;STORAGE FOR "AT LEAST ONE ERROR" INDICATOR.
FFREM:: .WORD 0         ;STO'G FOR ADR OF FIRST FREE WORD AFTER THE DIAG'TIC
GMANMD:: .WORD 0        ;WORD FOR GMANXX CALL RETURN PARAMETERS.
IB1:: .WORD 0           ;INACTIVE TX/RX BITS MASK.
IESTAT:: .WORD 0        ;STORAGE FOR STATES OF THE DUT INT ENABLE BITS.
LGRP1M:: .WORD 31463     ;BIT MAP OF LINES IN LINE GROUP I.
LGRP2M:: .WORD 146314    ;BIT MAP OF LINES IN LINE GROUP II.
PASCNT:: .WORD 0        ;STO'G FOR PASS COUNT USED IN ROM VERSION# TST.
PMSFLG:: .WORD 0        ;FLAG INDICATING WHETHER TO PRINT MODEM STATUS.
RXTOUT:: .WORD 0        ;TIME-OUT VALUE FOR WAITING FOR LAST RX CHAR.

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1287 002250 000000 SAVPRI:: .WORD 0 ;STO'G FOR PROCESSOR PRIORITY, (TXROFF, TXRON).
1288 002252 000000 SAVTEN:: .WORD 0 ;STORAGE FOR TX.ENABLE STATES, (TXROFF, TXRON).
1289 002254 000000 TP4FLG:: .WORD 0 ;FLAGS SET WHEN AN EXPECTED 004 TRAP OCCURS.
1290 002256 000000 TP4VEC:: .WORD 0 ;STORAGE FOR THE NORMAL 004 TRAP VECTOR.
1291 002260 000001 TSTNUM:: .WORD 1 ;STORAGE FOR THE TEST NUMBER.
1292 002262 000000 TXENBM:: .WORD 0 ;STORAGE FOR TX.ENABLE STATES, (BUFFER MGM'NT).
1293 002264 000000 TXINTF:: .WORD 0 ;STORAGE FOR TRANSMIT INTERRUPT FLAGS.
1294 002266 000000 WORD1:: .WORD 0 ;LOCATION FOR PASSING INDIRECT PARAMETERS.
1295
1296 ;*****
1297 ; LINE TIME CLOCK VARIABLES AND STORAGE.
1298 ;*****
1299 002270 177546 CLKCSR:: .WORD 177546 ;CSR ADDRESS OF THE LTC.
1300 002272 000300 CLKBRL:: .WORD PRI06 ;INTERRUPT PRIORITY LEVEL OF THE LTC.
1301 002274 000100 CLKVEC:: .WORD 100 ;INTERRUPT VECTOR ADDRESS OF THE LTC.
1302 002276 000074 CLKHRZ:: .WORD 60. ;INTERRUPT FREQUENCY OF THE LTC.
1303 002300 000000 TIMER1:: .WORD 0 ;HARDWARE CLOCK COUNTER #1.
1304 002302 000000 TIMER2:: .WORD 0 ;HARDWARE CLOCK COUNTER #2.
1305 002304 000170 TIMER3:: .WORD 120. ;HARDWARE BREAK COUNTER LOCATION.
1306 002306 000170 BCOUNT:: .WORD 120. ;BREAK COUNT VALUE IN CLOCK TICKS.
1307 002310 000021 MSTICK:: .WORD 17. ;NUMBER OF MILLI-SECONDS PER LTC TICK.
1308 002312 000062 MSLCNT:: .WORD 62 ;LOOP COUNT (USED BY MSLOOP) TO DELAY 1 MS.
1309
1310 ;*****
1311 ; MEMORY MANAGEMENT VARIABLES AND FLAGS.
1312 ;*****
1313 002314 177572 HMSRO:: .WORD 177572 ;ADDRESS OF MEM MGT STATUS REGISTER #0.
1314 002316 172516 HMSR3:: .WORD 172516 ;ADDRESS OF MEM MGT STATUS REGISTER #3.
1315 002320 000000 HMPRES:: .WORD 0 ;MEM MGT PRESENT FLAG (0 IF MM NOT PRESENT).
1316 002322 000000 HMENAB:: .WORD 0 ;MEM MGT ENABLED FLAG (0 IF MM NOT ENABLED).
1317
1318 002324 PARATB:: ;BASE OF MEM MGT PAR ADDRESS TABLE.
1319 002324 172340 PAR0A:: .WORD 172340 ;ADDRESS OF MEM MGT PAR #0.
1320 002326 172342 PAR1A:: .WORD 172342 ;ADDRESS OF MEM MGT PAR #1.
1321 002330 172344 PAR2A:: .WORD 172344 ;ADDRESS OF MEM MGT PAR #2.
1322 002332 172346 PAR3A:: .WORD 172346 ;ADDRESS OF MEM MGT PAR #3.
1323 002334 172350 PAR4A:: .WORD 172350 ;ADDRESS OF MEM MGT PAR #4.
1324 002336 172352 PAR5A:: .WORD 172352 ;ADDRESS OF MEM MGT PAR #5.
1325 002340 172354 PAR6A:: .WORD 172354 ;ADDRESS OF MEM MGT PAR #6.
1326 002342 172356 PAR7A:: .WORD 172356 ;ADDRESS OF MEM MGT PAR #7.
1327 002344 PARATE:: ;END OF PAR ADDRESS TABLE.
1328
1329 002344 PDRATB:: ;BASE OF MEM MGT PDR ADDRESS TABLE.
1330 002344 172300 PDR0A:: .WORD 172300 ;ADDRESS OF MEM MGT PDR #0.
1331 002346 172302 PDR1A:: .WORD 172302 ;ADDRESS OF MEM MGT PDR #1.
1332 002350 172304 PDR2A:: .WORD 172304 ;ADDRESS OF MEM MGT PDR #2.
1333 002352 172306 PDR3A:: .WORD 172306 ;ADDRESS OF MEM MGT PDR #3.
1334 002354 172310 PDR4A:: .WORD 172310 ;ADDRESS OF MEM MGT PDR #4.
1335 002356 172312 PDR5A:: .WORD 172312 ;ADDRESS OF MEM MGT PDR #5.
1336 002360 172314 PDR6A:: .WORD 172314 ;ADDRESS OF MEM MGT PDR #6.
1337 002362 172316 PDR7A:: .WORD 172316 ;ADDRESS OF MEM MGT PDR #7.
1338 002364 PDRATE:: ;END OF MEM MGT PDR ADDRESS TABLE.
1339
1340 ;*****
1341 ; TABLE OF WORDS WITH CORRESPONDING BIT SET FOR GENERATION OF BIT MAPS.
1342 ;*****
1343 002364 000001 BITTBL:: .WORD 1 ;BIT 0 SET.

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1344 002366 000002      .WORD 2      ;BIT 1 SET.
1345 002370 000004      .WORD 4      ;BIT 2 SET.
1346 002372 000010      .WORD 10     ;BIT 3 SET.
1347 002374 000020      .WORD 20     ;BIT 4 SET.
1348 002376 000040      .WORD 40     ;BIT 5 SET.
1349 002400 000100      .WORD 100    ;BIT 6 SET.
1350 002402 000200      .WORD 200    ;BIT 7 SET.
1351 002404 000400      .WORD 400    ;BIT 8 SET.
1352 002406 001000      .WORD 1000   ;BIT 9 SET.
1353 002410 002000      .WORD 2000   ;BIT 10 SET.
1354 002412 004000      .WORD 4000   ;BIT 11 SET.
1355 002414 010000      .WORD 10000  ;BIT 12 SET.
1356 002416 020000      .WORD 20000  ;BIT 13 SET.
1357 002420 040000      .WORD 40000  ;BIT 14 SET.
1358 002422 100000      .WORD 100000 ;BIT 15 SET.
1359
1360 ;*****
1361 ;*      TABLE OF DUT BAUDRATES
1362 ;*****
1363 002424      BRTBLB::      ;BASE OF DUT BAUD RATE TABLE.
1364 002424 000062      .WORD 50.     ;BAUD RATE ENTRY FOR CODE 0.
1365 002426 000113      .WORD 75.     ;BAUD RATE ENTRY FOR CODE 1.
1366 002430 000156      .WORD 110.    ;BAUD RATE ENTRY FOR CODE 2.
1367 002432 000206      .WORD 134.    ;BAUD RATE ENTRY FOR CODE 3.
1368 002434 000226      .WORD 150.    ;BAUD RATE ENTRY FOR CODE 4.
1369 002436 000454      .WORD 300.    ;BAUD RATE ENTRY FOR CODE 5.
1370 002440 001130      .WORD 600.    ;BAUD RATE ENTRY FOR CODE 6.
1371 002442 002260      .WORD 1200.   ;BAUD RATE ENTRY FOR CODE 7.
1372 002444 003410      .WORD 1800.   ;BAUD RATE ENTRY FOR CODE 8.
1373 002446 003720      .WORD 2000.   ;BAUD RATE ENTRY FOR CODE 9.
1374 002450 004540      .WORD 2400.   ;BAUD RATE ENTRY FOR CODE 10.
1375 002452 011300      .WORD 4800.   ;BAUD RATE ENTRY FOR CODE 11.
1376 002454 016040      .WORD 7200.   ;BAUD RATE ENTRY FOR CODE 12.
1377 002456 022600      .WORD 9600.   ;BAUD RATE ENTRY FOR CODE 13.
1378 002460 045400      .WORD 19200.  ;BAUD RATE ENTRY FOR CODE 14.
1379 002462 113000      .WORD 38400.  ;BAUD RATE ENTRY FOR CODE 15.
1380 002464      BRTBLE::      ;LABEL AFTER END OF DUT BAUDRATE TABLE.
1381 ;*****
1382 ;*      GP SAVE AREAS ZERO AND ONE.
1383 ;*****
1384 002464      GPRS0B::      ;BASE OF GPR SAVE AREA NUMBER ZERO.
1385 002464 000000      .WORD 0        ;WORD 1, STORAGE FOR R1.
1386 002466 000000      .WORD 0        ;WORD 2, STORAGE FOR R2.
1387 002470 000000      .WORD 0        ;WORD 3, STORAGE FOR R3.
1388 002472 000000      .WORD 0        ;WORD 4, STORAGE FOR R4.
1389 002474 000000      .WORD 0        ;WORD 5, STORAGE FOR R5.
1390 ;*****
1391 ;*      TRANSMISSION AND RECEPTION VARIABLES, POINTERS, AND FLAGS.
1392 ;*****
1393 002476 000000      CHRTOT:: .WORD 0      ;TOTAL RECEIVED CHARACTER COUNTER.
1394 002500 000000      ERSRFF:: .WORD 0     ;"PRINT ERROR SUMMARY" FLAGS.
1395 002502 000000      TXDONF:: .WORD 0     ;TRANSMISSION DONE FLAGS.
1396 002504 000000      RXDONF:: .WORD 0     ;RECEPTION DONE FLAGS.
1397 002506 000000      TXDBLF:: .WORD 0     ;"TX HAS BEEN DISABLED" FLAG.
1398 ;*****
1399 ;      STORAGE AREA FOR THE BMP CODE QUEUE.
1400 ;*****

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1401 002510 000000      BMPCQP::      .WORD    0      ; POINTER USED TO ACCESS THE NEXT CELL IN QUE.
1402 002512             BMPCQB::      .BLKW   64.      ; STORAGE FOR 32 CELLS, TEST# PLUS BMP CODE.
1403 002712             BMPCQE::      ; LAST ADDRESS PLUS 2 OF THE BMP CODE QUEUE.
1404
1405             ;*****
1406             ;*      RECEIVE BUFFER AND ASSOCIATED VARIABLES.
1407             ;*****
1407 002712 000000      RXBOPT:: .WORD    0      ; RX BUFFER OUTPUT POINTER.
1408 002714 000000      RXBIPT:: .WORD    0      ; RX BUFFER INPUT POINTER.
1409 002716 000000      RXBCNT:: .WORD    0      ; COUNT OF NUMBER OF CHARS IN RX BUFFER.
1410 002720             RXBSTA::      ; LABEL AT BEGINNING OF THE RX BUFFER.
1411 002720             .BLKW   RXBFUL      ; LEAVE ENOUGH ROOM FOR A FULL BUFFER.
1412 003120 000000      RXBEND:: .WORD    0      ; LABEL AFTER END OF RX BUFFER.
1413
1414             ;*****
1415             ;*      TX/RX CONTROL BLOCK.
1416             ;*****
1416 003122             CBB::      ; BASE OF TX/RX CONTROL BLOCK.
1417 003122 000000      CBLPRA:: .WORD    0      ; LINE PARAMETER REGISTER CONTENTS.
1418 003124 000000      CBLNCA:: .WORD    0      ; LINE CONTROL REGISTER CONTENTS.
1419 003126 000000      CBDPAA:: .WORD    0      ; START ADDRESS OF DATA PATTERN.
1420 003130 000000      CBDPLA:: .WORD    0      ; LENGTH OF DATA PATTERN.
1421 003132 000000      CBDPNA:: .WORD    0      ; NUMBER OF REPEAT TRANSMISSIONS OF THE DATA PATTERN.
1422 003134 000000      CBMAPA:: .WORD    0      ; BIT MAP OF LINES TO INITIALISE.
1423 003136 000000      CBLPBA:: .WORD    0      ; LOOPBACK MODE (AS IN LOPBCK).
1424 003140 000000      CBOFSA:: .WORD    0      ; AMOUNT OF OFFSET BETWEEN EACH TX START.
1425
1426             ;*****
1427             ;*      TRANSMISSION AND RECEPTION TABLES OF POINTERS AND COUNTERS.
1428             ;*****
1428 003142             DPENDB:: .BLKW   16.      ; TABLE OF END ADDRESSES OF DATA PATTERNS.
1429 003202             DPLENB:: .BLKW   16.      ; TABLE OF LENGTH OF DATA PATTERNS FOR LINES.
1430 003242             EXCNTB:: .BLKW   16.      ; EXTRA RECEIVED CHARACTER COUNTERS TABLE.
1431 003302             ERCNTB:: .BLKW   16.      ; CHARACTER RECEIVE ERROR COUNTERS TABLE.
1432 003342             TXPTRB:: .BLKW   16.      ; TRANSMISSION DATA POINTERS TABLE.
1433 003402             RXPTRB:: .BLKW   16.      ; RECEPTION DATA POINTERS TABLE.
1434 003442             CHCNTB:: .BLKW   16.      ; NUMBER OF CHARACTERS TO BE TXED AND RXED.
1435 003502             TXCNTB:: .BLKW   16.      ; TRANSMISSION CHARACTER COUNTERS TABLE.
1436 003542             RXCNTB:: .BLKW   16.      ; RECEPTION CHARACTER COUNTERS TABLE.
1437
1438             ;*****
1439             ;*      GENERAL TABLE AND BUFFER AREA--513 WORDS.
1440             ;*****
1440 003602             BUFBAS::      ; BASE OF MEMORY BUFFER.
1441 003602             ERLTBL::      .BLKW   128.     ; FIRST HALF OF GENERAL TABLE OR BUFFER.
1442 004202             BUFMID::      .BLKW   64.      ; SECOND HALF OF GENERAL TABLE OR BUFFER.
1443 004402             BUF3QT::      .BLKW   64.      ; LAST QUARTER OF THE BUFFER AREA.
1444 004602             BUFEND::      ; END OF GENERAL PURPOSE MEMORY BUFFER.
1445 004602             ENDETB::      .BLKW   16.      ; BUFFER OVERFLOW SPACE.
1446
1447             ;*****
1448             ;*      TABLE OF DATA PATTERN RESYNC QUEUES.
1449             ;*****
1449 004642             DPRSQB::      ; DATA PATTERN RESYNC QUEUES TABLE BASE.
1450 004642             .BLKW   4      ; DATA PATTERN RESYNC QUEUE FOR LINE 0.
1451 004652             .BLKW   4      ; DATA PATTERN RESYNC QUEUE FOR LINE 1.
1452 004662             .BLKW   4      ; DATA PATTERN RESYNC QUEUE FOR LINE 2.
1453 004672             .BLKW   4      ; DATA PATTERN RESYNC QUEUE FOR LINE 3.
1454 004702             .BLKW   4      ; DATA PATTERN RESYNC QUEUE FOR LINE 4.
1455 004712             .BLKW   4      ; DATA PATTERN RESYNC QUEUE FOR LINE 5.
1456 004722             .BLKW   4      ; DATA PATTERN RESYNC QUEUE FOR LINE 6.
1457 004732             .BLKW   4      ; DATA PATTERN RESYNC QUEUE FOR LINE 7.

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1458 004742          .BLKW 4          ;DATA PATTERN RESYNC QUEUE FOR LINE 8.
1459 004752          .BLKW 4          ;DATA PATTERN RESYNC QUEUE FOR LINE 9.
1460 004762          .BLKW 4          ;DATA PATTERN RESYNC QUEUE FOR LINE 10.
1461 004772          .BLKW 4          ;DATA PATTERN RESYNC QUEUE FOR LINE 11.
1462 005002          .BLKW 4          ;DATA PATTERN RESYNC QUEUE FOR LINE 12.
1463 005012          .BLKW 4          ;DATA PATTERN RESYNC QUEUE FOR LINE 13.
1464 005022          .BLKW 4          ;DATA PATTERN RESYNC QUEUE FOR LINE 14.
1465 005032          .BLKW 4          ;DATA PATTERN RESYNC QUEUE FOR LINE 15.
1466 005042          .BLKW 4          ;DATA PATTERN RESYNC QUEUE FOR LINE 15.
1467                DPRSQE::          ;END OF DATA PATTERN RESYNC QUEUES TABLE.
1468                ;*****
1469                ; SINGLE CHARACTER MODE LPR FIELD TABLES.
1470                ;*****
1470 005042          SCBCTB::          ;BASE OF NUMBER OF BITS PER CHAR FIELDS TABLE.
1471 005042 000000          .WORD 0          ;5 BITS/CHAR LPR FIELD.
1472 005044 000010          .WORD 10         ;6 BITS/CHAR LPR FIELD.
1473 005046 000020          .WORD 20         ;7 BITS/CHAR LPR FIELD.
1474 005050 000030          .WORD 30         ;8 BITS/CHAR LPR FIELD.
1475 005052          SCBCTE::          ;END OF NUMBER OF BITS/CHAR FIELDS TABLE.
1476 005052          SCBRTB::          ;BASE OF BAUDRATE FIELDS TABLE.
1477 005052 000000          .WORD 0          ;50 BAUD LPR FIELDS.
1478 005054 073400          .WORD 73400       ;1.2K BAUD LPR FIELDS.
1479 005056 177400          .WORD 177400      ;38.4K BAUD LPR FIELDS.
1480 005060          SCBRTE::          ;END OF BAUDRATE FIELDS TABLE.
1481 005060          SCNSTB::          ;BASE OF NUMBER OF STOP BITS FIELDS TABLE.
1482 005060 000000          .WORD 0          ;1 STOP BIT LPR FIELD.
1483 005062 000200          .WORD 200         ;2 STOP BITS LPR FIELD.
1484 005064          SCNSTE::          ;END OF BAUDRATE FIELDS TABLE.
1485 005064          SCTPTB::          ;BASE OF TYPE OF PARITY FIELDS TABLE.
1486 005064 000000          .WORD 0          ;NO PARITY LPR FIELD.
1487 005066 000040          .WORD 40         ;ODD PARITY LPR FIELD.
1488 005070 000140          .WORD 140        ;EVEN PARITY LPR FIELD.
1489 005072          SCTPTE::          ;END OF TYPE OF PARITY FIELDS TABLE.
1490
1491                ;*****
1492                ; DMA MODE LPR FIELD TABLES.
1493                ; SET UP WITH SPECIFIED BAUDRATES, 1 STOP BIT, ODD PARITY, 8 BITS/CHAR.
1494                ;*****
1495 005072          DLPRTB::          ;BASE OF DMA TEST LPR FIELDS TABLE.
1496 005072 156470          .WORD 156470       ;9.6K BAUD.
1497 005074 167070          .WORD 167070       ;19.2K BAUD.
1498 005076 177470          .WORD 177470       ;38.4K BAUD.
1499 005100          DLPRTE::          ;END OF DMA TEST LPR FIELDS TABLE.
1500                ;*****
1501                ; SPLIT SPEED LPR PARAMETER TABLE.
1502                ;*****
1503 005100          SPLPRB::          ;BASE OF SPLIT SPEED LPR TABLE.
1504 005100 170070          .WORD 170070       ;TX: 38.4K, RX: 50 BAUD, 1 STOP ODD PAR 8 BITS.
1505 005102 007470          .WORD 7470        ;TX: 50, RX: 38.4K BAUD, 1 STOP ODD PAR 8 BITS.
1506 005104 000001          .WORD 1          ;NUMBER OF REPEAT TRANSMISSIONS AT 50 BAUD.
1507 005106 000120          .WORD 80         ;NUMBER OF REPEAT TRANSMISSIONS AT 38.4K BAUD.
1508 005110 070470          .WORD 70470       ;TX: 1200, RX: 75 BAUD, 1 STOP ODD PAR 8 BITS.
1509 005112 013470          .WORD 13470       ;TX: 75, RX: 1200 BAUD, 1 STOP ODD PAR 8 BITS.
1510 005114 000001          .WORD 1          ;NUMBER OF REPEAT TRANSMISSIONS AT 75 BAUD.
1511 005116 000016          .WORD 16         ;NUMBER OF REPEAT TRANSMISSIONS AT 1200 BAUD.
1512 005120 115070          .WORD 115070      ;TX: 2000, RX: 2400 BAUD, 1 STOP ODD PAR 8 BITS.
1513 005122 124470          .WORD 124470      ;TX: 2400, RX: 2000 BAUD, 1 STOP ODD PAR 8 BITS.
1514 005124 000001          .WORD 1          ;NUMBER OF REPEAT TRANSMISSIONS AT 2400 BAUD.

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1515	005126	000002	.WORD	2	;NUMBER OF REPEAT TRANSMISSIONS A1 2000 BAUD.
1516	005130		SPLPRE::		;END OF SPLIT SPEED LPR TABLE.
1517			;*****		
1518			; SINGLE CHARACTER DATA PATTERN TABLE.		
1519			;*****		
1520	005130	000	SDPBAS::	.BYTE 0	;START OF SINGLE CHARACTER DATA PATTERN TABLE.
1521	005131	001		.BYTE 1	
1522	005132	010		.BYTE 10	
1523	005133	017		.BYTE 17	
1524	005134	063		.BYTE 63	
1525	005135	074		.BYTE 74	
1526	005136	125		.BYTE 125	
1527	005137	177		.BYTE 177	
1528	005140	200		.BYTE 200	
1529	005141	252		.BYTE 252	
1530	005142	303		.BYTE 303	
1531	005143	314		.BYTE 314	
1532	005144	360		.BYTE 360	
1533	005145	367		.BYTE 367	
1534	005146	376		.BYTE 376	
1535	005147	377		.BYTE 377	
1536	005150		SDPEND::		;END OF SINGLE CHARACTER DATA PATTERN TABLE.
1537	005150	000		.BYTE 0	;START OF FIRST SHORT DATA PATTERN OVERFLOW AREA.
1538	005151	001		.BYTE 1	
1539	005152	010		.BYTE 10	
1540	005153	017		.BYTE 17	
1541			;*****		
1542			; SINGLE CHARACTER DATA PATTERN TABLE NUMBER TWO.		
1543			;*****		
1544			;*****		
1545	005154	125	SDP2B::	.BYTE 125	;START OF SECOND SHORT DATA PATTERN.
1546	005155	252		.BYTE 252	
1547	005156	124		.BYTE 124	
1548	005157	253		.BYTE 253	
1549	005160	122		.BYTE 122	
1550	005161	255		.BYTE 255	
1551	005162	112		.BYTE 112	
1552	005163	265		.BYTE 265	
1553	005164	052		.BYTE 52	
1554	005165	325		.BYTE 325	
1555	005166	152		.BYTE 152	
1556	005167	225		.BYTE 225	
1557	005170	132		.BYTE 132	
1558	005171	245		.BYTE 245	
1559	005172	126		.BYTE 126	
1560	005173	251		.BYTE 251	
1561	005174		SDP2E::		;END OF SECOND SHORT DATA PATTERN.
1562	005174	125		.BYTE 125	;START OF SECOND SHORT DATA PATTERN OVERFLOW AREA.
1563	005175	252		.BYTE 252	
1564	005176	124		.BYTE 124	
1565	005177	253		.BYTE 253	
1566	005200	122		.BYTE 122	
1567	005201	255		.BYTE 255	
1568	005202	112		.BYTE 112	
1569	005203	265		.BYTE 265	
1570	005204	052		.BYTE 52	
1571	005205	325		.BYTE 325	

1572 005206 152
1573 005207 225
1574 005210 132
1575 005211 245
1576 005212 126
1577 005213 251

.BYTE 152
.BYTE 225
.BYTE 132
.BYTE 245
.BYTE 126
.BYTE 251

; SINGLE CHARACTER SAFE PROPORTIONAL DELAY TABLE.

1581 005214 372
1582 005215 252
1583 005216 167
1584 005217 143
1585 005220 132
1586 005221 062
1587 005222 036
1588 005223 024
1589 005224 021
1590 005225 020
1591 005226 017
1592 005227 015
1593 005230 014
1594 005231 014
1595 005232 013
1596 005233 012

PROTBL: .BYTE 250. ; DELAY IN MILLI SECONDS AT 50 BAUD
.BYTE 170. ; DELAY IN MILLI SECONDS AT 75 BAUD
.BYTE 119. ; DELAY IN MILLI SECONDS AT 110 BAUD
.BYTE 99. ; DELAY IN MILLI SECONDS AT 134.5 BAUD
.BYTE 90. ; DELAY IN MILLI SECONDS AT 150 BAUD
.BYTE 50. ; DELAY IN MILLI SECONDS AT 300 BAUD
.BYTE 30. ; DELAY IN MILLI SECONDS AT 600 BAUD
.BYTE 20. ; DELAY IN MILLI SECONDS AT 1200 BAUD
.BYTE 17. ; DELAY IN MILLI SECONDS AT 1800 BAUD
.BYTE 16. ; DELAY IN MILLI SECONDS AT 2000 BAUD
.BYTE 15. ; DELAY IN MILLI SECONDS AT 2400 BAUD
.BYTE 13. ; DELAY IN MILLI SECONDS AT 4800 BAUD
.BYTE 12. ; DELAY IN MILLI SECONDS AT 7200 BAUD
.BYTE 12. ; DELAY IN MILLI SECONDS AT 9600 BAUD
.BYTE 11. ; DELAY IN MILLI SECONDS AT 19200 BAUD
.BYTE 10. ; DELAY IN MILLI SECONDS AT 38400 BAUD
.EVEN

; * TABLE FOR STORAGE OF RX/TX LINE NUMBER ASSOCIATIONS.
; * THE ASSOCIATIONS ARE STORED AS LINE NUMBER TIMES 2 FOR USE AS OFFSETS
; * WHEN ACCESSING A TABLE OF WORDS.
; * NOTE: DO NOT WRITE A NON-ZERO VALUE INTO THE UPPER BYTE OF ANY ENTRY.

1604 005234
1605 005234 000000
1606 005236 000002
1607 005240 000004
1608 005242 000006
1609 005244 000010
1610 005246 000012
1611 005250 000014
1612 005252 000016
1613 005254 000020
1614 005256 000022
1615 005260 000024
1616 005262 000026
1617 005264 000030
1618 005266 000032
1619 005270 000034
1620 005272 000036
1621 005274

TXRXLB: .WORD 0 ; BASE OF TX/RX LINE NUMBER ASSOCIATION TABLE.
.WORD 2. ; TX/RX LINE OFFSET FOR RX/TX LINE 0.
.WORD 4. ; TX/RX LINE OFFSET FOR RX/TX LINE 1.
.WORD 6. ; TX/RX LINE OFFSET FOR RX/TX LINE 2.
.WORD 8. ; TX/RX LINE OFFSET FOR RX/TX LINE 3.
.WORD 10. ; TX/RX LINE OFFSET FOR RX/TX LINE 4.
.WORD 12. ; TX/RX LINE OFFSET FOR RX/TX LINE 5.
.WORD 14. ; TX/RX LINE OFFSET FOR RX/TX LINE 6.
.WORD 16. ; TX/RX LINE OFFSET FOR RX/TX LINE 7.
.WORD 18. ; TX/RX LINE OFFSET FOR RX/TX LINE 8.
.WORD 20. ; TX/RX LINE OFFSET FOR RX/TX LINE 9.
.WORD 22. ; TX/RX LINE OFFSET FOR RX/TX LINE 10.
.WORD 24. ; TX/RX LINE OFFSET FOR RX/TX LINE 11.
.WORD 26. ; TX/RX LINE OFFSET FOR RX/TX LINE 12.
.WORD 28. ; TX/RX LINE OFFSET FOR RX/TX LINE 13.
.WORD 30. ; TX/RX LINE OFFSET FOR RX/TX LINE 14.
TXRXLE: .EVEN ; END OF TX/RX LINE NUMBER ASSOCIATION TABLE.
; GUARANTEE THAT NEXT TABLE IS ON WORD BOUNDARY.

; * TABLE OF TX/RX LINE NUMBER ASSOCIATIONS IN STAGGERED LOOPBACK.
; * THE ASSOCIATIONS ARE STORED AS LINE NUMBER TIMES 2 FOR USE AS OFFSETS
; * WHEN ACCESSING A TABLE OF WORDS.
; * THIS IS A TABLE OF DATA FOR READING ONLY. USE TO LOAD THE ABOVE TABLE.
; * NOTE: MUST CONVERT FROM BYTES TO WORDS WHEN LOADING ABOVE TABLE.

1622
1623
1624
1625
1626
1627
1628

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1629 ;*****
1630 005274 STGTRB:: ;BASE OF STAGGERED TX/RX LINE NUMBER TABLE.
1631 005274      .BYTE 4. ;TX/RX LINE OFFSET FOR RX/TX LINE 0.
1632 005275      .BYTE 6. ;TX/RX LINE OFFSET FOR RX/TX LINE 1.
1633 005276      .BYTE 0. ;TX/RX LINE OFFSET FOR RX/TX LINE 2.
1634 005277      .BYTE 2. ;TX/RX LINE OFFSET FOR RX/TX LINE 3.
1635 005300      .BYTE 12. ;TX/RX LINE OFFSET FOR RX/TX LINE 4.
1636 005301      .BYTE 14. ;TX/RX LINE OFFSET FOR RX/TX LINE 5.
1637 005302      .BYTE 8. ;TX/RX LINE OFFSET FOR RX/TX LINE 6.
1638 005303      .BYTE 10. ;TX/RX LINE OFFSET FOR RX/TX LINE 7.
1639 005304      .BYTE 20. ;TX/RX LINE OFFSET FOR RX/TX LINE 8.
1640 005305      .BYTE 22. ;TX/RX LINE OFFSET FOR RX/TX LINE 9.
1641 005306      .BYTE 16. ;TX/RX LINE OFFSET FOR RX/TX LINE 10.
1642 005307      .BYTE 18. ;TX/RX LINE OFFSET FOR RX/TX LINE 11.
1643 005310      .BYTE 28. ;TX/RX LINE OFFSET FOR RX/TX LINE 12.
1644 005311      .BYTE 30. ;TX/RX LINE OFFSET FOR RX/TX LINE 13.
1645 005312      .BYTE 24. ;TX/RX LINE OFFSET FOR RX/TX LINE 14.
1646 005313      .BYTE 26. ;TX/RX LINE OFFSET FOR RX/TX LINE 15.
1647      .EVEN ;GUARANTEE THAT NEXT TABLE IS ON WORD BOUNDARY.
1660 005314 ERRTBL
      005314
      005314 000000
      005316 000000
      005320 000000
      005322 000000
1661
1662      .EVEN

ERRTYP:: .WORD 0
ERRNBR:: .WORD 0
ERRMSG:: .WORD 0
ERRBLK:: .WORD 0

L$ERRTBL::

```



```

1664 .SBTTL GPR HANDLING ROUTINES FOR SUBROUTINE CALLS.
1665 ;*****
1666 ;* THERE ARE 4 ROUTINES AND MACRO DEFINITIONS USED FOR THE HANDLING OF
1667 ;* GPR VALUES DURING SUBROUTINE CALLS WITHIN THIS PROGRAM. THE FOUR
1668 ;* ROUTINES/MACRO CALLS HAVE THE FOLLOWING NAMES:
1669 ;*
1670 ;* SAVE - MACRO DEFINITION USED AT THE BEGINNING OF A SUBROUTINE TO
1671 ;* SAVE THE GPR CONTENTS FOR LATER RESTORATION.
1672 ;* PASS - MACRO DEFINITION USED AT THE END OF A SUBROUTINE TO RESTORE
1673 ;* THE PREVIOUSLY SAVED GPR CONTENTS AND TO LEAVE THE CONTENTS
1674 ;* OF THE SPECIFIED GPR(S) INTACT (NOT RESTORED).
1675 ;* PREGOS - SUBROUTINE WHICH IS CALLED FROM THE SAVE AND PASS MACRO
1676 ;* EXPANSIONS WHICH ACTUALLY PERFORMS THE ACTIONS ON THE GPRS.
1677 ;*
1678 ;* DURING A SUBROUTINE WHICH USES THESE GPR SAVE ROUTINES THE VALUES
1679 ;* OF THE GPRS ARE STORED ON THE STACK IN THE FOLLOWING STACK FRAME:
1680 ;*
1681 ;*      SP      -> RET PC INTO PREGOS ROUTINE.
1682 ;*      SP+2    -> GPR R0 CONTENTS.
1683 ;*      SP+4    -> GPR R1 CONTENTS.
1684 ;*      SP+6    -> GPR R2 CONTENTS.
1685 ;*      SP+8    -> GPR R3 CONTENTS.
1686 ;*      SP+10   -> GPR R4 CONTENTS.
1687 ;*      SP+12   -> GPR R5 CONTENTS.
1688 ;*      SP+14   -> RET PC INTO CALLER OF SUB'TNE WHICH CALLED PREGOS.
1689 ;*
1690 ;* EACH LEVEL OF SUB'TNE CALLING USES 8 WORDS OF STACK OVERHEAD.
1691 ;* THE SAVE AND PASS MACROS CAN ALSO BE USED IN "STRAIGHT LINE CODE"
1692 ;* TO SAVE AND RESTORE THE GPR VALUES. IN ANY CASE, AFTER THE
1693 ;* ISSUING OF A PASS CALL THE GPRS WILL BE RESTORED TO THE VALUES
1694 ;* THEY HAD PRIOR TO THE LAST SAVE CALL (EXCEPT FOR THE EXCEPTED,
1695 ;* OR PASSED INTACT, GPRS SPECIFIED AS PARAMETERS TO THE PASS CALL)
1696 ;* AND THE SP WILL ALSO BE RESTORED TO ITS CONDITION BEFORE THE LAST
1697 ;* SAVE CALL. THE PROGRAMMER MUST BE SURE THAT THE SP HAS THE SAME
1698 ;* VALUE WHEN THE PASS MACRO IS CALLED AS IT HAD IMMEDIATELY AFTER
1699 ;* THE SAVE MACRO WAS CALLED.
1700 ;*****

```

C4

1702		.SBTTL GPR FRAME ACCESS EQUATES		
1703		;***		
1704		;EQUATES THAT ALLOW ACCESS TO THE STACK FRAME. THESE ARE THE		
1705		;OFFSETS INTO THE STACK FOR REGISTERS SAVED DURING THE PREGOS		
1706		;ROUTINE.		
1707		;--		
1708				
1709	000036	LPCSLT**	36	;OFFSET FOR LAST RETURN PC.
1710	000016	PCSL0T**	16	;OFFSET FOR RETURN PC.
1711	000014	R5SL0T**	14	;OFFSET FOR R5.
1712	000012	R4SL0T**	12	;OFFSET FOR R4.
1713	000010	R3SL0T**	10	;OFFSET FOR R3.
1714	000006	R2SL0T**	6	;OFFSET FOR R2.
1715	000004	R1SL0T**	4	;OFFSET FOR R1.
1716	000002	R0SL0T**	2	;OFFSET FOR R0.

```

1718 .SBTTL GLOBAL MACRO DEFINITION - SAVE -
1719 ;*****
1720 ;* THIS MACRO IS USED AT THE BEGINNING OF A SUBROUTINE TO SAVE THE
1721 ;* CONTENTS OF THE GPRS R0 THRU R5.
1722 ;*
1723 ;* INPUTS: SP - UNCHANGED SINCE SUBROUTINE WAS ENTERED
1724 ;* R5SLOT - OFFSET TO STACK SLOT FOR R5 (EQUATED TO 14 OCTAL)
1725 ;*
1726 ;* OUTPUTS: GPR SAVE AREA ON THE STACK IS LOADED WITH THE CONTENTS OF GPRS
1727 ;* TOP OF STACK - LOADED WITH THE RETURN ADDRESS INTO PREG05
1728 ;*
1729 ;* CALLING SEQUENCE: SAVE
1730 ;*
1731 ;* COMMENTS: NO ARGUMENTS ARE ALLOWED.
1732 ;* THE PASS MACRO SHOULD BE CALLED TO RESTORE THE GPR VALUES.
1733 ;*
1734 ;* SUBORDINATE ROUTINES CALLED: PREG05.
1735 ;*****
1736
1737 .MACRO SAVE
1738 .LIST
1739 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
1740 .NLIST
1741 .ENDM SAVE

```

```

1743 .SBTTL GLOBAL MACRO DEFINITION - PASS -
1744 ;*****
1745 ;* THIS MACRO IS USED IN CONJUNCTION WITH THE SAVE MACRO. IT IS
1746 ;* CALLED AT END OF A SUBROUTINE TO PASS PARAMETERS IN GPRS BACK TO THE
1747 ;* CALLING ROUTINE BY ALTERING THE GPR SAVE AREA ON THE STACK AND THEN
1748 ;* RETURNING TO PREG05 TO RESTORE THE GPRS TO THEIR SAVED VALUES.
1749 ;*
1750 ;* INPUTS: ONLY ALLOWED ARGUMENTS ARE "R0" THRU "R5".
1751 ;* ROSLOT THRU R5SLOT MUST BE EQUATED TO THEIR RESPECTIVE GPR SAVE
1752 ;* SLOT OFFSETS BEFORE CALLING THIS MACRO.
1753 ;*
1754 ;* OUTPUTS: THE GPR VALUES ARE PUT IN THEIR RESPECTIVE SLOTS ON THE STACK.
1755 ;*
1756 ;* CALLING SEQUENCE: PASS R0,R1,...
1757 ;*
1758 ;* COMMENTS: ANY COMBINATION OF GPR ARGUMENTS MAY BE LISTED IN ANY ORDER.
1759 ;* FOR EXAMPLE, THE FOLLOWING ARE LEGAL:
1760 ;* PASS R1
1761 ;* PASS R4,R0,R2
1762 ;* THE GPRS LISTED AS ARGUMENTS WILL BE PASSED INTACT TO THE
1763 ;* CALLING ROUTINE, ALL OTHER GPRS WILL BE RESTORED.
1764 ;* THE SP MUST BE AT ITS ORIGINAL VALUE WHEN PASS IS CALLED.
1765 ;*
1766 ;* THE MACRO CALL
1767 ;* PASS R0,R3
1768 ;* EXPANDS INTO THE FOLLOWING ASSEMBLY CODE:
1769 ;* MOV R0,ROSLOT(SP) ;PUT R0 IN STACK SLOT.
1770 ;* MOV R3,R3SLOT(SP) ;PUT R3 IN STACK SLOT.
1771 ;* JSR PC,8(SP)+ ;RETURN TO PREG05 SUBRT.
1772 ;* IN THIS EXAMPLE GPRS R1, R2, R4, AND R5 WILL BE RESTORED TO
1773 ;* THEIR VALUES CONTAINED IN THE STACK FRAME AND R0 AND R3
1774 ;* WILL BE LEFT AT THEIR VALUES PRIOR TO THIS PASS CALL.
1775 ;*
1776 ;* SUBORDINATE ROUTINES CALLED: (PREGPT - LABEL WITHIN PREG05, VALUE ON STACK.)
1777 ;*****
1778
1779 .MACRO PASS A,B,C,D,E,F
1780 .IRP X,<A,B,C,D,E,F>
1781 .IF NB,X
1782 .LIST
1783 MOV X,X'SLOT(SP) ;PUT X IN STACK SLOT.
1784 .NLIST
1785 .ENDC
1786 .ENDM
1787 .LIST
1788 JSR PC,8(SP)+ ;RETURN TO PREG05 SUBRT.
1789 .NLIST
1790 .ENDM PASS

```

```

1792 .SBTTL GLOBAL SUBROUTINE - PREG05 -
1793 ;*****
1794 ;* PRESERVE REGISTERS R0 THROUGH R5 FOR SUBROUTINE CALLS.
1795 ;*
1796 ;* INPUTS: THE RETURN ADDRESS BACK INTO THE CALLING ROUTINE MUST BE IN
1797 ;* GPR R5. (I.E.- MACROS USE "JSR R5,PREG05".)
1798 ;*
1799 ;* OUTPUTS: REGISTERS R0 THROUGH R5 ARE SAVED ON THE STACK.
1800 ;*
1801 ;* CALLING SEQUENCE: SAVE ;MACRO EXPANSION CALLS PREG05.
1802 ;* [SUBROUTINE CODE]...
1803 ;* PASS ;MACRO EXPANSION RECALLS PREG05.
1804 ;*
1805 ;* COMMENTS: THIS ROUTINE IS RE-ENTRANT.
1806 ;*
1807 ;* PARAMETERS MAY BE PASSED OUT OF A SUBROUTINE BY MODIFYING THE
1808 ;* REGISTER SAVE AREA ON THE STACK. USE THE PASS GPRN MACRO
1809 ;* TO RETURN GPR VALUES INTACT.
1810 ;* USE THE RNSLOT OFFSETS FROM THE SP TO PASS OTHER PARAMETERS.
1811 ;* [EXAMPLE: MOV VALUE,RNSLOT(SP) ]
1812 ;* MAKE SURE THE SP IS AT ITS ORIGINAL VALUE WHEN YOU DO THIS.
1813 ;*
1814 ;* SUBORDINATE ROUTINES CALLED: NONE.
1815 ;*****
1816
1817 005324 PREG05: ;R5 HAS BEEN LOADED ON THE STACK BY THE SUBROUTINE CALL
1818 005324 010446 MOV R4,-(SP) ;SAVE R4
1819 005326 010346 MOV R3,-(SP) ;SAVE R3
1820 005330 010246 MOV R2,-(SP) ;SAVE R2
1821 005332 010146 MOV R1,-(SP) ;SAVE R1
1822 005334 010046 MOV R0,-(SP) ;SAVE R0
1823 005336 010546 MOV R5,-(SP) ;PUSH RETURN PC ON TOP OF STACK
1824 005340 016605 000014 MOV R5SLOT(SP),R5 ;RESTORE R5 TO VALUE IT HAD BEFORE CALLS
1825
1826 005344 004736 JSR PC,B(SP)+ ;CALL THE SUBROUTINE AT THE RETURN ADDRESS
1827 ;FROM THE PREG05 CALL, PUTTING THE PRESENT
1828 ;PC ON THE STACK AS A RETURN ADDRESS INTO
1829 ;THIS (PREG05) ROUTINE.
1830
1831 ;+++
1832 ;THE FOLLOWING CODE IS EXECUTED WHEN THE CALLING ROUTINE DOES A
1833 ;"RETURN" [JSR PC,B(SP)+] USING THE PC DEPOSITED ON THE STACK ABOVE.
1834 ;---
1835
1836 005346 012605 PREGRT: MOV (SP)+,R5 ;PUT RETURN PC IN R5.
1837 005350 012600 MOV (SP)+,R0 ;RESTORE R0.
1838 005352 012601 MOV (SP)+,R1 ;RESTORE R1.
1839 005354 012602 MOV (SP)+,R2 ;RESTORE R2.
1840 005356 012603 MOV (SP)+,R3 ;RESTORE R3.
1841 005360 012604 MOV (SP)+,R4 ;RESTORE R4.
1842
1843 005362 000205 RTS R5 ;RETURN TO THE SUBROUTINE WHICH CALLED PREG05.
1844 ;RESTORING R5 IN THE PROCESS.

```

```
1846 .SBTTL GLOBAL TEXT SECTION
1848 ;*****
1849 ;
1850 ; FVTSKL1.P11
1851 ;
1852 ;*****
1853
1854
1855
1856 ;**
1857 ; THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
1858 ; MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
1859 ; MORE THAN ONE TEST.
1860 ;--
1861
1862 ;
1863 ; NAMES OF DEVICES SUPPORTED BY PROGRAM
1864 ;
1865 005364 DEVTYP <DHU-11>
005364
005364 104 110 125 L$DVTYP::
005367 055 061 061 .ASCIZ /DHU-11/
005372 000
.
.EVEN
1866
1872
1873 ; TEST DESCRIPTION
1874 ;
1875 005374 DESCRIPT <DHU-11 FUNC TST PART3>
005374
005374 104 110 125 L$DESC::
T3/ 005377 055 061 061 .ASCIZ /DHU 11 FUNC TST PAR
005402 040 106 125
005405 116 103 040
005410 124 123 124
005413 040 120 101
005416 122 124 063
005421 000
.
.EVEN
1876
1877
1884
```

```
1886
1887 ;*****
* 1888 ;
1889 ; FVTA.FMT
1890 ;
1891 ;*****
1892
1893
1894
1895 ;
1896 ; FORMAT STATEMENTS USED IN PRINT CALLS
1897 ;
1898
1909
1910
```



```

1919
1920 ;*****
1921 ;
1922 ; FVTC.MSG
1923 ;
1924 ;*****
**
1925
1926
1927 .NLIST BIN
1928 .SBTTL GLOBAL MESSAGE AREA
1929 ; ***** FORMAT STATEMENTS *****
1930 005422 MFUNIT:: .ASCIZ /%A TESTING UNIT :%D4%N/
1931 005453 EF0503:: .ASCIZ /%T%N/
1932 005460 EF1601:: .ASCIZ /%A %T%A, TEST ABORTED %N/
1933 005512 EF1603:: .ASCIZ /%A ACTUAL DATA: %06%A (0).%N/
1934 005554 EF4401:: .ASCII /%A DMA ADDRESS TEST SUCCESSFUL, BITS 0 TO %D2%A (D) TESTED/
1935 005650 .ASCIZ / (%D2%A BITS).%N/
1936 005671 EF6201:: .ASCIZ \%A FRAMING/PARITY ERROR DETECTION AND REPORTING BAD ON LINES:%D2%A : %D2%A\
1937 006004 EF6202:: .ASCIZ /%A CHAR RECEIVED WITH FRAMING ERROR BIT %T%A, SHOULD BE %T%N/
1938 006102 EF6203:: .ASCIZ /%A CHAR RECEIVED WITH PARITY ERROR BIT %T%A, SHOULD BE %T%N/
1939 006177 EF7801:: .ASCIZ /%T%A ON LINE %D2%A DECIMAL.%N/
1940 006235 EF9001:: .ASCIZ /%A UNEXPECTED %T%A FOUND IN RECEIVE CHAR FIFO:%N/
1941 006317 EF9002:: .ASCIZ /%A CODE IS ASSOCIATED WITH LINE: %D2%A%N/
1942 006371 EF9003:: .ASCIZ /%A CODE IS: %03%A%N/
1943 006420 EF9004:: .ASCIZ /%A %T%A VALUE: %03%A%N/
1944 006450 EF9005:: .ASCIZ /%A %T%A VALUE: NONE%N/
1945 006501 EF9006:: .ASCIZ /%A %T%A %D2%A%N/
1946 006520 EF9007:: .ASCIZ /%A CHARACTER RECEIVED WITH ERROR FLAG(S) SET ON LINE %D2%A%N/
1947 006614 EF9008:: .ASCIZ /%A CHARACTER READ AS: %03%A%N/
1948 006653 EF9009:: .ASCIZ /%A %T%A ERROR FLAG SET.%N/
1949 006712 EF9010:: .ASCIZ /%A NUMBER OF ERRORS DETECTED ON LINE %D2%A IS %D5%A%N/
1950 007001 EF9012:: .ASCII /%A LINE%D2%A ONLY %T%D5%A BYTES OF%D5%A BYTE/
1951 007055 .ASCIZ / DATA PAT'N TX'D FROM LINE%D2%A%N/
1952 007115 EF9013:: .ASCIZ /%A DATA PATTERN NOT COMPLETELY %T%N/
1953 007162 EF9019:: .ASCIZ /%A %T%A %06%A%N/
1954 007201 EF9020:: .ASCIZ /%A TOO FEW TX.ACTIONS GENERATED ON LINE %D2%A%N/
1955 007262 EF9101:: .ASCIZ /%N/
1956 007265 EF9103:: .ASCIZ /%A ERROR CONDITION ON LINE %D2%A%N/
1957 007333 EF9301:: .ASCIZ /%A %T%D2%A, BMP CODE REPORTED :%03%A%N/
1958 007401 EF9302:: .ASCIZ /%A OVERFLOW OCCURRED (MORE THAN 31 BMP CODES FOUND IN QUEUE)%N/
1959 007501 UBRFMT:: .ASCIZ /%D5%A IS NOT A SUPPORTED BAUDRATE, ENTER ANOTHER OR CTRL C.%N/
1960 007577 MSFMT1:: .ASCIZ /%AMODEM STATUS SIGNAL REPORT:%N/
1961 007637 MSFMT2:: .ASCIZ /%A LINE %D2%A: DSR=%B1%A, RI=%B1%A, DCD=%B1%A, CTS=%B1%A%N/
1962 007733 EDPFMT:: .ASCII /%AMODEM LOOPBACK TEST STATUS REPORT: /
1963 010001 .ASCIZ /PATTERN %D5%A (D) COMPLETED.%N/
1964
1965 ;***** MESSAGE AREA *****
1966 010041 EM0103:: .ASCIZ /DEVICE REGISTER ACCESS ERRORS/
1967 010077 EM0509:: .ASCIZ /SET/
1968 010103 EM1601:: .ASCIZ /TIMEOUT OCCURRED WAITING FOR MASTER RESET TO CLEAR/
1969 010166 EM4401:: .ASCIZ /DMA ADDRESS TEST FAILED/
1970 010216 EM4402:: .ASCIZ /NO SUITABLE ADDR FOUND,TEST ABANDONED/
1971 010264 EM4403:: .ASCIZ /**HOST FAILURE**,WRITE FAILED TO AN ADDR WHICH HAD BEEN READ/
1972 010361 EM4404:: .ASCIZ /NO ACTIVE LINES,TEST ABANDONED/
1973 010420 EM4405:: .ASCIZ /DMA_START BIT FOUND SET BEFORE DMA INITIATED,TEST ABANDONED/
1974 010514 EM4406:: .ASCIZ /TIME-OUT OCCURED WAITING FOR DMA TO FINISH/
1975 010570 EM4407:: .ASCIZ /TOO FEW CHARACTERS FOUND IN THE RXFIFO,DMA FAILED/

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1976 010652 EM4408:: .ASCIZ /TOO MANY BMP CODES FOUND IN RXFIFO/
1977 010715 EM4409:: .ASCIZ /BAD BITS BETWEEN BITS 0 AND /
1978 010752 EM4410:: .ASCIZ /RXFIFO FAILED TO PURGE/
1979 011001 EM4411:: .ASCIZ /**HOST FAILURE**WRITE ATTEMPT FAILED/
1980 011046 EM5303:: .ASCIZ /BMP CODE FOUND IN FIFO, TEST INVAILEDATED/
1981 011117 EM6201:: .ASCIZ /FRAMING ERROR TEST FAILED/
1982 011151 EM6202:: .ASCIZ /CLEAR /
1983 011160 EM6301:: .ASCIZ /PARITY ERROR TEST FAILED/
1984 011211 EM8901:: .ASCIZ /MODEM LOOPBACK TEST /
1985 011236 EM9003:: .ASCIZ /MODEM STATUS CODE/
1986 011260 EM9004:: .ASCIZ /SELFTEST CODE/
1987 011276 EM9006:: .ASCIZ /CHARACTER RECEIVED ON INACTIVE LINE, LINE:/
1988 011351 EM9007:: .ASCIZ /UNEXPECTED CHAR RECEIVED AFTER RX COMPLETE ON LINE/
1989 011434 EM9008:: .ASCIZ /RECEIVED CHAR MISCOMPARE AGAINST TX DATA ON LINE/
1990 011515 EM9009:: .ASCIZ /EXPECTED OR CORRECT/
1991 011541 EM9010:: .ASCIZ /ACTUAL OR MEASURED /
1992 011565 EM9011:: .ASCIZ /OVERRUN/
1993 011575 EM9012:: .ASCIZ /FRAMING/
1994 011605 EM9013:: .ASCIZ /PARITY/
1995 011614 EM9014:: .ASCIZ /SUMMARY REPORTS FOR LINES WITH EXCESSIVE NUMBERS OF ERRORS:/
1996 011710 EM9015:: .ASCIZ /TRANSMITTED/
1997 011724 EM9016:: .ASCIZ /RECV'D/
1998 011733 EM9017:: .ASCII / FIFO WILL NOT PURGE (DATA.VALID STUCK SET)./
1999 012010 .ASCIZ / REMAINDER OF TEST SKIPPED./
2000 012044 EM9025 : .ASCIZ /MORE THAN TWICE THE EXPECTED NUMBER OF CHARACTERS RECEIVED./
2001 012140 EM9026:: .ASCIZ / LPR CONTENTS: /
2002 012164 EM9027:: .ASCIZ /EXTRA CHAR RECEIVED WITHIN DATA PATTERN ON LINE/
2003 012244 EM9028:: .ASCIZ /SINGLE CHAR MISSING FROM RECEIVED DATA ON LINE/
2004 012323 EM9030:: .ASCIZ /*A (NO TX COMPLETION INTERRUPTS RECEIVED)*N/
2005 012400 EM9101:: .ASCIZ /DMA TRANSMISSION MODE TEST FAILED/
2006 012442 EM9102:: .ASCIZ /DMA_START BIT SET AFTER RESET OR TX.ACTION ON LINE(S):/
2007 012531 EM9104:: .ASCIZ / UNEXPECTED DATA FOUND IN FIFO FROM LINE: /
2008 012605 EM9201:: .ASCIZ /SPLIT SPEED TEST FAILED/
2009 012635 EM9301:: .ASCIZ /BMP CODES WERE REPORTED DURING THIS DIAGNOSTIC/
2010 012714 EM9302:: .ASCIZ /BMP CODE FOUND IN TEST /
2011 012744 EM9303:: .ASCIZ /THE LAST BMP CODE WAS FOUND IN TEST /
2012 013011 EM9304:: .ASCIZ /UNEXPECTED BMP CODES FOUND DURING THIS PASS/
2013 013065 EM9401:: .ASCIZ /KEYBOARD ECHO (DMU REMOTE LOOPBACK) TEST /
2014
2015 013137 BDRMSG:: .ASCIZ /MODEM BAUDRATE IN BPS:/
2016 013166 EMLMSG:: .ASCIZ /TYPE <CR> WHEN MODEM LINK ESTABLISHED:/
2017 013235 EXTMSG:: .ASCIZ /EXIT THE TEST (N = LOOP BACK TO SEND MORE DATA):/
2018 013316 NDPMSG:: .ASCII /NUMBER OF 256 BYTE PATTERNS TO SEND ON EACH SELECTED LINE/
2019 013407 .ASCIZ <15><12>/ (1-255, 0=SEND UNTIL ^C):/
2020 013444 PMSMSG:: .ASCIZ /PRINT MODEM STATUS SIGNAL REPORT AFTER EACH PATTERN:/
2021 013531 TERMSG:: .ASCIZ /TYPE <CR> TO TERMINATE THE TEST:/
2022
2023 .EVEN
2024 .LIST BIN

```

```
2026
2027 ;*****
2028 ;
2029 ;           FVTSKL2.P11
2030 ;
2031 ;*****
2032
2033
2034
2035 .SBTTL  GLOBAL ERROR REPORT SECTION
2036
2037 ;**
2038 ; THE GLOBAL ERROR REPORT SECTION CONTAINS MESSAGE PRINTING AREAS
2039 ; USED BY MORE THAN ONE TEST TO OUTPUT ADDITIONAL ERROR INFORMATION.  PRINTB
2040 ; (BASIC) AND PRINTX (EXTENDED) CALLS ARE USED TO CALL PRINT SERVICES.
2041 ;--
```

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2043 .SBTTL GLOBAL ERROR REPORTING ROUTINE - ER0101 -
2044 ;*****
2045 ;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH PRINTS ADDITIONAL ERROR
2046 ;* INFORMATION IF AN ERROR IS DETECTED IN TEST 1 (REGISTER ADDRESS
2047 ;* ACCESS TEST). IF THE "EXTENDED ERROR INFO" OPTION HAS BEEN SELECTED
2048 ;* THEN THIS SUBROUTINE WILL REPORT THE TYPE OF ACCESS (READ OR WRITE OR
2049 ;* BOTH) WHICH CAUSED A BUS TIME-OUT TRAP (004 TRAP). A MESSAGE INDICATING
2050 ;* THAT THE DHU MAY BE AT THE WRONG UNIBUS ADDRESS IS ALSO PRINTED.
2051 ;*
2052 ;* INPUTS: R5 - ERROR FLAG WORD.
2053 ;* IF BIT 0 IS SET, A READ ERROR OCCURED.
2054 ;* IF BIT 1 IS SET, A WRITE ERROR OCCURED.
2055 ;*
2056 ;* OUTPUTS: MESSAGES ARE PRINTED AT THE OPERATOR CONSOLE.
2057 ;*
2058 ;* CALLING SEQUENCE: INCLUDE THE LABEL "ER0101" AS THE MESSAGE POINTER
2059 ;* PARAMETER IN THE DRS ERROR REPORT MACRO CALL.
2060 ;*
2061 ;* COMMENTS:
2062 ;*
2063 ;* SUBORDINATE ROUTINES USED: NONE.
2064 ;*****
2065
2066 013572 BGNMSG ER0101
2067 013572 ER0101::
2068 013572 SAVE ;SAVE THE GPR CONTENTS.
2069 013572 004567 171526 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
2070 013576 012700 000100 MOV #BIT06,R0 ;SET-UP THE BIT MAP FOR 'REPORT EXT'D ERROR INFO'
2071 013602 046700 166354 BIC OPTION,R0 ;TRY AND CLEAR THE FLAG.
2072 013606 001036 BNE 6# ;EXIT IF OPTION NOT SELECTED.
2073 ;*
2074 ; REPORT EXTENDED ERROR INFORMATION
2075 ;*
2076 013610 032705 000001 BIT #BIT0,R5 ;TEST FOR READ ERROR.
2077 013614 001410 BEQ 2# ;SKIP READ ERROR MSG IF NO READ ERROR.
2078 013616 PRINTB #MSG1 ;PRINT READ ERROR MESSAGE.
2079 013616 012746 013710 MOV #MSG1,-(SP)
2080 013622 012746 000001 MOV #1,-(SP)
2081 013626 010600 MOV SP,R0
2082 013630 104414 TRAP C#PNTB
2083 013632 062706 000004 ADD #4,SP
2084 013636 032705 000002 2#: BIT #BIT1,R5 ;TEST FOR WRITE ERROR.
2085 013642 001410 BEQ 4# ;SKIP WRITE ERROR MSG IF NO WRITE ERROR.
2086 013644 PRINTB #MSG2 ;PRINT WRITE ERROR MESSAGE.
2087 013644 012746 013766 MOV #MSG2,-(SP)
2088 013650 012746 000001 MOV #1,-(SP)
2089 013654 010600 MOV SP,R0
2090 013656 104414 TRAP C#PNTB
2091 013660 062706 000004 ADD #4,SP
2092 013664 012746 014045 4#: PRINTX #MSG3 ;SUGGEST THAT DHU MAY BE AT WRONG ADDRESS.
2093 013670 012746 000001 MOV #MSG3,-(SP)
2094 013674 010600 MOV #1,-(SP)
2095 013676 104415 MOV SP,R0
2096 013700 062706 000004 TRAP C#PNTX
2097 ADD #4,SP

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2083 013704          64:    PASS          ;RESTORE THE GPR CONTENTS.
      013704 004736          JSR          PC,0(SP)+ ;RETURN TO PREG05 SUBRT.
2084 013706          ENDMSG
      013706          L10002:
      013706 104423          TRAP      C$MSG
2085
2086 013710          045      101      102 MSG1:: .ASCIZ  /$ABU"  IME-OUT TRAP CAUSED BY READ ATTEMPT.$N/
      013713          125      123      040
      013716          124      111      115
      013721          105      055      117
      013724          125      124      040
      013727          124      122      101
      013732          120      040      103
      013735          101      125      123
      013740          105      104      040
      013743          102      131      040
      013746          122      105      101
      013751          104      040      101
      013754          124      124      105
      013757          115      120      124
      013762          056      045      116
      013765          000
2087 013766          045      101      102 MSG2:: .ASCIZ  /$ABUS TIME-OUT TRAP CAUSED BY WRITE ATTEMPT.$N/
      013771          125      123      040
      013774          124      111      115
      013777          105      055      117
      014002          125      124      040
      014005          124      122      101
      014010          120      040      103
      014013          101      125      123
      014016          105      104      040
      014021          102      131      040
      014024          127      122      111
      014027          124      105      040
      014032          101      124      124
      014035          105      115      120
      014040          124      056      045
      014043          116      000
2088 014045          045      101      104 MSG3:: .ASCIZ  /$ADHU MAY BE AT THE WRONG UNIBUS ADDRESS.$N$N/
      014050          110      125      040
      014053          115      101      131
      014056          040      102      105
      014061          040      101      124
      014064          040      124      110
      014067          105      040      127
      014072          122      117      116
      014075          107      040      125
      014100          116      111      102
      014103          125      123      040
      014106          101      104      104
      014111          122      105      123
      014114          123      056      045
      014117          116      045      116
      014122          000
2089
2090          .EVEN

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2092 .SBTTL GLOBAL ERROR REPORTING ROUTINE - ER0503 -
2093 ;*****
2094 ;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH PRINTS AN ADDITIONAL ERROR
2095 ;* MESSAGE WHOSE ADDRESS IS PASSED AS AN INPUT PARAMETER, PROVIDED
2096 ;* EXTENDED ERROR REPORTING HAS BEEN REQUESTED.
2097 ;*
2098 ;* INPUTS: R1 - ADDRESS OF THE MESSAGE TO PRINT.
2099 ;*
2100 ;* OUTPUTS: A MESSAGES IS PRINTED AT THE OPERATOR CONSOLE.
2101 ;*
2102 ;* CALLING SEQUENCE: LOAD THE ADDRESS OF THE MESSAGE IN R1.
2103 ;* INCLUDE THE LABEL "ER0503" AS THE MESSAGE POINTER
2104 ;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
2105 ;*
2106 ;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC ERROR INFORMATION.
2107 ;*
2108 ;* SUBORDINATE ROUTINES USED: NONE.
2109 ;*****
2110
2111 014124 BGNMSG ER0503
2112 014124 ER0503::
2113 014124 012700 000100 MOV #BIT06,R0 ;TRY TO CLEAR THE
2114 014130 046700 166026 BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
2115 014134 001011 BNE 2$ ;EXIT IF FLAG NOT SET.
2116
2117
2118 014136 PRINTB #EF0503,R1 ;PRINT THE MESSAGE.
2119 014136 010146 MOV R1,-(SP)
2120 014140 012746 005453 MOV #EF0503,-(SP)
2121 014144 012746 000002 MOV #2,-(SP)
2122 014150 010600 MOV SP,R0
2123 014152 104414 TRAP C$PNTB
2124 014154 062706 000006 ADD #6,SP
2125
2126 2$: ENDMSG
2127
2128 014160 L10003:
2129 014160 TRAP C$MSG
2130 014160 104423

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2144 014162
2145 014162
2146 014162
2147 014166
2148 014172
2149 014176
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2152 014200
2153 014200
2154 014202
2155 014206
2156 014212
2157 014214
2158 014216
2159 014222
2160 014226
2161 014230
2162 014234
2163 014240
2164 014242
2165 014244
2166 014250
2167 014250
2168 014252
2169 014252
2170 014252

010146
012746 005453
012746 000002
010600
104414
062706 000006
016702 171072
010246
012746 005460
012746 000002
010600
104414
062706 000006
014250
014250 004736
014252
014252
104423

```
.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER1603 -
*****
; THIS ERROR REPORTING ROUTINE IS USED TO PRINT OUT A BASIC ERROR
; MESSAGE, ALONG WITH A MESSAGE INFORMING THE OPERATOR WHICH TEST IS
; ABOUT TO BE ABORTED, PROVIDED EXTENDED ERROR INFORMATION HAS BEEN
; REQUESTED, OTHERWISE ONLY A "TEST FAILURE" MESSAGE WILL BE PRINTED.
;
; INPUTS:      R1 - CONTAINS THE ADDRESS OF THE MESSAGE TO BE PRINTED.
;              ERRMSG - CONTAINS THE ADDRESS OF THE MESSAGE THAT INDICATES
;              THE TEST THAT IS BEING PERFORMED, EG DMA, BREAK ETC.
;
; OUTPUTS:     MESSAGES ARE PRINTED AT THE OPERATORS CONSOLE.
;              TESTNAME TEST ABORTED"
;
; CALLING SEQUENCE:  INCLUDE THE LABEL "ER1603" AS THE MESSAGE POINTER
;                   PARAMETER IN THE DRS ERROR REPORT MACRO CALL.
;
; COMMENTS:
;
; SUBORDINATE ROUTINES CALLED: NONE.
*****
BGNMSG ER1603
                                ER1603::
                                SAVE      R5,PREG05      ;SAVE THE CONTENTS OF THE GPRS.
                                JSR        ;CALL REGISTER SAVE SUBRT.
                                MOV      #BIT06,R0      ;TRY TO CLEAR THE
                                BIC      OPTION,R0      ;EXT'D ERROR REPORTING FLAG
                                BNE      24            ;EXIT IF FLAG NOT SET.
                                PRINTB #EF0503,R1      ;PRINT BASIC MESSAGE ON OPERATORS CONSOLE.
                                MOV      R1,-(SP)
                                MOV      #EF0503,-(SP)
                                MOV      #2,-(SP)
                                MOV      SP,R0
                                TRAP      C#PNTB
                                ADD      #6,SP
                                MOV      R2,-(SP)
                                MOV      #EF1601,-(SP)
                                MOV      #2,-(SP)
                                MOV      SP,R0
                                TRAP      C#PNTB
                                ADD      #6,SP
                                MOV      ERRMSG,R2      ;GET THE "TEST MESSAGE".
                                PRINTB #EF1601,R2      ;PRINT "TEST ABORTED" MESSAGE.
                                MOV      R2,-(SP)
                                MOV      #EF1601,-(SP)
                                MOV      #2,-(SP)
                                MOV      SP,R0
                                TRAP      C#PNTB
                                ADD      #6,SP
                                24:  PASS      ;RESTORE THE CONTENTS OF THE GPRS.
                                JSR        PC,8(SP)+    ;RETURN TO PREG05 SUBRT.
                                ENDMMSG
                                L10004:
                                TRAP      C#MSG
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2160 .SBTTL GLOBAL ERROR REPORTING ROUTINE ER6201
2161 ;*****
2162 ;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH IS INTENDED FOR USE IN THE
2163 ;* FRAMING ERROR AND PARITY ERROR TESTS. IT REPORTS ERROR INFORMATION
2164 ;* WHEN A CHARACTER HAS BEEN READ FROM THE DUT WITH THE INCORRECT
2165 ;* COMBINATION OF FRAMING AND PARITY ERROR BITS. THESE ERRORS ARE REPORTED
2166 ;* ONLY IF EXTENDED ERROR REPORTING HAS BEEN REQUESTED.
2167 ;*
2168 ;* INPUTS: R2 - DATA BYTE READ FROM THE DUT, INCLUDING ERROR FLAGS.
2169 ;* R3 - LINE NUMBER MULTIPLIED BY 2.
2170 ;* R5 - MESSAGE FLAGS, WHICH MESSAGES TO REPORT.
2171 ;* BIT1 AND BIT3 - INDICATE WHICH MESSAGES ARE TO BE
2172 ;* REPORTED, FRAMING OR PARITY RESPECTIVELY.
2173 ;* BIT 0 AND BIT 2 - "SET"/"CLEAR" MESSAGE FOR
2174 ;* FRAMING AND PARITY ERRORS BITS.
2175 ;*
2176 ;* OUTPUTS: MESSAGES ARE PRINTED AT THE OPERATOR CONSOLE.
2177 ;*
2178 ;* CALLING SEQUENCE: INCLUDE THE LABEL "ER6201" AS THE MESSAGE POINTER
2179 ;* PARAMETER IN THE DIAG SUPER ERROR REPO. MACRO CALL.
2180 ;*
2181 ;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC AND EXTENDED ERROR INFORMATION.
2182 ;* THE CONTENTS OF THE INDIRECT ADDRESS REGISTER FIELD OF THE DUT
2183 ;* CSR MAY BE ALTERED.
2184 ;*
2185 ;* SUBORDINATE ROUTINES USED: PRTLPR.
2186 ;*****
2187
2188 014254 BGNMSG ER6201
2189 014254 SAVE ER6201::
2190 014254 004567 171044 JSR R5,PREG05 ;SAVE THE CONTENTS OF THE GPR'S.
2191 ; ;CALL REGISTER SAVE SUBRT.
2192
2193 ;*
2194 ;* EXIT IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
2195 ;*
2196 ;* BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
2197 ;* BEQ 601 ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
2198 ;* ;DURING THE SOFTWARE QUESTIONS.
2199
2200 014270 016304 005234 MOV TXRXLB(R3),R4 ;GET THE ASSOCIATED TX LINE NUMBER.
2201 014274 006203 ASR R3 ;CALCULATE THE RX LINE NUMBER.
2202 014276 006204 ASR R4 ;CALCULATE THE ASSOCIATED LINE NUMBER.
2203 014300 PRINTB #EF6201,R3,R4 ;REPORT THE ERROR TYPE AND LINE NUMBERS.
2204 014300 010446 MOV R4,-(SP)
2205 014302 010346 MOV R3,-(SP)
2206 014304 012746 005671 MOV #EF6201,-(SP)
2207 014310 012746 000003 MOV #3,-(SP)
2208 014314 010600 MOV SP,R0
2209 014316 104414 TRAP C:PNTB
2210 014320 062706 000010 ADD #10,SP
2211
2212 ;*
2213 ;* REPORT FRAMING ERROR PROBLEM.
2214 ;*
2215 ;* MOV #EM6202,R4 ;SELECT THE "ERROR BIT CLEAR" MESSAGE.
2216 ;* MOV #EM0509,R1 ;SELECT EXPECTED "ERROR BIT SET" MESSAGE.
2217 ;* BIT #BIT1,R5 ;TEST IF FRAMING ERROR MESSAGE TO BE REPORTED.

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2208 014340 001427          BEQ      6:          ;BRANCH TO REPORT PARITY ERROR.
2209 014342 032705 000001    BIT      #BIT0,R5      ;TEST "ERROR BIT SET/CLEAR" MESSAGE FLAG.
2210 014346 001403          BEQ      2:          ;BRANCH TO REPORT ERROR BIT "CLEAR".
2211 014350 010401          MOV      R4,R1          ;SELECT EXPECTED "CLEAR" STATE MESSAGE.
2212 014352 012704 010077    MOV      #EM0509,R4    ;SELECT THE "ERROR BIT SET" MESSAGE.
2213 014356          2:      PRINTX  #EF6202,R4,R1    ;REPORT THE SOURCE OF THE PROBLEM.
                                MOV      R1,-(SP)
                                MOV      R4,-(SP)
                                MOV      #EF6202,-(SP)
                                MOV      #3,-(SP)
                                MOV      SP,R0
                                TRAP     C#PNTX
                                ADD      #10,SP
014356 010146
014360 010446
014362 012746 006004
014366 012746 000003
014372 010600
014374 104415
014376 062706 000010
2214 014402 032705 000010    BIT      #BIT3,R5      ;TEST IF PARITY ERROR MESSAGE TO BE REPORTED.
2215 014406 001424          BEQ      10:         ;EXIT IF PARITY ERROR REPORT TO BE SKIPPED.
2216 014410 012704 011151    MOV      #EM6202,R4    ;SELECT THE "CLEAR" MESSAGE.
2217 014414 012701 010077    MOV      #EM0509,R1    ;SELECT THE EXPECTED "SET" STATE MESSAGE.
2218          ;+
2219          ; REPORT PARITY ERROR PROBLEM.
2220          ; -
2221
2222 014420 032705 000004    6:      BIT      #BIT2,R5      ;TEST "SET"/"CLEAR" MESSAGE FLAG.
2223 014424 001403          BEQ      8:          ;BRANCH TO REPORT ERROR BIT CLEAR.
2224 014426 010401          MOV      R4,R1          ;SELECT THE EXPECTED "CLEAR" STATE MESSAGE.
2225 014430 012704 010077    MOV      #EM0509,R4    ;SELECT THE "ERROR BIT SET" MESSAGE.
2226 014434          8:      PRINTX  #EF6203,R4,R1    ;REPORT THE SOURCE OF THE PROBLEM.
                                MOV      R1,-(SP)
                                MOV      R4,-(SP)
                                MOV      #EF6203,-(SP)
                                MOV      #3,-(SP)
                                MOV      SP,R0
                                TRAP     C#PNTX
                                ADD      #10,SP
014434 010146
014436 010446
014440 012746 006102
014444 012746 000003
014450 010600
014452 104415
014454 062706 000010
2227
2228 014460          10:     PRINTX  #EF1603,R2      ;REPORT ACTUAL DATA RECEIVED.
014460 010246
014462 012746 005512
014466 012746 000002
014472 010600
014474 104415
014476 062706 000006
2229
2230 014502 004767 006152    JSR      PC,PRTLPR    ;REPORT THE CONTENTS OF THE LPR FOR THIS LINE.
2231 014506          60:     PASS                    ;RESTORE THE CONTENTS OF THE GPR'S.
014506 004736          JSR      PC,@(SP)+          ;RETURN TO PREG05 SUBRT.
2232 014510          ENDMSG
                                L10005:
014510 104423          TRAP     C#MSG

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2234 .SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9001 -
2235 ;*****
2236 ;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH REPORTS AN UNEXPECTED
2237 ;* CODE WHICH HAS BEEN FOUND IN THE DUT CSR. THIS CODE CAN BE A BMP
2238 ;* CODE, A SELF-TEST CODE, OR A MODEM STATUS CODE.
2239 ;*
2240 ;* INPUTS: R1 - ADDRESS OF MESSAGE TO PRINT FIRST.
2241 ;* R2 - SINGLE BYTE CODE WHICH HAS BEEN READ FROM THE DUT.
2242 ;* R4 - LINE NUMBER ASSOCIATED WITH THE CODE.
2243 ;*
2244 ;* OUTPUTS: A MESSAGES IS PRINTED AT THE OPERATOR CONSOLE.
2245 ;*
2246 ;* CALLING SEQUENCE: INCLUDE THE LABEL "ER9001" AS THE MESSAGE POINTER
2247 ;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
2248 ;*
2249 ;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC AND EXTENDED ERROR INFORMATION.
2250 ;*
2251 ;* SUBORDINATE ROUTINES USED: NONE.
2252 ;*****
2253
2254 014512 BGNMSG ER9001
2255 014512 ER9001::
2256
2257 ;*
2258 ;* EXIT IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
2259 014512 032767 000100 165442 BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
2260 014520 001433 BEQ 21 ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
2261 ;* DURING THE SOFTWARE QUESTIONS.
2262
2263 014522 PRINTB #EF9001,R1 ;REPORT TYPE OF CODE FOUND.
2264 014522 010146 MOV R1,-(SP)
2265 014524 012746 006235 MOV #EF9001,-(SP)
2266 014530 012746 000002 MOV #2,-(SP)
2267 014534 010600 MOV SP,R0
2268 014536 104414 TRAP C#PNTB
2269 014540 062706 000006 ADD #6,SP
2270 014544 PRINTX #EF9002,R4 ;REPORT THE LINE NUMBER OF THE CODE.
2271 014544 010446 MOV R4,-(SP)
2272 014546 012746 006317 MOV #EF9002,-(SP)
2273 014552 012746 000002 MOV #2,-(SP)
2274 014556 010600 MOV SP,R0
2275 014560 104415 TRAP C#PNTX
2276 014562 062706 000006 ADD #6,SP
2277 014566 PRINTX #EF9003,R2 ;REPORT THE CODE WHICH WAS FOUND.
2278 014566 010246 MOV R2,-(SP)
2279 014570 012746 006371 MOV #EF9003,-(SP)
2280 014574 012746 000002 MOV #2,-(SP)
2281 014600 010600 MOV SP,R0
2282 014602 104415 TRAP C#PNTX
2283 014604 062706 000006 ADD #6,SP
2284
2285 21: ENDMSG
2286
2287 014610 L10006:
2288 014610 TRAP C#MSG
2289 014610 104423

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014612

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2296 014612 032767 000100 165342
2297 014620 001462
2298
2299

2300 014622 006203
2301 014624 042702 177400
2302 014630

014630 010346
014632 010146
014634 012746 006501
014640 012746 000003
014644 010600
014646 104414
014650 062706 000010

2303 014654
014654 010246
014656 012746 011541
014662 012746 006420
014666 012746 000003
014672 010600
014674 104415
014676 062706 000010

2304 014702 005704
2305 014704 100414
2306 014706

014706 010446
014710 012746 011515
014714 012746 006420
014720 012746 000003

.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9002 -
;*****
;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH IS INTENDED FOR USE IN THE
;* TRANSMISSION AND RECEPTION TESTS. IT REPORTS THE TYPE OF ERROR WHICH
;* HAS OCCURRED WHEN INCORRECT DATA IS RECEIVED FROM THE DUT. THIS
;* ROUTINE ALSO REPORTS THE READ AND EXPECTED DATA VALUES.
;*
;* INPUTS: R1 - ADDRESS OF MESSAGE TO PRINT FIRST.
;* R2 - DATA BYTE READ FROM THE DUT.
;* R3 - LINE NUMBER MULTIPLIED BY 2.
;* R4 - EXPECTED DATA BYTE, BIT 15 SET IF "NONE".
;*
;* OUTPUTS: A MESSAGES IS PRINTED AT THE OPERATOR CONSOLE.
;*
;* CALLING SEQUENCE: INCLUDE THE LABEL "ER9002" AS THE MESSAGE POINTER
;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
;*
;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC AND EXTENDED ERROR INFORMATION.
;*
;* SUBORDINATE ROUTINES USED: PRTLPR.
;*****

BGNMSG ER9002

ER9002::

;*
; EXIT IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
;*

BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
REQ 62; ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
;DURING THE SOFTWARE QUESTIONS.

ASR R3 ;CALCULATE THE LINE NUMBER.
BIC #177400,R2 ;MASK OUT ALL BUT DATA IN READ CHAR.
PRINTB #EF9006,R1,R3 ;PRINT THE FIRST LINE OF THE MESSAGE.

MOV R3,-(SP)
MOV R1,-(SP)
MOV #EF9006,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C:PNTB
ADD #10,SP

PRINTX #EF9004,#EM9010,R2 ;PRINT ACTUAL DATA.

MOV R2,-(SP)
MOV #EM9010,-(SP)
MOV #EF9004,-(SP)
MOV #3,-(SP)
MOV SP,R0
TRAP C:PNTX
ADD #10,SP

TST R4 ;CHECK FOR "NONE" CODE SET IN EXPECTED DATA.
BMI 2; ;BRANCH TO PRINT "NONE" MESSAGE IF FLAG SET.
PRINTX #EF9004,#EM9009,R4 ;PRINT EXPECTED DATA.

MOV R4,-(SP)
MOV #EM9009,-(SP)
MOV #EF9004,-(SP)
MOV #3,-(SP)

	014724	010600							MOV	SP,R0
	014726	104415							TRAP	C\$PNTX
	014730	062706	000010						ADD	#10,SP
2307	014734	000412		BR	60\$;EXIT THIS ROUTINE.			
2308	014736			PRINTX	#EF9005,#EM9009		;PRINT MESSAGE INDICATING NO EXPECTED DATA.			
	014736	012746	011515						MOV	#EM9009,-(SP)
	014742	012746	006450						MOV	#EF9005,-(SP)
	014746	012746	000002						MOV	#2,-(SP)
	014752	010600							MOV	SP,R0
	014754	104415							TRAP	C\$PNTX
	014756	062706	000006						ADD	#6,SP
2309	014762	004767	005672	60\$: JSR	PC,PRTLPR		;PRINT CONTENTS OF THE LPR REGISTER.			
2310	014766			62\$:	ENDMSG					
	014766							L10007:		
	014766	104423						TRAP	C\$MSG	

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2312 .SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9003 -
2313 ;*****
2314 ;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH IS INTENDED FOR USE IN THE
2315 ;* TRANSMISSION AND RECEPTION TESTS. IT REPORTS ERROR INFORMATION WHEN
2316 ;* A CHARACTER HAS BEEN READ FROM THE DUT WITH AN ERROR FLAG OR FLAGS
2317 ;* SET (IE. OVER-RUN, FRAMING, OR PARITY FLAG).
2318 ;*
2319 ;* INPUTS: R2 - DATA BYTE READ FROM THE DUT, INCLUDING ERROR FLAGS.
2320 ;* R3 - LINE NUMBER MULTIPLIED BY 2.
2321 ;*
2322 ;* OUTPUTS: A MESSAGES IS PRINTED AT THE OPERATOR CONSOLE.
2323 ;*
2324 ;* CALLING SEQUENCE: INCLUDE THE LABEL "ER9003" AS THE MESSAGE POINTER
2325 ;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
2326 ;*
2327 ;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC AND EXTENDED ERROR INFORMATION.
2328 ;* THE CONTENTS OF THE INDIRECT ADDRESS REGISTER FIELD OF THE DUT
2329 ;* CSR MAY BE ALTERED.
2330 ;*
2331 ;* SUBORDINATE ROUTINES USED: NONE.
2332 ;*****
2333
2334 014770 BGNMSG ER9003
2335 014770 ER9003::
2336
2337 ;*
2338 ;* EXIT THE TEST IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
2339 014770 032767 000100 165164
2340 014776 001470
2341
2342
2343 015000 006203
2344 015002
2345 015002 010346
2346 015004 012746 006520
2347 015010 012746 000002
2348 015014 010600
2349 015016 104414
2350 015020 062706 000006
2351 015024 010201
2352 015026 042701 177400
2353 015032
2354 015032 010146
2355 015034 012746 006614
2356 015040 012746 000002
2357 015044 010600
2358 015046 104415
2359 015050 062706 000006
2360
2361 ;*
2362 ;* REPORT OVERRUN FLAG SET IF NECESSARY.
2363 ;*
2364 ;*
2365 ;*
2366 ;*
2367 ;*
2368 ;*
2369 ;*
2370 ;*
2371 ;*
2372 ;*
2373 ;*
2374 ;*
2375 ;*
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2356      ; REPORT FRAMING FLAG SET IF NECESSARY.
2357      ;-
2358 015072 012701 011575      2$:      MOV      #EM9012,R1      ;SELECT THE FRAMING ERROR MESSAGE.
2359 015076 032702 020000      ;      BIT      #BIT13,R2      ;CHECK FRAMING ERROR FLAG IN PASSED IN CHAR.
2360 015102 001402              ;      BEQ      4$      ;SKIP ERROR IF FRAMING ERROR FLAG WAS CLEAR.
2361 015104 004767 000020      ;      JSR      PC,50$      ;REPORT THE FRAMING ERROR MESSAGE.
2362
2363      ; REPORT PARITY FLAG SET IF NECESSARY.
2364      ;-
2365 015110 012701 011605      4$:      MOV      #EM9013,R1      ;SELECT THE PARITY ERROR MESSAGE.
2366 015114 032702 010000      ;      BIT      #BIT12,R2      ;CHECK PARITY ERROR FLAG IN PASSED IN CHAR.
2367 015120 001415              ;      BEQ      60$      ;EXIT ROUTINE IF PARITY ERROR FLAG WAS CLEAR.
2368 015122 004767 000002      ;      JSR      PC,50$      ;REPORT THE PARITY ERROR MESSAGE.
2369 015126 000412              ;      BR       60$      ;EXIT THIS ROUTINE.
2370
2371      ;-
2372      ; LOCAL SUBROUTINE TO REPORT AN ERROR FLAG STATUS.
2373      ;-
2374 015130      50$:      PRINTX  #EF9009,R1
2375 015130 010146              ;
2376 015132 012746 006653              ;
2377 015136 012746 000002              ;
2378 015142 010600              ;
2379 015144 104415              ;
2380 015146 062706 000006              ;
2381 015152 000207              ;
2382      RTS      PC
2383
2384 60$:      JSR      PC,PRTLPR      ;REPORT THE LPR CONTENTS FOR THIS LINE.
2385 62$:      ENDMSG
2386
2387      L10010:      TRAP      C#MSG

```



```

2380 .SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9004 -
2381 ;*****
2382 ;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH REPORTS ERROR SUMMARIES
2383 ;* FOR LINES WHICH HAVE EXCEEDED THE SPECIFIED MAXIMUM NUMBER OF
2384 ;* INDIVIDUAL RECEPTION ERRORS, PROVIDED EXTENDED ERROR REPORTING HAS
2385 ;* BEEN REQUESTED BY THE OPERATOR.
2386 ;*
2387 ;* INPUTS: R1 - ADDRESS OF MESSAGE TO PRINT FIRST.
2388 ;* ERCNTB - LABEL AT BASE OF LINE ERROR COUNTERS TABLE.
2389 ;* ERSRFR - "REPORT ERROR SUMMARY FOR LINE" FLAGS.
2390 ;*
2391 ;* OUTPUTS: A MESSAGE IS PRINTED AT THE OPERATOR CONSOLE.
2392 ;*
2393 ;* CALLING SEQUENCE: INCLUDE THE LABEL "ER9004" AS THE MESSAGE POINTER
2394 ;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
2395 ;*
2396 ;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC AND EXTENDED ERROR INFORMATION.
2397 ;* THE CONTENTS OF GPR'S R2, R3, R4, AND R5 ARE DESTROYED.
2398 ;*
2399 ;* SUBORDINATE ROUTINES USED: NONE.
2400 ;*****
2401
2402 015162 BGNMSG ER9004
2403 015162 ER9004::
2404 015162 012700 000100 MOV #BIT06,R0 ;TRY TO CLEAR THE
2405 015166 046700 164770 BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
2406 015172 001040 BNE 6# ;EXIT IF FLAG NOT SET.
2407
2408 015174 PRINTB #EF0503,#EM9014 ;REPORT THE SECONDARY ERROR MESSAGE.
2409 015174 012746 011614 MOV #EM9014,-(SP)
2410 015200 012746 005453 MOV #EF0503,-(SP)
2411 015204 012746 000002 MOV #2,-(SP)
2412 015210 010600 MOV SP,R0
2413 015212 104414 TRAP C#PNTB
2414 015214 062706 000006 ADD #6,SP
2415 015220 005002 CLR R2 ;CLEAR THE LINE COUNTER.
2416 015222 016703 165252 MOV ERSRFR,R3 ;GET THE ERROR SUMMARY FLAGS.
2417 015226 005004 CLR R4 ;CLEAR "LINE COUNTER TIMES 2" OFFSET.
2418 015230 000241 2# CLC ;CLEAR THE CARRY FOR THE FOLLOWING ROTATE.
2419 015232 006003 ROR R3 ;SHIFT ANOTHER ERROR SUMMARY FLAG INTO CARRY.
2420 015234 103013 BCC 4# ;SKIP PRINTING MESSAGE IF FLAG FOR LINE CLEAR.
2421 015236 PRINTX #EF9010,R2,ERCNTB(R4)
2422 015236 016446 003302 MOV ERCNTB(R4),-(SP)
2423 015242 010246 MOV R2,-(SP)
2424 015244 012746 006712 MOV #EF9010,-(SP)
2425 015250 012746 000003 MOV #3,-(SP)
2426 015254 010600 MOV SP,R0
2427 015256 104415 TRAP C#PNTX
2428 015260 062706 000010 ADD #10,SP
2429 015264 012405 4# MOV (R4)+,R5 ;INCREMENT THE LINE OFFSET BY 2.
2430 015266 005202 INC R2 ;INCREMENT THE LINE COUNTER.
2431 015270 005703 TST R3 ;CHECK THE ERROR SUMMARY FLAGS.
2432 015272 001356 BNE 2# ;IF MORE FLAGS SET, LOOP TO DO OTHER LINES.
2433 015274 6# ENDMSG
2434 015274

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L10011:

K5

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GLOBAL ERROR REPORTING ROUTINE ER9004 -

SEQ 62

015274 104423

TRAP C\$MSG

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2423 .SBTTL GLOBAL ERROR REPORTING ROUTINE ER9005 -
2424 ;*****
2425 ;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH REPORTS INCOMPLETE DATA
2426 ;* TRANSMISSIONS OR RECEPTIONS.
2427 ;*
2428 ;* INPUTS: R1 - EITHER "TRANSMITTED" OR "RECEIVED" TO INDICATE TX OR RX.
2429 ;* R2 - BIT MAP OF LINES WHICH DID NOT COMPLETE TX OR RX.
2430 ;* R4 - ADDRESS OF BASE OF THE CORRECT CHARACTER COUNTERS TABLE.
2431 ;* DPLENB - LABEL AT BASE OF DATA PATTERN LENGTH TABLE.
2432 ;* EM9015 - SYMBOLIC ADDRESS OF THE "TRANSMITTED" MESSAGE.
2433 ;*
2434 ;* OUTPUTS: A MESSAGE IS PRINTED AT THE OPERATOR CONSOLE.
2435 ;*
2436 ;* CALLING SEQUENCE: INCLUDE THE LABEL "ER9005" AS THE MESSAGE POINTER
2437 ;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
2438 ;*
2439 ;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC AND EXTENDED ERROR INFORMATION.
2440 ;* THE CONTENTS OF THE INDIRECT ADDRESS FIELD IN THE OUT CSR MAY
2441 ;* BE ALTERED.
2442 ;*
2443 ;* SUBORDINATE ROUTINES USED: PRTLPR.
2444 ;*****
2445
2446 015276 BGNMSG ER9005
2447 015276 ER9005::
2448 015276 004567 170022 SAVE JSR R5,PREG05 ;SAVE THE CONTENTS OF THE GPR'S.
2449 015302 012700 000100 ;CALL REGISTER SAVE SUBRT.
2450 015306 046700 164650 MOV #BIT06,R0 ;TRY TO CLEAR THE
2451 015312 001107 BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
2452 BNE 10$ ;EXIT IF FLAG NOT SET.
2453 015314 PRINTB #EF9013,R1 ;REPORT THE SECONDARY ERROR MESSAGE.
2454 015314 010146 MOV R1,-(SP)
2455 015316 012746 007115 MOV #EF9013,-(SP)
2456 015322 012746 000002 MOV #2,-(SP)
2457 015326 010600 MOV SP,R0
2458 015330 104414 TRAP C$PNTB
2459 015332 062706 000006 ADD #6,SP
2460 015336 005003 CLR R3 ;CLEAR THE LINE COUNTER.
2461 015340 022701 011710 CMP #EM9015,R1 ;CHECK IF ADDRESS CORRESPONDS TO TX MESSAGE.
2462 015344 001032 BNE 6$ ;BRANCH IF RECEPTION MESSAGE TO BE PRINTED.
2463 ;*
2464 ;* PERFORM TX INCOMPLETE ERROR MESSAGE REPORTING.
2465 ;*
2466 PRINTX #EM9030 ;PRINT "NO TX COMPLETION INTERRUPTS RECEIVED"
2467 015346 012746 012323 MOV #EM9030,-(SP)
2468 015352 012746 000001 MOV #1,-(SP)
2469 015356 010600 MOV SP,R0
2470 015360 104415 TRAP C$PNTX
2471 015362 062706 000004 ADD #4,SP
2472 2$: CLC ;CLEAR THE CARRY FOR THE FOLLOWING ROTATE.
2473 ROR R2 ;SHIFT "TX NOT DONE" FLAG INTO CARRY.
2474 BCC 4$ ;SKIP PRINTING MESSAGE IF FLAG FOR LINE CLEAR.
2475 PRINTX #EF9020,R3 ;PRINT "TOO FEW TX ACTIONS GENERATED" MSG.
2476 015374 010346 MOV R3,-(SP)

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015376 012746 007201
015402 012746 000002
015406 010600
015410 104415
015412 062706 000006
2466 015416 004767 005236
2467 015422 005203
2468 015424 005702
2469 015426 001357
2470 015430 000440
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2474 015432 000241
2475 015434 006002
2476 015436 103031
2477 015440 006303
2478 015442 016305 005234
2479 015446 010246
2480 015450 010502
2481 015452 016505 003442
2482 015456 006202
2483 015460 006203
2484 015462
015462 010246
015464 010546
015466 011446
015470 010146
015472 010346
015474 012746 007001
015500 012746 000006
015504 010600
015506 104415
015510 062706 000016
2485 015514 012602
2486 015516 004767 005136
2487 015522 005724
2488 015524 005203
2489 015526 005702
2490 015530 001340
2491 015532
015532 004736
2492 015534
015534 104423

;+
; PERFORM RX INCOMPLETE ERROR MESSAGE REPORTING.
;-
6$: CLC ;CLEAR THE CARRY FOR THE FOLLOWING ROTATE.
ROR R2 ;SHIFT "RX NOT DONE" FLAG INTO CARRY.
BCC 8$ ;SKIP PRINTING MESSAGE IF FLAG FOR LINE CLEAR.
ASL R3 ;SHIFT LINE # TO GIVE CORRECT TABLE OFFSET.
MOV TXRXLB(R3),R5 ;GET THE "ASSOCIATED" RECEIVE LINE OFFSET.
MOV R2,-(SP) ;SAVE THE "RX NOT DONE" FLAGS ON THE STACK.
MOV R5,R2 ;COPY THE ASSOCIATED TX LINE OFFSET.
MOV CHCNTB(R5),R5 ;GET THE TOTAL NUMBER OF EXPECTED CHARS.
ASR R2 ;SHIFT THE TABLE OFFSET TO GIVE A LINE NUMBER.
ASR R3 ;SHIFT TABLE OFFSET TO GIVE LINE NUMBER.
PRINTX #EF9012,R3,R1,(R4),R5,R2 ;REPORT NUMBER OF CHARS ON LINE.
MOV R2,-(SP)
MOV R5,-(SP)
MOV (R4),-(SP)
MOV R1,-(SP)
MOV R3,-(SP)
MOV #EF9012,-(SP)
MOV #6,-(SP)
MOV SP,R0
TRAP C#PNTX
ADD #16,SP

MOV #EF9020,(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C#PNTX
ADD #6,SP

4$: JSR PC,PRTLPR ;REPORT CONTENTS OF LPR REGISTER FOR THIS LINE.
INC R3 ;INCREMENT LINE COUNTER.
TST R2 ;CHECK THE "TX NOT DONE FLAGS".
BNE 2$ ;IF MORE FLAGS SET, LOOP TO DO OTHER LINES.
BR 10$ ;EXIT THIS ROUTINE.

;+
; PERFORM RX INCOMPLETE ERROR MESSAGE REPORTING.
;-
6$: CLC ;CLEAR THE CARRY FOR THE FOLLOWING ROTATE.
ROR R2 ;SHIFT "RX NOT DONE" FLAG INTO CARRY.
BCC 8$ ;SKIP PRINTING MESSAGE IF FLAG FOR LINE CLEAR.
ASL R3 ;SHIFT LINE # TO GIVE CORRECT TABLE OFFSET.
MOV TXRXLB(R3),R5 ;GET THE "ASSOCIATED" RECEIVE LINE OFFSET.
MOV R2,-(SP) ;SAVE THE "RX NOT DONE" FLAGS ON THE STACK.
MOV R5,R2 ;COPY THE ASSOCIATED TX LINE OFFSET.
MOV CHCNTB(R5),R5 ;GET THE TOTAL NUMBER OF EXPECTED CHARS.
ASR R2 ;SHIFT THE TABLE OFFSET TO GIVE A LINE NUMBER.
ASR R3 ;SHIFT TABLE OFFSET TO GIVE LINE NUMBER.
PRINTX #EF9012,R3,R1,(R4),R5,R2 ;REPORT NUMBER OF CHARS ON LINE.
MOV R2,-(SP)
MOV R5,-(SP)
MOV (R4),-(SP)
MOV R1,-(SP)
MOV R3,-(SP)
MOV #EF9012,-(SP)
MOV #6,-(SP)
MOV SP,R0
TRAP C#PNTX
ADD #16,SP

MOV (SP)+,R2 ;RESTORE THE "RX NOT DONE" FLAGS.
JSR PC,PRTLPR ;REPORT CONTENTS OF LPR REGISTER FOR THIS LINE.
8$: TST (R4)+ ;INCREMENT THE CHARACTER COUNTER TABLE.
INC R3 ;INCREMENT THE LINE COUNTER.
TST R2 ;CHECK THE "RX NOT DONE FLAGS".
BNE 6$ ;IF MORE FLAGS SET, LOOP TO DO OTHER LINES.
10$: PASS ;RESTORE THE CONTENTS OF THE GPRS.
JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.

ENDMSG
L10012: TRAP C#MSG

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2494 .SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9101 -
2495 ;*****
2496 ;* THIS IS A GENERAL ERROR REPORTING SUBROUTINE WHICH REPORTS A MESSAGE
2497 ;* WHICH TAKES A SINGLE, 2 DIGIT DECIMAL ARGUMENT AFTER THE END OF AN
2498 ;* ASCII MESSAGE.
2499 ;*
2500 ;* INPUTS: R1 - VALUE TO BE PRINTED AFTER MSG AS 2 DECIMAL DIGITS.
2501 ;* R2 - ADDRESS OF MESSAGE TO PRINT FIRST.
2502 ;*
2503 ;* OUTPUTS: A MESSAGES IS PRINTED AT THE OPERATOR CONSOLE.
2504 ;*
2505 ;* CALLING SEQUENCE: INCLUDE THE LABEL "ER9101" AS THE MESSAGE POINTER
2506 ;* PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
2507 ;*
2508 ;* COMMENTS: THE MESSAGE IS PRINTED AS BASIC ERROR INFORMATION.
2509 ;*
2510 ;* SUBORDINATE ROUTINES USED: NONE.
2511 ;*****
2512
2513 015536 BGNMSG ER9101
2514 015536 ER9101::
2515 015536 012700 000100 MOV #BIT06,R0 ;TRY TO CLEAR THE
2516 015542 046700 164414 BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
2517 015546 001012 BNE 2$ ;EXIT IF FLAG NOT SET.
2518
2519
2520 015550 PRINTB #EF9006,R2,R1 ;REPORT THE STRING FOLLOWED BY THE NUMBER.
2521 015550 010146 MOV R1,-(SP)
2522 015552 010246 MOV R2,-(SP)
2523 015554 012746 006501 MOV #EF9006,-(SP)
2524 015560 012746 000003 MOV #3,-(SP)
2525 015564 010600 MOV SP,R0
2526 015566 104414 TRAP C#PNTB
2527 015570 062706 000010 ADD #10,SP
2528
2529 2$: ENDMMSG
2530
2531 L10013:
2532 TRAP C#MSG

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2524 .SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9102 -
2525 ;*****
2526 ;* THIS IS AN ERROR REPORTING SUBROUTINE WHICH PRINTS ADDITIONAL ERROR
2527 ;* INFORMATION AFTER THE ERROR MESSAGE HEADER, PROVIDED EXTENDED ERROR
2528 ;* REPORTING HAS BEEN REQUESTED.
2529 ;* THIS ROUTINE IS PASSED A BIT MAP WHICH SPECIFIES THE LINES FOR WHICH
2530 ;* THE ERROR CONDITION SHOULD BE REPORTED.
2531 ;*
2532 ;* INPUTS: R1 - ADDRESS OF THE MESSAGE TO BE PRINTED BY THIS ROUTINE.
2533 ;* R2 - BIT MAP OF LINES FOR WHICH TO REPORT ERRORS.
2534 ;*
2535 ;* OUTPUTS: MESSAGES ARE PRINTED AT THE OPERATOR CONSOLE.
2536 ;*
2537 ;* CALLING SEQUENCE: LOAD THE ADDRESS OF THE MESSAGE IN R1.
2538 ;* LOAD THE BIT MAP OF LINES WITH ERRORS IN R2.
2539 ;* INCLUDE THE LABEL "ER9102" AS THE MESSAGE POINTER
2540 ;* (ERRBLK) IN THE DIAG SUPER ERROR REPORT MACRO CALL.
2541 ;*
2542 ;* COMMENTS: THE OUTPUT FORMAT OF THIS MESSAGE IS:
2543 ;* "TEXT MESSAGE POINTED TO BY R1"
2544 ;* ERROR CONDITION ON LINE NN"
2545 ;* "ERROR CONDITION ON LINE ..."
2546 ;* THE TOP MESSAGE, AND THE MESSAGE FOR EACH LINE ARE PRINTED
2547 ;* AS BASIC ERROR INFORMATION.
2548 ;*
2549 ;* SUBORDINATE ROUTINES USED: NONE.
2550 ;*****
2551
2552 015576 BGNMSG ER9102
2553 015576 ER9102::
2554 015576 004567 167522 SAVE JSR R5,PREG05 ;SAVE THE CONTENTS OF THE GPRS.
2555 ;* ;EXIT IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED ;CALL REGISTER SAVE SUBRT.
2556 ;*
2557 015602 032767 000100 164352 BIT #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
2558 015610 001441 BEQ 60# ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
2559 ;* ;DURING THE SOFTWARE QUESTIONS.
2560
2561 015612 PRINTB #EF0503,R1 ;PRINT THE FIRST LINE OF THE MESSAGE.
2562 015612 010146 MOV R1,-(SP)
2563 015614 012746 005453 MOV #EF0503,-(SP)
2564 015620 012746 000002 MOV #2,-(SP)
2565 015624 010600 MOV SP,R0
2566 015626 104414 TRAP C#PNTB
2567 015630 062706 000006 ADD #6,SP
2568 21: CLR R3 ;CLEAR THE LINE NUMBER.
2569 CLC ;PREPARE TO ROTATE NEXT BIT OUT OF MAP.
2570 ROR R2 ;GET THE NEXT BIT OF THE BIT MAP.
2571 BCC 4# ;SKIP PRINTING MESSAGE IF THE BIT IS CLEAR.
2572 PRINTB #EF9103,R3 ;REPORT THIS LINE HAD THE ERROR.
2573 MOV R3,-(SP)
2574 MOV #EF9103,-(SP)
2575 MOV #2,-(SP)
2576 MOV SP,R0
2577 TRAP C#PNTB
2578 ADD #6,SP

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2567 015666 005203          4$:   INC   R3          ;INCREMENT THE LINE COUNTER.
2568 015670 005702          TST   R2          ;CHECK THE BIT MAP.
2569 015672 001361          BNE   2$          ;LOOP IF NOT ALL SET BITS REMOVED FROM BIT MAP.
2570 015674          PRINTB 0EF9101          ;PRINT A BLANK LINE.
      015674 012746 007262          MOV   0EF9101,-(SP)
      015700 012746 000001          MOV   01,-(SP)
      015704 010600          MOV   SP,R0
      015706 104414          TRAP  C#PNTB
      015710 062706 000004          ADD   04,SP
2571 015714          60$:   PASS          JSR   ;RESTORE THE SAVED CONTENTS OF THE GPRS
      015714 004736          PC,0(SP).      ;RETURN TO PREG05 SUBRT.
2572 015716          ENOMSG
      015716 104423          L10014:      TRAP  C#MSG

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2595 015720
015720
2596 015720 004567 167400
015720
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2598 015724 012700 000100
2599 015730 046700 164226
2600 015734 001064
2601
2602 015736
015736 010146
015740 012746 005453
015744 012746 000002
015750 010600
015752 104414
015754 062706 000006
2603 015760 012703 002512
2604 015764 012705 012714
2605 015770 012301
2606 015772 012304
2607 015774 004767 000056
2608 016000 020302
2609 016002 103772
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2616 016004 020227 002706
2617 016010 001036
2618 016012 005762 000002
2619 016016 001433
2620 016020 012301
2621 016022 011304
2622 016024 012705 012744

.SBTTL GLOBAL ERROR REPORTING ROUTINE - ER9301 -

; THIS IS AN ERROR REPORTING SUBROUTINE WHICH PRINTS ANY BMP CODES
; THAT ARE FOUND IN THE BMP CODE QUEUE, TOGETHER WITH THE NUMBER OF
; THE TEST THAT WAS EXECUTING AT THE TIME THE BMP CODE WAS LOGGED.
; PROVIDED EXTENDED ERROR REPORTING HAS BEEN ENABLED.
; INPUTS: R1 - THE ADDRESS OF THE FIRST MESSAGE TO BE REPORTED.
; R2 - THE ADDRESS OF THE NEXT EMPTY CELL IN THE QUEUE.
; OUTPUTS: THE TEST NUMBER FOLLOWED BY THE BMP CODE ARE PRINTED AT THE
; OPERATOR CONSOLE.
; CALLING SEQUENCE: INCLUDE THE LABEL "ER9301" AS THE MESSAGE POINTER
; PARAMETER IN THE DIAG SUPER ERROR REPORT MACRO CALL.
; COMMENTS: THE MESSAGE IS PRINTED AS BASIC ERROR INFORMATION.
; SUBORDINATE ROUTINES USED: NONE.

BGNMSG ER9301
ER9301::
SAVE JSR ;SAVE THE GPRS ON THE STACK.
R5,PREG05 ;CALL REGISTER SAVE SUBRT.
MOV #BIT06,R0 ;TRY TO CLEAR THE
BIC OPTION,R0 ;EXT'D ERROR REPORTING FLAG
BNE 604 ;EXIT IF FLAG NOT SET.
PRINTB #CF0503,R1 ;REPORT UNEXPECTED BMP CODES FOUND.
MOV R1,-(SP)
MOV #EF0503,-(SP)
MOV #2,-(SP)
MOV SP,R0
TRAP C#PNTB
ADD #6,SP
MOV #BMPQ08,R3 ;GET THE START / DRESS OF THE BMP CODE QUEUE.
MOV #EM9302,R5 ;GET THE MESSAGE TO BE REPORTED.
24: MOV (R3)+,R1 ;GET THE NUMBER OF THE TEST THAT WAS EXECUTING.
MOV (R3)+,R4 ;GET BMP CODE THAT WAS REPORTED OFF THE QUEUE.
JSR PC,504 ;GO REPORT THE BMP CODE.
CMP R3,R2 ;CHECK IF ALL CODES HAVE BEEN REPORTED.
BLO 24 ;IF IT IS NOT THE LAST BMP CODE THEN LOOP.
; CHECK IF OVERFLOW HAS OCCURRED.
; THE CONDITIONS FOR OVERFLOW ARE: THE POINTER CONTAINS THE ADDRESS OF THE
; LAST CELL IN THE QUEUE, AND A BMP CODE HAS ALREADY BEEN WRITTEN INTO THAT
; CELL.
;- CMP R2,#BMPQ0E-4 ;CHECK IF THE POINTER IS AT THE LAST LOCATION.
BNE 604 ;EXIT IF NOT AT THE LAST LOCATION.
TST 2(R2) ;CHECK FOR A BMP CODE IN THE LAST CELL
BEQ 604 ;EXIT IF NO OVERFLOW HAS OCCURED, CELL EMPTY.
MOV (R3)+,R1 ;GET THE TEST NUMBER OFF THE QUEUE.
MOV (R3),R4 ;GET THE BMP CODE OFF THE QUEUE.
MOV #EM9303,R5 ;SELECT THE MESSAGE TO BE REPORTED.

Address	Hex	Op-Code	Op-Code Hex	Op-Code Symbolic	Comments	Op-Code Hex	Op-Code Symbolic	Comments
2623	016030			PRINTX	#EF9302			;REPORT OVERFLOW CONDITION.
	016030	012746	007401					MOV #EF9302,-(SP)
	016034	012746	000001					MOV #1,-(SP)
	016040	010600						MOV SP,R0
	016042	104415						TRAP C#PNTX
	016044	062706	000004					ADD #4,SP
2624	016050	004767	000002	JSR	PC,50\$;REPORT THE LAST BMP CODE PLACED ON THE QUEUE.
2625	016054	000414		BR	60\$;EXIT.
2626								
2627	016056			50\$:	PRINTX	#EF9301,R5,R1,R4		;PRINT THE MESSAGE.
	016056	010446						MOV R4,-(SP)
	016060	010146						MOV R1,-(SP)
	016062	010546						MOV R5,-(SP)
	016064	012746	007333					MOV #EF9301,-(SP)
	016070	012746	000004					MOV #4,-(SP)
	016074	010600						MOV SP,R0
	016076	104415						TRAP C#PNTX
	016100	062706	000012					ADD #12,SP
2628	016104	000207		RTS	PC			;RETURN.
2629	016106			60\$:	PASS			;RESTORE THE GPR CONTENTS.
	016106	004736				JSR	PC,8(SP)+	;RETURN TO PREG05 SUBRT.
2630								
2631	016110			ENDMSG				
	016110							L10015:
	016110	104423						TRAP C#MSG

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2633 .SBTTL GLOBAL SUBROUTINES SECTION
2635 ;*****
2636 ;
2637 ; FVTSKL3.P11
2638 ;
2639 ;*****
2641
2642
2643 ;**
2644 ; THE GLOBAL SUBROUTINES SECTION CONTAINS THE SUBROUTINES
2645 ; THAT ARE USED IN MORE THAN ONE TEST.
2646 ; -
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2648 .SBTTL GLOBAL SUBROUTINE - ALTFLD -
2649 ;* *****
2650 ;* - ALTER DEVICE REGISTER FIELDS ROUTINE -
2651 ;* THIS SUBROUTINE ALTERS THE SPECIFIED FIELD OF THE SPECIFIED DEVICE
2652 ;* REGISTER FOR THE SPECIFIED LINES. THIS ROUTINE CAN BE USED TO SET
2653 ;* OR CLEAR BITS WITHIN SELECTED FIELDS OF SELECTED REGISTERS.
2654 ;* USE EXAMPLES: SET RX.BAUD.RATE FIELDS ON LINES 3 AND 6.
2655 ;* CLEAR TX.DMA BITS ON ALL LINES.
2656 ;*
2657 ;* INPUTS: R1 - ADDRESS OF THE REGISTERS TO ALTER.
2658 ;* R2 - BIT FIELDS SET TO DESIRED STATES.
2659 ;* R3 - BIT MAP OF LINES FOR WHICH TO ALTER REGISTER.
2660 ;* R4 - MASK OF BITS TO ALTER (1 INDICATES CHANGE BIT).
2661 ;* CSRA - CONTAINS THE ADDRESS OF THE DEVICE CSR.
2662 ;* IESTAT - SAVED STATES OF THE INTERRUPT ENABLE BITS.
2663 ;*
2664 ;* OUTPUTS: DEVICE REGISTERS - SPECIFIED REGISTER FIELDS ALTERED.
2665 ;* CSR IND.ADR.REG FIELD - DESTROYED.
2666 ;*
2667 ;* CALLING SEQUENCE: JSR PC,ALTFLD
2668 ;*
2669 ;* COMMENTS: THIS ROUTINE READS THE SPECIFIED REGISTERS FOR ALL LINES
2670 ;* WITH NUMBERS LOWER THAN THE HIGHEST SPECIFIED LINE.
2671 ;* THIS ROUTINE DOES NOT READ THE CSR.
2672 ;*
2673 ;* SUBROUTINES CALLED: NONE.
2674 ;* -- *****
2675
2676 016112 ALTFLD:: SAVE JSR ,SAVE CONTENTS OF GPRS R0 THRU R5.
016112 004567 167206 R5,PREG05 ,CALL REGISTER SAVE SUBRT.
2677
2678 ;*
2679 ;* SET UP TO LOOP FOR EACH LINE:
2680 ;* PREPARE THE WORD TO BE ORED INTO THE REGISTER CONTENTS.
2681 ;* SET UP THE WORD TO WRITE INTO THE IND.ADR.REG FIELD OF THE CSR.
2682 ;*
2683 016116 010400 MOV R4,R0 ,CALCULATE THE NEW CONTENTS OF THE
2684 016120 005100 COM R0 , REGISTER FIELDS WHICH ARE TO BE
2685 016122 040002 BIC R0,R2 , ALTERED BY THIS ROUTINE.
2686 016124 016705 164104 MOV IESTAT,R5 ,SET UP TO WRITE IND.ADR.REG FIELD TO 0.
2687 ;*
2688 ;* LOOP ONCE FOR EACH LINE, ALTERING THE SPECIFIED FIELD IN THE SPECIFIED
2689 ;* REGISTER IF THE LINE HAS BEEN SELECTED FOR ALTERING.
2690 ;* EXIT THE LOOP IF NO MORE LINES TO ALTER, OR IF WE HAVE ALTERED THE MAX
2691 ;* ALLOWABLE NUMBER OF LINES (AS SPECIFIED BY NUMLNS).
2692 ;*
2693 016130 000241 CLC ,PREPARE FOR ROTATE, "TST R5" DOES THIS BELOW.
2694 016132 006003 20: ROR R3 ,GET THE LINE SELECT BIT FOR THIS LINE.
2695 016134 103006 BCC 40 ,SKIP SETUP IF LINE IS NOT SELECTED.
2696 016136 010577 164036 MOV R5,BCSRA ,SET OUT CSR IND.ADR.REG FIELD TO THIS LINE.
2697 016142 011100 MOV (R1),R0 ,GET THE PRESENT CONTENTS OF THE REG TO ALTER.
2698 016144 040400 BIC R4,R0 ,CLEAR THE BIT FIELDS WE ARE TO ALTER.
2699 016146 050200 BIS R2,R0 ,OR IN THE NEW STATES OF THE FIELDS.
2700 016150 010011 MOV R0,(R1) ,WRITE THE NEW REGISTER CONTENTS TO THE REG.
2701 016152 005205 40: INC R5 ,SET LINE NUMBER TO THE NEXT LINE.
2702 016154 005703 TST R3 ,CHECK FOR UNHANDLED LINES, CLEAR CARRY FLAG.
2703 016156 001365 BNE 20 ,LOOP IF SELECTED LINE(S) IS NOT HANDLED.

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2704  
2705 016160          604: PASS          ;RESTORE GPRS.  
      016160 004736          JSR      PC,8(SP)+ ;RETURN TO PREG05 SUBRT.  
2706 016162 000207          RTS      PC      ;RETURN TO CALLING ROUTNE.
```

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2708 .SBTTL GLOBAL SUBROUTINE - CALMSL -
2709 ;** *****
2710 ;* - CALIBRATE MILLI SECOND LOOP COUNT SUBROUTINE -
2711 ;* THIS SUBROUTINE CALIBRATES THE TIMING LOOP WHICH IS USED IN THE MSLOOP
2712 ;* ROUTINE. THIS SUBROUTINE CALCULATES A VALUE FOR THE MSLCNT VARIABLE
2713 ;* WHICH IS THE NUMBER OF SOFTWARE LOOPS WHICH TAKES 1 MS TO EXECUTE IN
2714 ;* THE MSLOOP ROUTINE. THIS ROUTINE CALIBRATES THE COUNT BY USING THE
2715 ;* LINE TIME CLOCK (LTC), SO IF NO LTC IS AVAILABLE THE DEFAULT VALUE FOR
2716 ;* THE DELAY COUNT MUST BE USED.
2717 ;*
2718 ;*
2719 ;* INPUTS: MSLCNT - DEFAULT 1 MS DELAY LOOP COUNT VALUE, OR
2720 ;* VALUE FROM PREVIOUS CALIBRATION.
2721 ;* MSTICK - NUMBER OF MS PER LTC CLOCK TICK.
2722 ;* TIMER1 - TIMER COUNTER CHANGED BY LTC INTERRUPT SERVICE RTN.
2723 ;* CLKHRZ - NUMBER OF LTC CLICKS PER SECOND (50 OR 60).
2724 ;*
2725 ;* OUTPUTS: CARRY - SET IF LTC IS AVAILABLE, AND NEW CALIBRATION PERFORMED.
2726 ;* MSLCNT - NEW 1 MS DELAY LOOP COUNT VALUE IF LTC AVAILABLE, OR
2727 ;* UNCHANGED IF NO LTC IS AVAILABLE.
2728 ;*
2729 ;* CALLING SEQUENCE: JSR PC,CALMSL
2730 ;*
2731 ;* COMMENTS:
2732 ;*
2733 ;* SUBORDINATE ROUTINES CALLED: UNSDIV,OOPS.
2734 ;*-- *****
2735
2736 016164 CALMSL:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
2737 016164 004567 167134 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
2738 016170 005067 000210 CLR 62$ ;CLEAR THE 2ND TIME FLAG.
2739 ;*
2740 ;* SYNCHRONIZE WITH THE LTC.
2741 016174 012705 000001 2$: MOV #1,R5 ;SET OUTER LOOP COUNTER TO 1 LOOP.
2742 ;* ;INCREASE THE VALUE LOADED INTO THIS COUNTER IF THE < **
2743 ;* ;FOLLOWING LOOP FAILS ON FUTURE, FASTER PROCESSORS. < **
2744 016200 005000 CLR R0 ;CLEAR THE WAIT FOR CLOCK INT COUNTER.
2745 016202 012767 000001 164070 MOV #1,TIMER1 ;SET UP COUNT OF 1 TO SYNCH WITH LTC.
2746 016210 005767 164064 4$: TST TIMER1 ;CHECK FOR COUNTER HAVING GONE TO ZERO.
2747 016214 001410 BEQ 6$ ;JUMP OUT OF LOOP IF LTC HAS INTERRUPTED.
2748 016216 005200 INC R0 ;COUNT THIS ITERATION OF THE INNER LOOP.
2749 016220 001373 BNE 4$ ;LOOP IF COUNTER HAS NOT TURNED OVER.
2750 016222 005305 DEC R5 ;DECREMENT THE INNER LOOP COUNTER.
2751 016224 003371 BGT 4$ ;LOOP IF OUTER LOOP COUNT NOT UP.
2752 ;*
2753 ;* IF WE GOT NO LTC INTERRUPT, INDICATE THAT THERE IS NO LTC AVAILABLE.
2754 ;* LTC MUST BE FLAKEY, OR NOT REALLY AN LTC AT ALL.
2755 ;*--
2756 016226 005067 164044 CLR CLKHRZ ;CLEAR LTC FREQUENCY WORD TO INDICATE NO LTC.
2757 016232 000241 CLC ;INDICATE FAILURE FOR RETURN.
2758 016234 000461 BR 60$ ;BYPASS THE FOLLOWING CALIBRATION PROCEDURES.
2759 ;*
2760 ;* WE ARE NOW SYNCHRONIZED WITH THE LTC.
2761 ;* SET UP FOR THE CALIBRATION LOOP.
2762 ;*--
2763 016236 012704 002300 6$: MOV #TIMER1,R4 ;WILL TEST TIMER1 IN THE LOOP BELOW.

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2764 016242 005001          CLR    R1          ;CLEAR THE OUTER LOOP COUNTER.
2765 016244 005002          CLR    R2          ;INDICATE TO CHECK ALL BITS OF TIMER1.
2766 016246 005003          CLR    R3          ;INDICATE TO CHECK FOR TIMER1 CLEAR.
2767 016250 012714 000001    MOV    #1,(R4)      ;LOAD TIMER1 WITH COUNT OF 1.
2768
2769 016254 016705 164032    8$:  MOV    MSLCNT,R5      ;LOAD MS LOOP COUNT.
2770 016260 011400          10$: MOV    (R4),R0      ;GET THE TIMER1 VALUE.
2771 016262 010067 000120    MOV    R0,64$      ;SAVE WORD (LIKE IN THE REAL LOOP).
2772 016266 040200          BIC    R2,R0          ;LEAVE ALL THE BITS.
2773 016270 020003          CMP    R0,R3          ;COMPARE AGAINST ZERO.
2774 016272 000261          SEC                    ;SET CARRY IN CASE OF SUCCESS.
2775 016274 001406          BEQ    12$            ;EXIT LOOP IF TIMER1 HAS CLEARED.
2776 016276 005305          DEC    R5            ;COUNT DOWN THE INSIDE MS LOOP COUNT.
2777 016300 001367          BNE    10$            ;LOOP IF MS NOT UP.
2778 016302 005301          DEC    R1            ;DECREMENT THE MS TIME COUNT.
2779 016304 001363          BNE    8$             ;KEEP LOOPING.
2780 016306 004767 003604    JSR    PC,OOPS        ;WE OVERFLOWED, SOMETHING IS WRONG, ABORT.
2781
2782          ;+
2783          ; WE HAVE NOW HAVE LOOP COUNT INFORMATION FOR ONE CLOCK TICK.
2784          ; WE HAVE NEGATIVE OF NUMBER OF OUTER LOOPS IN R1, EACH IS MSLCNT INNER LOOPS.
2785          ; WE HAVE THE PORTION OF THE LAST OUTER LOOP NOT EXECUTED, IN R5.
2786          ; NOW WE CALCULATE THE TOTAL NUMBER OF INNER LOOPS EXECUTED.
2787
2787 016312 005401          12$: NEG    P1            ;GET NUMBER OF OUTER LOOPS.
2788 016314 016702 163772    MOV    MSLCNT,R2      ;GET THE NUMBER OF INNER LOOPS PER OUTER LOOP.
2789 016320 010203          MOV    R2,R3          ;COPY NUMBER OF LOOPS FOR MULTIPLY.
2790 016322 160502          SUB    R5,R2          ;CALC # OF INNER LOOPS DONE IN LAST OUTER LOOP
2791 016324 010204          MOV    R2,R4          ; AND ADD TO ACCUMULATOR LSWORD.
2792 016326 005005          CLR    R5            ;CLEAR ACCUMULATOR MSWORD.
2793 016330 005301          14$: DEC    R1            ;CHECK R1 FOR 0 CONDITION
2794 016332 100403          BMI    16$            ; SKIP MULTIPLICATION IF ZERO
2795 016334 060304          ADD    R3,R4          ;MULTIPLY NUMBER OF INNER
2796 016336 005505          ADC    R5            ; LOOPS PER OUTER LOOP BY
2797 016340 000773          BR     14$            ;NUMBER OF OUTER LOOPS PERFORMED.
2798
2799          ;+
2800          ; DIVIDE THE TOTAL NUMBER OF INNER LOOPS BY THE NUMBER OF MS PER LTC TICK.
2801
2801 016342 016701 163742    16$: MOV    MSTICK,R1      ;# OF MS PER LTC TICK IS DIVISOR.
2802 016346 010403          MOV    R4,R3          ;LSWORD OF LOOP COUNT IS LSWORD OF DIVIDEND.
2803 016350 010502          MOV    R5,R2          ;MSWORD OF LOOP COUNT IS MSWORD OF DIVIDEND.
2804 016352 004767 010076    JSR    PC,UNSDIV      ;DIVIDE NUMBER OF LOOPS BY MS PER LTC TICK.
2805 016356 103402          BCS    18$            ;BYPASS OOPS IF WE'RE OK.
2806 016360 004767 003532    JSR    PC,OOPS        ;CLOCK ROUTINES ARE NOT LONG ENOUGH, OR BUG.
2807 016364 010167 163722    18$: MOV    R1,MSLCNT    ;SET NEW VALUE FOR MS LOOP COUNT.
2808 016370 005167 000010    COM     62$          ;SET THE 2ND ITERATION FLAGS IF 1ST ITERATION.
2809 016374 001277          BNE    2$             ;BRANCH IF ONLY ONE ITERATION DONE.
2810 016376 000261          SEC                    ;SET THE SUCCESS FLAG FOR EXIT.
2811
2812 016400          60$:  PASS                    ;RESTORE GPRS.
2813 016402 004736          RTS    PC              JSR    PC,(SP)+ ;RETURN TO PREG05 SUBRT.
2814          ; CARRY - SUCCESS FLAG. SET IF SUCCESS.
2815 016404 000000          62$: .WORD    0          ;2ND CALIBRATION ITERATION FLAGS.
2816 016406 000000          64$: .WORD    0          ;DUMMY WORD FOR STORAGE OF THE READ WORD.
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2818 .SBTTL GLOBAL SUBROUTINE - CHKEXT -
2819 ;* *****
2820 ;* - CHECK FOR EXTRA CHARACTER ROUTINE -
2821 ;* THIS SUBROUTINE CHECKS FOR THE CONDITION WHICH INDICATES THAT AN EXTRA
2822 ;* CHARACTER HAS BEEN RECEIVED DURING THE RECEPTION OF A DATA PATTERN.
2823 ;* IF THIS ROUTINE DETERMINES THAT IT IS LIKELY THAT AN EXTRA CHARACTER
2824 ;* HAS BEEN RECEIVED IT INDICATES THIS IN THE STATUS INFORMATION RETURNED
2825 ;* TO THE CALLING ROUTINE.
2826 ;*
2827 ;* INPUTS: R3 - RX LINE NUMBER MULTIPLIED BY 2 (OFFSET INTO WORD TABLES).
2828 ;* R4 - BASE ADDRESS OF RESYNC QUE CONTAINING RX CHARS.
2829 ;* R5 - MASK OF "INACTIVE" (NON-DATA) BITS OF RX AND TX CHARS.
2830 ;* CHCNTB - BASE OF NUMBER OF CHARS TO TX ON EACH LINE TABLE.
2831 ;* RXCNTB - BASE OF THE RX CHARACTER COUNTERS TABLE.
2832 ;* RXPTRB - BASE OF THE RX CHARACTER POINTERS TABLE.
2833 ;* TXRXLB - BASE OF TX/RX LINE NUMBER ASSOCIATION TABLE.
2834 ;*
2835 ;* OUTPUTS: CARRY - SET IF EXTRA CHARACTER CONDITION IS VERIFIED.
2836 ;*
2837 ;* CALLING SEQUENCE: JSR PC,CHKEXT
2838 ;*
2839 ;* COMMENTS: THE FOLLOWING SYMBOLS ARE USED IN LINE COMMENTS:
2840 ;* CHR0 - CHARACTER AT BOTTOM OF RESYNC QUE (FIRST RECEIVED).
2841 ;* CHR1, CHR2 - 2 CHARACTERS RECEIVED AFTER CHR0.
2842 ;* EXPO - CHARACTER EXPECTED TO BE RECEIVED NEXT.
2843 ;* EXP1, EXP2 - CHARACTER EXPECTED TO BE RECEIVED AFTER EXPO, ETC.
2844 ;*
2845 ;* SUBORDINATE ROUTINES CALLED: NONE.
2846 ;* *****
2847 016410 004567 166710 CHKEXT:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
016410 004567 166710 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
2848 016414 016302 003402 MOV RXPTRB(R3),R2 ;GET THE RX DATA POINTER.
2849 016420 005724 TST (R4)+ ;INCREMENT R4 BY 2 TO POINT TO CHR1.
2850 016422 012400 MOV (R4)+,R0 ;GET CHR1 FROM THE QUE, DATA.VALID INTO N FLAG.
2851 016424 100026 BPL 52$ ;EXIT WITH "FAILURE" IF CHR1 NOT VALID.
2852 016426 040500 BIC R5,R0 ;REMOVE INACTIVE BITS FROM CHR1 VALUE.
2853 016430 112201 MOVB (R2)+,R1 ;GET EXPO FROM THE DATA PATTERN.
2854 016432 040501 BIC R5,R1 ;REMOVE INACTIVE BITS FROM EXPO VALUE.
2855 016434 120100 CMPB R1,R0 ;COMPARE CHR1 AND EXPO.
2856 016436 001021 BNE 52$ ;EXIT WITH "FAILURE" IF CHR1 <> EXPO.
2857 016440 016300 003542 MOV RXCNTB(R3),R0 ;COMPARE THE PRESENT RX CHARACTER COUNT PLUS 1
2858 016444 005200 INC R0 ; WITH THE EXPECTED NUMBER OF CHARS TO RX ON
2859 016446 016301 005234 MOV TXRXLB(R3),R1 ; LINE (NUMBER TRANSMITTED AND LOOPED BACK) TO
2860 016452 020061 003442 CMP R0,CHCNTB(R1) ; DETERMINE IF CHR1 IS LAST EXPECTED CHAR.
2861 016456 001407 BEQ 50$ ;EXIT WITH "SUCCESS" IF CHR1 IS LAST CHAR.
2862 016460 011400 MOV (R4),R0 ;GET CHR2 FROM THE QUE, DATA.VALID INTO N FLAG.
2863 016462 100005 BPL 50$ ;EXIT WITH "SUCCESS" IF CHR1 WAS LAST IN QUE.
2864 016464 040500 BIC R5,R0 ;REMOVE INACTIVE BITS FROM CHR2 VALUE.
2865 016466 111201 MOVB (R2),R1 ;GET THE EXP1 VALUE.
2866 016470 040501 BIC R5,R1 ;REMOVE INACTIVE BITS FROM EXP1 VALUE.
2867 016472 020001 CMP R0,R1 ;COMPARE CHR2 AND EXP1.
2868 016474 001002 BNE 52$ ;EXIT WITH "FAILURE" IF CHR2 <> EXP1.
2869
2870 ;*
2871 ;* IT IS LIKELY THAT WE RECEIVED AN EXTRA CHARACTER WITHIN THE DATA PATTERN.
2872 ;* INDICATE "SUCCESS" AND EXIT.
2873 ;*

```



```

2874 016476 000261      50$:      SEC      ;SET THE SUCCESS FLAG.
2875 016500 000401      BR      60$      ;EXIT THE ROUTINE.
2876
2877      ;*
2878      ; WE DIDN'T RECEIVE A SINGLE EXTRA CHARACTER AT THIS POINT IN THE DATA PATTERN.
2879      ; INDICATE "FAILURE" AND EXIT.
2880      ;-
2881 016502 000241      52$:      CLC      ;CLEAR THE SUCCESS FLAG.
2882
2883 016504      60$:      PASS      ;RESTORE GPRS.
      016504 004736      JSR      PC,0(SP)+ ;RETURN TO PREG05 SUBRT.
2884 016506 000207      RTS      PC      ;CARRY - SET IF SUCCESS (EXTRA CHAR RXED).

```

M6

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2886 .SBTTL GLOBAL SUBROUTINE - CHKLOS -
2887 ;* *****
2888 ;* - CHECK FOR LOST CHARACTER ROUTINE -
2889 ;* THIS SUBROUTINE CHECKS FOR THE CONDITION WHICH INDICATES THAT A CHAR
2890 ;* HAS BEEN "LOST" FROM THE LOOPED BACK DATA PATTERN DURING A TRANSMISSION
2891 ;* AND RECEPTION TEST. IF THIS ROUTINE DETERMINES THAT IT IS LIKELY THAT
2892 ;* A CHARACTER HAS BEEN LOST, IT INDICATES THIS IN THE STATUS INFORMATION
2893 ;* RETURNED TO THE CALLING ROUTINE.
2894 ;*
2895 ;* INPUTS: R3 - RX LINE NUMBER MULTIPLIED BY 2 (OFFSET INTO WORD TABLES).
2896 ;* R4 - BASE ADDRESS OF RESYNC QUE CONTAINING RX CHARS.
2897 ;* R5 - MASK OF "INACTIVE" (NON-DATA) BITS OF RX AND TX CHARS WITH
2898 ;* ALL SET BITS IN A SINGLE, LEFT JUSTIFIED GROUP.
2899 ;* CHCNTB - BASE OF NUMBER OF CHARS TO TX ON EACH LINE TABLE.
2900 ;* RXCNTB - BASE OF THE RX CHARACTER COUNTERS TABLE.
2901 ;* RXPTRB - BASE OF THE RX CHARACTER POINTERS TABLE.
2902 ;* TXRXLB - BASE OF TX/RX LINE NUMBER ASSOCIATION TABLE.
2903 ;*
2904 ;* OUTPUTS: CARRY - SET IF LOST CHARACTER CONDITION IS VERIFIED.
2905 ;*
2906 ;* CALLING SEQUENCE: JSR PC,CHKLOS
2907 ;*
2908 ;* COMMENTS: THE FOLLOWING SYMBOLS ARE USED IN LINE COMMENTS:
2909 ;* CHR0 - CHARACTER AT BOTTOM OF RESYNC QUE (FIRST RECEIVED).
2910 ;* CHR1, CHR2 - 2 CHARACTERS RECEIVED AFTER CHR0.
2911 ;* EXPO - CHARACTER EXPECTED TO BE RECEIVED NEXT.
2912 ;* EXP1, EXP2 - CHARACTER EXPECTED TO BE RECEIVED AFTER EXPO, ETC.
2913 ;*
2914 ;* SUBORDINATE ROUTINES CALLED: NONE.
2915 ;* -- *****
2916 CHKLOS:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
2917 016510 004567 166610 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
2918 016514 016301 003542 MOV RXCNTB(R3),R1 ;COMPARE THE PRESENT RX CHARACTER COUNT PLUS 1
;R1 ;WITH THE EXPECTED NUMBER OF CHARS TO RX ON
2919 016520 005201 INC R1 ;LINE (NUMBER TXED AND LOOPED BACK) TO
2920 016526 016002 003442 MOV TXRXLB(R3),R0 ;DETERMINE IF THE POSSIBLE LOST CHAR
;R0 ;WOULD BE THE LAST EXPECTED RX CHAR.
2921 016532 020102 MOV CHCNTB(R0),R2 ;EXIT WITH "FAILURE" IF LOST CHR WOULD BE LAST.
2922 016534 001423 CMP R1,R2 ;DETERMINE (AS ABOVE) IF CHR0 WOULD BE THE LAST
2923 016536 005201 BEQ 524 ;RX CHAR AND SAVE RESULT FOR LATER.
2924 016540 160201 INC R1 ;GET THE RX DATA POINTER.
2925 016542 016302 0034 MOV RXPTRB(R3),R2 ;CALCULATE POINTER TO EXP1 LOCATION.
2926 016546 005202 INC R2 ;GET EXP1 VALUE FROM DATA PATTERN.
2927 016550 112200 MOVB (R2)+,R0 ;COMPARE CHR0 AND EXP1 VALUES.
2928 016552 162400 SUB (R4)+,R0 ;REMOVE INACTIVE BITS FROM RESULT. (NO ACTIVE
2929 016554 040500 BIC R5,R0 ;BITS ALLOWED TO LEFT OF ANY INACTIVE BITS.)
2930 ;EXIT WITH "FAILURE" IF CHR0 <> EXP1.
2931 016556 001012 BNE 524 ;CHECK CHR0 TEST RESULT SAVED ABOVE.
2932 016560 005701 TST R1 ;EXIT WITH "SUCCESS" IF CHR0 IS LAST CHAR.
2933 016562 001406 BEQ 504 ;GET CHR1 FROM THE QUE, DATA.VALID INTO N FLAG.
2934 016564 011401 MOV (R4),R1 ;EXIT WITH "SUCCESS" IF CHR0 WAS LAST QUE CHAR.
2935 016566 100004 BPL 504 ;GET THE EXP2 VALUE FROM THE DATA PATTERN.
2936 016570 111200 MOVB (R2),R0 ;COMPARE THE EXP2 AND THE CHR1 VALUES.
2937 016572 160001 SUB R0,R1 ;REMOVE INACTIVE BITS FROM RESULT OF COMPARE.
2938 016574 040501 BIC R5,R1 ;(NO ACTIVE BITS LEFT OF INACTIVE BITS.)
2939 ;EXIT WITH "FAILURE" IF CHR1 <> EXP2.
2940 016576 001002 BNE 524
2941

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2942      ;*
2943      ; IT IS LIKELY THAT WE LOST A CHARACTER FROM THE DATA PATTERN.
2944      ; INDICATE "SUCCESS" AND EXIT.
2945      ;-
2946 016600 000261      50$:      SEC          ;SET THE SUCCESS FLAG.
2947 016602 000401      BR          60$      ;EXIT THE ROUTINE.
2948
2949      ;*
2950      ; WE DIDN'T LOSE A SINGLE EXTRA CHARACTER AT THIS POINT IN THE DATA PATTERN.
2951      ; INDICATE "FAILURE" AND EXIT.
2952      ;-
2953 016604 000241      52$:      CLC          ;CLEAR THE SUCCESS FLAG.
2954
2955 016606          60$:      PASS          ;RESTORE GPRS.
2956 016610 000207      RTS      PC      JSR      PC,0(SP);RETURN TO PREG05 SUBRT.
          ;CARRY - SET IF SUCCESS (LOST CHAR LIKELY).

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2958 .SBTTL GLOBAL SUBROUTINE - CKCHR -
2959 ;* *****
2960 ;* - CHECK CHARACTER FOR ERRORS ROUTINE -
2961 ;* THIS SUBROUTINE CHECKS THE CHARACTER AT THE BOTTOM OF THE RESYNC QUEUE
2962 ;* TO DETERMINE IF IT IS CORRECT. POINTERS AND COUNTERS WHICH ARE RELATED
2963 ;* TO THE RECEPTION OF THE CHARACTER ARE UPDATED. IF THE CHARACTER IS
2964 ;* INCORRECT, AN ANALYSIS OF THE ERROR IS DONE AND PARAMETERS ARE SET UP
2965 ;* FOR THE REPORTING OF THE CORRECT ERROR.
2966 ;*
2967 ;* INPUTS: R3 - LINE OFFSET FOR ACCESS OF WORD TABLES OF LINE VARIABLES.
2968 ;* R4 - BASE ADDRESS OF THE RESYNC QUEUE FOR THIS LINE.
2969 ;* R5 - MASK OF THE INACTIVES BITS IN A TX OR RX CHAR BYTE.
2970 ;* BITTBL - TABLE OF WORDS WITH BITS SET FOR USE IN FORMING MAPS.
2971 ;* DPRSQ - DATA PATTERN RESYNC QUE WITH VALID CHAR AT BOTTOM.
2972 ;* EXCNTB - BASE OF THE EXTRA CHARACTER COUNTERS TABLE.
2973 ;* RXDNFB - RECEIVE DONE FLAGS.
2974 ;* RXPTRB - BASE OF THE RX CHARACTER POINTERS TABLE.
2975 ;* ERROR MESSAGE LABELS - EM9007,EM9008,EM9027,EM9028
2976 ;*
2977 ;* OUTPUTS: R1 - CONTAINS THE ADDRESS OF THE ERROR MESSAGE TO BE REPORTED.
2978 ;* R2 - CONTAINS THE ACTUAL RECEIVED DATA.
2979 ;* R4 - CONTAINS THE EXPECTED DATA.
2980 ;* CARRY - "SUCCESS" FLAG (SET IF NO ERROR IS FOUND).
2981 ;* FOLLOWING VARIABLES UPDATED FOR LINE ON WHICH CHAR WAS RECEIVED:
2982 ;* EXCNT - COUNT OF THE NUMBER OF EXTRA CHARS RECEIVED ON LINE.
2983 ;* RXCNT - COUNT OF THE NUMBER OF CHARACTERS RECEIVED ON LINE.
2984 ;* RXPTR - UPDATED TO POINT TO THE NEXT EXPECTED CHAR ON LINE.
2985 ;* ERRBLK - CONTENTS DESTROYED.
2986 ;*
2987 ;* CALLING SEQUENCE: JSR PC,CKCHR
2988 ;*
2989 ;* COMMENTS:
2990 ;*
2991 ;* SUBORDINATE ROUTINES CALLED: CHKEXT,CHKLOS,UPDCHR.
2992 ;* *****
2993 016612 004567 166506 CKCHR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
2994 ;* JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
2995 ;*
2996 ;* CHECK FOR THE RX OF A CHAR AFTER RX SHOULD BE COMPLETE ON THIS LINE.
2997 016616 036367 002364 163660 ;*
2998 016624 001407 ;* BIT BITTBL(R3),RXDNFB ;TEST THE RX DONE FLAG FOR THIS LINE.
2999 ;* BEQ 24 ;SKIP ERROR REPORT IF RX NOT COMPLETE ON LINE.
3000 ;*
3001 ;* WE HAVE RECEIVED AN EXTRA CHARACTER ON THIS LINE.
3002 ;* SET UP FOR ERROR REPORT AND EXIT TO REPORT THE ERROR.
3003 ;* COUNT THE EXTRA CHARACTER.
3004 ;* EXIT TO REPORT "UNEXPECTED CHAR RECEIVED AFTER RX COMPLETE ON LINE: NN"
3005 016626 012701 011351 ;*
3006 016632 011402 ;* MOV #EM9007,R1 ;SELECT "EXTRA CHAR ON LINE" ERROR MESSAGE.
3007 016634 040502 ;* MOV (R4),R2 ;GET THE ACTUAL DATA FOR ERROR REPORT.
3008 016636 052704 100000 ;* BIC R5,R2 ;REMOVE THE INACTIVE BITS.
3009 016642 000452 ;* BIS #BIT15,R4 ;INDICATE "NONE" EXPECTED DATA FOR ERROR RPT.
3010 ;* BR 124 ;GO COUNT EXTRA CHAR AND EXIT WITH "FAILURE".
3011 ;*
3012 ;* GET THE POINTER TO THE NEXT EXPECTED RECEIVE DATA CHARACTER.
3013 016644 016302 003402 ;*
3014 ;* MOV RXPTRB(R3),R2

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3014
3015
3016
3017 016650 011400
3018 016652 040500
3019 016654 111201
3020 016656 040501
3021 016660 120001
3022 016662 001003
3023 016664 004767 007720
3024 016670 000446
3025
3026
3027
3028
3029 016672 004767 177512
3030 015676 103010
3031
3032
3033
3034
3035
3036 016700 012701 012164
3037 016704 111200
3038 016706 040500
3039 016710 011402
3040 016712 040502
3041 016714 010004
3042 016716 000424
3043
3044
3045
3046
3047
3048 016720 004767 177564
3049 016724 103012
3050
3051
3052
3053
3054
3055
3056 016726 012701 012244
3057 016732 111200
3058 016734 040500
3059 016736 011402
3060 016740 040502
3061 016742 010004
3062 016744 004767 007640
3063 016750 000404
3064
3065
3066
3067
3068 016752 010002
3069 016754 010104
3070 016756 012701 011434

;
; COMPARE THE ACTUAL DATA WITH THE EXPECTED DATA.
;
MOV (R4),R0 ;GET THE ACTUAL DATA.
BIC R5,R0 ;REMOVE THE INACTIVE BITS.
MOVB (R2),R1 ;GET THE EXPECTED DATA.
BIC R5,R1 ;REMOVE THE INACTIVE BITS.
CMPB R0,R1 ;COMPARE ACTUAL AND EXPECTED.
BNE 48 ;CHECK FURTHER IF DATA MISCOMPARE.
JSR PC,UPDCHR ;UPDATE PTRS AND COUNTERS FOR THE CHAR.
BR 50 ;EXIT WITH "SUCCESS", NO ERROR FOUND.

;
; ACTUAL AND EXPECTED DATA MISCOMPARE.
; DETERMINE IF IT'S LIKELY WE RECEIVED AN EXTRA CHAR WITHIN THE DATA PATTERN.
;
48: JSR PC,CHKEXT ;CHECK FOR EXTRA CHAR RX'ED IN PATTERN.
BCC 64 ;GO CHECK FOR LOST CHAR IF NO EXTRA CHAR.

;
; IT IS LIKELY THAT WE RECEIVED AN EXTRA CHARACTER WITHIN THE DATA PATTERN.
; COUNT THE CHAR AS AN EXTRA CHAR, DON'T COUNT AS A STANDARD CHAR.
; REPORT "EXTRA CHAR RECEIVED WITHIN DATA PATTERN ON LINE NN"
;
MOV #EM9027,R1 ;SELECT "EXTRA CHAR ON LINE" ERROR MSG.
MOVB (R2),R0 ;GET THE EXPECTED RECEIVE DATA.
BIC R5,R0 ;REMOVE THE INACTIVE BITS FROM EXPECTED DATA.
MOV (R4),R2 ;GET THE ACTUAL RECEIVE DATA.
BIC R5,R2 ;REMOVE THE INACTIVE BITS FROM ACTUAL DATA.
MOV R0,R4 ;PASS EXPECTED DATA TO ERROR REPORT ROUTINE.
BR 126 ;GO COUNT EXTRA CHAR AND EXIT WITH "FAILURE".

;
; ACTUAL AND EXPECTED DATA MISCOMPARE.
; NOT LIKELY THAT WE RECEIVED AN EXTRA CHARACTER WITHIN THE DATA PATTERN.
; DETERMINE IF IT'S LIKELY WE LOST A CHARACTER FROM THE DATA PATTERN.
;
64: JSR PC,CHKLOS ;CHECK FOR A LOST CHAR CONDITION.
BCC 84 ;GO REPORT BAD RX DATA IF NOT LOST CHAR.

;
; IT IS LIKELY THAT WE LOST A CHARACTER FROM THE DATA PATTERN.
; COUNT THE CHAR IN THE RX CHAR COUNT AS IF IT HAD BEEN RECEIVED.
; ALSO, COUNT CHRO AS A VALID CHAR, BECAUSE WE HAVE VERIFIED IT ABOVE.
; REPORT "SINGLE CHAR MISSING FROM RECEIVED DATA ON LINE NN"
;
MOV #EM9028,R1 ;SELECT "LOST CHAR ON LINE" ERROR MSG.
MOVB (R2),R0 ;GET THE EXPECTED RECEIVE DATA.
BIC R5,R0 ;REMOVE THE INACTIVE BITS FROM EXPECTED DATA.
MOV (R4),R2 ;GET THE ACTUAL RECEIVE DATA.
BIC R5,R2 ;REMOVE THE INACTIVE BITS FROM ACTUAL DATA.
MOV R0,R4 ;PASS EXPECTED DATA TO ERROR REPORT ROUTINE.
JSR PC,UPDCHR ;UPDATE PTRS AND COUNTERS FOR THE CHAR.
BR 104 ;GO EXIT WITH "FAILURE".

;
; DID NOT LOSE OR GAIN A SINGLE CHARACTER FROM/TO THE DATA PATTERN.
; REPORT "RECEIVED CHAR MISCOMPARE AGAINST TX DATA ON LINE NN"
;
84: MOV R0,R2 ;PASS ACTUAL DATUM TO ERROR REPORT ROUTINE.
MOV R1,R4 ;PASS EXPECTED DATUM TO ERROR REPORT ROUTINE.
MOV #EM9008,R1 ;SELECT THE "DATA MISCOMPARE" MESSAGE.

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3071
3072
3073
3074 016762 004767 007622
3075 016766 000405
3076
3077
3078
3079 016770 005263 003242
3080 016774 001002
3081 016776 005363 003242
3082
3083
3084
3085 017002 000241
3086 017004 000401
3087
3088
3089
3090
3091
3092 017006 000261
3093
3094 017010
      017010 010166 000004
      017014 010266 000006
      017020 010466 000012
      017024 004736
3095
3096
3097
3098 017026 000207

      ;*
      ; UPDATE THE CHARACTER COUNTER AND RX DATA PATTERN POINTER FOR THIS LINE.
      ;*
      10$: JSR PC,UPDCHR ;UPDATE RX PTR AND COUNTER FOR THIS LINE.
           BR 14$ ;GO EXIT WITH "FAILURE".

      ;*
      ; COUNT THE CHARACTER AS AN EXTRA CHARACTER.
      ;*
      12$: INC EXCNTB(R3) ;INCREMENT THE EXTRA CHAR COUNT FOR THIS LINE.
           BNE 14$ ;EXIT WITH FAILURE IF NO OVERFLOW.
           DEC EXCNTB(R3) ;DECREMENT BACK TO -1 (MAX VALUE) IF OVERFLOW.

      ;*
      ; INDICATE "FAILURE" AND EXIT.
      ;*
      14$: CLC ;CLEAR THE "SUCCESS" FLAG.
           BR 60$ ;EXIT THE ROUTINE.

      ;*
      ; NO ERROR WAS FOUND.
      ; SET "SUCCESS" FLAG AND EXIT.
      ;*
      50$: SEC ;SET THE "SUCCESS" FLAG.

      60$: PASS R1,R2,R4 ;RESTORE GPRS, EXCEPT
                        MOV R1,R1SLOT(SP) ;PUT R1 IN STACK SLOT.
                        MOV R2,R2SLOT(SP) ;PUT R2 IN STACK SLOT.
                        MOV R4,R4SLOT(SP) ;PUT R4 IN STACK SLOT.
                        JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
                        ;R1 - CONTAINS THE ADDRESS OF THE ERROR REPORT.
                        ;R2 - CONTAINS THE ACTUAL DATA RECEIVED.
                        ;R4 - CONTAINS THE EXPECTED DATA.

      RTS PC

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E7

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3100 .SBTTL GLOBAL SUBROUTINE - CKFRPR -
3101 ;* *****
3102 ;* - CHECK FRAMING AND PARITY ERROR REPORTING -
3103 ;* THIS SUBROUTINE IS USED IN THE FRAMING ERROR AND PARITY ERROR TESTS.
3104 ;* IT READS THE CHARACTERS FROM THE DUT RECEIVER CHARACTER FIFO,
3105 ;* AND CHECKS FOR THE CORRECT COMBINATION OF PARITY AND FRAMING
3106 ;* ERROR BITS IN THE MSB. IF CHARACTERS STOP APPEARING IN THE FIFO WITH
3107 ;* DATA.VALID SET OR IF MORE THAN THE ALLOWABLE NUMBER OF CHAR/CTERS
3108 ;* HAS BEEN READ FROM THE DUT THIS ROUTINE EXITS WITH AN RX COMPLETE
3109 ;* INDICATION. EACH READ CHAR IS ANALYSED AND ANY NECESSARY ERRORS ARE
3110 ;* REPORTED.
3111 ;*
3112 ;* INPUTS: R5 - TEST FLAG, BIT15 SET = FRAMING ERR, CLEAR = PARITY ERR.
3113 ;* ERRNBR - SET TO ERROR NUMBER OF FIRST ERROR IN THIS ROUTINE.
3114 ;* OSTEND - ADDRESS OF THE END OF THE OUTPUT STORAGE FIFO BUFFER.
3115 ;* OSTPTR - POINTER TO THE NEXT BYTE TO READ FROM OSTORE.
3116 ;*
3117 ;* OUTPUTS: RXCNTB - RECEIVE CHARACTER COUNT UPDATED FOR EACH LINE.
3118 ;* RXPNTB - RECEIVE CHARACTER PIONTER IS UPDATED FOR EACH LINE.
3119 ;*
31120 ;* CALLING SEQUENCE: JSR PC,CKFRPR
3121 ;*
3122 ;* COMMENTS: THIS ROUTINE REPORTS ERRORS WITH NUMBERS INITIAL ERRNBR
3123 ;* THRU INITIAL ERRNBR + 4.
3124 ;* ERRNBR IS RESTORED BEFORE THIS ROUTINE RETURNS.
3125 ;*
3126 ;* SUBORDINATE ROUTINES CALLED: PRFRME,PRPARE,WAIBIS.
3127 ;*
3128 ;* -- *****
3129 017030 CKFRPR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
3130 017030 004567 166270 ;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3131 017034 016704 166256 ;PRESERVE THE INITIAL ERROR NUMBER.
3132 017040 004767 006706 ;ENABLE TX INTERRUPTS.
3133 ;*
3134 ;* WAIT FOR A CHARACTER TO APPEAR IN THE FIFO.
3135 ;* IF NO CHARACTER APPEARS WITHIN TIME-OUT PERIOD: EXIT ROUTINE, WE'RE DONE.
3136 017044 016701 163176 ;GET MINIMUM TIME OUT VALUE.
3137 017050 026767 163426 163114 20: CMP TXDNF,ACTLNS ;CHECK FOR TRANSMISSION DONE ON ACTIVE LINES.
3138 017056 001402 ;SKIP ADDING 50 MS DELAY IF TX DONE ALL LINES.
3139 017060 062701 000062 ;ADD 50 MILLI SEC TO DELAY IF NOT LAST CHAR.
3140 017064 052701 170000 40: BIS #170000,R1 ;INDICATE TO TEST DATA.VALID BIT.
3141 017070 016702 163106 ;INDICATE TO CHECK DUT RECEIVE BUFFER (FIFO).
3142 017074 004767 010004 ;WAIT FOR RECEIVED CHAR OR TIME-OUT.
3143 017100 103054 ;EXIT ROUTINE IF TIME-OUT, WE'RE DONE.
3144 ;*
3145 017102 005367 163370 ;DECREMENT THE TOTAL CHAR COUNTER.
3146 017106 001014 ;SKIP ERROR IF NOT TOO MANY CHARS RECEIVED.
3147 017110 010467 166202 ;SET ERROR NUMBER TO INITIAL ERRNBR.
3148 017114 012701 012044 ;SELECT THE ERROR MESSAGE TO BE REPORTED.
3149 017120 012767 01124 166174 ;SELECT THE ERROR REPORT ROUTINE.
3150 ;*
3151 ;* REPORT ERROR AT INITIAL ERRNBR.
3152 ;* "MORE THAN TWICE THE EXPECTED NUMBER OF CHARACTERS RECEIVED"
3153 ;*
3154 017126 ERROR ; >>>>> ERROR <<<<<.
017126 104460 TRAP C$ERROR

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3155 017130 012767 000001 163066      MOV    #1,FERROR      ;INDICATE THAT AN ERROR HAS BEEN FOUND.
3156
3157 017136 000435                      BR      60$          ;EXIT THIS ROUTINE WE HAVE GIVEN UP.
3158
3159
3160      ;*
3161      ; EXTRACT THE LINE NUMBER OF THE NEW CHARACTER.
3162      ; CALCULATE OFFSET FOR ACCESSING TABLES OF LINE VARIABLES.
3163
3163 017140 010203      6$:      MOV    R2,R3          ;COPY THE READ CHARACTER.
3164 017142 000303      SWAB   R3              ;GET THE LINE NUMBER IN THE LSB.
3165 017144 042703      BIC    #177760,R3        ;CLEAR THE UNWANTED BITS.
3166 017150 006303      ASL    R3              ;SHIFT LEFT TO FORM OFFSET INTO TABLES.
3167
3168      ;*
3169      ; PROCESS THE READ CHARACTERS AS DICTATED BY THE TEST FLAG.
3170
3170 017152 010505      ;*
3171 017154 100012      MOV    R5,R5          ;DETERMINE WHICH TEST CALLED THIS ROUTINE.
3172
3172 017156 004767 003162      BPL    8$          ;BRANCH TO PROCESS CHARACTER IN PARITY TEST.
3173 017162 005767 163036      JSR    PC,PRFRME      ;PROCESS FRAMING ERRORS RECEIVED.
3174 017166 001416      TST    FERROR          ;HAS AN ERROR BEEN DETECTED ?
3175
3175 017170 032767 000100 162764      BEQ    10$        ;NO, THEN SKIP PROCESSING CHARACTERS FOR PARITY
3176
3176 017176 001012      BIT     #BIT06,OPTION      ;TEST.
3177 017200 000414      BNE    10$        ;HAS EXTENDED ERROR REPORTING BEEN ENABLED ?
3178
3178 017202 004767 003242      BR      60$        ;BRANCH IF IT HAS,
3179 017206 001404      BR      60$        ;OTHERWISE EXIT.
3180
3180 017212 005767 163012      8$:      JSR    PC,PRPARE      ;PROCESS PARITY ERRORS RECEIVED.
3181 017216 001404      TST    FERROR          ;HAS AN ERROR BEEN DETECTED ?
3182 017220 001404      BEQ    10$        ;NO, THEN BRANCH TO UPDATE POINTERS.
3183 017224 032767 000100 162740      BIT     #BIT06,OPTION      ;HAS EXTENDED ERROR REPORTING BEEN ENABLED ?
3184 017228 001403      BEQ    60$        ;EXIT IF IT HASN'T.
3185
3185 017232 004767 007360      10$:      JSR    PC,UPDCHR      ;UPDATE POINTERS AND COUNTERS FOR THIS LINE.
3186 017236 000707      BR      2$          ;LOOP TO READ NEXT CHAR FROM FIFO.
3187
3187 017240 010467 166060      60$:      MOV    R4,ERRNBR      ;RESTORE THE ERROR NUMBER TO ITS INITIAL VALUE.
3188 017244 004736      PASS                     ;RESTORE GPRS.
3189 017248 000207      RTS     PC              ;RETURN TO PREG05 SUBRT.
3190
3190 017252 004736      JSR    PC,8(SP)
3191 017256 000207
3192 017260 000207

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3194      SBTTL GLOBAL SUBROUTINE - CKINAC -
3195      ;* *****
3196      ;* - CHECK FOR NEW CHARACTER ON INACTIVE LINE ROUTINE -
3197      ;* THIS SUBROUTINE CHECKS A CHARACTER TO DETERMINE IF THE CHARACTER
3198      ;* WAS RECEIVED ON AN ACTIVE LINE. IF THE CHARACTER WAS RECEIVED ON
3199      ;* AN INACTIVE LINE THIS ROUTINE RECORDS THE FACT THAT THE CHARACTER
3200      ;* WAS RECEIVED ON AN INACTIVE LINE, PREPARES AN ERROR MESSAGE FOR
3201      ;* THE CALLING ROUTINE, AND RETURNS A "FAILURE" STATUS.
3202      ;*
3203      ;* INPUTS:      R2 - THE RX CHARACTER INCLUDING ERROR FLAGS AND LINE NUMBER.
3204      ;*              ACTLNS - BIT MAP OF ACTIVE OUT LINES.
3205      ;*              BITTBL - TABLE OF WORDS WITH BITS SET FOR FORMING BIT MAPS.
3206      ;*              EM9006 - LABEL AT "RX ON INACTIVE LINE" ERROR MESSAGE.
3207      ;*              EXCNTB - BASE OF THE EXTRA CHARACTER COUNTERS TABLE.
3208      ;*              TXRXLB - BASE OF TX/RX LINE NUMBER ASSOCIATION TABLE.
3209      ;*
3210      ;* OUTPUTS:     CARRY - "SUCCESS" FLAG (SET IF NO ERROR FOUND).
3211      ;*              R1 - IF ERROR FOUND, ADDRESS OF ERROR MESSAGE.
3212      ;*              R3 - LINE NUMBER OFFSET OF PASSED IN CHARACTER.
3213      ;*              R4 - IF ERROR FOUND, EXPECTED DATA INDICATION FOR ERROR RPT.
3214      ;*              EXCNT - EXTRA CHARACTER COUNT FOR LINE (UPDATED IF ERROR).
3215      ;*
3216      ;* CALLING SEQUENCE: JSR PC,CKINAC
3217      ;*
3218      ;* COMMENTS:
3219      ;*
3220      ;* SUBORDINATE ROUTINES CALLED: NONE.
3221      ;* *****
3222
3223      CKINAC:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
3224      017242 004567 166056 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3225
3226      ;* EXTRACT THE LINE NUMBER FROM THE PASSED IN CHARACTER AND USE THE LINE
3227      ;* NUMBER TO FORM AN OFFSET FOR ACCESSING TABLES OF LINE VARIABLES.
3228      ;*
3229      ;* MOV R2,R3 ;EXTRACT THE LINE NUMBER
3230      ;* SWAB R3 ; FROM THE CHARACTER WE
3231      ;* BIC #177760,R3 ; ARE COMPARING.
3232      ;* ASL R3 ;FORM OFFSET INTO WORD TABLE FROM LINE NUMBER.
3233
3234      ;* IF THE CHARACTER IN QUESTION IS NOT A VALID CHARACTER, EXIT WITH "SUCCESS".
3235      ;*
3236      ;* TST R2 ;CHECK DATA.VALID BIT.
3237      ;* BPL 50# ;EXIT WITH SUCCESS IF CHAR IS NOT VALID.
3238
3239      ;* IF THE TX LINE WHICH IS ASSOCIATED WITH THIS RX LINE IS AN ACTIVE LINE,
3240      ;* EXIT THE ROUTINE WITH "SUCCESS".
3241      ;*
3242      ;* MOV TXRXLB(R3),R1 ;GET THE TX LINE # OFFSET FOR THIS RX LINE.
3243      ;* BIT BITTBL(R1),ACTLNS ;DETERMINE IF TX LINE IS AN ACTIVE LINE.
3244      ;* BNE 50# ;EXIT ROUTINE WITH SUCCESS IF LINE IS ACTIVE.
3245
3246      ;* THE CHARACTER IN QUESTION WAS RECEIVED ON AN INACTIVE LINE.
3247      ;* COUNT THIS CHARACTER AS AN EXTRA CHAR.
3248      ;* SET UP ERROR INFORMATION.
3249      ;* EXIT ROUTINE WITH "FAILURE" INDICATION.

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H-7

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3250 017300 005263 003242          INC    EXCNTB(R3)      ;INCREMENT THE EXTRA CHAR COUNT FOR THIS LINE.
3251 017304 001002                  BNE     2$           ;SKIP SETTING TO MAX VALUE IF NO OVERFLOW.
3252 017306 005363 003242          DEC    EXCNTB(R3)      ;DECREMENT BACK TO -1 (MAX VALUE) IF OVERFLOW.
3253 017312 012701 011276          2$:  MOV    #EM9006,R1    ;SET UP RX ON INACTIVE LINE MESSAGE.
3254 017316 012704 100000          MOV    #BIT15,R4        ;SET UP "NONE" EXPECTED DATA INDICATION.
3255 017322 000241                  CLC                     ;CLEAR THE "SUCCESS" FLAG.
3256 017324 000401                  BR     60$           ;GO REPORT RX CHAR ON INACTIVE LINE.
3257
3258
3259
3260          ;*
          ; WE HAVE NOT FOUND A "CHAR ON INACTIVE LINE" ERROR SITUATION.
3261          ; SET THE "SUCCESS" FLAG AND EXIT THE ROUTINE.
3262 017326 000261          50$:  SEC                     ;SET THE "SUCCESS" FLAG.
3263
3264 017330          60$:  PASS    R1,R3,R4                ;RESTORE GPRS, EXCEPT OUTPUT GPRS.
          MOV    R1,R1SLOT(SP)      ;PUT R1 IN STACK SLOT.
          MOV    R3,R3SLOT(SP)      ;PUT R3 IN STACK SLOT.
          MOV    R4,R4SLOT(SP)      ;PUT R4 IN STACK SLOT.
          JSR    PC,@(SP)+           ;RETURN TO PREG05 SUBRT.
3265 017346 000207          RTS     PC                    ;CARRY - SUCCESS FLAG (SET IF NO ERROR).

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3267 .SBTTL GLOBAL SUBROUTINE - CKTRAP -
3268 ;*****
3269 ;* CHECK TRAP ROUTINE -
3270 ;* THIS SUBROUTINE IS USED TO CHECK FOR A BUS TIME-OUT TRAP (004 TRAP)
3271 ;* WHICH IS CAUSED BY AN ACCESS TO A NON-EXISTENT MEMORY OR I/O LOCATION.
3272 ;* IF THE TRAP DOES NOT OCCUR, THIS ROUTINE RETURNS A SUCCESS INDICATION.
3273 ;*
3274 ;* INPUTS: R0 - SOURCE ADDRESS FOR MOVE.
3275 ;* R1 - DESTINATION ADDRESS FOR MOVE.
3276 ;* (R0) - SOURCE FOR THE MOVE.
3277 ;*
3278 ;* OUTPUTS: (R1) - WRITTEN TO THE CONTENTS OF (R0).
3279 ;* CARRY FLAG - SET ON RETURN IF NO 004 TRAP DETECTED.
3280 ;* TP4FLG - NONZERO IF TRAP OCCURRED, CLEARED OTHERWISE.
3281 ;*
3282 ;* CALLING SEQUENCE: JSR PC,CKTRAP
3283 ;*
3284 ;* COMMENTS: IF THIS SUBROUTINE CAUSES A TRAP, EITHER THE ADDRESS WHICH
3285 ;* IS LABELED ADRPTR WILL BE THE TRAP PC ADDRESS ON THE STACK.
3286 ;*
3287 ;* SUBORDINATE ROUTINES CALLED: NONE.
3288 ;*****
3289
3290 017350 CKTRAP:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
3291 017350 004567 165750 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3292 017354 005067 162674 CLR TP4FLG ;CLEAR THE 004 TRAP FLAGS.
3293 017360 011011 MOV (R0),(R1) ;PERFORM THE MOVE IN QUESTION.
3294 017362 005767 162666 ADRPTR:: TST TP4FLG ;CHECK FOR OCCURENCE OF TRAP.
3295 017366 000261 SEC ;INDICATE SUCCESS.
3296 017370 001401 BEQ 60$ ;EXIT WITH SUCCESS IF TRAP DID NOT OCCUR.
3297 017372 000241 CLC ;INDICATE FAILURE.
3298 017374 004736 60$: PASS ;RESTORE GPRS.
3299 017376 000207 RTS PC JSR PC,8(SP) ;RETURN TO PREG05 SUBRT.

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3300 .SBTTL GLOBAL SUBROUTINE - CKTRPB -
3301 ;*****
3302 ;* - CHECK FOR TRAP -
3303 ;* THIS SUBROUTINE IS USED TO CHECK FOR A BUS TIME-OUT TRAP (004 TRAP)
3304 ;* WHICH IS CAUSED BY AN ACCESS TO A NON-EXISTENT MEMORY OR I/O LOCATION
3305 ;* IF A TRAP DOES NOT OCCUR, THIS ROUTINE RETURNS A SUCCESS INDICATION.
3306 ;*
3307 ;* INPUTS: R0 - SOURCE ADDRESS FOR MOVE
3308 ;* R1 - DESTINATION ADDRESS FOR MOVE
3309 ;* (R0) - SOURCE FOR THE MOVE
3310 ;*
3311 ;* OUTPUTS: (R1) - WRITTEN TO THE CONTENTS OF (R0)
3312 ;* CARRY FLAG - SET ON RETURN IF NO 004 TRAP DETECTED
3313 ;* TP4FLG - NONZERO IF TRAP OCCURED, CLEARED OTHERWISE.
3314 ;*
3315 ;* CALLING SEQUENCE: JSR PC,CKTRPB
3316 ;*
3317 ;* COMMENTS: IF THIS SUBROUTINE CAUSES A TRAP, EITHER THE ADDRESS
3318 ;* WHICH IS LABELED TRPAD2 WILL BE THE TRAP PC ADDRESS ON
3319 ;* THE STACK OR SOME OTHER ADDRESS WHICH WAS PLACED ON
3320 ;* THE STACK BY AN UNEXPECTED TRAP.
3321 ;* THIS ROUTINE PERFORMS A BYTE MOV .
3322 ;*
3323 ;* SUBORDINATE ROUTINES CALLED: NONE.
3324 ;*****
3325
3326 017400 CKTRPB:: SAVE JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3327 017400 004567 165720
3328 017404 005067 162644 CLR TP4FLG ;CLEAR THE 004 TRAP FLAGS
3329 017410 111011 MOV (R0),(R1) ;PERFORM THE BYTE MOVE
3330 017412 005767 162636 TRPAD2:: TST TP4FLG ;CHECK FOR OCCURENCE OF TRAP
3331 017416 000261 SEC ;INDICATE SUCCESS
3332 017420 001401 BEQ 604 ;EXIT WITH SUCCESS IF TRAP DID NOT OCCUR
3333 017422 000241 CLC ;INDICATE FAILURE
3334 017424 604: PASS
3335 017424 004736 JSR PC,B(SP)+ ;RETURN TO PREG05 SUBRT.
3335 017426 000207 RTS PC ;RETURN

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3337 .SBTTL GLOBAL SUBROUTINE - CLNRST -
3338 ;*****
3339 ;* - CLEAN RESET OF THE DEVICE UNDER TEST -
3340 ;* THIS SUBROUTINE IS USED TO RESET THE DUT TO A KNOWN STATE.
3341 ;* THE DUT'S SELF-TEST IS SKIPPED, AND THE FIFO IS PURGED OF ANY ERROR
3342 ;* CODES, ETC.
3343 ;* IF THE RESET DOES NOT SUCCESSFULLY COMPLETE, THEN THE CARRY BIT IS
3344 ;* PASSED BACK TO THE CALLING ROUTINE (CLEAR).
3345 ;*
3346 ;* INPUTS: CSRA - CONTAINS THE ADDRESS OF THE CSR
3347 ;* TXBFCA - CONTAINS ADDRESS OF DUT DMA BUFFER COUNT REGISTER.
3348 ;* ERRNBR - ERROR NUMBER FOR POSSIBLE ERROR REPORT.
3349 ;* ERRCTL - ERRTP,ERNBR,AND ERRMSG SET UP CORRECTLY.
3350 ;*
3351 ;* OUTPUTS: THE DUT PERFORMS ITS RESET FUNCTION INTO A KNOWN STATE.
3352 ;* CARRY - CLEAR INDICATES THE TEST IS TO BE ABORTED.
3353 ;* ERRLK - VALUE MAY BE DESTROYED.
3354 ;* IESTAT - TX AND RX INTERRUPT FLAGS ARE CLEARED.
3355 ;* TX AND RX INTERRUPT ENABLE BITS IN THE DUT'S CSR ARE CLEARED.
3356 ;*
3357 ;* CALLING SEQUENCE: JSR PC,CLNRST
3358 ;*
3359 ;* COMMENTS: THIS SUBROUTINE CAN REPORT ERRORS WITH NUMBERS ERRNBR.
3360 ;* THIS ROUTINE DOES NOT DESTROY THE VALUE OF ERRNBR.
3361 ;*
3362 ;* SUBORDINATE ROUTINES CALLED: DELAY,MSLGET,PUFIFO,RESETT.
3363 ;*****
3364
3365 017430 CLNRST:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
017430 004567 165670 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3366
3367 ;*
3368 ;* RESET THE DUT.
3369 ;* THIS ROUTINE REPORTS ERRORS WITH NUMBERS FROM ERRNBR THRU ERRNBR+2.
3370 017434 004767 004510 ;- JSR PC,RESETT ;RESET THE DUT TO A KNOWN STATE.
3371 017440 103002 BCC 60$ ;EXIT ROUTINE WITH ABORT TEST INDICATOR.
3372
3373 ;*
3374 ;* PURGE THE FIFO OF ERROR CODES, SAVE ANY BMP CODES FOUND.
3375 017442 004767 003274 ;- JSR PC,PUFIFO ;PURGE THE FIFO.
3376
3377 017446 60$: ;EXIT THE TEST USING RESETT OR PUFIFO STATUS.
3378 017446 PASS ;RESTORE GPRS, PASS THE FOLLOWING INTACT:
017446 004736 JSR PC,0(SP), ;RETURN TO PREG05 SUBRT.
3379 ;CARRY BIT:IF CLEAR, THEN ABORT THE TEST.
3380 017450 000207 RTS PC

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3382 .SBTTL GLOBAL SUBROUTINE - CLR16W -
3383 ;* *****
3384 ;* - CLEAR SIXTEEN WORDS ROUTINE -
3385 ;* THIS SUBROUTINE CLEARS 16 WORDS STARTING WITH THE SPECIFIED WORD.
3386 ;*
3387 ;* INPUTS: R0 - ADDRESS OF THE FIRST WORD TO CLEAR.
3388 ;*
3389 ;* OUTPUTS: (R0) TO (R0+15) - 16 WORDS OF MEMORY ARE CLEARED TO 0.
3390 ;*
3391 ;* CALLING SEQUENCE: JSR PC,CLR16W
3392 ;*
3393 ;* COMMENTS:
3394 ;*
3395 ;* SUBORDINATE ROUTINES CALLED: NONE.
3396 ;* - *****
3397
3398 017452 CLR16W:: SAVE
3399 017452 004567 165646 JSR R5,PREG05 ;SAVE CONTENTS OF GPRS R0 THRU R5.
3400 017456 012701 000020 ;CALL REGISTER SAVE SUBRT.
3401 017462 005020 ;SET THE LOOP COUNTER TO 16.
3402 017464 005301 ;CLEAR A WORD OF MEMORY.
3403 017470 001375 ;COUNT THIS LOOP.
3404 017472 004736 ;LOOP IF NOT 16 WORD CLEARED.
3405 017474 000207 ;RESTORE GPRS.
3406 017476 000000 JSR PC,0(SP)+ ;RETURN TO PREG05 SUBRT.
3407 017478 000000 RTS PC
  
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3406 .SBTTL GLOBAL SUBROUTINE - CONMAP
3407 ;** *****
3408 ;* - CONVERT LINE BIT MAP.
3409 ;* THIS SUBROUTINE IS USED TO CONVERT A BIT MAP PASSED TO IT , INTO
3410 ;* ANOTHER LINE BIT MAP THAT IS BASED UPON THE ASSOCIATED TX/RX LINE
3411 ;* NUMBER/OFFSET TABLE.
3412 ;*
3413 ;* INPUTS: R5 - CONTAINS THE LINE BIT MAP TO BE TRANSFORMED.
3414 ;* TXRXLB - BASE ADDRESS OF ASSOCIATED TX/RX LINE NUMBER TABLE.
3415 ;*
3416 ;* OUTPUTS: R5 - CONTAINS AN ASSOCIATED LINE BIT MAP.
3417 ;*
3418 ;* CALLING SEQUENCE: JSR PC,CONMAP
3419 ;*
3420 ;* COMMENTS: THE TX/RX ASSOCIATION TABLE MUST BE INITIALISED BEFORE THIS
3421 ;* ROUTINE IS CALLED.
3422 ;*
3423 ;* SUBORDINATE ROUTINES CALLED: NONE.
3424 ;-- *****
3425
3426 017474 004567 165624 CONMAP::SAVE
3427 017474 004567 005234 JSR ;SAVE CONTENTS OF GPRS R0 THRU R5.
3428 017504 010503 MOV #TXRXLB,R2 R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3429 017506 012704 000020 MOV R5,R3 ;GET THE BASE ADDRESS OF THE LINE ASSOC TABLE.
3430 017512 005005 MOV #NUMLNS,R4 ;COPY THE BIT MAP TO BE TRANSFORMED.
3431 017514 006203 CLR R5 ;SET MAX LINE COUNTER.
3432 017516 103005 2$: ASR R3 ;CLEAR ASSOCIATED LINE BIT MAP.
3433 017520 011201 BCC 4$ ;SHIFT ACTLNS BIT MAP INT BOOLEAN REGISTER.
3434 017522 006201 MOV (R2),R1 ;SKIP SETTING ASSOCIATED LINE NUMBER BIT MAP.
3435 017524 004767 001250 ASR R1 ;GET ASSOCIATED LINE NUMBER OFFSET FROM TABLE.
3436 017530 050005 JSR PC,LINBIT ;SHIFT RIGHT TO GET LINE NUMB FROM OFFSET.
3437 017532 005722 4$: BIS R0,R5 ;GENERATE AN SINGLE BIT MAP FOR THIS LINE.
3438 017534 005304 DEC R4 ;SET BIT FOR THIS LINE IN ASSOCIATED BIT MAP.
3439 017536 001366 BNE 2$ ;INCREMENT ADDRESS FOR THE NEXT LINE NUMBER.
3440 017540 010566 000014 60$: PASS R5 ;DECREMENT LINE COUNT.
3441 017544 004736 MOV R5,R5SLOT(SP) ;LOOP IF NOT DONE.
3442 017546 000207 RTS PC ;RESTORE GPRS, EXCEPT
;PUT R5 IN STACK SLOT.
;RETURN TO PREG05 SUBRT.
;R5 - CONTAINS THE ASSOCIATED LINE BIT MAP.

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3444 .SBTTL GLOBAL SUBROUTINE - DELAY
3445 ;*****
3446 ;* - DELAY SUBROUTINE -
3447 ;* THIS SUBROUTINE IS USED TO DELAY A VARIABLE NUMBER OF MILLI-SECONDS.
3448 ;*
3449 ;* INPUTS: R4 - CONTAINS THE NUMBER OF MS TO DELAY.
3450 ;* MSLCNT.
3451 ;*
3452 ;* OUTPUTS: NONE.
3453 ;*
3454 ;* CALLING SEQUENCE: JSR PC,DELAY
3455 ;*
3456 ;* COMMENTS: IF NO HARDWARE CLOCK INTERRUPTS ARE OCCURRING, CONTROL-CS WILL
3457 ;* NOT BE HONORED FOR THE DURATION OF THE DELAY.
3458 ;*
3459 ;* SUBORDINATE ROUTINES CALLED: NONE.
3460 ;*****
3461
3462 017550 DELAY:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
017550 004567 165550 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3463 017554 010401 MOV R4,R1 ;PASS NUMBER OF MS DELAY AS TIME-OUT VALUE.
3464 017556 012702 177777 MOV #-1,R2 ;TELL MSLOOP ROUTINE TO CHECK ALL BITS.
3465 017562 005003 CLR R3 ;TELL MSLOOP RTN TO CHECK FOR ALL BITS CLEAR.
3466 017564 012704 017606 MOV #62,R4 ;TELL MSLOOP TO CHECK DUMMY NON-ZERO WORD.
3467 017570 004767 001506 JSR PC,MSLOOP ;DELAY THE REQUESTED # OF MS.
3468 017574 103002 BCC 60$ ;EXIT ROUTINE IF WE TIMED-OUT.]
3469 017576 004767 002314 JSR PC,00PS ;IF NO TIME-OUT, BAD PROGRAM OR HOST MACHINE.
3470 017602 017602 004736 60$: PASS ;RESTORE GPRS.
017602 000207 RTS PC JSR PC,0(SP)+ ;RETURN TO PREG05 SUBRT.
3471 017604 000207
3472
3473 017606 177777 62$: .WORD -1 ;DUMMY, NON-ZERO WORD.

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3475 .SBTTL GLOBAL SUBROUTINE - DM168
3476 ;* *****
3477 ;* CONVERT TO A 16-BIT PHYSICAL ADDRESS -
3478 ;* THIS ROUTINE CONVERTS FROM PAR FORM TO A 16-BIT PHYSICAL ADDRESS,
3479 ;* OF ALTERNATE 1'S AND 0'S.
3480 ;*
3481 ;* INPUTS: DMTSTA: - CONTAINS THE ADDRESS IN PAR FORM
3482 ;*
3483 ;* OUTPUTS: R0 - CONTAINS THE 16 BIT PHYSICAL ADDRESS
3484 ;*
3485 ;* CALLING SEQUENCE: JSR PC,DM168
3486 ;*
3487 ;* COMMENTS: USED IN THE DMA ADDRESS TEST
3488 ;*
3489 ;* SUBROUTINES CALLED: NONE.
3490 ;* *****
3491
3492 017610 DM168:: SAVE
3493 017610 004567 165510 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3494 017614 016700 162402 ;SHIFT THE DMA TEST ADDRESS
3495 017620 012702 000006 MOV DMTSTA,R0 ;SIX PLACES LEFT , TO
3496 017624 006300 26: ASL R0 ;CONVERT IT INTO A
3497 017626 005302 DEC R2 ;16-BIT PHYSICAL ADDRESS
3498 017630 001375 BNE 26 ;
3499 017632 012701 000052 MOV #52,R1 ;SET UP THE 6 LSB'S
3500 017636 032700 000100 BIT #100,R0 ;IF BIT #6 OF THE PHYSICAL
3501 017642 001402 BEQ 46 ;ADDRESS IS CLEAR THEN BRANCH
3502 017644 012701 000025 MOV #25,R1 ;OTHERWISE CORRECT THE LSB'S
3503
3504 017650 060100 46: ADD R1,R0 ;MREGE THE LSB'S WITH THE PHY ADDR
3505
3506 017652 PASS R0 ;RETURN WITH THE PHY ADDR.
3507 017652 010066 000002 MOV R0,ROSLOT(SP) ;PUT R0 IN STACK SLOT.
3508 017656 004736 JSR PC,8(SP) ;RETURN TO PREG05 SUBRT.
3509 017660 000207 RTS PC

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.SBTTL GLOBAL SUBROUTINE - DMRW -
*** *****
;* - READ/WRITE DATA FROM/TO (DMTSTA)
;* THIS ROUTINE READS DATA BYTES FROM OR WRITES DATA BYTES TO AN ADDR OF
;* ALTERNATE 1'S AND 0'S . BITS 21 TO 6 OF THE ADDR ARE CONTAINED AT
;* DMTSTA. THE ROUTINE APPENDS THE 6 LSB'S TO PRODUCE AN ADDR OF
;* ALTERNATE 1'S AND 0'S. THIS ROUTINE IS CALLED FROM THE DMA ADDRESS TEST.
;*
;* INPUTS:
;* R0 - ADDRESS OF THE DATA TO BE WRITTEN TO (DMTSTA),
;* IF A WRITE IS SPECIFIED.
;* R1 - ADDRESS OF THE AREA IN WHICH DATA FROM (DMTSTA),
;* IS TO BE SAVED, IF A READ IS SPECIFIED.
;* R3 - NUMBER OF DATA BYTES TO BE READ/WRTTEN
;* R5 - CLEAR , SPECIFIES A READ FROM (DMTSTA)
;* SET , SPECIFIES A WRITE TO (DMTSTA).
;* DMTSTA - CONTAINS BITS 21 TO 6 OF THE ADDR.
;* MMSRO - ADDRESS OF MEM MGT STATUS REG #0
;* MIPRES - BIT #0 SET, INDICATES MEM MGT PRESENT
;* PARA3 - ADDRESS OF MEM MGT PAR #3
;* TP4FLG - 004 TRAP FLAGS
;*
;* OUTPUTS:
;* DATA AT (DMTSTA) SAVED OR WRITTEN
;* PAR #3 - CONTENTS SET TO CONTENTS OF DMTSTA
;* TP4FLG - CLEAR IF READ/WRITE SUCCESSFUL
;* SET IF FAIL.
;*
;* CALLING SEQUENCE: JSR PC,DMRW
;*
;* COMENTS: IF MEM MGT IS PRESENT THE SUBROUTINE USES (DMTSTA)
;* AS THE PAGE ADDRESS , PLACING IT IN PAR #3, AND CREATES
;* A VIRTUAL ADDR IN THE RANGE OF PAR #3 WHICH CONTAINS
;* THE SIX LSB'S.
;* IF IT IS NOT PRESENT THE (DMTSTA) IS CONVERTED INTO
;* THE EQUIVALENT 16 BIT PHYSICAL ADDRESS.
;*
;* SUBORDINATE ROUTINES CALLED: CKTRAP,DM168.
;-- *****
DMRW:: SAVE JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.

MOV R0,R4 ;SAVE THE SOURCE ADDR
TST MIPRES ;IF MEM MGT IS PRESENT THEN
BNE 61 ;JUMP AND SET UP THE PAR #3
JSR PC,DM168 ;OTHERWISE CONVERT DMTSTA INTO A 16-BIT
;PHYSICAL ADDRESS, IN R0.
BR 101 ;JUMP TO PERFORM THE MOVE
MOV DMTSTA,PAR3A ;SET PAR #3
MOV #60052,R0 ;SET THE SIX LSB'S AND CONVERT TO
;A VIRTUAL ADDRESS WITHIN THE INFLUENCE
;OF PAR #3.
BIT #1,DMTSTA ;IF BIT #0 OF DMTSTA IS CLEAR THEN
BEQ 81 ;AVOID CHANGING THE LSB'S
MOV #60025,R0 ;CHANGE THE LSB'S
MOV #BIT0,MMSRO ;ENABLE MEM MGT.
TST R5 ;IF A READ IS SPECIFIED THEN

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017662	004567	165436			
017666	010004				
017670	005767	162424			
017674	001003				
017676	004767	177706			
017702	000416				
017704	016777	162312	162420	61:	
017712	012700	060052			
017716	032767	000001	162276		
017724	001402				
017726	012700	060025			
017732	012777	000001	162354	81:	
017740	005705			101:	

PC	Address	Op Code	Op Name	Comments
3566	017742	001402	BEQ 12:	AVOID SWAPING THE SOURCE AND DESTINATION.
3567	017744	010001	MOV R0,R1	SWAP
3568	017746	010400	MOV R4,R0	RESTORE THE ORIGINAL SOURCE FOR THE MOVE.
3569	017750	004767	JSR PC,CKTRPB	PERFORM THE BYTE MOVE.
3570	017754	103004	BCC 14:	EXIT IF A TRAP OCCURED.
3571	017756	005201	INC R1	INCREMENT THE DESTINATION ADDRESS
3572	017760	005200	INC R0	INCREMENT THE SOURCE ADDR.
3573	017762	005303	DEC R3	DECREMENT THE DATA
3574	017764	001371	BNE 12:	REPEAT UNTIL ALL DATA READ/WITTEN,
3575	017766	005767	TST MMRES	IF MEM MGT IS PRESENT THEN
3576	017772	001402	BEQ 16:	
3577	017774	005077	CLR MMMSRO	DISABLE IT.
3578	020000		PASS	
	020000	004736		
3579	020002	000207	RTS PC	JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
3580				

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3582 .SBTTL GLOBAL SUBROUTINE - DODMA -
3583 ;* *****
3584 ;* - INITIATE DMA TRANSMISSION ROUTINE -
3585 ;* THIS ROUTINE WRITES THE DMA PARAMETER TO THE SPECIFIED DEVICE AND
3586 ;* INITIATES THE DMA TRANSMISSION.
3587 ;*
3588 ;* INPUTS: R1 - LINE NUMBER ON WHICH TO INITIATE THE DMA.
3589 ;* R2 - START ADDRESS OF THE DMA BUFFER (16 BIT VIRTUAL).
3590 ;* R3 - CHARACTER COUNT OF THE DMA BUFFER.
3591 ;* CSRA - CONTAINS ADDRESS OF THE DUT CSR.
3592 ;* IESTAT - STORAGE FOR STATES OF THE INTERRUPT ENABLE BITS.
3593 ;* TXAD1A - CONTAINS ADDRESS OF DMA TX BUFFER ADDRESS REG #1.
3594 ;* TXAD2A - CONTAINS ADDRESS OF DMA TX BUFFER ADDRESS REG #2.
3595 ;* TXBFCA - CONTAINS ADDRESS OF DMA CHARACTER COUNT REGISTER.
3596 ;*
3597 ;* OUTPUTS: CARRY - SUCCESS FLAG (SET IF DMA_START FOUND CLEAR).
3598 ;* DUT TBUFFAD1 - LS 16 BITS OF DMA BUFFER ADDRESS (INITIALIZED).
3599 ;* DUT TBUFFAD2 - MS 6 BITS OF DMA BUFFER ADDRESS (INITIALIZED).
3600 ;* DMA_START BIT SET.
3601 ;* DUT TBUFFCT - DMA BUFFER CHARACTER COUNT (INITIALIZED).
3602 ;*
3603 ;* CALLING SEQUENCE: JSR PC,DODMA
3604 ;*
3605 ;* COMMENTS: THIS ROUTINE ASSUMES MEMORY MANAGEMENT IS DISABLED AND
3606 ;* CLEARS THE TWO MSB OF THE DMA ADDRESS, I.E. BITS 0 AND 1
3607 ;* OF THE TBUFFAD2 REG.
3608 ;*
3609 ;* SUBORDINATE ROUTINES CALLED: NONE.
3610 ;*
3611 ;* -- *****
3612 020004 DODMA:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
3613 020004 004567 165314 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3614 020010 012704 000200 MOV #200,R4 ;PREPARE TO CLEAR UPPER 6 BITS OF DMA BUFF ADR.
3615 ;*
3616 ;* WRITE THE DMA PARAMETERS OUT TO THE DUT DMA REGISTERS.
3617 ;* DISABLE INTERRUPTS.
3618 ;* SET UP DUT CSR IND.ADR.REG FIELD.
3619 ;* WRITE THE DMA TRANSMIT CHARACTER COUNT.
3620 ;* WRITE THE LEAST SIGNIFICANT 16 BITS OF THE DMA BUFFER START ADDRESS.
3621 ;* WRITE THE MOST SIGNIFICANT 6 BITS OF THE ADDRESS.
3622 ;* SETTING THE DMA_START BIT, AND INITIATING THE DMA TRANSMISSION.
3623 ;*
3624 020014 64: GETPRI R5 ;GET THE PRESENT PROCESSOR PRIORITY.
3625 020014 104440 TRAP C#GPRI
3626 020016 010005 MOV R0,R5
3627 020020 SETPRI #PRI07 ;DISABLE ALL HARDWARE INTERRUPTS.
3628 020020 012700 000340 MOV #PRI07,R0
3629 020024 104441 TRAP C#SPRI
3630 020026 056701 162202 BIS IESTAT,R1 ;PREPARE FOR SETUP OF LINE NUMBER IN DUT CSR.
3631 020032 010177 162142 MOV R1,BCSRA ;SET UP THE DUT CSR IND.ADR.REG FIELD.
3632 020036 105777 162152 TSTB BTXAD2A ;TEST THE DUT DMA_START BIT.
3633 020042 000241 CLC ;INDICATE FAILURE IN CASE DMA.HO BIT IS SET.
3634 020044 100411 BMI 606 ;EXIT WITH FAILURE IF DMA.HO BIT IS SET.
3635 020046 010377 162144 MOV R3,BTXBFCA ;WRITE THE DMA CHARACTER COUNT.
3636 020052 010277 162134 MOV R2,BTXAD1A ;WRITE THE LS 16 BITS OF BUFFER ADDRESS.
3637 020056 110477 162132 MOVB R4,BTXAD2A ;WRITE MS 6 BITS OF ADR AND START DMA TX.

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3634 020062          SETPRI R5          ;RESTORE THE PROCESSOR PRIORITY.
      020062 010500          ;
      020064 104441          MOV R5,R0
3635 020066 000261          TRAP C:SPRI
3636          SEC          ;INDICATE SUCCESS.
3637 020070 604: PASS          ;RESTORE GPRS.
      020070 004736          PC,0(SP)+ JSR          ;RETURN TO PREG05 SUBRT.
3638 020072 000207          RTS PC          ; CARRY - SUCCESS FLAG (SET IF SUCCESS).
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3640 .SBTTL GLOBAL SUBROUTINE - FINACT -
3641 ;* *****
3642 ;* - FIND FIRST ACTIVE LINE -
3643 ;* THIS SUBROUTINE CALCULATES THE NUMBER OF THE FIRST ACTIVE LINE THAT
3644 ;* IS FOUND IN THE ACTIVE LINE BIT MAP ACTLNS.
3645 ;*
3646 ;* INPUTS: ACTLNS - CONTAINS THE ACTIVE LINE BIT MAP.
3647 ;*
3648 ;* OUTPUTS: R1 - CONTAINS THE NUMBER OF THE FIRST ACTIVE LINE.
3649 ;* R5 - CONTAINS THE BIT MAP REPRESENTATION OF THE ACTIVE LINE.
3650 ;* CARRY SET INDICATES SUCCESS.
3651 ;*
3652 ;* CALLING SEQUENCE: JSR PC,FINACT
3653 ;*
3654 ;* COMMENTS:
3655 ;*
3656 ;* SUBORDINATE ROUTINES CALLED: NONE.
3657 ;*
3658 ;* *****
3659 020074 FINACT:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
020074 004567 165224 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3660 ;*
3661 ;* FIND AN ACTIVE LINE ON WHICH TO PERFORM THE TEST.
3662 ;*
3663 020100 005001 CLR R1 ;CLEAR THE LINE NUMBER COUNTER.
3664 020102 012703 000020 MOV #NUMLNS,R3 ;GET MAX LINE NUMBER.
3665 020106 016700 162060 MOV ACTLNS,R0 ;GET THE ACTIVE LINE BIT MAP.
3666 020112 012705 000001 MOV #1,R5 ;SET UP A LINE BIT MASK.
3667 020116 030500 2#: BIT R5,R0 ;LOOK FOR AN ACTIVE LINE.
3668 020120 001006 BNE 4# ;BRANCH TO BEGIN TEST IF A LINE HAS BEEN FOUND.
3669 020122 006305 ASL R5 ;SHIFT THE BIT MASK FOR THE NEXT LINE.
3670 020124 005201 INC R1 ;INCREMENT THE LINE NUMBER COUNTER.
3671 020126 020103 CMP R1,R3 ;CHECK IF ALL LINES HAVE BEEN TRIED.
3672 020130 002772 BLT 2# ;LOOP TO TRY THE NEXT LINE.
3673 020132 000241 CLC ;CLEAR CARRY BIT, NO ACTIVE LINE FOUND.
3674 020134 000401 BR 60# ;EXIT WITH FAILURE.
3675 020136 000261 4#: SEC ;SET CARRY, SUCCESS.
3676
3677 020140 60#: PASS R1,R5 ;RESTORE GPRS, EXCEPT
020140 010166 000004 MOV R1,R1SLOT(SP) ;PUT R1 IN STACK SLOT.
020144 010566 000014 MOV R5,R5SLOT(SP) ;PUT R5 IN STACK SLOT.
020150 004736 JSR PC,B(SP) ;RETURN TO PREG05 SUBRT.
3678 ;R1 - CONTAINS THE NUMBER OF FIRST ACTIVE LINE.
3679 ;R5 - CONTAINS THE BIT MAP OF THE ACTIVE LINE.
3680 ;CARRY - SET INDICATES SUCCESS.
3681 020152 000207 RTS PC

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3683 .SBTTL GLOBAL SUBROUTINE - FRPSUP -
3684 ;* *****
3685 ;* - FRAMING AND PARITY ERROR TRANSMISSION/RECEPTION SET-UP -
3686 ;*
3687 ;* THIS ROUTINE IS USED TO INITIALISE BOTH THE DUT AND THE
3688 ;* TRANSMISSION/RECEPTION CONTROL PARAMETERS TO THE CORRECT
3689 ;* STATE, PRIOR TO A FRAMING OR PARITY ERROR DETECTION AND
3690 ;* REPORTING TEST.
3691 ;*
3692 ;* INPUTS: R0 - LPR CONTENTS FOR LINES IN THE BIT MAP IN GPR4.
3693 ;* R1 - LPR CONTENTS FOR LINES NOT IN THE BIT MAP IN GPR4.
3694 ;* R2 - START ADDRESS OF DATA PATTERN TO TRANSMIT.
3695 ;* R3 - LENGTH OF THE DATA PATTERN TO TX.
3696 ;* R4 - LOCAL LINE GROUP BIT MAP.
3697 ;* ACTLNS - CONTAINS A BIT MAP OF ALL CURRENTLY ACTIVE LINES.
3698 ;* LOPBCK - CONTAINS THE TYPE OF LOOPBACK MODE SELECTED.
3699 ;* CBB - LABEL AT BASE OF TX/RX CONTROL BLOCK.
3700 ;*
3701 ;* OUTPUTS: THE CONTENTS OF THE TXRCB ARE DESTROYED.
3702 ;* THE INDIRECT ADDRESS FIELD OF THE DUT CSR MAY BE DESTROYED.
3703 ;* THE DUT'S LPR'S AND LNC'S MAY BE MODIFIED.
3704 ;* THE FOLLOWING POINTERS AND COUNTERS ARE INITIALISED;
3705 ;* CHCNT,CHRTOT,DPEND,DPLEN,EXCNT,RXCNT,RXDONF,RXPTR,TXCNT,
3706 ;* TXDONF,TXPTR,TXRXL.
3707 ;*
3708 ;* CALLING SEQUENCE: JSR PC,FRPSUP
3709 ;*
3710 ;* COMMENTS: THIS ROUTINE SHOULD BE CALLED TWICE DURING THE TESTING OF
3711 ;* THE FRAMING AND PARITY ERROR DETECTION AND REPORTING TEST.
3712 ;* SO THAT BOTH LINE GROUPS ARE TESTED ON TRANSMISSION AND
3713 ;* RECEPTION.
3714 ;* JSR PC,FRPSUP ; DO SET-UP.
3715 ;* EXECUTE TEST FOR THE ABOVE SET-UP.
3716 ;* COMPLEMENT THE LINE GROUP BIT MAP.
3717 ;* JSR PC,FRPSUP ;DO SET UP AGAIN.
3718 ;* EXECUTE TEST AGAIN.
3719 ;*
3720 ;* SUBORDINATE ROUTINES CALLED: TXRINI.
3721 ;*
3722 ;* -- *****
3723 FRPSUP: SAVE JSR ;SAVE THE CONTENTS OF THE GPR'S.
3724 020154 004567 165144 R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3725 020160 010067 000230 MOV R0,70H ;SAVE LPR PARAMETER FOR LINE TX.
3726 020164 010167 000226 MOV R1,72H ;SAVE LPR PARAMETER FOR LINE RX.
3727 ;*
3728 ;* SET UP THE TRANSMISSION/RECEPTION CONTROL BLOCK TO INITIALISE THE
3729 ;* ACTIVE LINES IN THE BIT MAP PASSED INTO THIS ROUTINE.
3730 ;*
3730 020170 010067 162726 MOV R0,CBB ;SET CONTENTS OF LPR PARAMS IN TX/RX C.BLK.
3731 020174 012700 003124 MOV #CBB+2,R0 ;GET ADDRESS OF THE NEXT WORD IN THE CNTRL BLK.
3732 020200 012720 000004 MOV #4,(R0)+ ;LNCRTL PARAMETER, ENABLE RECEIVERS.
3733 020204 010220 MOV R2,(R0)+ ;START ADDRESS OF DATA PATTERN.
3734 020206 010320 MOV R3,(R0)+ ;SET DATA PATTERN LENGTH.
3735 020210 012720 000001 MOV #1,(R0)+ ;NUMBER OF DATA PATTERNS TO TRANSMIT.
3736 020214 016710 161752 MOV ACTLNS,(R0) ;BIT MAP OF LINES TO INITIALISE.
3737 020220 005104 COM R4 ;GENERATE A BIT MAP OF ACTIVE LINES IN GRP1.
3738 020222 040420 BIC R4,(R0)+ ;CLEAR THE UNWANTED LINES.

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3739 020224 116720 161744          MOV8  LOPBCK,(R0)+ ;SET LOOPBACK MODE,STAGGARED.
3740 020230 005200                INC    R0          ;INCREMENT ADDRESS TO GET NEXT WORD IN TABLE.
3741 020232 012710 000001          MOV    #1,(R0)      ;SET AMMOUNT OF OFFSET FOR EACH TX START.
3742
3743
3744          ;*
3745          ; INITIALISE THE DUT AND THE ASSOCIATED POINTERS AND COUNTERS, TO THE STATE
3746          ; DICTATED BY THE CONTENTS OF THE TX/RX CONTROL BLOCK.
3747 020236 004767 005534          JSR     PC,TXRINI      ;INITIALISE DUT.
3748
3749          ;*
3750          ; SET UP CONTROL BLOCK FOR LINES IN GROUP 2.
3751 020242 012700 003122          MOV     #CBB,R0        ;GET START ADDRESS OF CONTROL BLOCK.
3752 020246 010120                MOV     R1,(R0)+       ;SET LPR PARAMETER FOR RX LINES.
3753 020250 062700 000010          ADD     #10,R0        ;SELECT THE ADDRESS OF THE LINE BIT MAP IN C.B.
3754 020254 016710 161712          MOV     ACTLNS,(R0)    ;BIT MAP OF LINES TO INITIALISE.
3755 020260 005104                COM      R4            ;GENERATE A BIT MAP OF LINES IN GRP 2.
3756 020262 040410                BIC      R4,(R0)        ;CLEAR THE UNWANTED LINES.
3757
3758          ;*
3759          ; INITIALISE THE DUT AND THE ASSOCIATED POINTERS AND COUNTERS, TO THE STATE
3760          ; DICTATED BY THE CONTENTS OF THE TX/RX CONTROL BLOCK.
3761 020264 004767 005506          JSR     PC,TXRINI      ;INITIALISE DUT.
3762
3763          ;*
3764          ; SET-UP THE REQUIRED LPR PARAMETERS NEEDED FOR THE CORRECT RECEPTION OF DATA
3765          ; ON ASSOCIATED IN-ACTIVE LINES.
3766
3767          ;*
3768          ; INITIALISE LPR PARAMETERS FOR INACTIVE LINES IN GROUP 2.
3769
3770 020270 012701 177777          MOV     #MAPLNS,R1    ;SET UP BIT MAP CORRESPONDING TO ALL LINES.
3771 020274 016702 161672          MOV     ACTLNS,R2    ;GET THE ACTIVE (TX) LINE BIT MAP.
3772 020300 005101                COM      R1            ;GENERATE A BIT MAP OF NONE EXISTANT LINES.
3773 020302 005102                COM      R2            ;GENERATE A BIT MAP OF INACTIVE LINES.
3774 020304 040102                BIC      R1,R2        ;CLEAR ANY "NONE EXISTANT" INACTIVE LINES.
3775 020306 040402                BIC      R4,R2
3776 020310 010267 162620          MOV     R2,CBMAPA    ;SET UP BIT MAP IN CONTROL BLOCK.
3777 020314 005067 162612          CLR      CBDPNA      ;CLEAR REPEAT TX COUNT IN CONTROL BLOCK.
3778 020320 016767 000072 162574  MOV     72,CBLPRA    ;SET-UP COMPLEMENTARY LPR PARAM.
3779 020326 004767 005444          JSR     PC,TXRINI      ;INITIALISE INACTIVE LINES.
3780
3781          ;*
3782          ; INITIALISE LPR PARAMETERS FOR INACTIVE LINES IN GROUP 1.
3783 020332 016702 161634          MOV     ACTLNS,R2    ;GET THE ACTIVE (TX) LINE BIT MAP.
3784 020336 005102                COM      R2            ;GENERATE A BIT MAP OF INACTIVE LINES.
3785 020340 040102                BIC      R1,R2        ;CLEAR ANY NONE EXISTANT INACTIVE LINES.
3786 020342 005104                COM      R4
3787 020344 040402                BIC      R4,R2
3788 020346 010267 162562          MOV     R2,CBMAPA    ;ONLY PASS LGRP2 ASSOCIATED LINE BIT MAP.
3789 020352 016767 000036 162542  MOV     70,CBLPRA    ;SET-UP BIT MAP IN CONTROL BLOCK.
3790 020360 004767 005412          JSR     PC,TXRINI      ;SET-UP COMPLEMENTARY LPR PARAM FOR LGRP1.
3791
3792          ;*
3793          ; DISABLE RECEIVERS ON ALL LINES TO ENSURE THAT ONLY THE RECEIVERS OF THE
3794          ; ASSOCIATED ACTIVE (TX) LINES ARE ENABLED.(STAGGARED LOOPBACK)
3795          ; RE-ENABLE RECEPTION ON THE CORRECT ASSOCIATED LINES.

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3796	020364	012705	177777	MOV	MAPLNS,R5	;	SET-UP BIT MAP FOR ALL LINES.
3797	020370	004767	004002	JSR	PC,RXDSBL	;	DISABLE RX ON ALL LINES.
3798							
3799				;			
3800				;	ENABLE RECEIVERS ON ASSOCIATED (RX) LINES.		
3801	020374	016705	161572	MOV	ACTLNS,R5	;	GET ACTIVE (TX) LINE BIT MAP.
3802	020400	004767	177070	JSR	PC,CONMAP	;	GENERATE AN ASSOCIATED (RX) LINE BIT MAP.
3803	020404	004767	004062	JSR	PC,RXENBL	;	ENABLE RECEIVERS ON ASSOCIATED LINES.
3804							
3805	020410			60:	PASS	;	RESTORE GRP'S.
	020410	004736				PC,8(SP);	RETURN TO PREG05 SUBRT.
3806	020412	000207		RTS	PC		
3807	020414	000000		70:	.WORD 0	;	LOCAL STORAGE OF LPR PARAMETER TX.
3808	020416	000000		72:	.WORD 0	;	LOCAL STORAGE OF LPR PARAMETER RX.
3809							

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3811 .SBTTL GLOBAL SUBROUTINE - GETBDR -
3812 ;+ *****
3813 ;* - GET BAUDRATE SUBROUTINE -
3814 ;* THIS ROUTINE REQUESTS A BAUDRATE INPUT FROM THE OPERATOR. THIS
3815 ;* BAUDRATE IS LOOKED UP IN A TABLE TO GIVE THE LPR BAUDRATE FIELD
3816 ;* VALUE WHICH IS ASSOCIATED WITH THAT BAUDRATE.
3817 ;*
3818 ;* INPUTS: BDRMSG - LABEL AT THE BAUDRATE PROMPT MESSAGE.
3819 ;* BRTBLE - LABEL AFTER END OF THE BAUDRATE TABLE.
3820 ;* UBRFMT - LABEL AT THE UNSUPPORTED BAUDRATE MESSAGE.
3821 ;*
3822 ;* OUTPUTS: R1 - BAUDRATE CODE IN LS 4 BITS.
3823 ;*
3824 ;* CALLING SEQUENCE: JSR PC.GETBDR
3825 ;*
3826 ;* COMMENTS:
3827 ;*
3828 ;* SUBORDINATE ROUTINES CALLED: NONE.
3829 ;-- *****
3830
3831 020420 GETBDR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
3832 020420 004567 164700 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3833 020424 016705 161600 MOV GMANWD,R5 ;SAVE THE GMAINIX VALUE.
3834 ;+
3835 ; PROMPT THE OPERATOR: "MODEM BAUDRATE IN BPS: (D) 1200 ?"
3836 ;--
3837 020430 012767 002260 161572 2+: MOV #1200.,GMANWD ;SET UP DEFAULT VALUE TO 1200 BAUD.
3838 020436 104443 GMANID BDRMSG,GMANWD,D,177777,0,38400.,YES
3839 020440 000406 TRAP C:GMAN
3840 020442 002230 BR 10000$
3841 020444 000052 .WORD GMANWD
3842 020446 013137 .WORD T:CODE
3843 020450 177777 .WORD BDRMSG
3844 020452 000000 .WORD 177777
3845 020454 113000 .WORD T:LLOIM
3846 020456 .WORD T:HILIM
3847 020456 016702 161546 MOV GMANWD,R2 10000$:
3848 ;+
3849 ; ATTEMPT TO LOOK THE VALUE UP IN THE BAUDRATE TABLE.
3850 ;--
3851 020462 012701 000017 MOV #15.,R1 ;INITIALIZE BAUDRATE CODE TO HIGHEST BAUDRATE.
3852 020466 012703 002464 MOV #BRTBLE,R3 ;INITIALIZE BAUDRATE POINTER.
3853 4+: CMP R2,-(R3) ;COMPARE BAUDRATE WITH A TABLE ENTRY.
3854 BEQ 60$ ;BAUDRATES COMPARE? YES, EXIT WITH CODE.
3855 DEC R1 ;NO, SET BAUDRATE CODE TO NEXT LOWER BAUDRATE.
3856 BNE 4$ ;DONE? NO, LOOP.
3857 CMP R2,-(R3) ;CHECK IF LAST BAUDRATE MATCHES.
3858 BEQ 60$ ;BAUDRATES MATCH? YES, EXIT WITH CODE.
3859 ;+
3860 ; REPORT "NNNNN IS NOT A SUPPORTED BAUDRATE, ENTER ANOTHER OR CTRL C."
3861 ;--
3862 020506 010246 PRINTF #UBRFMT,R2
3863 MOV R2,-(SP)

```

Address	Hex	Hex	Hex	Label	Operation	Comment
	020510	012746	007501		MOV	#UBRFMT, -(SP)
	020514	012746	000002		MOV	#2, -(SP)
	020520	010600			MOV	SP, R0
	020522	104417			TRAP	C#PNTF
	020524	062706	000006		ADD	#6, SP
3857	020530	000737		BR 2\$;LOOP TO GET ANOTHER BAUDRATE.
3858						
3859	020532	010567	161472	60\$:	MOV	R5, GMANWD ;RESTORE THE GMANIX PARAMETER VALUE.
3860	020536				PASS	R1 ;RESTORE GPRS, EXCEPT THE FOLLOWING:
	020536	010166	000004		MOV	R1, R1SLOT(SP) ;PUT R1 IN STACK SLOT.
	020542	004736			JSR	PC, @ (SP)+ ;RETURN TO PREGOS SUBRT.
3861	020544	000207		RTS	PC	; R1 - BAUDRATE CODE.

```

3863 .SBTTL GLOBAL SUBROUTINE - GETCHR -
3864 ;** *****
3865 ;* - GET A CHARACTER FROM THE RX BUFFER ROUTINE -
3866 ;* THIS SUBROUTINE GETS A CHARACTER FROM THE RX BUFFER WHICH IS IN THE
3867 ;* HOST SYSTEM MEMORY. IF THE BUFFER IS EMPTY UPON ENTRY OF THIS ROUTINE
3868 ;* THIS ROUTINE RETURNS A NULL CHARACTER WITH DATA.VALID CLEAR AND A
3869 ;* BUFFER EMPTY INDICATION.
3870 ;*
3871 ;* INPUTS: RXBCNT - RX BUFFER CHARACTER COUNT.
3872 ;* RXBEND - LABEL AFTER END OF THE RX BUFFER AREA IN MEMORY.
3873 ;* RXBETX - EQUATED TO RX BUFFER LEVEL AT WHICH TO ENABLE TX.
3874 ;* RXBOPT - POINTER TO NEXT AVAILABLE INPUT SLOT OF RX BUFFER.
3875 ;* RXBSTA - LABEL AT START OF RX BUFFER AREA IN MEMORY.
3876 ;*
3877 ;* OUTPUTS: R2 - CHARACTER WHICH IS READ FROM THE BUFFER.
3878 ;* RXBOPT - UPDATED TO POINT TO NEXT INPUT SLOT OF RX BUFFER.
3879 ;* RXBCNT - RX BUFFER CHARACTER COUNT (UPDATED).
3880 ;* CARRY - "SUCCESS" FLAG (SET IF BUFFER IS NOT EMPTY ON ENTRY).
3881 ;*
3882 ;* CALLING SEQUENCE: JSR PC,GETCHR
3883 ;*
3884 ;* COMMENTS:
3885 ;*
3886 ;* SUBORDINATE ROUTINES CALLED: NONE.
3887 ;*
3888 ;* *****
3889 GETCHR:: SAVE R0 THRU R5.
3890 020546 004567 164552 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3891 020552 005000 CLR R0 ;CLEAR THE "RE-ENABLE" TX FLAG (SUBRTN OUTPUT).
3892 020554 005002 CLR R2 ;GET NULL CHAR IN CASE BUFFER IS EMPTY.
3893 020556 005767 162134 TST RXBCNT ;CHECK FOR RX BUFFER EMPTY. CLEAR CARRY.
3894 020562 001416 BEQ 60$ ;EXIT THE ROUTINE IF BUFFER IS EMPTY.
3895 020564 016704 162122 MOV RXBOPT,R4 ;GET THE BUFFER OUTPUT POINTER.
3896 020570 011402 MOV (R4),R2 ;GET A CHARACTER FROM THE BUFFER.
3897 020572 005024 CLR (R4)+ ;DELETE THE READ CHARACTER FROM THE BUFFER.
3898 020574 020427 003120 CMP R4,#RXBEND ;CHECK IF POINTER SHOULD WRAP AROUND.
3899 020600 103402 BLO 2$ ;SKIP WRAPAROUND IF POINTER IS NOT AT END.
3900 020602 012704 002720 MOV #RXBSTA,R4 ;WRAP INPUT POINTER AROUND.
3901 020606 010467 162100 2$: MOV R4,RXBOPT ;UPDATE THE OUTPUT POINTER STORAGE.
3902 020612 005367 162100 DEC RXBCNT ;REMOVE THIS CHAR FROM THE BUFFER COUNT.
3903 020616 000261 SEC ;SET SUCCESS FLAG, BUFFER WAS NOT EMPTY.
3904
3905 020620 010266 000006 60$: PASS R2 ;RESTORE GPRS, EXCEPT
3906 020624 004736 MOV R2,R2SLOT(SP) ;PUT R2 IN STACK SLOT.
3907 JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
3908 020626 000207 RTS PC ;R2 - CONTAINS THE CHARACTER READ FROM BUFFER.
;CARRY-"SUCCESS" FLAG, SET IF BUFFER NOT EMPTY.

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3910 .SBTTL GLOBAL SUBROUTINE - GETTIM -
3911 ;** *****
3912 ;* GET TIME-OUT VALUE BASED ON MINIMUM BAUDRATE ROUTINE -
3913 ;* THIS SUBROUTINE GETS THE NECESSARY TIME-OUT VALUE TO VERIFY THAT ALL
3914 ;* CHARS HAVE BEEN RECEIVED AT THE COMPLETION OF THE TX/RX OF A DATA
3915 ;* PATTERN. THIS USES THE SLOWEST BAUDRATE WHICH IS SPECIFIED IN THE
3916 ;* PASSED IN DUT LPR CONTENTS TO CALCULATE THIS TIME-OUT VALUE.
3917 ;*
3918 ;* INPUTS: R1 - DUT LPR CONTENTS.
3919 ;*
3920 ;* OUTPUTS: RXTOUT - TIME-OUT VALUE FOR WAITING FOR LAST RX CHAR.
3921 ;*
3922 ;* CALLING SEQUENCE: JSR PC,GETTIM
3923 ;*
3924 ;* COMMENTS:
3925 ;*
3926 ;* SUBORDINATE ROUTINES CALLED: NONE.
3927 ;-- *****
3928
3929 020630 GETTIM:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
020630 004567 164470 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
3930 020634 000301 SWAB R1 ;PUT THE BAUD RATE FIELDS IN THE LOW BYTE.
3931 020636 042701 177400 BIC #177400,R1 ;CLEAR STOP,PARITY,AND CHAR FIELDS.
3932 020642 010102 MOV R1,R2 ;COPY BAUD RATE FIELDS.
3933 020644 042701 000360 BIC #360,R1 ;SELECT RX BAUD RATE FIELD ONLY.
3934 020650 006202 ASR R2 ;SHIFT TX BAUD RATE FIELD
3935 020652 006202 ASR R2 ; TO OCCUPY THE LOW FOUR BYTES.
3936 020654 006202 ASR R2 ;
3937 020656 006202 ASR R2 ;
3938 020660 020102 CMP R1,R2 ;CHECK IF SAME BAUD RATE IN EACH FIELD.
3939 020662 101401 BLOS 2# ;BRANCH IF RX BAUD RATE IS LOWER OR SAME.
3940 020664 010201 MOV R2,R1 ;TX BAUD RATE IS THE SLOWER OF THE TWO.
3941 020666 116102 005214 2# MOVB PROTB(R1),R2 ;GET PROPORTIONAL DELAY FROM TABLE.
3942 020672 042702 177400 BIC #177400,R2 ;CLEAR UPPER BYTE BECAUSE OF SIGN EXTENSION.
3943 020675 010267 161344 MOV R2,RXTOUT ;LOAD THE RX TIME-OUT VARIABLE.
3944
3945 020702 004736 60# PASS ;RESTORE GPRS.
020702 000207 RTS PC JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
3946 020704

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[illegible]

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4004 020744 004767 177034      JSR    PC,DODMA
4005 020750 103403              BCS    6$      ;SKIP ERROR IF DODMA WAS SUCCESSFUL.
4006                               ;*
4007                               ; SET THE PROPER BIT OF THE TX INTERRUPT FLAGS TO INDICATE THE LINE ERROR.
4008                               ;*
4009 020752 050567 161306      BIS    R5,TXINTF      ;INDICATE THE ERROR.
4010 020756 000402              BR     10$      ;SKIP UPDATING POINTERS AND COUNTERS.
4011                               ;*
4012                               ; UPDATE THE TX CHARACTER COUNT FOR THIS LINE.
4013                               ;*
4014 020760 060364 003502      6$:   ADD    R3,TXCNTB(R4) ;ADD THE DATA PATTERN LENGTH TO TX CHAR COUNT.
4015                               ;*
4016                               ; INCREMENT LINE COUNTER,GOTO NEXT LINE IF NOT DONE.
4017                               ;*
4018 020764 005201              10$:  INC    R1          ;INCREMENT THE LINE COUNTER.
4019 020766 020127 000020      CMP    R1,#NUMLNS      ;COMPARE THE LINE COUNTER WITH NUMBER OF LINES.
4020 020772 002752              BLT    2$             ;LOOP TO SEND CHAR TO ANOTHER LINE IF NOT DONE.
4021                               ;*
4022 020774              60$:  PASS                    ;RESTORE GPRS.
4023 020776 004736              RTS    PC              JSR    PC,8(SP) ;RETURN TO PREGOS SUBRT.

```

D9

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4025 .SBTTL GLOBAL SUBROUTINE - LINBIT -
4026 ;* *****
4027 ;* - LINE NUMBER TO BIT MAP CONVERSION SUBROUTINE -
4028 ;* THIS SUBROUTINE IS USED TO GENERATE A BIT MAP (ONE BIT OF 16 SET)
4029 ;* BASED ON A LINE NUMBER (RANGE: 1 TO 16). ONLY THE LS 4 BITS OF THE
4030 ;* LINE NUMBER WORD ARE USED, THE OTHERS ARE MASKED OUT (SO UNMASKED
4031 ;* MSBYTES OF DUT CSRS CAN BE PASSED TO THIS ROUTINE WITHOUT ERROR).
4032 ;*
4033 ;* INPUTS: R1 - LINE NUMBER (ONLY LS 4 BITS USED, OTHERS DISREGARDED).
4034 ;* BITTBL - BASE LABEL OF A 16 WORD BIT TABLE.
4035 ;*
4036 ;* OUTPUTS: R0 - BIT MAP, BIT CORRESPONDING TO LINE NUMBER IS SET:
4037 ;* IF LINE NUMBER IS 3, THEN BIT3 IS SET, ETC.
4038 ;*
4039 ;* CALLING SEQUENCE: JSR PC,LINBIT
4040 ;*
4041 ;* COMMENTS: NO CHECKING IS PERFORMED TO VERIFY THAT THE LINE NUMBER IS
4042 ;* A LEGAL LINE NUMBER FOR THE DUT (IE - LESS THAN NUMLNS).
4043 ;* NOTE: THE LINE NUMBER IS NOT DESTROYED OR ALTERED, SO THIS
4044 ;* ROUTINE CAN BE USED EASILY IN LOOPS.
4045 ;*
4046 ;* SUBORDINATE ROUTINES CALLED: NONE.
4047 ;* --- *****
4048
4049 021000 LINBIT:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
4050 021000 004567 164320 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4051 021004 042701 177760 BIC #177760,R1 ;MASK OUT ALL BUT 4 LSBITS OF THE LINE #.
4052 021010 006301 ASL R1 ;MULTIPLY LINE # BY 2 TO GET WORD TABLE OFFSET.
4053 021012 016100 002364 MOV BITTBL(R1),R0 ;GET THE SINGLE BIT BIT MAP.
4054 021016 010066 000002 604: PASS R0 ;RESTORE GPRS, EXCEPT THE FOLLOWING.
4055 021022 004736 MOV R0,ROSLOT(SP) ;PUT R0 IN STACK SLOT.
4056 021024 000207 RTS PC JSR PC,B(SP)+ ;RETURN TO PREG05 SUBRT.
4057 ;R0 - BIT MAP WITH LINE # BIT SET.

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E9

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4056 .SBTTL GLOBAL SUBROUTINE - MODSUP -
4057 ;* *****
4058 ;* - MODEM LOOPBACK TX/RX SET-UP ROUTINE -
4059 ;*
4060 ;* THIS ROUTINE IS USED TO INITIALISE BOTH THE DUT AND THE
4061 ;* TRANSMISSION/RECEPTION CONTROL PARAMETERS TO THE CORRECT
4062 ;* STATE, PRIOR TO A MODEM LOOPBACK TEST DATA PATTERN TX/RX.
4063 ;*
4064 ;* INPUTS: R1 - TX, RX LPR CONTENTS.
4065 ;* R2 - START ADDRESS OF DATA PATTERN TO TRANSMIT.
4066 ;* R3 - LENGTH OF DATA PATTERN.
4067 ;* ACTLNS - CONTAINS A BIT MAP OF ALL CURRENTLY ACTIVE LINES.
4068 ;* CBB - LABEL AT BASE OF TX/RX CONTROL BLOCK.
4069 ;*
4070 ;* OUTPUTS: THE CONTENTS OF THE TX/RX CONTROL BLOCK (CCB) ARE DESTROYED.
4071 ;* THE INDIRECT ADDRESS FIELD OF THE DUT CSR MAY BE DESTROYED.
4072 ;* THE DUT'S LPR'S AND LNC'S MAY BE MODIFIED.
4073 ;* THE FOLLOWING POINTERS AND COUNTERS ARE INITIALISED;
4074 ;* CHCNT,CHRTOT,DPEND,DPLEN,EXCNT,RXCNT,RXPTR,TXCNT,
4075 ;* TXPTR,TXRXL.
4076 ;* CHRTOT, RXDNF, TXDNF AND TXINTF ARE CLEARED.
4077 ;*
4078 ;* CALLING SEQUENCE: JSR PC,MODSUP
4079 ;*
4080 ;* COMMENTS: DUT IS SET UP WITH DSR AND DTR SET. ONE DATA PATTERN IS
4081 ;* SENT AND RECEIVED FROM EACH LINE.
4082 ;*
4083 ;* SUBORDINATE ROUTINES CALLED: CONMAP,RXENBL,TXRINI.
4084 ;* -- *****
4085
4086 021026 004567 164272 MODSUP:: SAVE
4087 021026 005067 161440 JSR ;SAVE CONTENTS OF THE GPR'S R0 THRU R5.
4088 021032 005067 161222 CLR CHRTOT ;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4089 021042 005067 161434 CLR TXINTF ;CLEAR TOTAL RECEIVED CHAR COUNTER.
4090 021046 005067 161432 CLR TXDNF ;CLEAR FLAGS USED TO LOG DMA H.OVER ERRORS.
4091 ;* CLR RXDNF ;CLEAR THE TX DONE FLAGS.
4092 ;*
4093 ;* SET UP THE TRANSMISSION/RECEPTION CONTROL BLOCK TO THE DESIRED STATE.
4094 021052 010167 162044 MOV R1,CBB ;SET CONTENTS OF LPR PARAMS IN TX/RX C.BLK.
4095 021056 012701 003122 MOV #CBB,R1 ;GET BASE ADDRESS OF CONTROL BLOCK.
4096 021062 005201 INC R1 ;INCREMENT ADDRESS FOR NEXT WORD
4097 021064 005201 INC R1 ;INITIALISE THE FOLLOWING IN THE CNTRL.BLK:
4098 021066 012721 011004 MOV #11004,(R1)+ ; LNCRTL; RTS, DTR, ENABLE RECEIVERS.
4099 021072 010221 MOV R2,(R1)+ ; START ADDRESS OF DATA PATTERN.
4100 021074 010321 MOV R3,(R1)+ ; DATA PATTERN LENGTH.
4101 021076 012721 000001 MOV #1,(R1)+ ; NUMBER OF DATA PATTERNS TO TRANSMIT.
4102 021102 016721 161064 MOV ACTLNS,(R1)+ ; BIT MAP OF LINES TO INITIALISE.
4103 021106 112721 000003 MOVB #3,(R1)+ ;SET LOOPBACK MODE TO H325.
4104 021112 005201 INC R1 ;INCREMENT ADDRESS FOR THE NEXT WORD.
4105 021114 012711 000002 MOV #2,(R1) ;SET AMOUNT OF OFFSET EACH TX STARTS AT TO 2.
4106 ;*
4107 ;* INITIALISE THE DUT AND THE ASSOCIATED POINTERS AND COUNTERS, TO THE STATE
4108 ;* DICTATED BY THE CONTENTS OF THE TX/RX CONTROL BLOCK.
4109 ;*
4110 021120 004767 004652 JSR PC,TXRINI ;INITIALISE DUT.
4111 ;*

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4112      ; INITIALISE POINTERS AND COUNTERS FOR INACTIVE LINES TO ZERO.
4113      ; -
4114 021124 012701 177777      MOV    #MAPLNS,R1      ;GET THE LINE BIT MAP FOR ALL LINES.
4115 021130 016702 161036      MOV    ACTLNS,R2      ;GET THE ACTIVE LINE BIT MAP.
4116 021134 005101              COM    R1              ;
4117 021136 005102              COM    R2              ;
4118 021140 040102              BIC    R1,R2            ;GENERATE AN IN-ACTIVE LINE BIT MAP.
4119 021142 010267 161766      MOV    R2,CBMAPA        ;MOVE BIT MAP TO THE CONTROL BLOCK.
4120 021146 005067 161752      CLR    CBLNCA           ;CLEAR THE LNCTRL SET UP PARAMETERS.
4121 021152 005067 161754      CLR    CBOPNA           ;CLEAR THE REPEAT TX COUNT IN CNTRL BLCK.
4122 021156 004767 004614      JSR    PC,TXRINI        ;SET UP PARAMETERS FOR INACTIVE LINES.
4123
4124 021162      601: PASS      ;RESTORE GPR'S.
      021162 004736              JSR    PC,0(SP),      ;RETURN TO PREG05 SUBRT.
4125 021164 000207      RTS    PC

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4127 .SBTTL GLOBAL SUBROUTINE - MSLGET -
4128 *****
4129 ;* - MILLI SECONDS LOOP WHICH RETURNS READ WORD AND REMAINING TIME -
4130 ;* THIS SUBROUTINE IS A GENERAL PURPOSE TEST LOOP SUBROUTINE. IT IS USED
4131 ;* TO VERIFY THAT A CERTAIN ACTION OCCURS BEFORE A TIME-OUT PERIOD. THE
4132 ;* CALLING ROUTINE PASSES IN WHICH BITS SHOULD BE SET AND CLEARED FOR THE
4133 ;* DESIRED CONDITION AND THE TIME-OUT VALUE IN MILLI-SECONDS.
4134 ;* THIS ROUTINE CHECKS FOR THE DESIRED CONDITION UPON ENTRANCE INTO THE
4135 ;* ROUTINE AND THEN ONCE EACH MILLI-SECOND THERE AFTER.
4136 ;* UPON RETURN, THE LAST WORD WHICH WAS READ TO CHECK FOR THE CONDITION
4137 ;* IS RETURNED BY THIS SUBROUTINE.
4138 ;*
4139 ;* INPUTS: R1 - TIME-OUT VALUE IN MILLI-SECONDS (UP TO 64K MS).
4140 ;* R2 - BIT MAP OF BITS TO TEST (1 INDICATES TO TEST THE BIT).
4141 ;* R3 - DESIRED STATES OF THE INDICATED FIELDS IN R2.
4142 ;* R4 - ADDRESS OF THE WORD TO TEST.
4143 ;* MSLCNT - MILLI SECOND SOFTWARE LOOP COUNT.
4144 ;*
4145 ;* OUTPUTS: R0 - THE LAST WORD WHICH WAS READ TO CHECK FOR THE CONDITION.
4146 ;* R1 - REMAINING NUMBER OF MS IN TIME-OUT TIME.
4147 ;* CARRY - SUCCESS FLAG (SET IF CONDITION IS MET BEFORE TIME-OUT).
4148 ;*
4149 ;* CALLING SEQUENCE: JSR PC,MSLGET
4150 ;*
4151 ;* COMMENTS: THIS ROUTINE WORKS WITH OR WITHOUT A HARDWARE CLOCK, BUT THE
4152 ;* CALIBRATION IS ONLY GUARENTEED WHEN A LINE CLOCK IS AVAILABLE
4153 ;* ON THE SYSTEM.
4154 ;* THIS ROUTINE CAN BE USED AS A DELAY ROUTINE, BY SPECIFYING THE
4155 ;* DESIRED DELAY AS THE TIME-OUT AND SPECIFYING A CONDITION TO
4156 ;* LOOK FOR WHICH WILL NOT BE MET DURING THE DELAY.
4157 ;* IF A TIME-OUT VALUE OF 0 IS SPECIFIED, THIS ROUTINE CHECKS FOR
4158 ;* THE DESIRED CONDITION BEFORE RETURNING. IT INDICATES SUCCESS
4159 ;* IF THE CONDITION IS MET, FAILURE OTHERWISE.
4160 ;*
4161 ;*
4162 ;* SUBORDINATE ROUTINES CALLED: NONE.
4163 ;*
4164 ;* *****
4165 021166 MSLGET:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
4166 021166 004567 164132 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4167 ;*
4168 ;* SET UP MASK FOR REMOVING UNUSED BITS IN THE TEST WORD, AND CLEAR UNUSED
4169 ;* BITS IN THE DESIRED STATE WORD TO ALLOW DIRECT COMPARISON.
4170 021172 005102 ;*
4171 021174 040203 COM R2 ;GET MASK OF UNUSED BITS.
4172 BIC R2,R3 ;MASK OUT UNUSED BITS IN DESIRED STATE WORD.
4173 ;*
4174 ;* HANDLE THE TEST AND EXIT IF WE HAVE A 0 TIME-OUT VALUE.
4175 021176 005701 ;*
4176 021200 001011 TST R1 ;TEST THE TIME-OUT VALUE FOR ZERO.
4177 021202 011400 BNE 2$ ;IF NON-ZERO TIME-OUT, GO LOOP AND TEST.
4178 021204 010067 000070 MOV (R4),R0 ;GET THE WORD TO TEST BEFORE EXITING.
4179 021210 040200 MOV R0,62$ ;SAVE VALUE SO WE CAN RETURN IT.
4180 021212 020003 BIC R2,R0 ;MASK OUT UNTESTED BITS OF WORD.
4181 021214 000261 CMP R0,R3 ;COMPARE AGAINST DESIRED STATE WORD.
4182 021216 001420 SEC ;INDICATE SUCCESS IN CASE WORDS ARE EQUAL.
4183 BEQ 6$ ;EXIT WITH SUCCESS IF WORDS ARE EQUAL.

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4183 021220 000241          CLC          ;INDICATE FAILURE (TIME-OUT).
4184 021222 000416          BR          6$ ;EXIT WITH FAILURE, WORDS AREN'T EQUAL.
4185
4186          ;+
4187          ; NON-ZERO TIME-OUT VALUE.  LOOP, WAITING FOR CONDITION OR TIME-OUT.
4188 021224 016705 161062    2$:      MOV      MSLCNT,R5      ;LOAD MS LOOP COUNT.
4189 021230 011400          4$:      MOV      (R4),R0        ;GET THE WORD TO TEST.
4190 021232 010067 000042    ;      MOV      R0,62$         ;SAVE WORD IN CASE THIS IS THE LAST.
4191 021236 040200          ;      BIC      R2,R0           ;MASK OUT UNTESTED BITS OF WORD.
4192 021240 020003          ;      CMP      R0,R3           ;COMPARE AGAINST DESIRED STATE WORD.
4193 021242 000261          ;      SEC                     ;SET CARRY IN CASE OF SUCCESS.
4194 021244 001405          ;      BEQ      6$              ;EXIT WITH SUCCESS IF WORDS ARE EQUAL.
4195 021246 005305          ;      DEC      R5              ;COUNT DOWN THE INSIDE MS LOOP COUNT.
4196 021250 001367          ;      BNE     4$              ;LOOP IF MS NOT UP.
4197 021252 005301          ;      DEC      R1              ;DECREMENT THE MS TIME COUNT.
4198 021254 001363          ;      BNE     2$              ;IF TIME NOT UP, LOOP TO COUNT ANOTHER MS.
4199 021256 000241          ;      CLC                     ;CLEAR CARRY, WE TIMED-OUT.
4200
4201          ;+
4202          ; HAVE EITHER FOUND CONDITION, OR TIMED-OUT (POSSIBLY FROM 0 TIME-OUT VALUE).
4203          ; RESTORE THE LAST CONTENTS READ FROM THE TEST WORD.  EXIT ROUTINE.
4204 021260 016700 000014    6$:      MOV      62$,R0        ;PASS OUT THE LAST READ WORD.
4205 021264          60$:     PASS      R0,R1              ;RESTORE GPRS, EXCEPT THE FOLLOWING:
                                MOV      R0,R0SLOT(SP)      ;PUT R0 IN STACK SLOT.
                                MOV      R1,R1SLOT(SP)      ;PUT R1 IN STACK SLOT.
                                JSR      PC,8(SP)+          ;RETURN TO PREGOS SUBRT.
4206          ;R0 - LAST READ WORD CHECKED FOR CONDITION.
4207          ;R1 - REMAINING TIME (0 IF TIME-OUT OCCURED).
4208 021276 000207          ;      RTS      PC              ;CARRY - SET IF SUCCESS, CLEAR IF TIME-OUT.
4209
4210          ;+
4211          ; LOCAL STORAGE.
4212 021300 000000          62$:     .WORD  0              ;STORAGE FOR THE LAST READ WORD.

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4214 .SBTTL GLOBAL SUBROUTINE - MSLOOP -
4215 ;*****
4216 ;* - TEST LOOP SUBROUTINE -
4217 ;* THIS SUBROUTINE IS A GENERAL PURPOSE TEST LOOP SUBROUTINE. IT IS USED
4218 ;* TO VERIFY THAT A CERTAIN ACTION OCCURS BEFORE A TIME-OUT PERIOD. THE
4219 ;* CALLING ROUTINE PASSES IN WHICH BITS SHOULD BE SET AND CLEARED FOR THE
4220 ;* DESIRED CONDITION AND THE TIME-OUT VALUE IN MILLI-SECONDS.
4221 ;* THIS ROUTINE CHECKS FOR THE DESIRED CONDITION UPON ENTRANCE INTO THE
4222 ;* ROUTINE AND THEN ONCE EACH MILLI-SECOND THEREAFTER.
4223 ;*
4224 ;* INPUTS: R1 - TIME-OUT VALUE IN MILLI-SECONDS (UP TO 64K MS).
4225 ;* R2 - BIT MAP OF BITS TO TEST (1 INDICATES TO TEST THE BIT).
4226 ;* R3 - DESIRED STATES OF THE INDICATED FIELDS IN R2.
4227 ;* R4 - ADDRESS OF THE WORD TO TEST.
4228 ;* MSLCNT - MILLI SECOND SOFTWARE LOOP COUNT.
4229 ;*
4230 ;* OUTPUTS: CARRY - SUCCESS FLAG (SET IF CONDITION IS MET BEFORE TIME-OUT).
4231 ;*
4232 ;* CALLING SEQUENCE: JSR PC,MSLOOP
4233 ;*
4234 ;* COMMENTS: THIS ROUTINE WORKS WITH OR WITHOUT A HARDWARE CLOCK, BUT THE
4235 ;* CALIBRATION IS ONLY GUARENTEED WHEN A LINE CLOCK IS AVAILABLE
4236 ;* ON THE SYSTEM.
4237 ;* THIS ROUTINE CAN BE USED AS A DELAY ROUTINE, BY SPECIFYING THE
4238 ;* DESIRED DELAY AS THE TIME-OUT AND SPECIFYING A CONDITION TO
4239 ;* LOOK FOR WHICH WILL NOT BE MET DURING THE DELAY.
4240 ;* IF A TIME-OUT VALUE OF 0 IS SPECIFIED, THIS ROUTINE CHECKS FOR
4241 ;* THE DESIRED CONDITION BEFORE RETURNING. IT INDICATES SUCCESS
4242 ;* IF THE CONDITION IS MET, FAILURE OTHERWISE.
4243 ;*
4244 ;* SUBORDINATE ROUTINES CALLED: MSLGET.
4245 ;*****
4246
4247 021302 MSLOOP:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
021302 004567 164016 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4248
4249 ;*
4250 ;* CALLING THE MSLGET ROUTINE FROM THE MSLOOP ROUTINE ISOLATES THE CALLER OF
4251 ;* MSLOOP FROM THE RETURNED TEST WORD AND REMAINING TIME-OUT VALUES.
4252 ;*
4253 021306 004767 177654 JSR PC,MSLGET ;CALL THE MULTI-PURPOSE MS LOOP AND SEARCH RTN.
4254
4255 021312 004736 604: PASS ;RESTORE GPRS,
021312 000207 JSR PC,B(SP)+ ;RETURN TO PREG05 SUBRT.
4256 021314 000207 RTS PC ;CARRY - SET IF SUCCESS, CLEAR IF TIME-OUT.

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J9

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4258 .SBTTL GLOBAL SUBROUTINE - MSSRPT -
4259 ;* *****
4260 ;* - MODEM STATUS SIGNAL REPORT ROUTINE -
4261 ;* THIS SUBROUTINE IS USED TO REPORT THE STATES OF THE MODEM STATUS
4262 ;* SIGNALS FOR ALL ACTIVE LINES.
4263 ;*
4264 ;* INPUTS: ACTLNS - BIT MAP OF ACTIVE LINES.
4265 ;* CSRA - CONTAINS ADDRESS OF THE DUT CSR.
4266 ;* EF9101 - LABEL AT FORMAT STATEMENT FOR BLANK LINE.
4267 ;* IESTAT - CONTAINS STATES OF THE DUT INTERRUPT ENABLE BITS.
4268 ;* STATA - CONTAINS ADDRESS OF THE DUT STAT REGISTER.
4269 ;* NUMLNS - EQUATED TO THE NUMBER OF LINES ON THE DEVICE.
4270 ;*
4271 ;* OUTPUTS: DUT CSR IND.ADR.REG FIELD - CONTENTS DESTROYED.
4272 ;* REPORT MESSAGES ARE PRINTED ON THE OPERATOR'S CONSOLE.
4273 ;*
4274 ;* CALLING SEQUENCE: JSR PC,MSSRPT
4275 ;*
4276 ;* COMMENTS:
4277 ;*
4278 ;* SUBORDINATE ROUTINES CALLED: NONE.
4279 ;* - *****
4280
4281 021316 MSSRPT:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
021316 004567 164002 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4282
4283 ; PRINT THE BASIC MODEM STATUS MESSAGE.
4284 ; "MODEM STATUS SIGNAL REPORT:"
4285 ; -
4286 021322 PRINTF #MSFMT1
021322 012746 007577 MOV #MSFMT1,-(SP)
021326 012746 000001 MOV #1,-(SP)
021332 010600 MOV SP,R0
021334 104417 TRAP C#PNTF
021336 062706 000004 ADD #4,SP
4287
4288 021342 CLR R1 ;START WITH LINE 0.
4289 021344 MOV #1,R2
4290 021350 MOV CSRA,R3 ;GET THE CSR ADDRESS.
4291 021354 MOV IESTAT,R4 ;GET THE STATES OF THE INTERRUPT ENABLE BITS.
4292 021360 MOV ACTLNS,R5 ;GET THE ACTIVE LINES BIT MAP.
4293
4294 021364 030205 2#: BIT R2,R5 ;TEST LINE BIT IN ACTIVE LINES BIT MAP.
4295 021366 001442 BEQ 4# ;LINE ACTIVE? NO, SKIP REPORT FOR LINE.
4296
4297 021370 MOV R4,R0 ;SET UP DUT CSR IND.ADR.REG FIELD
4298 021372 BIS R1,R0 ; LEAVING THE INTERRUPT ENABLE
4299 021374 MOV R0,(R3) ; BITS IN THE SPECIFIED STATE.
4300 021376 017700 160604 MOV #FSLSA,R0 ;READ THE DUT STATUS REG FOR THIS LINE.
4301 021402 SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
021402 004567 163716 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4302 021406 CLR R2 ;CLEAR THE SIGNAL STATUS INDICATORS.
4303 021410 CLR R3
4304 021412 CLR R4
4305 021414 CLR R5
4306 021416 006300 ASL R0 ;SHIFT DSR INTO CARRY,
4307 021420 006102 ROL R2 ; THEN ROTATE INTO INDICATOR.

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4308 021422 006300          ASL    R0          ;SHIFT BLANK SLOT INTO CARRY,
4309 021424 006300          ASL    R0          ;  SHIFT RI INTO CARRY,
4310 021426 006103          ROL    R3          ;  THEN ROTATE INTO INDICATOR.
4311 021430 006300          ASL    R0          ;SHIFT DCD INTO CARRY,
4312 021432 006104          ROL    R4          ;  THEN ROTATE INTO INDICATOR.
4313 021434 006300          ASL    R0          ;SHIFT CTS INTO CARRY,
4314 021436 006105          ROL    R5          ;  THEN ROTATE INTO INDICATOR.
4315
4316          ;*
4317          ; PRINT THE STATUS FOR THIS LINE.
4318          ; "LINE #N: DSR=N, RI=N, DCD=N, CTS=N"
4319          ;-
          PRINTF  #MSFMT2,R1,R2,R3,R4,R5
          MOV      R5,-(SP)
          MOV      R4,-(SP)
          MOV      R3,-(SP)
          MOV      R2,-(SP)
          MOV      R1,-(SP)
          MOV      #MSFMT2,-(SP)
          MOV      #6,-(SP)
          MOV      SP,R0
          TRAP     C#PNTF
          ADD      #16,SP
4320 021440 010546          PASS
          021442 010446          JSR      ;RESTORE ALL THE GPRS.
          021444 010346          PC,#(SP)+ ;RETURN TO PREG05 SUBRT.
          021446 010246
          021450 010146
          021452 012746 007637
          021456 012746 000006
          021462 010600
          021464 104417
          021466 062706 000016
          4321 021472          PASS
          021474 004736          JSR      ;RESTORE GPRS.
          4322 021474 006302          44:  ASL    R2          ;SHIFT LINE BIT MAP TO NEXT LINE.
          4323 021476 005201          INC    R1          ;INCREMENT THE LINE COUNTER.
          4324 021500 020127 000020          CMP    R1,#NUMLNS ;CMP LINE COUNTER WITH # OF LINES ON DEVICE.
          4325 021504 002727          BLT    24          ;ALL LINES DONE? NO, LOOP TO DO NEXT LINE.
          4326
          4327 021506          PRINTF  #EF9101          ;PRINT A BLANK LINE.
          021506 012746 007262          MOV      #EF9101,-(SP)
          021512 012746 000001          MOV      #1,-(SP)
          021516 010600          MOV      SP,R0
          021520 104417          TRAP     #PNTF
          021522 062706 000004          ADD      #4,SP
          4328
          4329 021526          604:  PASS
          021526 004736          JSR      ;RESTORE GPRS.
          4330 021530 000207          RTS     PC          PC,(SP)+ ;RETURN TO PREG05 SUBRT.

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4332 .SBTTL GLOBAL SUBROUTINE - MUL16U -
4333 ;** *****
4334 ;* - 16 BIT UNSIGNED MULTIPLY ROUTINE -
4335 ;* THIS ROUTINE MULTIPLIES 2 16 BIT UNSIGNED NUMBERS AND RETURNS A 16 BIT
4336 ;* UNSIGNED RESULT. THE MULTIPLICATION IS PERFORMED BY ITERATIVE
4337 ;* ADDITION OF ONE NUMBER TO A SUM WHILE DECREMENTING THE OTHER NUMBER
4338 ;* TO ZERO. IF OVERFLOW OCCURS (177777 TO 0) THE PRODUCT IS INVALID.
4339 ;*
4340 ;* INPUTS: R1 - MULTIPLICAND (16 BIT UNSIGNED).
4341 ;* R2 - MULTIPLIER (16 BIT UNSIGNED).
4342 ;*
4343 ;* OUTPUTS: R1 - PRODUCT (16 BIT UNSIGNED), -1 IF OVERFLOW.
4344 ;* CARRY - SET IF SUCCESS (NO OVERFLOW), CLEAR OTHERWISE.
4345 ;*
4346 ;* CALLING SEQUENCE: JSR PC,MUL16U
4347 ;*
4348 ;* COMMENTS: NOTE: FOR MINIMUM EXECUTION TIME R2 SHOULD CONTAIN THE
4349 ;* SMALLER OF THE 2 ARGUMENTS.
4350 ;*
4351 ;* SUBORDINATE ROUTINES CALLED: NONE.
4352 ;-- *****
4353
4354 021532 004567 163566 MUL16U:: SAVE ;SAVE CONTENTS OF GPRS R0 HRU R5.
4355 021532 005003 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4356 021540 005702 CLR R3 ;CLEAR THE PRODUCT.
4357 021542 001003 TST R2 ;CHECK THE MULTIPLIER.
4358 021544 005001 BNE 2$ ;GO TO DO MULTIPLICATION IF NOT ZERO.
4359 021546 000261 CLR R1 ;RETURN A PRODUCT OF ZERO.
4360 021550 000412 SEC ;INDICATE SUCCESS.
4361 BR 60$ ;EXIT THE ROUTINE.
4362 021552 060103 2$: ADD R1,R3 ;ADD THE MULTIPLICAND TO THE PRODUCT.
4363 021554 103405 BCS 50$ ;EXIT WITH OVERFLOW IF ONE OCCURRED.
4364 021556 005302 DEC R2 ;DECREMENT THE MULTIPLIER.
4365 021560 001374 BNE 2$ ;LOOP IF MULTIPLIER NOT ZERO.
4366 021562 010301 MOV R3,R1 ;PREPARE TO PASS OUT THE PRODUCT.
4367 021564 000261 SEC ;INDICATE SUCCESS.
4368 021566 000403 BR 60$ ;EXIT WITH SUCCESS.
4369
4370 021570 012701 177777 50$: MOV #-1,R1 ;FORCE PRODUCT TO MAX VALUE, WE OVERFLOWED.
4371 021574 000241 CLC ;INDICATE FAILURE.
4372
4373 021576 010166 000004 60$: PASS R1 ;RESTORE GPRS, EXCEPT THE FOLLOWING:
4374 021602 004736 MOV R1,R1$LOT(SP) ;PUT R1 IN STACK SLOT.
4375 021604 000207 JSR PC,0(SP) ;RETURN TO PREG05 SUBRT.
; R1 - PRODUCT (16 BIT UNSIGNED),
; CARRY - SET IF SUCCESS (NO OVERFLOW).

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4377 .SBTTL GLOBAL SUBROUTINE - NEWCHR -
4378 ;* *****
4379 ;* - NEW CHARACTER HANDLING ROUTINE -
4380 ;* THIS SUBROUTINE HANDLES A NEW CHARACTER WHICH HAS BEEN READ FROM
4381 ;* THE DUT. THE COUNTERS AND POINTERS WHICH ARE INVOLVED WITH THE
4382 ;* CHARACTER ARE UPDATED. THE CHARACTER IS CHECKED FOR ERRORS AND
4383 ;* ANY ERRORS WHICH ARE FOUND ARE REPORTED.
4384 ;*
4385 ;* INPUTS: R2 - THE READ CHARACTER INCLUDING ERROR FLAGS AND LINE NUMBER.
4386 ;* R3 - MASK OF THE INACTIVES BITS IN A TX OR RX CHAR BYTE.
4387 ;* ACTLNS - BIT MAP OF ACTIVE DUT LINES.
4388 ;* DPRSQB - LABEL AT DATA PATTERN RESYNC QUEUES TABLE BASE.
4389 ;* TXRXLB - BASE OF TX/RX LINE NUMBER ASSOCIATION TABLE.
4390 ;* BITTBL - TABLE OF WORDS WITH BITS SET FOR USE IN FORMING MAPS.
4391 ;* ERSNRF - "PRINT ERROR SUMMARY FOR LINE" FLAGS.
4392 ;* ERRRTBL - ERROR INFORMATION (ERRNBR, ERRMSG, ERRTP).
4393 ;* ERCNTB - BASE OF THE RX CHARACTER ERROR COUNTERS TABLE.
4394 ;* NDERPT - CONTAINS NUMBER OF CHAR ERRORS TO REPORT ON A LINE.
4395 ;* INPUTS TO SUBROUTINES: CHCNTB, DPENDB, DPLEN, DPRSQE, EXCNTB, RXCNTB,
4396 ;* RXPTRB, ERRNBR, ERRMSG, ERRTP.
4397 ;*
4398 ;* OUTPUTS: ERRBLK - CONTENTS DESTROYED.
4399 ;* FOLLOWING VARIABLES UPDATED FOR LINE ON WHICH CHAR WAS RECEIVED:
4400 ;* DPRSQ - DATA PATTERN RESYNC QUE OF RECEIVED CHARACTERS.
4401 ;* ERCNT - COUNT OF THE NUMBER OF CHARACTER ERRORS ON LINE.
4402 ;* ERSNRF - UPDATED "PRINT ERROR SUMMARY FOR LINE" FLAGS.
4403 ;* EXCNT - COUNT OF THE NUMBER OF EXTRA CHARS RECEIVED ON LINE.
4404 ;* RXCNT - COUNT OF THE NUMBER OF CHARACTERS RECEIVED ON LINE.
4405 ;* RXPTR - UPDATED TO POINT TO THE NEXT EXPECTED CHAR ON LINE.
4406 ;*
4407 ;* CALLING SEQUENCE: JSR PC,NEWCHR
4408 ;*
4409 ;* COMMENTS: THIS ROUTINE CAN REPORT ERRORS WITH NUMBERS INITIAL ERRNBR
4410 ;* AND INITIAL ERRNBR + 1. ERRNBR IS RESTORED TO ITS INITIAL
4411 ;* VALUE BEFORE THIS ROUTINE RETURNS.
4412 ;*
4413 ;* SUBROUTINES CALLED: CKCHR,CKINAC,TXROFF,TXRON.
4414 ;* INDIRECT SUBROUTINES: CHKEXT,CHKLOS,ER9002,ER9003,UPDCHR.
4415 ;* - - - - -
4416
4417 021606 NEWCHR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
4418 021606 004567 163512 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4419 021612 010305 MOV R3,R5 ;GET THE BIT MAP OF INACTIVE DATA BYTE BITS.
4420 021614 052705 BIS #177400,R5 ;ALL UPPER BITS OF EXPECTED DATA ARE INACTIVE.
4421 021620 005067 000270 CLR 70# ;CLEAR THE "ERROR FOUND" FLAG.
4422 ;*
4423 ;* IF THE NEW CHARACTER IS VALID ON AN INACTIVE LINE, GO REPORT ERROR.
4424 ;* ROUTINE USED ALSO EXTRACTS LINE NUMBER FROM THE NEW CHARACTER.
4425 ;*
4426 021624 004767 175412 JSR PC,CKINAC ;CHECK FOR CHAR ON INACTIVE LINE.
4427 021630 103052 BCC 4# ;GO REPORT ERROR IF ON INACTIVE LINE.
4428 ;*
4429 ;* PUSH THE NEW CHARACTER ON THE RESYNC QUE FOR THIS LINE.
4430 ;*
4431 021632 010304 MOV R3,R4 ;CALCULATE BASE ADDRESS OF THE
4432 021634 006304 ASL R4 ; DATA PATTERN RESYNCH QUEUE
4433 021636 006304 ASL R4 ; (QUEUE IS 4 WORDS LONG) FOR

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4433 021640 062704 004642      ADD    #DPRSQB,R4      ; THIS LINE.
4434 021644 010401      MOV     R4,R1      ;GET THE BASE OF THE QUEUE.
4435 021646 016121 000002      MOV     2(R1),(R1)+    ;MOVE FROM CHR1 SLOT TO CHR0 SLOT.
4436 021652 016121 000002      MOV     2(R1),(R1)+    ;MOVE FROM CHR2 SLOT TO CHR1 SLOT.
4437 021656 010211      MOV     R2,(R1)      ;PUT NEW CHAR INTO CHR2 SLOT.
4438
4439      ;+
4440      ; CHECK THE DATA.VALID FOR THE CHARACTER AT THE BOTTON OF THE QUEUE.
4441      ; IF DATA.VALID IS CLEAR, EXIT THE ROUTINE--NOTHING TO ANALYZE.
4442 021660 011402      ; -
4443 021662 100112      MOV     (R4),R2      ;GET CHR0 VALUE, SET FLAGS.
4444      BPL     60$      ;EXIT ROUTINE IF DATA.VALID IS CLEAR.
4445      ;+
4446      ; TEST FOR ANY OF THE ERROR BITS SET IN CHR0.
4447 021664 032702 070000      ; -
4448 021670 001427      BIT     #70000,R2      ;TEST FOR ANY CHR0 ERROR BITS SET.
4449      BEQ     2$      ;SKIP THIS ERROR IF NO ERROR BITS SET.
4450      ;+
4451      ; WE HAVE AT LEAST ONE ERROR FLAG SET ON THE RECEIVED CHAR.
4452      ; REPORT DATA ERROR FLAG ERROR IF NOT IN SUMMARY MODE.
4453 021672 005367 000216      ; -
4454 021676 016300 005234      DEC     70$      ;SET THE "ERROR FOUND" FLAG.
4455 021702 036067 002364 160570      MOV     TXRXLB(R3),R0      ;GET THE TX LINE OFFSET FOR THIS RX LINE.
4456 021710 001017      BIT     BITTBL(R0),ERSMRF    ;CHECK THE ERROR SUMMARY FLAG FOR TX LINE.
4457 021712 012767 014770 163402      BNE     2$      ;IF ERROR SUMMARY FLAG SET, SKIP NEXT REPORT.
4458 021720 004767 004326      MOV     #ER9003,ERRBLK    ;SELECT THE ER9003 ERROR REPORT ROUTINE.
4459 021724      JSR     PC,TXROFF      ;TURN OFF TX AND RX DURING ERROR REPORTING.
4460 021724 104460      ERROR      ;
4461 021726 012767 000001 160270      ;>>>>> ERROR <<<<<.
4462 021734 032767 000100 160220      TRAP     C$ERROR
4463 021742 001462      MOV     #1,FERROR      ;INDICATE AN ERROR HAS BEEN FOUND.
4464 021744 004767 004342      BIT     #BIT06,OPTION    ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
4465      BEQ     60$      ;EXIT IF IT HASN'T.
4466      JSR     PC,TXRON      ;TURN TX AND RX BACK ON.
4467      ;+
4468      ; CHECK THE CHARACTER AT THE BOTTOM OF THE RESYNC QUE FOR DATA ERRORS.
4469 021750 004767 174636      ; -
4470 021754 103433      JSR     PC,CKCHR      ;CHECK THE CHR0 CHAR FOR ERRORS.
4471      BCS     6$      ;SKIP ERROR REPORT IF CHR0 IS CORRECT.
4472      ;+
4473      ; WE HAVE SOME SORT OF DATA ERROR SO REPORT IT (UNLESS IN SUMMARY REPORT MODE).
4474 021756 005367 000132      ; -
4475 021762 016300 005234      4$: DEC     70$      ;SET THE "ERROR FOUND" FLAG.
4476 021766 036067 002364 160504      MOV     TXRXLB(R3),R0      ;GET THE TX LINE OFFSET FOR THIS RX LINE.
4477 021774 001023      BIT     BITTBL(R0),ERSMRF    ;CHECK THE ERROR SUMMARY FLAG FOR THIS LINE.
4478 021776 012767 014612 163316      BNE     6$      ;SKIP ERROR REPORT IF ERROR SUMMARY FLAG SET.
4479 022004 005267 163306      MOV     #ER9002,ERRBLK    ;SELECT THE ER9002 ERROR REPORT ROUTINE.
4480 022010 004767 004236      INC     ERRNBR      ;SELECT INITIAL ERRNBR + 1.
4481 022014      JSR     PC,TXROFF      ;TURN OFF TX AND RX DURING ERROR REPORTING.
4482 022016 012767 000001 160200      ERROR      ;
4483 022024 032767 000100 160130      ;>>>>> ERROR <<<<<.
4484 022032 001426      TRAP     C$ERROR
4485 022034 004767 004252      MOV     #1,FERROR      ;INDICATE AN ERROR HAS BEEN FOUND.
4486 022040 005367 163252      BIT     #BIT06,OPTION    ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
4487      BEQ     60$      ;EXIT IF IT HASN'T.
4488      JSR     PC,TXRON      ;TURN TX AND RX BACK ON.
4489      DEC     ERRNBR      ;RESTORE INITIAL ERRNBR.

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4488      ; COUNT A CHARACTER ERROR IF ONE OCCURRED.
4489      ; UPDATE THE "REPORT ERROR SUMMARY" FLAG FOR LINE BASED ON ERROR COUNT.
4490      ;
4491 022044 005767 000044      60:      TST      700      ;CHECK THE "ERROR FOUND" FLAG.
4492 022050 001417      BEQ      600      ;SKIP COUNTING AN ERROR IF FLAG IS CLEAR.
4493 022052 005263 003302      INC      ERCNTB(P3) ;INCREMENT THE ERROR COUNTER FOR THIS LINE.
4494 022056 001002      BNE      800      ;SKIP SETTING COUNTER TO MAX IF NO OVERFLOW.
4495 022060 005363 003302      DEC      ERCNTB(R3) ;RESET THE ERROR COUNTER TO -1 (MAX VALUE).
4496 022064 005767 160074      80:      TST      NDERPT ;DISABLE ERROR SUMMARY FUNCTION IF
4497 022070 001407      BEQ      600      ; NUMBER OF DATA ERRORS TO REPORT IS 0.
4498 022072 026367 003302 160064      CMP      ERCNTB(R3),NDERPT ;COMPARE ERROR COUNT WITH # OF ERR'S TO RPT.
4499 022100 103403      BLO      600      ;SKIP SETTING OF SUMMARY FLAG IF NOT TOO MANY.
4500 022102 056367 002364 160370      BIS      BITTBL(R3),ERSMRF ;SET "PRINT ERROR SUMMARY" FLAG FOR LINE.
4501
4502 022110      600:      PASS      ;RESTORE GPRS.
      022110 004736      JSR      PC,8(SP). ;RETURN TO PREG05 SUBRT.
4503 022112 000207      RTS      PC
4504
4505 022114 000000      700:      .WORD      0      ;LOCAL STORAGE FOR ERROR OCCURRED FLAG.

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4507 .SBTTL GLOBAL SUBROUTINE - OOPS -
4508 ;... *****
4509 ;* - PROGRAM ABORT SUBROUTINE -
4510 ;* THIS SUBROUTINE IS USED TO ABORT THE PROGRAM WHEN A FATAL ERROR IS
4511 ;* DETECTED IN THE PROGRAM OR THE HOST SYSTEM HARDWARE. AN ERROR MESSAGE
4512 ;* IS PRINTED GIVING SOME INFORMATION ABOUT THE NATURE OF THE ABORT.
4513 ;*
4514 ;* INPUTS: R1 - ERROR CODE GIVING REASON FOR ABORT.
4515 ;*
4516 ;* OUTPUTS: AN ERROR MESSAGE IS PRINTED.
4517 ;* A LIST OF RETURN PC VALUES FOR ALL SUBROUTINE CALLS IS PRINTED.
4518 ;*
4519 ;* CALLING SEQUENCE: JSR PC,OOPS
4520 ;*
4521 ;* COMMENTS:
4522 ;*
4523 ;* SUPERORDINATE ROUTINES CALLED: NONE.
4524 ;-- *****
4525
4526 022116 004567 163202 OOPS:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
4527 ; REPORT "HOST COMPUTER HARDWARE OR SOFTWARE BUG ENCOUNTERED." ERROR.
4528 022122 104454 ;CALL REGISTER SAVE SUBRT.
4529 022124 000145 TRAP C$ERSF
4530 022126 022162 .WORD 101
4531 022130 000000 .WORD EM0101
4532 ; REPORT "PROGRAM HUNG, WAITING FOR A CONTROL-C."
4533 022132 012746 022246 PRINTF @EM0102
4534 022136 012746 000001 MOV @EM0102,-(SP)
4535 022142 010600 MOV #1,-(SP)
4536 022144 104417 MOV SP,R0
4537 022146 062706 000004 TRAP C$PNTF
4538 2$: BREAK ;LOOK FOR OPERATOR CONTROL C INPUT.
4539 022152 104422 TRAP C$BRK
4540 022154 000776 ;INFINITE LOOP.
4541 022156 004736 ;DON'T NEED THIS, BUT SOMEBODY MAY CHANGE THIS
4542 022160 000207 JSR PC,@(SP); RETURN TO PREG05 SUBRT.
4543 ; ROUTINE IN THE FUTURE, SO BE CONSISTANT.
4544
4545 EM0101:: .ASCIZ /HOST COMPUTER HARDWARE OR SOFTWARE BUG ENCOUNTERED./
4546
4547 022162 110 117 123
4548 022165 124 040 103
4549 022170 117 115 120
4550 022173 125 124 105
4551 022176 122 040 110
4552 022201 101 122 104
4553 022204 127 101 122
4554 022207 105 040 117
4555 022212 122 040 123
4556 022215 117 106 124
4557 022220 127 101 122
4558 022223 105 040 102
4559 022226 125 107 040
4560 022231 105 116 103
4561 022234 117 125 116
4562 022237 124 105 122

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	022242	105	104	056	
	022245	000			
4537	022246	045	116	045	EM0102:: .ASCIZ /ENAPROGRAM HUNG, WAITING FOR A CONTROL-C. <*****NNN/
	022251	101	120	122	
	022254	117	107	122	
	022257	101	115	040	
	022262	110	125	116	
	022265	107	054	040	
	022270	127	101	111	
	022273	124	111	116	
	022276	107	040	106	
	022301	117	122	040	
	022304	101	040	103	
	022307	117	116	124	
	022312	122	117	114	
	022315	055	103	056	
	022320	040	074	052	
	022323	052	052	052	
	022326	052	052	052	
	022331	052	052	052	
	022334	052	052	052	
	022337	045	116	045	
4538	022342	116	000		

.EVEN

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4540 .SBTTL GLOBAL SUBROUTINE - PRFRME -
4541 ;* *****
4542 ;* - PROCESS FRAMING ERRORS -
4543 ;* THIS SUBROUTINE IS USED IN THE FRAMING ERROR BIT TEST, TO VERIFY THAT
4544 ;* ALL RECEIVED CHARACTERS HAVE THEIR FRAMING ERROR BIT SET AND PARITY
4545 ;* ERROR BIT CLEAR.
4546 ;*
4547 ;* INPUTS: R2 - CONTAINS THE CHARACTER READ FROM THE FIFO.
4548 ;* ERRNBR - ERROR NUMBER OF ERRORS IN THIS ROUTINE.
4549 ;* ERSMBF - "REPORT ERROR SUMMARY "LINE" FLAGS
4550 ;*
4551 ;* OUTPUTS: ERBLK - THE CONTENTS OF THIS WORD ARE DESTROYED.
4552 ;* ERCNTB - THE ERROR COUNT FOR THIS LINE IS UPDATED.
4553 ;* MESSAGES MAY BE PRINTED AT THE OPERATORS CONSOLE.
4554 ;*
4555 ;*
4556 ;* CALLING SEQUENCE: JSR PC,PRFRME
4557 ;*
4558 ;* COMMENTS: THIS ROUTINE REPORTS ERRORS WITH INITIAL NUMBER.
4559 ;* ERRNBR IS RESTORED TO ITS INITIAL VALUE BEFORE THIS SUBROUTINE
4560 ;* RETURNS.
4561 ;*
4562 ;* SUBORDINATE ROUTINES CALLED: ER6201.
4563 ;* - *****
4564
4565 022344 PRFRME::SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
4566 022344 004567 162754 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4567 022350 016704 162742 MOV ERRNBR,R4 ;SAVE THE CONTENTS OF THE INITIAL ERROR NUMBER.
4568 022354 005005 CLR R5 ;CLEAR ERROR/MESSAGE FLAGS.
4569
4570 ;*
4571 ;* TEST FRAMING AND PARITY ERROR BITS IN TURN. REPORT ANY ERRORS FOUND, IE.
4572 ;* FRAMING ERROR BIT CLEAR, OR PARITY ERROR BIT SET.
4573 022356 012767 014254 162736 MOV #ER6201,ERBLK ;SET UP THE ADDRESS OF THE ERROR ROUTINE.
4574 022364 032702 020000 BIT #BIT13,R2 ;CHECK ON STATE OF THE FRAMING ERROR BIT.
4575 022370 001002 BNE 61 ;BRANCH IF FRAMING ERROR BIT SET.
4576 022372 052705 000002 BIS #BIT1,R5 ;SET REPORT FRAMING ERROR FLAG.
4577
4578 022376 032702 010000 61: BIT #BIT12,R2 ;CHECK ON THE STATE OF THE PARITY ERROR BIT.
4579 022402 001402 BEQ 81 ;BRANCH IF PARITY ERROR BIT CLEAR.
4580 022404 052705 000014 BIS #14,R5 ;SET REPORT "PARITY ERROR SET" FLAGS.
4581 022410 005705 81: TST R5 ;CHECK IF ANY ERROR FLAGS SET.
4582 022412 001412 BEQ 601 ;EXIT IF ALL FLAGS CLEAR.
4583 022414 036367 002364 160056 BIT BITTBL(R3),ERSMBF ;CHECK THE ERROR SUMMARY FLAG FOR THIS LINE.
4584 022422 001004 BNE 101 ;SKIP ERROR REPORT IF ERROR SUMMARY FLAG SET.
4585
4586 ;REPORT ERROR "CHARACTER RECEIVED WITH PARITY/FRAMING ERROR BIT SET".
4587 022424 ERROR ;
4588 022424 104460 >>>> ERROR <<<<. TRAP C$ERROR
4589 022426 012767 000001 157570 MOV #1,FERROR ;INDICATE AN ERROR HAS BEEN DETECTED.
4590
4591 022434 005263 003302 101: INC ERCNTB(R3) ;INCREMENT ERROR COUNT FOR THIS LINE.
4592 022440 010467 162652 601: MOV R4,ERRNBR ;RESTORE ERROR NUMBER.
4593 022444 004736 PASS ;RESTORE GPRS.
4594 JSR PC,B(SP)+ ;RETURN TO PREG05 SUBRT.

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GLOBAL SUBROUTINE PRFRME -

F10

SEQ 122

4594 022446 000207

RTS PC

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4596 .SBTTL GLOBAL SUBROUTINE - PRPARE -
4597 ;* *****
4598 ;* - PROCESS PARITY ERRORS -
4599 ;* THIS SUBROUTINE IS USED IN THE PARITY ERROR TEST, TO VERIFY THAT
4600 ;* ALL RECEIVED CHARACTERS HAVE THEIR PARITY ERROR BIT SET AND FRAMMING
4601 ;* ERROR BIT CLEAR.
4602 ;*
4603 ;* INPUTS: R2 - CONTAINS THE CHARACTER READ FROM THE FIFO.
4604 ;* R3 - CONTAINS 2 * LINE NUMBER OF THE READ CHAR.
4605 ;* ERRNBR - ERROR NUMBER OF ERRORS IN THIS ROUTINE.
4606 ;* ERSRFR - "REPORT ERROR SUMMARY FOR LINE" FLAGS.
4607 ;* ERROR - "AT LEAST ONE ERROR FOUND" FLAG.
4608 ;*
4609 ;* OUTPUTS: ERRBLK - THE CONTENTS OF THIS WORD ARE DESTROYED.
4610 ;* ERCNTB - THE ERROR COUNT FOR THIS LINE IS UPDATED.
4611 ;* MESSAGES MAY BE PRINTED AT THE OPERATORS CONSOLE.
4612 ;*
4613 ;*
4614 ;* CALLING SEQUENCE: JSR PC,PRPARE
4615 ;*
4616 ;* COMMENTS: THIS ROUTINE REPORTS ERRORS WITH INITIAL ERRNBR THRU ERRNBR+1.
4617 ;* ERRNBR IS RESTORED TO ITS INITIAL VALUE BEFORE THIS SUBROUTINE
4618 ;* RETURNS.
4619 ;* THE CONTENTS OF THE ERRBLK ARE DESTROYED.
4620 ;*
4621 ;* SUBORDINATE ROUTINES CALLED: ER9002,ER6201.
4622 ;* *****
4623
4624 022450 PRPARE::SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
4625 022450 004567 162650 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4626 022454 016746 162636 MOV ERRNBR,-(SP) ;SAVE THE CONTENTS OF THE INITIAL ERROR NUMBER.
4627 022460 005005 CLR R5 ;CLEAR ERROR/MESSAGE FLAGS.
4628
4629 ;*
4630 ;* TEST FRAMMING AND PARITY ERROR BITS IN TURN. REPORT ANY ERRORS FOUND, IE.
4631 ;* PARITY ERROR BIT CLEAR, OR FRAMMING ERROR BIT SET.
4632 022462 012767 014254 162632 MOV #ER6201,ERRBLK ;SET UP THE ADDRESS OF THE ERROR ROUTINE.
4633 022470 032702 010000 BIT #BIT12,R2 ;CHECK ON STATE OF THE PARITY ERROR BIT.
4634 022474 001002 BNE 6# ;BRANCH IF PARITY ERROR BIT SET.
4635 022476 052705 000010 BIS #BIT3,R5 ;SET REPORT PARITY ERROR FLAG.
4636 022502 032702 020000 6#: BIT #BIT13,R2 ;CHECK ON THE STATE OF THE FRAMMING ERROR BIT.
4637 022506 001402 BEQ 8# ;BRANCH IF FRAMMING ERROR BIT CLEAR.
4638 022510 052705 000003 BIS #3,R5 ;SET REPORT "FRAMMING ERROR SET" FLAGS.
4639 022514 005705 8#: TST R5 ;CHECK IF ANY ERROR FLAGS SET.
4640 022516 001414 BEQ 12# ;BRANCH TO MAKE DATA CHECK IF ALL FLAGS CLEAR.
4641 022520 036367 002364 157752 BIT BITTBL(R3),ERSRFR ;CHECK THE ERROR SUMMARY FLAG FOR THIS LINE.
4642 022526 0010J5 BNE 14# ;SKIP ALL ERROR REP IF IN ERROR SUMMARY MODE.
4643 ;REPORT ERROR "CHAR RECEIVED WITH PARITY/FRAMMING ERROR BIT SET/CLEAR".
4644 022530 104460 .ERROR ;
4645 022530 104460 TRAP C#ERROR
4646 022532 012767 000001 157464 MOV #1,FERROR ;INDICATE AN ERROR HAS BEEN FOUND.
4647 022540 032767 000100 157414 BIT #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
4648 022546 001440 BEQ 18# ;EXIT IF IT HASN'T.
4649
4650

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4651                                     ; COMPARE ACTUAL DATA WITH EXPECTED DATA TO CHECK FOR MULTIPLE ERRORS.
4652                                     ;
4653 022550 005267 162542                12$: INC   ERRNBR           ; INCREMENT ERROR NUMBER.
4654 022554 016304 003402                MOV   RXPTRB(R3),R4       ; GET THE POINTER TO THE EXPECTED DATA.
4655 022560 111404                      MOVB   (R4),R4             ; GET THE EXPECTED DATA.
4656 022562 042704 177400                BIC    #177400,R4         ; CLEAR THE UPPER BYTE.
4657 022566 120204                      CMPB   R2,R4               ; COMPARE ACTUAL AND EXPECTED DATA.
4658 022570 001427                      BEQ     18$                 ; SKIP ERROR REPORT IF DATA CORRECT.
4659 022572 042704 100000                BIC    #BIT15,R4          ; CLEAR "NONE" EXPECTED MESSAGE FLAG.
4660 022576 036367 002364 157674        BIT    BITTBL(R3),ERSMRF   ; CHECK THE ERROR SUMMARY FLAG FOR THIS LINE.
4661 022604 001017                      BNE     16$                 ; SKIP ERROR REPORT IF ERROR SUMMARY FLAG SET.
4662 022606 036367 002364 157670        BIT    BITTBL(R3),RXDNF     ; CHECK FOR RECEPTION COMPLETE ON THIS LINE.
4663 022614 001402                      BEQ     14$                 ; SKIP SETTING NONE EXPECTED FLAG.
4664 022616 052704 100000                BIS    #BIT15,R4          ; SET "NONE" EXPECTED MESSAGE FLAG.
4665 022622 012701 011434                14$: MOV    #EM9008,R1      ; SELECT ERROR MESSAGE TO BE REPORTED.
4666 022626 012767 014612 162466        MOV    #ER9002,ERRBLK     ; SELECT ERROR REPORTING ROUTINE.
4667                                     ; REPORT ERROR "RECEIVE CHARACTER MISCOMPARE"
4668 022634                                ERROR
4669 022636 012767 000001 157360        MOV    #1,FERROR          ; INDICATE AN ERROR HAS BEEN FOUND. TRAP C$ERROR
4670
4671
4672 022644 005263 003302                16$: INC   ERCNTB(R3)       ; INCREMENT ERROR COUNT FOR THIS LINE.
4673 022650 012667 162442                18$: MOV   (SP)+,ERRNBR    ; RESTORE ERROR NUMBER.
4674
4675 022654                                60$: PASS
4676 022656 004736 000207                JSR     PC,8(SP)+          ; RESTORE GPRS.
4676 022656 000207                RTS     PC                        ; RETURN TO PREG05 SUBRT.

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4678 .SBTTL GLOBAL SUBROUTINE - PRTLPR -
4679 ;* *****
4680 ;* -PRINT THE CONTENTS OF THE LPR.
4681 ;* THIS ROUTINE IS USED TO PRINT OUT EXTENDED INFORMATION ON THE
4682 ;* CONTENTS OF THE LINE PARAMETER REGISTER (LPR).
4683 ;*
4684 ;* INPUTS: R3 - CONTAINS THE NUMBER OF THE LINE YOU WISH TO EXAMINE.
4685 ;* CSRA - CONTAINS THE ADDRESS OF THE DUT'S CSR.
4686 ;* IESTAT - CONTAINS THE CURRENT STATUS OF THE TX AND RX INTERRUPT
4687 ;* ENABLE BITS IN THE DUT'S CSR.
4688 ;* LPRA - CONTAINS THE ADDRESS OF THE DUT'S LPR REGISTER.
4689 ;*
4690 ;* OUTPUTS: AN EXTENDED INFORMATION MESSAGE IS PRINTED ON THE OPERATORS
4691 ;* CONSOLE.
4692 ;*
4693 ;* CALLING SEQUENCE: JSR PC,PRTLPR
4694 ;*
4695 ;* COMMENTS: THIS ROUTINE CHANGES THE INDIRECT ADDRESS FIELD OF THE DEVICE
4696 ;* UNDER TEST'S CSR.
4697 ;*
4698 ;* SUBORDINATE ROUTINES CALLED: NONE.
4699 ;* - *****
4700
4701 022660 PRTLPR::SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
4702 022660 004567 162440 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4703 022664 016701 157310 MOV CSP,R1 ;GET THE CSR ADDRESS.
4704 022670 016702 157310 MOV LPRA,R2 ;GET THE LPR ADDRESS.
4705 022674 042703 177760 BIC #177760,R3 ;CLEAR ANY UNWANTED BITS.
4706 022700 056703 157330 BIS IESTAT,R3 ;SET STATE OF TX AND RX INTERRUPT ENABLE BITS.
4707 022704 010311 MOV R3,(R1) ;SELECT LINE.
4708 022706 011204 MOV (R2),R4 ;GET CONTENTS OF THE LPR.
4709 022710 ;PRINT MESSAGE"CONTENTS OF THE LPR:NNNNNN"
022710 010446 MOV R4,-(SP)
022712 012746 012140 MOV #EM9026,-(SP)
022716 012746 007162 MOV #EF9019,-(SP)
022722 012746 000003 MOV #3,-(SP)
022726 010600 MOV SP,R0
022730 104415 TRAP C#PNTX
022732 062706 000010 ADD #10,SP
4710 022736 601: PASS ;RESTORE GPRS.
022736 004736 JSR PC,8(SP)+ ;RETURN TO PREG05 SUBRT.
4711 022740 000207 RTS PC

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4713 .SBTTL GLOBAL SUBROUTINE - PUFIFO -
4714 ;*****
4715 ;* - PURGE THE FIFO
4716 ;* THIS ROUTINE TRIES TO REMOVE ALL THE CHARACTERS FROM THE FIFO.
4717 ;* ANY BMP CODES THAT ARE FOUND ARE SAVED ON THE BMP CODE QUEUE.
4718 ;*
4719 ;* INPUTS: RBUFA- CONTAINS THE ADDRESS OF THE RECEIVER.
4720 ;*
4721 ;*
4722 ;* OUTPUTS: CARRY BIT - INDICATES THE STATE OF THE FIFO, SET: = PURGED.
4723 ;* BMPCQ - THE CONTENTS OF THE BMP CODE QUEUE MAY BE UPDATED.
4724 ;*
4725 ;* CALLING SEQUENCE: JSR PC,PUFIFO
4726 ;*
4727 ;* COMMENTS:
4728 ;*
4729 ;* SUBORDINATE ROUTINES CALLED: SAVBMP.
4730 ;*****
4731
4732 022742 PUFIFO:SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
022742 004567 162356 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4733 022746 012701 001000 MOV #512.,R1 ;SET MAXIMUM TRY COUNT OF 512.
4734 022752 016704 157224 MOV RBUFA,R4 ;GET ADDRESS OF THE RECEIVER BUFFER REGISTER.
4735
4736 022756 011402 2$: MOV (R4),R2 ;GET THE CONTENTS OF THE RECEIVER BUFFER REG.
4737 022760 100016 BPL 6$ ;EXIT IF THE FIFO IS EMPTY, DATA_VALID CLR.
4738
4739 ;*
4740 ; CHECK IF THE READ CHARACTER IS ACTUALLY A BMP CODE.
4741 ; IF IT IS, THEN SAVE IT ON THE BMP CODE QUEUE TO BE REPORTED LATER.
4742 022762 012700 070000 ;*
4743 022766 040200 MOV #70000,R0 ;GENERATE A BIT MAP OF CHAR ERROR BITS
4744 022770 001006 BIC R2,R0 ; WHICH ARE NOT SET FOR CHAR.
4745 ;*
4746 ;*
4747 ; CHECK IF THE READ DATA IS MODEM STATUS , BMP OR SELFTEST?.
4748 022772 012700 000301 ;*
4749 022776 040200 MOV #301,R0 ; CHECK IF BMP.
4750 023000 001002 BIC R2,R0 ;TRY TO CLEAR BMP FLAGS IN THE READ DATA.
4751 023002 004767 001644 BNE 4$ ;IF IT IS MODEM OR SELFTEST CODE THROW IT AWAY.
4752 JSR PC,SAVBMP ;SAVE BMP CODE ON THE QUEUE.
4753 023006 005301 4$: DEC R1 ;DECREMENT THE TRY COUNT.
4754 023010 001362 BNE 2$ ;LOOP TO TRY AGAIN.
4755 023012 000241 CLC ;CLEAR CARRY, TO INDICATE FIFO NOT PURGED.
4756 023014 000401 BR 60$ ;EXIT WITH CARRY CLEAR.
4757 023016 000261 6$: SEC ;SET CARRY, TO INDICATE FIFO PURGED.
4758
4759 023020 023020 004736 60$: PASS ;RESTORE GPRS.
023020 004736 JSR PC,B(SP)+ ;RETURN TO PREG05 SUBRT.
4760 ;CARRY BIT, SET INDICATES FIFO PURGED.
4761 023022 000207 RTS PC

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4763 .SBTTL GLOBAL SUBROUTINE - PUFIFR -
4764 ;*****
4765 ;* - PURGE FIFO REPORT ANY ERRORS FOUND.
4766 ;* THIS ROUTINE REMOVES ALL DATA FROM THE FIFO. ANY BMP CODES THAT ARE
4767 ;* FOUND ARE SAVE ON THE QUEUE TO BE REPORTED LATER IN THE BMP REPORT TEST.
4768 ;* ANY UNEXPECTED DATA (IE ANY NON-STATUS INFORMATION) THAT ARE FOUND,
4769 ;* ARE REPORTED AS AN ERROR.
4770 ;* IF THE FIFO WILL NOT PURGE AFTER 512 ATTEMPTS, THEN THE CURRENT TEST
4771 ;* THAT CALLED THIS ROUTINE RECEIVES A FAILURE FLAG THAT SHOULD BE USED
4772 ;* TO ABORT THE TEST.
4773 ;*
4774 ;* INPUTS: ERRIBL - ERRTYPE, ERRMSG, ERRNBR ARE SET UP CORRECTLY.
4775 ;* RBUFA- CONTAINS THE ADDRESS OF THE RECEIVER.
4776 ;*
4777 ;* OUTPUTS: CARRY BIT - ABORT TEST FLAG, CLR = ABORT TEST, SET = OK.
4778 ;* ERRLK - VALUE WILL BE DESTROYED.
4779 ;* BMPQIP - THE BMP CODE QUEUE POINTER MAY BE UPDATED.
4780 ;* THE CONTENTS OF THE BMP CODE QUEUE MAY BE UPDATED.
4781 ;*
4782 ;* CALLING SEQUENCE: JSR PC,PUFIFR
4783 ;*
4784 ;* COMMENTS: THIS ROUTINE REPORTS ERRORS WITH NUMBERS INITIAL ERRNBR
4785 ;* THRU TO ERRNBR+2.
4786 ;* THE ERRNBR IS RESTORED TO ITS INITIAL VALUE BEFORE RETURNING.
4787 ;*
4788 ;* SUBORDINATE ROUTINES CALLED: ER1603,ER9001,ER9002,SAVBMP.
4789 ;*****
4790
4791 023024 PUFIFR::SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
4792 023024 004567 162274 ;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4793 023030 016746 162262 ;MOV ERRNBR, -(SP) ;SAVE THE CONTENTS OF THE ERROR NUMBER.
4794 023034 012705 001000 ;MOV #512, R5 ;SET MAXIMUM READ COUNTER TO 2*FIFO SIZE.
4795 ;*
4796 ; READ DATA FROM THE FIFO UNTIL DATA VALID IS CLEAR OF READ COUNTER IS ZERO.
4797 ; REPORT ANY BMP OR UNEXPECTED DATA AS ERRORS.
4798 023040 017702 157136 ;*
4799 023044 100063 2# : MOV BRBUFA, R2 ;GET THE CONTENTS OF THE RECEIVER BUFFER REG.
4800 ; BPL 8# ;EXIT IF DATA VALID CLEAR, IE. FIFO PURGED.
4801 ;*
4802 ; CHECK IF READ DATA IS STATUS OR UNEXPECTED CHARACTER.
4803 023046 012700 070000 ;*
4804 023052 040200 ; MOV #70000, R0 ;GENERATE A BIT MAP OF CHAR ERROR BITS
4805 023054 001012 ; BIC R2, R0 ; WHICH ARE NOT SET FOR CHAR.
4806 ; BNE 4# ;SKIP BMP CHECK IF IT IS UNEXPECTED DATA.
4807 ;*
4808 ; CHECK IF THE READ DATA IS MODEM STATUS, BMP OR SELFTEST?.
4809 ; IF IT IS A BMP CODE THEN SAVE IT ON THE QUEUE.
4810 023056 012767 014512 162236 ;*
4811 023064 012700 000300 ; MOV #ER9001, ERRLK ;SET UP THE CORRECT ERROR REPORTING ROUTINE.
4812 023070 040200 ; MOV #300, R0 ; CHECK IF BMP OR SELFTEST?.
4813 023072 001003 ; BIC R2, R0 ;TRY TO CLEAR BMP FLAGS IN THE READ DATA.
4814 023074 004767 001552 ; BNE 4# ;SKIP BMP ERROR REPORT IF MODEM OR SELFTEST?.
4815 023100 000430 ; JSR PC, SAVBMP ;SAVE THE BMP CODE ON THE QUEUE.
4816 ; BR 6# ;BRANCH TO CHECK READ COUNT.
4817 ;*
4818 ; CHECK IF THE READ DATA IS MODEM, SELFTEST OR UNEXPECTED DATA.

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4819 023102 032702 000001      4$:   BIT    #BIT0,R2      ;TEST THE MODEM STATUS INDICATION BIT.
4820 023106 001425              BEQ     6$              ;DO NOT REPORT ANY ERROR IF MODEM STATUS.
4821 023110 012701 012531      MOV     #EM9104,R1      ;PASS THE CORRECT ERROR MESSAGE TO REPORT.
4822 023114 010203              MOV     R2,R3          ;EXTRACT THE LINE NUMBER FROM
4823 023116 000303              SWAB    R3              ; THE READ DATA.
4824 023120 042703 177760      BIC     #177760,R3      ;
4825 023124 006303              ASL     R3              ;FORM LINE NUMBER TIMES 2 FOR ER9002 ROUTINE.
4826 023126 052704 100000      BIS     #BIT15,R4      ;SET THE "NONE" EXPECTED MESSAGE FLAG.
4827 023132 005267 162160      INC     ERNBR          ;SET ERROR NUMBER TO INITIAL ERNBR+1.
4828 023136 012767 014612 162156 MOV     #ER9002,ERRBLK ;SELECT THE CORRECT ERROR REPORTING ROUTINE.
4829                                ;REPORT ERROR "UNEXPECTED DATA FOUND IN FIFO".
4830 023144                                ;ERROR                                ;>>>>> ERROR <<<<<.
                                ;                                TRAP      C$ERROR
4831                                ;+
4832                                ;EXIT WITH FAILURE IF EXTENDED ERROR REPORTING HAS NOT BEEN ENABLED
4833                                ;-
4834 023146 032767 000100 157006      BIT     #BIT06,OPTION ;EXIT WITH TEST FAILURE MESSAGE IF
4835 023154 001415              BEQ     7$              ;NO EXTENDED ERROR REPORTING HAS BEEN REQUESTED
4836                                ;DURING THE SOFTWARE QUESTIONS.
4837                                ;
4838 023156 005367 162134      DEC     ERNBR          ;RESTORE ERROR NUMBER TO INITIAL ERNBR.
4839                                ;
4840 023162 005305              6$:   DEC     R5              ;DECREMENT READ COUNTER.
4841 023164 001325              BNE     2$              ;LOOP TO READ NEXT CHAR FROM FIFO IF COUNT > 0.
4842                                ;+
4843                                ;THE FIFO WILL NOT CLEAR, REPORT THE ERROR AND INDICATE THAT THE TEST IS TO
4844                                ;BE ABORTED.
4845                                ;-
4846 023166 062767 000002 162122      ADD     #2,ERNBR      ;SET ERROR NUMBER TO INITIAL ERNBR+2.
4847 023174 012767 014162 162120      MOV     #ER1603,ERRBLK ;SELECT THE CORRECT ERROR REPORTING ROUTINE.
4848 023202 012701 011733      MOV     #EM9017,R1      ;PASS THE MESSAGE TO BE REPORTED.
4849                                ;REPORT THE ERROR "FIFO WILL NOT PURGE, (DATA VALID STUCK SET)"
4850                                ;"?????? TEST ABORTED".
4851 023206                                ;ERROR                                ;>>>>> ERROR <<<<<.
                                ;                                TRAP      C$ERROR
4852 023206 104460              7$:   CLC              ;INDICATE THE TEST IS TO BE ABORTED.
4853 023210 000241              BR      10$             ;EXIT THIS ROUTINE AND ABORT THE CURRENT TEST.
4854 023212 000401              8$:   SEC              ;SET THE CARRY, DO NOT ABORT THE TEST.
4855 023214 000261              10$:  MOV     (SP)+,ERNBR ;RESTORE INITIAL ERROR NUMBER.
4856 023216 012667 162074      60$:  PASS            ;RESTORE GPRS.
4857 023222 004736              JSR     PC,#(SP)+      ;RETURN TO PREG05 SUBRT.
4858 023222 004736              ;CARRY BIT, SET INDICATES FIFO PURGED, DO NOT
4859                                ;ABORT THE TEST.
4860                                ;
4861 023224 000207              RTS     PC

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4863 .SBTTL GLOBAL SUBROUTINE - PURRXB -
4864 ;** *****
4865 ;* - PURGE THE RX BUFFER IN MEMORY ROUTINE -
4866 ;* THIS SUBROUTINE IS USED BEFORE THE BEGINNING OF A TX/RX OF DATA
4867 ;* PATTERNS TO CLEAR OUT THE RX BUFFER AND TO INITIALIZE THE VARIOUS
4868 ;* COUNTERS AND POINTERS RELATED TO THAT BUFFER.
4869 ;*
4870 ;* INPUTS: RXBSTA - LABEL AT THE BEGINNING OF THE RX BUFFER.
4871 ;*
4872 ;* OUTPUTS: RXBCNT - COUNT OF # OF CHARS IN RX BUFFER (CLEARED).
4873 ;* RXBIPT - INPUT POINTER TO RX BUFFER (INITIALIZED).
4874 ;* RXBOPT - OUTPUT POINTER TO RX BUFFER (INITIALIZED).
4875 ;* THE CONTENTS OF THE RX BUFFER ARE CLEARED.
4876 ;*
4877 ;* CALLING SEQUENCE: JSR PC,PURRXB
4878 ;*
4879 ;* COMMENTS:
4880 ;*
4881 ;* SUBORDINATE ROUTINES CALLED: NONE.
4882 ;-- *****
4883
4884 023226 PURRXB:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
023226 004567 162072 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4885
4886 023232 MOV #RXBOPT,R1 ;GET THE ADDRESS OF THE RX OUTPUT POINTER.
4887 023236 MOV #RXBSTA,(R1)+ ;INITIALIZE THE RX BUFFER OUTPUT POINTER.
4888 023242 MOV #RXBSTA,(R1)+ ;INITIALIZE THE RX BUFFER INPUT POINTER.
4889 023246 005021 2$: CLR (R1)+ ;CLEAR CHAR COUNT AND THE BUFFER AREA.
4890 023250 020127 003120 CMP R1,#RXBEND ;CHECK IF LAST LOCATION HAS BEEN CLEARED.
4891 023254 101774 BLOS 2$ ;LOOP IF NOT DONE.
4892
4893 023256 60$: PASS ;RESTORE GPRS.
023256 004736 JSR PC,B(SP)+ ;RETURN TO PREG05 SUBRT.
4894 023260 000207 RTS PC

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4896 .SBTTL GLOBAL SUBROUTINE - RDCMRS -
4897 ;* *****
4898 ;* - READ AND COMPARE INPUT CHARACTERS ROUTINE -
4899 ;* THIS SUBROUTINE READS THE CHARACTERS FROM THE RX BUFFER IN MEMORY.
4900 ;* IF CHARACTERS STOP APPEARING IN THE BUFFER WITH DATA.VALID SET
4901 ;* OR IF MORE THAN THE ALLOWABLE NUMBER OF CHARACTERS HAS BEEN READ FROM
4902 ;* THE BUFFER THIS ROUTINE EXITS WITH AN RX COMPLETE INDICATION.
4903 ;* EACH READ CHAR IS ANALYZED AND ANY NECESSARY ERRORS ARE REPORTED.
4904 ;*
4905 ;* INPUTS: ACTLNS - BIT MAP OF THE ACTIVE DUT LINES.
4906 ;* ERRNBR - SET TO ERROR NUMBER OF FIRST ERROR IN THIS ROUTINE.
4907 ;* IBM - MASK OF THE INACTIVE BITS IN A TX OR RX CHAR BYTE.
4908 ;* OSTEND - ADDRESS OF THE END OF THE OUTPUT STORAGE FIFO BUFFER.
4909 ;* OSTPTR - POINTER TO THE NEXT BYTE TO READ FROM OSTORE.
4910 ;* RXBOPT - POINTER INTO THE RX CHAR BUFFER IN MEMORY.
4911 ;* RXTOUT - TIME-OUT VALUE FOR RX OF LAST CHAR.
4912 ;*
4913 ;* OUTPUTS: ERROR MESSAGES MAY BE PRINTED AT THE OPERATOR'S CONSOLE.
4914 ;* TXDBLF - TX/RX DISABLED FLAG (CLEARED).
4915 ;* TXENBM - TX.ENABLE STATE MASK (DESTROYED).
4916 ;* SAVPRI - STORAGE FOR PROCESSOR PRIORITY (DESTROYED).
4917 ;* SAVTEN - STORAGE FOR TX.ENABLE STATES (DESTROYED).
4918 ;*
4919 ;* CALLING SEQUENCE: JSR PC,RDCMRS
4920 ;*
4921 ;* COMMENTS: THIS ROUTINE REPORTS ERRORS WITH NUMBERS INITIAL ERRNBR
4922 ;* THRU INITIAL ERRNBR + 4.
4923 ;* ERRNBR IS RESTORED BEFORE THIS ROUTINE RETURNS.
4924 ;*
4925 ;* SUBROUTINES CALLED: CKCHR,NEWCHR,REPCOD,RXIE0,RXIE1,TXENBL,TXIE0,TXIE1,
4926 ;* WAIBIS.
4927 ;*
4928 ;* *****
4929 023262 RDCMRS:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
4930 023262 004567 162036 ;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
4931 023266 016704 162024 ;PRESERVE THE INITIAL ERROR NUMBER.
4932 023276 005067 157204 ;GET THE INACTIVE BIT MASK.
4933 023302 004767 001320 ;CLEAR THE TX DISABLED FLAG.
4934 023306 004767 002440 ;TURN ON DUT RECEPTION INTERRUPTS.
4935 ;TURN ON DUT TRANSMISSION INTERRUPTS.
4936 ;
4937 ;* CLEAR ALL RESYNC QUEUES FOR ALL LINES.
4938 ;*
4939 023312 012701 004642 ;GET BASE ADDRESS OF RESYNC QUEUES TABLE.
4940 023316 012702 005042 ;GET END ADDRESS OF RESYNC QUEUES TABLE.
4941 023322 005021 ;CLEAR A WORD OF THE TABLE.
4942 023324 020102 ;CHECK IF POINTER AT END OF TABLE.
4943 023326 103775 ;LOOP UNTIL TABLE IS CLEAR.
4944 ;*
4945 ;* WAIT FOR A CHARACTER TO APPEAR IN THE FIFO.
4946 ;* IF NO CHARACTER APPEARS WITHIN TIME-OUT PERIOD: EXIT ROUTINE. WE'RE DONE.
4947 ;*
4948 023330 016701 156712 ;GET TIME-OUT FOR SLOWEST BAUD RATE IN USE.
4949 023334 026767 157142 156630 ;CHECK FOR TRANSMISSION DONE ON ACTIVE LINES.
4950 023342 001402 ;SKIP ADDING 50 MS DELAY IF TX DONE ALL LINES.
4951 023344 062701 000062 ;ADD 50 MILLI SEC TO DELAY IF NOT LAST CHAR.

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4952 023350 052701 170000      64:    BIS    #170000,R1    ;INDICATE TO TEST DATA.VALID BIT.
4953 023354 016702 157332      MOV    RXBOPT,R2    ;INDICATE TO CHECK MEMORY RECEIVE BUFFER.
4954 023360 004767 003520      JSR     PC,WAIBIS    ;WAIT FOR RECEIVED CHAR OR TIME-OUT.
4955 023364 103117              BCC     184          ;EXIT ROUTINE IF TIME-OUT, WE'RE DONE.
4956
4957 023366 004767 175154      JSR     PC,GETCHR    ;READ A CHARACTER FROM THE MEMORY BUFFER.
4958
4959      ;*
4960      ; CHECK IF THE TX ISR IS DISABLED.
4961      ; RE-ENABLE RX ISR IF THE SPACE FOR NEW CHARS IS LOW ENOUGH.
4962      ; IF THE BUFFER CAN ACCOMODATE MORE CHARS THEN RE-ENABLE TRANSMISSION.
4963 023372 005767 157110      84:    TST     TXDBLF          ;CHECK IF TX IS DISABLED.
4964 023376 100027              BPL     104          ;SKIP RX/TX CHECK IF TX NOT DISABLED.
4965 023400 026727 157312 000020    CMP    RXBCNT,#RXBETX    ;COMPARE BUFFER COUNT WITH LEVEL TO ENABLE RX.
4966 023406 101023              BMI     104          ;SKIP ENABLE RX IF BUFFER TOO FULL.
4967 023410 004767 001212      JSR     PC,RXIE1      ;ENABLE RECEPTION INTERRUPTS.
4968 023414 016705 156642      MOV    TXENBM,R5      ;GET THE PRESERVED TX.ENABLE STATES.
4969 023420 026727 157272 000020    CMP    RXBCNT,#RXBETX    ;COMPARE BUFFER COUNT WITH LEVEL TO ENABLE TX.
4970 023426 101013              BMI     104          ;SKIP ENABLING TX IF BUFFER TOO FULL.
4971 023430              GETPRI    R1          ;SAVE THE CURRENT PROCESSOR PRIORITY.
4972 023432 010001              TRAP    C$GPRI
4973 023434              MOV      R0,R1
4974 023442 004767 002054      SETPRI   #PRI07          ;DISABLE INTERRUPTS.
4975 023446 005067 157034      JSR     PC,TXENBL      ;ENABLE TRANSMISSION.
4976 023452 010100              CLR     TXDBLF          ;CLEAR THE TX DISABLE FLAG.
4977 023454 104441              SETPRI   R1          ;RE-ENABLE INTERUPTS.
4978 023456              MOV      R1,R0
4979 023462 005367 157014      TRAP    C$SPRI
4980 023464 010467 161626      DEC     CHRTOT          ;DECREMENT THE TOTAL CHAR COUNTER.
4981 023470 012701 012044      BNE     124          ;SKIP ERROR IF NOT TOO MANY RECEIVED.
4982 023474 012767 014124 161620    MOV    R4,ERRNBR      ;SET ERROR NUMBER TO INITIAL ERRNBR.
4983              MOV    #EM9025,R1    ;SELECT THE PROPER ERROR MESSAGE.
4984              MOV    #ER0503,ERRBLK ;SELECT THE PROPER ERROR REPORT ROUTINE.
4985
4986      ;*
4987      ; REPORT ERROR AT INITIAL ERRNBR.
4988      ; "MORE THAN TWICE THE EXPECTED NUMBER OF CHARACTERS RECEIVED."
4989      ;-
4990      ; ERROR
4991      ; >>>>> ERROR <<<<<.
4992      ; TRAP    C$ERROR
4993      ; MOV     #1,FERROR          ;INDICATE THAT AN ERROR HAS BEEN FOUND.
4994      ; BR      604              ;EXIT THE ROUTINE, WE'RE GIVING UP.
4995      ;*
4996      ; DETERMINE IF THE CHARACTER IS DATA OR A STATUS CODE.
4997      ;-
4998      ; 124:    MOV     #70000,R0    ;GENERATE A BIT MAP OF CHARACTER ERROR BITS
4999      ;          BIC     R2,R0        ; WHICH ARE NOT SET FOR THE CHARACTER.
5000      ;          BNE     144          ;SKIP REPORTING OF ERROR CODE IF WE HAVE CHAR.
5001      ;*
5002      ; THE DATA IS EITHER A BMP CODE OR A MODEM STATUS CODE.
5003      ; REPORT THAT THE CODE WAS FOUND.
5004      ; ERRORS REPORTED WITH ERROR NUMBERS >>>>> ERRNBR+1 AND ERRNBR+2 <<<<<.
5005      ;-

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5002	023524	010467	161566		MOV	R4,ERRNBR	;GET THE ERROR NUMBER PASSED INTO THIS ROUTINE.
5003	023530	005267	161562		INC	ERRNBR	;SET ERROR NUMBER TO INITIAL ERRNBR+1.
5004	023534	004767	000222		JSR	PC,REPCOD	;REPORT THE BMP OR MODEM STATUS CHANGE CODE.
5005							
5006	023540	005767	156460		TST	FERROR	;HAS AN ERROR BEEN DETECTED ?
5007	023544	001423			BEQ	16:	;NO, THEN BRANCH.
5008	023546	032767	000100	156406	BIT	#BIT06,OPTION	;HAS EXTENDED ERROR REPORTING BEEN REQUESTED.
5009	023554	001456			BEQ	60:	;YES, THEN EXIT WITH TEST FAIL MESSAGE.
5010							
5011	023556	000416			BR	16:	;BRANCH TO GET THE NEXT CHARACTER.
5012							
5013							
5014							
5015							
5016							
5017							
5018	023560	010467	161532				
5019	023564	062767	000003	161524	14:	MOV	R4,ERRNBR
5020	023572	004767	176010			ADD	#3,ERRNBR
5021	023576	005767	156422			JSR	PC,NEWCHR
5022	023602	001404				TST	FERROR
5023	023604	032767	000100	156350		BEQ	16:
5024	023612	001437				BIT	#BIT06,OPTION
5025						BEQ	60:
5026							
5027							
5028							
5029							
5030							
5031	023614	004767	174726		16:	JSR	PC,GETCHR
5032	023620	103664				BCS	8:
5033	023622	000642				BR	4:
5034							
5035							
5036							
5037	023624	004767	000736		18:	JSR	PC,RXIEO
5038	023630	004767	001462			JSR	PC,TXDONE
5039	023634	005002				CLR	R2
5040	023636	005001				CLR	R1
5041	023640	004767	175742		20:	JSR	PC,NEWCHR
5042							
5043	023644	005767	156354			TST	FERROR
5044	023650	001404				BEQ	22:
5045	023652	032767	000100	156302		BIT	#BIT06,OPTION
5046	023660	001414				BEQ	60:
5047							
5048	023662	062702	000400		22:	ADD	#400,R2
5049	023666	005201				INC	R1
5050	023670	120127	000020			CMPB	R1,#NUMLNS
5051	023674	002761				BLT	20:
5052	023676	005701				TST	R1
5053	023700	100404				BMI	60:
5054	023702	005002				CLR	R2
5055	023704	012701	100000			MOV	#100000,R1
5056	023710	000753				BR	20:
5057							
5058	023712	010467	161400		60:	MOV	R4,ERRNBR

5059 023716
023716 004736
5060 023720 000207

PASS

RTS PC

JSR

IRESTORE GPRS.
PC,8(SP).

RETURN TO PREG05 SUBRT.

E11

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5062 .SBTTL GLOBAL SUBROUTINE - RDMAST -
5063 ;* *****
5064 ;* - REPORT DMA_START BIT ERRORS ROUTINE -
5065 ;* THIS SUBROUTINE CHECKS FOR LINES WHICH HAVE DMA_START BIT ERRORS
5066 ;* DURING THE JUST COMPLETED DMA TRANSMISSION. IF ANY ARE FOUND,
5067 ;* THEY ARE REPORTED.
5068 ;*
5069 ;* INPUTS: ERRMSG - ADDRESS OF PRIMARY ERROR MESSAGE FOR THIS ROUTINE.
5070 ;*          ERRNBR - ERROR NUMBER OF ERROR REPORTED IN THIS ROUTINE.
5071 ;*          TXINTF - CONTAINS BIT MAP OF LINES WITH DMA_START BIT ERRORS.
5072 ;*
5073 ;* OUTPUTS: ERRBLK - ADDRESS OF THE ERROR REPORTING ROUTINE (DESTROYED).
5074 ;*           MESSAGES MAY BE PRINTED AT THE OPERATOR CONSOLE.
5075 ;*
5076 ;* CALLING SEQUENCE: JSR PC,RDMAST
5077 ;*
5078 ;* COMMENTS: IF NO LINES HAVE DMA_START BIT ERRORS, NO MESSAGES ARE PRINTED.
5079 ;*
5080 ;* SUBORDINATE ROUTINES CALLED: ER9102.
5081 ;* *****
5082
5083 RDMAST:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5084 023722 004567 161376 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5085 023726 016702 156332 MOV TXINTF,R2 ;GET COPY OF THE DMA_START ERRORS BIT MAP.
5086 023732 001411 BEQ 60$ ;EXIT IF NO DMA_START ERROR BITS ARE SET.
5087 ;*
5088 ;* WE HAVE SOME DMA_START BIT ERRORS TO REPORT.
5089 023734 012767 015576 161360 MOV #ER9102,ERRBLK ;SELECT THE ERROR REPORTING ROUTINE.
5090 023742 012701 012442 MOV #EM9102,R1 ;INDICATE THAT WE HAVE DMA_START BIT ERROR.
5091 ;*
5092 ;* REPORT "DMA_START BIT SET AFTER RESET OR TX.ACTION ... ON LINE(S):"
5093 ;*
5094 023746 023746 104460 ERROR ;>>>> ERROR <<<<<.
5095 023750 012767 000001 156246 MOV #1,FERROR ;INDICATE AN ERROR HAS BEEN DETECTED. TRAP C#ERROR
5096 ;*
5097 023756 004736 60$: PASS ;RESTORE GPRS.
5098 023760 000207 RTS PC JSR PC,0(SP)+ ;RETURN TO PREG05 SUBRT.

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5100 .SBTTL GLOBAL SUBROUTINE - REPCOD -
5101 ;* *****
5102 ;* - ROUTINE TO REPORT ERROR CODE FROM DUT -
5103 ;* THIS ROUTINE REPORTS AN ERROR CODE WHICH HAS BEEN READ FROM THE DUT
5104 ;* FIFO. THE CODE IS CHECKED TO DETERMINE WHETHER IT IS A SELFTEST CODE
5105 ;* AN MODEM STATUS CHANGE CODE OR A BMP CODE. THIS ROUTINE ASSUMES THAT
5106 ;* THE CODE INDICATES AN ERROR. IF A BMP CODE IS FOUND IT IS NOT REPORTED
5107 ;* IMMEDIATELY, BUT IS SAVED ON THE BMP CODE QUEUE TO BE REPORTED LATER.
5108 ;*
5109 ;* INPUTS: R2 - CONTAINS THE ERROR CODE COMPLETE WITH FLAGS AND LINE #.
5110 ;* ERRIBL - ERRITP,ERRNBR,AND ERRMSG SET UP CORRECTLY.
5111 ;*
5112 ;* OUTPUTS: ERRBLK - VALUE MAY BE DESTROYED.
5113 ;* BMPCQP - MAYBE UPDATED IF A BMP CODE IS ADDED TO THE QUEUE.
5114 ;*
5115 ;* CALLING SEQUENCE: JSR PC,REPCOD
5116 ;*
5117 ;* COMMENTS: ERRNBR IS RESTORED TO ITS ENTERING VALUE BY THIS ROUTINE.
5118 ;* THIS ROUTINE REPORTS ERRORS WITH NUMBERS ERRNBR THRU ERRNBR+1.
5119 ;*
5120 ;* SUBORDINATE ROUTINES CALLED: ER9001,SAVBMP.
5121 ;* *****
5122 REPCOD:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5123 023762 004567 161336 JSR R5,PREGOS ;CALL REGISTER SAVE SUBRT.
5124 023766 012767 014512 161326 MOV #ER9001,ERRBLK ;SELECT THE ERROR REPORTING ROUTINE.
5125 023774 016703 161316 MOV ERRNBR,R3 ;PRESERVE THE ERROR NUMBER.
5126 024000 010204 MOV R2,R4 ;EXTRACT THE LINE NUMBER FIELD
5127 024002 000304 SWAB R4 ; FROM THE ERROR CODE WHICH WAS
5128 024004 042704 177760 BIC #177760,R4 ; PASSED INTO THIS ROUTINE.
5129 ;*
5130 ;* DETERMINE THE TYPE OF CODE WHICH IS TO BE REPORTED.
5131 ;*
5132 024010 012701 011236 MOV #EM9003,R1 ;SELECT MODEM STATUS CODE MESSAGE.
5133 024014 032702 000001 BIT #BIT0,R2 ;TEST THE MODEM STATUS INDICATION BIT.
5134 024020 001422 BEQ 4# ;GOTO REPORT ERROR IF MODEM STATUS CODE.
5135 024022 005267 161270 INC ERRNBR ;SELECT THE SELFTEST CODE ERROR NUMBER.
5136 024026 012701 011260 MOV #EM9004,R1 ;SELECT SELFTEST CODE MESSAGE.
5137 024032 012700 000300 MOV #300,R0 ;CHECK IF SELF-TEST OR BMP CODE.
5138 024036 040200 BIC R2,R0 ;TRY TO CLEAR BMP BITS.
5139 024040 001003 BNE 2# ;GO CHECK FOR SELFTEST CODE IF NOT BMP.
5140 024042 004767 000604 JSR PC,SAVBMP ;SAVE THE BMP CODE ON THE QUEUE.
5141 024046 000423 BR 6# ;EXIT THIS ROUTINE.
5142 024050 122702 000201 2#: CMPB #201,R2 ;CHECK FOR SELF TEST NULL CODE.
5143 024054 001416 BEQ 6# ;EXIT ROUTINE IF NULL CODE FOUND.
5144 024056 122702 000203 CMPB #203,R2 ;CHECK FOR SKIP SELF TEST CODE.
5145 024062 001413 BEQ 6# ;EXIT ROUTINE IF SKIP SELF TEST CODE FOUND.
5146 024064 000400 BR 4# ;GO REPORT SELF TEST ERROR.
5147 ;*
5148 ;* REPORT "UNEXPECTED XXXXX CODE FOUND IN RECEIVE CHAR FIFO."
5149 ;*
5150 024066 042702 177400 4#: BIC #177400,R2 ;REMOVE UPPER BYTE OF CODE TO BE REPORTED.
5151 024072 004767 002154 JSR PC,TXROFF ;TURN OFF TX AND RX DURING ERROR REPORTING.
5152 024076 104460 ERROR ; >>>> ERROR <<<<.
5153 024100 012767 000001 156116 MOV #1,FERROR TRAP C#ERROR
5154 024106 004767 002200 JSR PC,TXRON ;TURN TX AND RX BACK ON.
```

DMU-11 FUNCTIONAL VERIFICATION
GLOBAL SUBROUTINE

MACRO M1200 12-DEC-83 16:16 PAGE 88-1
- REPCOD -

5155				;*
5156				; RESTORE THE INITIAL ERROR NUMBER.
5157				;-
5158	024112	010367	161200	64: MOV R3,ERRNBR
5159				
5160	024116			60\$: PASS ;RESTORE GPRS.
	024116	004736		JSR PC,@(SP), ;RETURN TO PREGOS SUBRT.
5161	024120	000207		RTS PC

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5163 .SBTTL GLOBAL SUBROUTINE - REPSMR -
5164 ;* *****
5165 ;* - REPORT ERROR SUMMARY ROUTINE -
5166 ;* THIS SUBROUTINE REPORTS AN ERROR SUMMARY FOR THOSE LINES WHICH HAVE
5167 ;* EXCEEDED THE NUMBER OF INDIVIDUAL ERRORS TO REPORT FOR A SINGLE LINE
5168 ;* IN A SINGLE TEST. THIS PARAMETER CAN BE SPECIFIED BY THE OPERATOR IF
5169 ;* HE/SHE ANSWERS THE SOFTWARE PARAMETER QUESTIONS.
5170 ;*
5171 ;* INPUTS: ERCNTB - LABEL AT BASE OF LINE ERROR COUNTERS TABLE.
5172 ;*          ERRMSG - ADDRESS OF PRIMARY ERROR MESSAGE.
5173 ;*          ERRNBR - ERROR NUMBER OF ERRORS IN THIS ROUTINE.
5174 ;*          ERSMRF - "REPORT ERROR SUMMARY FOR LINE" FLAGS.
5175 ;*
5176 ;* OUTPUTS: ERRBLK - ADDRESS OF ERROR REPORTING ROUTINE (DESTROYED).
5177 ;*          SUMMARY MESSAGES MAY BE PRINTED AT THE OPERATOR CONSOLE.
5178 ;*
5179 ;* CALLING SEQUENCE: JSR PC,REPSMR
5180 ;*
5181 ;* COMMENTS: IF NO LINES HAVE EXCEEDED THE MAXIMUM NUMBER OF INDIVIDUAL
5182 ;*            ERRORS TO REPORT, NO MESSAGES ARE PRINTED BY THIS ROUTINE.
5183 ;*            ERROR SUMMARIES IN THIS ROUTINE ARE REPORTED AS ERRORS.
5184 ;*            THE CONTENTS OF ERRBLK ARE DESTROYED.
5185 ;*
5186 ;* SUBORDINATE ROUTINES CALLED:
5187 ;* - *****
5188
5189 024122 REPSMR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5190 024122 004567 161176 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5191 024126 005767 156346 TST ERSMRF ;CHECK THE "PRINT LINE ERROR SUMMARY" FLAGS.
5192 024132 001404 BEQ 60$ ;EXIT WITHOUT ACTION IF NO SUMMARY FLAGS SET.
5193
5194 ;* WE HAVE SOME ERROR SUMMARIES TO REPORT.
5195 024134 012767 015162 161160 MOV #ER9004,ERRBLK ;SELECT ERROR REPORTING ROUTINE.
5196
5197 ;* REPORT
5198 ;* "ERROR SUMMARY REPORT FOR LINES WITH EXCESSIVE NUMBERS OF ERRORS:"
5199 ;*
5200 024142 ERROR
5201 024142 104460 TRAP C$ERROR
5202 024144 004736 60$: PASS ;RESTORE GPRS.
5203 024146 000207 RTS PC JSR PC,8(SP)+ ;RETURN TO PREG05 SUBRT.

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5205 .SBTTL GLOBAL SUBROUTINE - RESETT -
5206 ;*****
5207 ;* - RESET DEVICE UNDER TEST -
5208 ;* THIS SUBROUTINE IS USED TO RESET THE DUT TO A KNOWN STATE.
5209 ;* IF RESET DOES NOT SUCCESSFULLY COMPLETE, IE. TIME-OUT OCCURS, THEN
5210 ;* AN ABORT TEST ERROR MESSAGE IS REPORTED.
5211 ;*
5212 ;* INPUTS: CSRA - CONTAINS THE ADDRESS OF THE CSR
5213 ;* TXBFCA - CONTAINS ADDRESS OF DUT DMA BUFFER COUNT REGISTER.
5214 ;* ERRRTL- ERRTP,ERNBR,AND ERRMSG SET UP CORRECTLY.
5215 ;*
5216 ;* OUTPUTS: THE DUT PERFORMS ITS RESET FUNCTION INTO A KNOWN STATE.
5217 ;* CARRY - CLEAR INDICATES THE TEST IS TO BE ABORTED.
5218 ;* ERRBLK - VALUE MAY BE DESTROYED.
5219 ;* IESTAT - TX AND RX INTERRUPT FLAGS ARE CLEARED.
5220 ;* TX AND RX INTERRUPT ENABLE BITS IN THE DUT'S CSR ARE CLEARED.
5221 ;*
5222 ;* CALLING SEQUENCE: JSR PC,RESETT
5223 ;*
5224 ;* COMMENTS: THIS SUBROUTINE CAN REPORT ERRORS WITH NUMBERS INITIAL ERNBR
5225 ;* THIS ROUTINE DOES NOT DESTROY THE VALUE OF ERNBR.
5226 ;*
5227 ;* SUBORDINATE ROUTINES CALLED: DELAY,MSLGET.
5228 ;*****
5229
5230 RESETT:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5231 024150 004567 161150 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5232 024154 012702 000040 MOV #BIT05,R2 ;SET BIT MASK OF MASTER RESET BIT.
5233
5234 ;*
5235 ;* TEST THE STATE OF THE MASTER RESET BIT IN THE CSR.
5236 ;* IF MR IS SET THEN WAIT FOR SELF-TEST TO COMPLETE.
5237 ;* IF TIME-OUT OCCURS, REPORT THE ERROR AND PASS-OUT ABORT TEST INDICATOR.
5238 ;*
5239 024160 016704 156014 MOV CSRA,R4 ;GET THE ADDRESS OF THE DUT'S CSR.
5240 024164 030214 BIT R2,(R4) ;CHECK STATE OF MASTER RESET BIT.
5241 024166 001406 BEQ 2$ ;DON'T DELAY IF MR IS ALREADY CLEAR.
5242 024170 005003 CLR R3 ;SET UP DESIRED STATE OF MASTER RESET BIT.
5243 024172 012701 011610 MOV #5000.,R1 ;PASS TIME-OUT VALUE OF 5 SECONDS.
5244 024176 004767 174764 JSR PC,MSLGET ;WAIT FOR SELF-TEST TO COMPLETE, MR CLEAR.
5245 024202 103012 BCC 4$ ;GO REPORT ERROR IF TIMEOUT OCCURRED.
5246
5247 ;*
5248 ;* SET MASTER RESET BIT IN CSR. CLEAR TX AND RX ENABLE BITS, ETC.
5249 ;* SKIP THE SELFTEST.
5250 ;* TIME-OUT OF 5 SECS, JUST IN CASE THE SELF-TEST EXECUTES.
5251 ;*
5252 2$: MOV R2,BCSRA ;SET MASTER RESET BIT, DISABLE TX AND RX INTS.
5253 JSR PC,SKPSTS ;TRY TO SKIP THE SELFTEST.
5254
5255 ;*
5256 ;* SET SELF-TEST TIME-OUT OF 5 SECONDS, AND WAIT FOR M.R TO CLEAR.
5257 ;* IF TIME-OUT OCCURS, THEN REPORT THE FATAL ERROR AND PASS-OUT THE ABORT
5258 ;* TEST INDICATOR.
5259 ;*
5260 024214 005003 CLR R3 ;SET UP DESIRED STATE OF MASTER RESET BIT.
5261 024216 012701 011610 MOV #5000.,R1 ;PASS TIME-OUT VALUE OF 5 SECONDS.
5262 024222 004767 174740 JSR PC,MSLGET ;WAIT FOR SELF-TEST TO COMPLETE, MR CLEAR.
5263 024226 103410 BCS 6$ ;SKIP ERROR REPORT IF MR CLEARED IN TIME.

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5261
5262
5263
5264
5265 024230 012701 010103
5266 024234 012767 014162 161060
5267
5268
5269 024242
      024242 104460
5270 024244 000241
5271 024246 000403
5272
5273
5274
5275
5276 024250 005067 155760
5277 024254 000261
5278
5279 024256
      024256 004736
5280
5281 024260 000207
5282
; *
; SET UP ERROR MESSAGE TO REPORT "FATAL ERROR FOUND DURING RESET, TEST ABORTED".
; INDICATE TEST IS TO BE ABORTED BY CLEARING THE CARRY BIT.
; -
4$:  MOV     #EM1601,R1      ;PASS ERROR MESSAGE TO REPORT.
      MOV     #ER1603,ERRBLK ;PASS ADDRESS OF ERROR HANDLING ROUTINE.
      ;REPORT ERROR "TIME-OUT OCCURRED WAITING FOR MASTER RESET TO CLEAR"
      ; "TEST ABORTED"
      ERROR
;          >>>>> ERROR <<<<<
;                                TRAP    C#ERROR
      CLC
      BR      60$          ;INDICATE TEST IS TO BE ABORTED.
;EXIT THIS SUBROUTINE, ABORT TEST INDICATOR.
; *
; CLEAR TX AND RX INTERRUPT ENABLE STATUS FLAGS IN IESTAT.
; EXIT WITH CONTINUE TEST INDICATOR SET (IE,CARRY SET).
; -
6$:  CLR     IESTAT        ;CLEAR TX AND RX INTERRUPT STATUS FLAGS.
      SEC
;INDICATE SUCCESS, CONTINUE TEST.
60$: PASS
;RESTORE GPRS, PASS THE FOLLOWING INTACT:
;PC,8(SP)+
;RETURN TO PREG05 SUBRT.
;CARRY BIT:IF CLEAR,INDICATES ABORT TEST.
      JSR
      RTS     PC

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5284 .SBTTL GLOBAL SUBROUTINE - RRXNDN -
5285 ;* *****
5286 ;* REPORT RECEPTION NOT COMPLETED ROUTINE -
5287 ;* THIS SUBROUTINE CHECKS FOR LINES WHICH DID NOT RECEIVE THE COMPLETE
5288 ;* DATA PATTERN. IF ANY ARE FOUND, THEY ARE REPORTED.
5289 ;*
5290 ;* INPUTS: R5 - LOCAL ACTIVE LINES BIT MAP.
5291 ;* DPLENB - BASE OF TABLE OF DATA PATTERN LENGTHS.
5292 ;* ERRMSG - ADDRESS OF PRIMARY ERROR MESSAGE FOR THIS ROUTINE.
5293 ;* ERRNR - ERROR NUMBER OF ERROR REPORTED IN THIS ROUTINE.
5294 ;* RXCNTB - LABEL AT BASE OF THE RX CHARACTER COUNTERS TABLE.
5295 ;* RXDNF - RECEPTION DONE FLAGS.
5296 ;*
5297 ;* OUTPUTS: ERRBLK - ADDRESS OF THE ERROR REPORTING ROUTINE (DESTROYED).
5298 ;* MESSAGES MAY BE PRINTED AT THE OPERATOR CONSOLE.
5299 ;*
5300 ;* CALLING SEQUENCE: JSR PC,RRXNDN
5301 ;*
5302 ;* COMMENTS: IF NO LINES FAILED TO COMPLETE THEIR RECEPTION, NO MESSAGES
5303 ;* ARE PRINTED.
5304 ;*
5305 ;* SUBORDINATE ROUTINES CALLED: ER9005.
5306 ;* - - - - -
5307
5308 024262 RRXNDN:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5309 024262 004567 161036 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5310 024266 010502 MOV R5,R2 ;GET COPY OF THE LOCAL ACTIVE LINES BIT MAP.
5311 024270 046702 156210 BIC RXDNF,R2 ;GET MAP OF ACTIVE LINES WITH RX DONE FLAG CLR.
5312 024274 001413 BEQ 60$ ;EXIT IF NO ACTIVE LINES HAVE RX DONE FLAG CLR.
5313
5314 ;* WE HAVE SOME "RX NOT COMPLETED" ERRORS TO REPORT.
5315 024276 012767 015276 161016 MOV #ER9005,ERRBLK ;SELECT THE ERROR REPORTING ROUTINE.
5316 024304 012701 011724 MOV #EM9016,R1 ;INDICATE THAT WE ARE DEALING WITH RECEPTION.
5317 024310 012704 003542 MOV #RXCNTB,R4 ;PASS BASE OF RX CHAR COUNTERS TABLE TO ER9005.
5318
5319 ;* REPORT "SINGLE CHARACTER MODE TEST ERROR:"
5320 ;* "DATA PATTERN NOT COMPLETELY RECEIVED ON ALL LINES:"
5321 ;* ...
5322 ;* - - - - -
5323 024314 ERROR
5324 024314 104460 MOV #1,FERROR ;INDICATE AN ERROR HAS BEEN FOUND. TRAP C$ERROR
5325 024316 012767 000001 155700
5326 024324 004736 60$: PASS ;RESTORE GPRS.
5327 024326 000207 RTS PC JSR PC,B(SP)+ ;RETURN TO PREG05 SUBRT.

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5329 .SBTTL GLOBAL SUBROUTINE - RTXNDN -
5330 ;* *****
5331 ;* - REPORT TRANSMISSION NOT COMPLETED ROUTINE -
5332 ;* THIS SUBROUTINE CHECKS FOR LINES WHICH DID NOT TRANSMIT THE COMPLETE
5333 ;* DATA PATTERN. IF ANY ARE FOUND, THEY ARE REPORTED.
5334 ;*
5335 ;* INPUTS: R5 - LOCAL ACTIVE LINES BIT MAP.
5336 ;* DPLENB - LABEL AT BASE OF DATA PATTERN LENGTH TABLE.
5337 ;* ERRMSG - ADDRESS OF PRIMARY ERROR MESSAGE FOR THIS ROUTINE.
5338 ;* ERRNR - ERROR NUMBER OF ERROR REPORTED IN THIS ROUTINE.
5339 ;* TXCNTB - LABEL AT BASE OF THE TX CHARACTER COUNTERS TABLE.
5340 ;* TXDNF - TRANSMISSION DONE FLAGS.
5341 ;*
5342 ;* OUTPUTS: ERRBLK - ADDRESS OF THE ERROR REPORTING ROUTINE (DESTROYED).
5343 ;* MESSAGES MAY BE PRINTED AT THE OPERATOR CONSOLE.
5344 ;*
5345 ;* CALLING SEQUENCE: JSR PC,RTXNDN
5346 ;*
5347 ;* COMMENTS: IF NO LINES FAILED TO COMPLETE THEIR TRANSMISSION, NO MESSAGES
5348 ;* ARE PRINTED.
5349 ;*
5350 ;* SUBORDINATE ROUTINES CALLED: ER9005.
5351 ;* *****
5352
5353 024330 RTXNDN:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5354 024330 004567 160770 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5355 024334 010502 MOV R5,R2 ;GET COPY OF THE LOCAL ACTIVE LINES BIT MAP.
5356 024336 046702 156140 BIC TXDNF,R2 ;GET MAP OF ACTIVE LINES WITH TX DONE FLAG CLR.
5357 024342 001413 BEQ 60$ ;EXIT IF NO ACTIVE LINES HAVE TX DONE FLAG CLR.
5358 ;*
5359 ;* WE HAVE SOME "TX NOT COMPLETED" ERRORS TO REPORT.
5360 024344 012767 015276 160750 MOV #ER9005,ERRBLK ;SELECT THE ERROR REPORTING ROUTINE.
5361 024352 012701 011710 MOV #EM9015,R1 ;INDICATE WE ARE DEALING WITH TRANSMISSION.
5362 024356 012704 003502 MOV #TXCNTB,R4 ;PASS BASE OF TX CHAR COUNTERS TO TABLE ER0805.
5363
5364 ;*
5365 ;* REPORT "SINGLE CHARACTER MODE TEST ERROR:"
5366 ;* "DATA PATTERN NOT COMPLETELY TRANSMITTED ON ALL LINES:"
5367 ;* ...
5368 ;*
5369 024362 ERROR ;
5370 024362 104460 >>>>> ERROR <<<<<.
5371 024364 012767 000001 155632 MOV #1,FERROR ;INDICATE THAT AN ERROR HAS BEEN FOUND.
5372 024372 004736 60$: PASS ;RESTORE GPRS.
5373 024374 000207 RTS PC JSR PC,B(SP)+ ;RETURN TO PREG05 SUBRT.

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5375 .SBTTL GLOBAL SUBROUTINE - RXDSBL -
5376 ;* *****
5377 ;* - DISABLE RECEIVERS -
5378 ;* THIS SUBROUTINE IS USED TO DISABLE RECEPTION ON SELECTED LINES BY,
5379 ;* CLEARING THE ASSOCIATED RX_ENABLE BIT ON THE DUT.
5380 ;*
5381 ;* INPUTS: R5 - BIT'S SET CORRESPOND TO LINES ON WHICH TO CLEAR RX_ENABLE.
5382 ;* CSRA - CONTAINS THE ADDRESS OF THE DUT CSR.
5383 ;* IESTAT - CONTAINS THE STATE OF TXIE AND RXIE BITS IN THE CSR.
5384 ;* NUMLNS - EQUATED TO BE THE MAXIMUM NUMBER OF LINES AVAILABLE.
5385 ;* LNCTRA - CONTAINS THE ADDRESS OF THE LNCTRL REGISTER.
5386 ;*
5387 ;* OUTPUTS: R5 - BIT'S SET INDICATE INITIAL STATES OF ALL RX_ENABLE BITS.
5388 ;* LNCTRA - THE STATE OF THE RX_ENABLE BIT MAY BE ALTERED.
5389 ;* THE CONTENTS OF THE IND_ADD_REG FIELD IN THE CSR ARE DESTROYED.
5390 ;*
5391 ;* CALLING SEQUENCE: JSR PC,RXDSBL
5392 ;*
5393 ;* COMMENTS:
5394 ;*
5395 ;* SUBORDINATE ROUTINES CALLED: NONE.
5396 ;* - *****
5397
5398 RXDSBL:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5399 024376 004567 160722 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5400 024402 010500 MOV R5,R0 ;COPY BIT MAP OF LINES TO DISABLE RECEPTION.
5401 024404 012701 000001 MOV #BIT0,R1 ;INITIALIZE THE SELECTED LINE BIT MASK.
5402 024410 016702 155574 MOV LNCTRA,R2 ;GET THE ADDRESS OF THE LNCTRL REGISTER.
5403 024414 012703 000020 MOV #NUMLNS,R3 ;GET MAXIMUM LINE NUMBER PLUS ONE.
5404 024420 016704 155610 MOV IESTAT,R4 ;GET THE STATES OF THE INT ENABLE BITS.
5405 024424 005005 CLR R5 ;LOG POSSIBLE RX DISABLED ON ALL LINES.
5406
5407 ;* SELECT EVERY LINE IN TURN, AND LOG THE STATE OF EACH RX_ENABLE BIT.
5408 024426 010477 155546 2$: MOV R4,BCSRA ;WRITE TO DUT CSR TO SELECT LINE REGISTERS.
5409 024432 032712 000004 BIT #BIT2,(R2) ;CHECK STATE OF RX_ENABLE BIT ON SELECTED LINE.
5410 024436 001401 BEQ 4$ ;SKIP NEXT INSTRUCTION IF RX_ENABLE CLEAR.
5411 024440 050105 BIS R1,R5 ;LOG RX_ENABLE BIT SET FOR SELECTED LINE.
5412
5413 ;* CLEAR RX_ENABLE ON LINES THAT HAVE A CORRESPONDING BIT SET IN THE RX_DISABLE
5414 ;* LINE BIT MAP.
5415
5416 024442 030100 4$: BIT R1,R0 ;CHECK STATE OF DISABLE LINE BIT MAP.
5417 024444 001402 BEQ 6$ ;BRANCH IF THIS LINE TO REMAIN UNALTERED.
5418 024446 042712 000004 BIC #BIT2,(R2) ;CLEAR RX_ENABLE BIT ON SELECTED LINE.
5419 024452 005204 6$: INC R4 ;PREPARE TO SELECT REGISTERS FOR NEXT LINE.
5420 024454 006301 ASL R1 ;SHIFT BIT MAP FOR NEXT LINE.
5421 024456 005303 DEC R3 ;DECREMENT LINE NUMBER.
5422 024460 001362 BNE 2$ ;LOOP TO CHECK NEXT LINE.
5423
5424 024462 010566 000014 60$: PASS R5 ;RESTORE GPRS,EXCEPT
5425 024462 004736 MOV R5,R5SLOT(SP) ;PUT R5 IN STACK SLOT.
5426 024470 000207 JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
;R5 - PREVIOUS STATES OF ALL RX_ENABLE BITS.
RTS PC

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5428 .SBTTL GLOBAL SUBROUTINE - RXENBL -
5429 ;+ *****
5430 ;* - ENABLE RECEIVER -
5431 ;* THIS SUBROUTINE IS USED TO ENABLE RECEPTION ON SELECTED LINES BY
5432 ;* SETTING THE ASSOCIATED RX.ENABLE BIT ON THE DUT.
5433 ;*
5434 ;* INPUTS: R5 - BIT'S SET CORRESPOND TO LINES ON WHICH TO SET RX.ENABLE.
5435 ;* CSRA - CONTAINS THE ADDRESS OF THE DUT CSR.
5436 ;* IESTAT - CONTAINS THE STATE OF TXIE AND RXIE BITS IN THE CSR.
5437 ;* NUMLNS - EQUATED TO BE THE MAXIMUM NUMBER OF LINES AVAILABLE.
5438 ;* LNCTRA - CONTAINS THE ADDRESS OF THE LNCTRL REGISTER.
5439 ;*
5440 ;* OUTPUTS: R5 - BIT'S SET INDICATE PREVIOUSLY DISABLED LINES.
5441 ;* LNCTRA - THE STATE OF THE RX.ENABLE BIT MAY BE ALTERED.
5442 ;* THE CONTENTS OF THE IND.ADD.REG FIELD IN THE CSR ARE DESTROYED.
5443 ;*
5444 ;* CALLING SEQUENCE: JSR PC,RXENBL
5445 ;*
5446 ;* COMMENTS:
5447 ;*
5448 ;* SUBORDINATE ROUTINES CALLED: NONE.
5449 ;-- *****
5450
5451 024472 RXENBL:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5452 024472 004567 160626 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5453 024476 010500 MOV R5,R0 ;COPY BIT MAP OF LINES TO ENABLE.
5454 024500 012701 000001 MOV #BIT0,R1 ;INITIALIZE THE SELECTED LINE BIT MASK.
5455 024504 016702 155500 MOV LNCTRA,R2 ;GET THE ADDRESS OF THE LNCTRL REGISTER.
5456 024510 012703 000020 MOV #NUMLNS,R3 ;GET MAXIMUM LINE NUMBER.
5457 024514 016704 155514 MOV IESTAT,R4 ;GET THE STATES OF THE INT ENABLE BITS.
5458 024520 005005 CLR R5 ;CLEAR RX.ENABLE BIT LOG OF DISABLED LINES.
5459 ;+
5460 ; SELECT EVERY LINE IN TURN,AND LOG ANY RX.ENABLE BIT THAT IS CLEAR.
5461 024522 010477 155452 2$: MOV R4,BCSRA ;WRITE TO DUT CSR TO SELECT LINE REGISTERS.
5462 024526 032712 000004 BIT #BIT2,(R2) ;CHECK STATE OF RX.ENABLE BIT ON SELECTED LINE.
5463 024532 001001 BNE 4$ ;SKIP NEXT INSTRUCTION IF RX.ENABLE SET.
5464 024534 050105 BIS R1,R5 ;LOG RX ENABLE BIT CLEAR FOR SELECTED LINE.
5465 ;+
5466 ; SET RX.ENABLE ON LINES THAT HAVE A CORRESPONDING BIT SET IN THE RX ENABLE
5467 ; LINE BIT MAP.
5468 ;+
5469 024536 030100 4$: BIT R1,R0 ;CHECK STATE OF RX.ENABLE LINE BIT MAP.
5470 024540 001402 BEQ 6$ ;BRANCH IF THIS LINE TO REMAIN UNALTERED.
5471 024542 052712 000004 BIS #BIT2,(R2) ;ENABLE RECEPTION ON SELECTED LINE.
5472 024546 005204 6$: INC R4 ;PREPARE TO SELECT REGISTERS FOR NEXT LINE.
5473 024550 006301 ASL R1 ;SHIFT BIT MAP FOR NEXT LINE.
5474 024552 005303 DEC R3 ;DECREMENT LINE NUMBER.
5475 024554 001362 BNE 2$ ;LOOP TO CHECK NEXT LINE.
5476
5477 024556 60$: PASS R5 ;RESTORE GPRS,EXCEPT
5478 024556 010566 000014 MOV R5,R5SLOT(SP) ;PUT R5 IN STACK SLOT.
5479 024562 004736 JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
5480 024564 000207 RTS PC ;R5 - LINE BIT MAP CORRESPONDING TO THE
; PREVIOUS LINES THAT WERE DISABLED.

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5482 .SBTTL GLOBAL SUBROUTINE RXIE0 -
5483 ;* *****
5484 ;* - RECEIVER INTERRUPT DISABLE -
5485 ;* THIS ROUTINE IS USED TO DISABLE RECEIVER INTERRUPTS IN THE DMU11.
5486 ;*
5487 ;* INPUTS: NONE.
5488 ;*
5489 ;* OUTPUTS: THE RX.INT.ENBL BIT IS CLEARED IN THE DUT CSR.
5490 ;* IESTAT -CONTAINS THE UPDATED STATUS OF THE TX AND RX INTERRUPT
5491 ;* ENABLE BITS.
5492 ;*
5493 ;* CALLING SEQUENCE: JSR PC,RXIE0
5494 ;*
5495 ;* COMMENTS: THE CONTENTS OF THE INDIRECT ADDRESS REGISTER FIELD IN
5496 ;* THE DUT CSR ARE DESTROYED.
5497 ;*
5498 ;* SUBORDINATE ROUTINES CALLED: NONE.
5499 ;* *****
5500 024566 010046 RXIE0:: MOV RO,-(SP) ;SAVE CONTENTS OF RO ON THE STACK.
5501 024570 GETPRI -(SP) ;SAVE PROCESSOR PRIORITY ON STACK.
5502 024570 104440 TRAP C:GPRI
5502 024572 010046 MOV RO,-(SP)
5502 024574 012700 000340 SETPRI #PRI07 ;IGNORE ANY INTERRUPT THAT MAY BE GENERATED.
5502 024600 104441 MOV #PRI07,RO
5503 024602 042767 137777 155424 BIC #137777,IESTAT ;CLEAR RX.INT.ENBL BIT IN IESTAT.
5504 024610 016777 155420 155362 MOV IESTAT,DCSRA ;DISABLE RX INTERRUPTS.
5505 024616 SETPRI (SP)+ ;ENABLE INTERRUPTS TO THE PROCESSOR AGAIN.
5505 024616 012600 MOV (SP)+,RO
5505 024620 104441 TRAP C:SPRI
5506 024622 012600 MOV (SP)+,RO
5507 024624 000207 RTS PC ;RESTORE RO.

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5509      .SBTTL  GLOBAL SUBROUTINE                - RXIE1 -
5510      ;* *****
5511      ;*          - RECEIVER INTERRUPT ENABLE -
5512      ;*      THIS ROUTINE IS USED TO ENABLE RECEIVER INTERRUPTS IN THE DMU11.
5513      ;*
5514      ;* INPUTS:      NONE.
5515      ;*
5516      ;* OUTPUTS:     THE RX.INT.ENBL BIT IS SET IN THE DUT CSR.
5517      ;*               IESTST -CONTAINS THE UPDATED STATUS OF THE TX AND RX INTERRUPT
5518      ;*               ENABLE BITS.
5519      ;*
5520      ;* CALLING SEQUENCE:  JSR      PC,RXIE1
5521      ;*
5522      ;* COMMENTS:     THE CONTENTS OF THE INDIRECT ADDRESS REGISTER FIELD IN
5523      ;*               THE DUT CSR ARE DESTROYED.
5524      ;*
5525      ;* SUBORDINATE ROUTINES CALLED: NONE.
5526      ;* -- *****
5527
5528 024626 052767 000100 155400 RXIE1:: BIS      #BIT06,IESTAT      ;SET RX.INT.ENBL BIT IN IESTAT.
5529 024634 042767 137677 155372      BIC      #137677,IESTAT      ;CLEAR ALL OTHER BITS, EXCEPT TX AND RX I.E.
5530 024642 016777 155366 155330      MOV      IES'AT,&CSRA      ;ENABLE RX INTERRUPTS.
5531 024650 000207                      RTS      PC

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5533 .SBTTL GLOBAL SUBROUTINE - SAVBMP -
5534 ;* *****
5535 ;* - SAVE BMP CODES ROUTINE -
5536 ;* THIS ROUTINE SAVES THE PARAMETER PASSED IN, ONTO THE BMP CODE QUEUE
5537 ;* TOGETHER WITH THE NUMBER OF THE CURRENTLY EXECUTING TEST.
5538 ;*
5539 ;* INPUTS: R2 - CONTAINS THE BMP CODE THAT IS TO BE PLACED ON THE QUEUE.
5540 ;* BMPCP - CONTAINS ADDRESS OF NEXT LOCATION IN THE BMP QUEUE.
5541 ;* BMPCQB - LABEL AT BASE OF THE BMP CODE QUEUE.
5542 ;* BMPCQE - LABEL OF NEXT LOCATION AFTER THE END OF THE BMP QUEUE.
5543 ;* TSTNUM - CONTAINS THE NUMBER OF THE CURRENT TEST.
5544 ;*
5545 ;* OUTPUTS: BMPCP - INCREMENTED BY 4.
5546 ;* THE CONTENTS OF THE BMP CODE QUEUE ARE UPDATED.
5547 ;*
5548 ;* CALLING SEQUENCE: JSR PC,SAVBMP
5549 ;*
5550 ;* COMMENTS: IF THE OVERFLOW OCCURS THEN THE LAST LOCATION WILL BE
5551 ;* OVERWRITTEN BY ANY SUBSEQUENT ATTEMPTS TO UPDATE THE QUEUE.
5552 ;*
5553 ;* SUBORDINATE ROUTINES CALLED: NONE.
5554 ;* - *****
5555
5556 024652 SAVBMP:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5557 024652 004567 160446 ;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5558 024656 016704 155626 ;GET THE POINTER TO THE NEXT LOCATION IN QUEUE.
5559 024662 116724 155372 ;SAVE THE CURRENT TEST NUMBER ON THE QUEUE.
5560 024666 005204 ;INCREMENT THE POINTER TO GIVE AN EVEN ADDRESS.
5561 024670 042702 177400 ;CLEAR THE UNWANTED BITS FROM THE BMP CODE.
5562 024674 010224 ;SAVE THE BMP CODE ON THE QUEUE.
5563 024676 020427 002712 ;CHECK IF OVERFLOW WILL OCC. THE NEXT TIME.
5564 024702 103402 ;GO SAVE THE POINTER IF WE WILL NOT OVERFLOW.
5565 024704 162704 000004 ;RESET THE POINTER TO THE LAST LOCATION IN QUE.
5566 024710 010467 155574 ;SAVE THE POINTER.
5567 024714 ;RESTORE GPRS.
5568 024714 004736 PC,B(SP). ;RETURN TO PREG05 SUBRT.
5568 024716 000207 RTS PC

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5570 .SBTTL GLOBAL SUBROUTINE - SKPSTS -
5571 ;* *****
5572 ;* - SKIP SELFTEST ROUTINE -
5573 ;* THIS SUBROUTINE IS USED TO SKIP THE SELFTEST AFTER A DUT RESET HAS BEEN
5574 ;* INITIATED. IT MUST BE ENTERED IMMEDIATELY AFTER SETTING THE DUT MASTER
5575 ;* RESET ROUTINE OR AFTER THE EXECUTION OF A BUS RESET (BECAUSE OF TIMING
5576 ;* CONSIDERATIONS).
5577 ;*
5578 ;* INPUTS: CSRA - CONTAINS ADDRESS OF THE DUT CSR.
5579 ;* TXBFCA - CONTAINS ADDRESS OF DUT DMA BUFFER COUNT REGISTER.
5580 ;*
5581 ;* OUTPUTS: SKIP SELFTEST CODES ARE WRITTEN TO THE DUT REGISTERS.
5582 ;*
5583 ;* CALLING SEQUENCE: JSR PC,SKPSTS
5584 ;*
5585 ;* COMMENTS:
5586 ;*
5587 ;* SUBORDINATE ROUTINES CALLED: DELAY.
5588 ;* - *****
5589
5590 024720 SKPSTS:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5591 024720 004567 160400 ;R5 ,PREG05 ;CALL REGISTER SAVE SUBRT.
5592 024730 004767 172614 ;" SS DELAY VALUE OF 10 MILLI-SECONDS.
5593 ;* ;*
5594 ;* WRITE SKIP SELF-TEST CODE (52'25) TO ALL THE INDEXED DUT REGISTERS.
5595 ;* -
5596 024734 012701 000060 ;* MOV #NUMLNS!BIT05,R1 ;FORM IND.ADR.REG FIELD (PLUS M.R. BIT) WORD.
5597 ;* ;THE ABOVE INCLUSION OF THE M.R. BIT IS NECESSARY BECAUSE OF THE
5598 ;* ;LACK OF A M.R. BIT WRITE LOCK-OUT ON THE DHU-11.
5599 024740 012703 052525 ;* MOV #52525,R3 ;INITIALISE THE SKIP SELF-TEST CODE.
5600 024744 005301 4*: DEC R1 ;SELECT THE NEXT SET OF DEVICE REGISTERS.
5601 024746 016704 155226 ;* MOV CSRA,R4 ;GET THE ADDRESS OF THE CSR OF THE DUT.
5602 024752 010124 ;* MOV R1,(R4)+ ;SELECT A BANK OF DUT REGISTERS.
5603 024754 010324 6*: MOV R3,(R4)+ ;WRITE THE CODE TO A DUT REGISTER.
5604 024756 020467 155234 ;* CMP R4,TXBFCA ;COMPARE POINTER WITH LAST REGISTER ADDRESS.
5605 024762 103774 ;* BLO 6* ;LOOP IF NOT ALL REGS DONE IN THIS BANK.
5606 024764 032701 000017 ;* BIT #17,R1 ;TEST FOR IND.ADR.REG FIELD DECREMENTED TO 0.
5607 024770 001365 ;* BNE 4* ;LOOP UNTIL ALL REGISTERS CONTAIN THE CODE.
5608
5609 024772 60*: PASS ;RESTORE GPRS.
5610 024772 004736 ;* JSR PC,B(SP)+ ;RETURN TO PREG05 SUBRT.
5610 024774 000207 ;* RTS PC

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5612 .SBTTL GLOBAL SUBROUTINE - SPLSUP -
5613 ;* *****
5614 ;* - SPLIT SPEED TRANSMISSION/RECEPTION SET-UP -
5615 ;*
5616 ;* THIS ROUTINE IS USED TO INITIALISE BOTH THE DUT AND THE
5617 ;* TRANSMISSION/RECEPTION CONTROL PARAMETERS TO THE CORRECT
5618 ;* STATE, PRIOR TO SPLIT SPEED TRANSMISSION/RECEPTION.
5619 ;*
5620 ;* INPUTS: R0 - TX,RX LPR CONTENTS FOR LINES IN GROUP II.
5621 ;* R1 - TX,RX LPR CONTENTS FOR LINES IN GROUP I.
5622 ;* R2 - START ADDRESS OF DATA PATTERN TO TRANSMIT.
5623 ;* R3 - NUMBER OF TIME DATA PATTERN TO BE TX ON LINES IN LINGRP1.
5624 ;* R4 - NUMBER OF TIME DATA PATTERN TO BE TX ON LINES IN LINGRP2.
5625 ;* ACTLNS - CONTAINS A BIT MAP OF ALL CURRENTLY ACTIVE LINES.
5626 ;* LGRP1M - CONTAINS THE BIT MAP OF LINE GROUP I LINES.
5627 ;* LOPBCK - CONTAINS THE TYPE OF LOOPBACK MODE SELECTED.
5628 ;* CBB - LABEL AT BASE OF TX/RX CONTROL BLOCK.
5629 ;*
5630 ;* OUTPUTS: THE CONTENTS OF THE CONTROL BLOCK ARE DESTROYED.
5631 ;* THE INDIRECT ADDRESS FIELD OF THE DUT CSR MAY BE DESTROYED.
5632 ;* THE DUT'S LPR'S AND LNC'S MAY BE MODIFIED.
5633 ;* THE FOLLOWING POINTERS AND COUNTERS ARE INITIALISED;
5634 ;* CHCNT,CHRTOT,DPEND,DPLEN,EXCNT,RXCNT,RXDONE RXPTR,TXCNT,
5635 ;* TXDONE,TXPTR,TXRXL.
5636 ;*
5637 ;* CALLING SEQUENCE: JSR PC,SPLSUP
5638 ;*
5639 ;* COMMENTS: THIS ROUTINE SHOULD BE CALLED TWICE DURING THE TESTING OF
5640 ;* THE SPLIT SPEED CAPABILITIES OF THE DUT.
5641 ;* SO THAT BOTH LINE GROUPS ARE TESTED ON TRANSMISSION AND
5642 ;* RECEPTION.
5643 ;* EG, R1 - LPR CONTENTS FOR LINES IN LGRP2M,TX=Y,RX=Z BAUD.
5644 ;* R2 - LPR CONTENTS FOR LINES IN LGRP1M,TX=Z,RX=Y BAUD.
5645 ;* R3 - REPEAT TX ON LINES IN LINE GROUP 1 = X TIMES.
5646 ;* R4 - REPEAT TX ON LINES IN LINE GROUP 2 = W TIMES.
5647 ;* JSR PC,SPLSUP ;DO SET-UP.
5648 ;* EXECUTE TEST FOR THE ABOVE SET-UP.
5649 ;* SWAP THE CONTENTS OF R1 AND R2.
5650 ;* SWAP THE CONTENTS OF R3 AND R4.
5651 ;* R1 - LPR CONTENTS FOR LINES IN LGRP2M,TX=Z,RX=Y BAUD.
5652 ;* R2 - LPR CONTENTS FOR LINES IN LGRP1M,TX=Y,RX=Z BAUD.
5653 ;* R3 - REPEAT TX ON LINES IN LINE GROUP 1 = W TIMES.
5654 ;* R4 - REPEAT TX ON LINES IN LINE GROUP 2 = X TIMES.
5655 ;* JSR PC,SPLSUP ;DO SET UP AGAIN.
5656 ;* EXECUTE TEST AGAIN.
5657 ;*
5658 ;* SUBORDINATE ROUTINES CALLED: CONMAP,RXDSBL,RXENBL,TXRINI.
5659 ;* - - - - -
5660
5661 024776 004567 160322 SPLSUP:: SAVE
5662 025002 010067 000264 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5663 025006 010167 000262 MOV R0,70H ;SAVE LPR PARAMETER FOR LINE GRP2.
5664 025012 005067 155464 MOV R1,72H ;SAVE LPR PARAMETER FOR LINE GRP1.
5665 025016 005067 155462 CLR TXDONE ;CLEAR THE TX DONE FLAGS FOR ALL LINES.
5666 CLR RXDONE ;CLEAR THE RX DONE FLAGS FOR ALL LINES.
5667 ;*
; SET UP THE TRANSMISSION/RECEPTION CONTROL BLOCK TO INITIALISE THE LINES

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5668      ; IN GROUP II.
5669      ;
5670 025022 010067 156074      MOV    R0,CBB      ;SET CONTENTS OF LPR PARAMS IN TX/RX C.BLK.
5671 025026 012700 003124      MOV    #CBB+2,R0      ;GET BASE ADDRESS OF CONTROL BLOCK.
5672 025032 012720 000004      MOV    #4,(R0)+      ;LNCTRL PARAMETER, ENABLE RECEIVERS.
5673 025036 010220      MOV    R2,(R0)+      ;START ADDRESS OF DATA PATTERN.
5674 025040 012720 000020      MOV    #16,(R0)+      ;DATA PATTERN LENGTH SET TO 16.
5675 025044 010420      MOV    R4,(R0)+      ;NUMBER OF DATA PATTNS TO TRANSMIT ON LINGRP2.
5676 025046 016710 155120      MOV    ACTLNS,(R0)      ;BIT MAP OF LINES TO INITIALISE.
5677 025052 046720 155160      BIC    LGRP1M,(R0)+      ;CLEAR THE UNWANTED LINES FROM BIT MAP.
5678 025056 116720 155112      MOV    LOPBCK,(R0)+      ;SET LOOPBACK MODE.
5679 025062 005200      INC    R0      ;INCREMENT ADDRESS TO ACCESS NEXT WORD.
5680 025064 012710 000002      MOV    #2,(R0)      ;SET OFFSET FOR EACH TRANSMISSION START TO 2.
5681      ;
5682      ; INITIALISE THE DUT AND THE ASSOCIATED POINTERS AND COUNTERS, TO THE STATE
5683      ; DICTATED BY THE CONTENTS OF THE TX/RX CONTROL BLOCK.
5684      ;
5685 025070 004767 000702      JSR    PC,TXRINI      ;INITIALISE DUT.
5686      ;
5687      ; SET UP CONTROL BLOCK FOR LINES IN GROUP I.
5688      ;
5689 025074 012700 003122      MOV    #CBB,R0      ;GET START ADDRESS OF CONTROL BLOCK.
5690 025100 010120      MOV    R1,(R0)+      ;SET LPR PARAMETER FOR LINES TO RECEIVE DATA.
5691 025102 012720 000004      MOV    #4,(R0)+      ;LNCTRL PARAMETER, ENABLE RECEIVERS.
5692 025106 010220      MOV    R2,(R0)+      ;START ADDRESS OF DATA PATTERN.
5693 025110 012720 000020      MOV    #16,(R0)+      ;DATA PATTERN LENGTH SET TO 16.
5694 025114 010320      MOV    R3,(R0)+      ;NUMBER OF DATA PATTNS TO TRANSMIT ON LINGRP1.
5695 025116 016710 155050      MOV    ACTLNS,(R0)      ;BIT MAP OF LINES TO INITIALISE.
5696 025122 046720 155112      BIC    LGRP2M,(R0)+      ;CLEAR THE UNWANTED LINES FROM BIT MAP.
5697 025126 116720 155042      MOV    LOPBCK,(R0)+      ;SET LOOPBACK MODE.
5698 025132 005200      INC    R0      ;INCREMENT ADDRESS TO ACCESS NEXT WORD.
5699 025134 012710 000002      MOV    #2,(R0)      ;SET OFFSET FOR EACH TRANSMISSION START TO 2.
5700      ;
5701      ; INITIALISE THE DUT AND THE ASSOCIATED POINTERS AND COUNTERS, TO THE STATE
5702      ; DICTATED BY THE CONTENTS OF THE TX/RX CONTROL BLOCK.
5703      ;
5704 025140 004767 000632      JSR    PC,TXRINI      ;INITIALISE DUT.
5705      ;
5706      ;
5707      ; SET-UP THE REQUIRED LPR PARAMETERS NEEDED FOR THE CORRECT RECEPTION OF DATA
5708      ; ON ASSOCIATED IN-ACTIVE LINES.
5709      ;
5710      ;
5711      ; INITIALISE LPR PARAMETERS FOR LINE GROUP 1.
5712      ;
5713 025144 012701 177777      MOV    #MAPLNS,R1      ;SET UP BIT MAP CORRESPONDING TO ALL LINES.
5714 025150 016702 155016      MOV    ACTLNS,R2      ;GET THE ACTIVE (TX) LINE BIT MAP.
5715 025154 005101      COM    R1      ;GENERATE A BIT MAP OF NONE EXISTANT LINES.
5716 025156 005102      COM    R2      ;GENERATE A BIT MAP OF INACTIVE LINES.
5717 025160 040102      BIC    R1,R2      ;CLEAR ANY "NONE EXISTANT" INACTIVE LINES.
5718 025162 046702 155052      BIC    LGRP2M,R2      ;ONLY PASS LGRP1 ASSOCIATED LINE BIT MAP.
5719 025166 010267 155742      MOV    R2,CBMAPA      ;SET UP BIT MAP IN CONTROL BLOCK.
5720 025172 005067 155734      CLR    CBDPNA      ;CLEAR REPEAT TX COUNT IN CONTROL BLOCK.
5721 025176 016767 000072      MOV    72,CBLPRA      ;SET-UP COMPLEMENTARY LPR PARM FOR LGRP2.
5722 025204 004767 000566      JSR    PC,TXRINI      ;INITIALISE INACTIVE LINES IN LGRP2.
5723      ;
5724      ; INITIALISE LPR PARAMETERS FOR LINE GROUP 2.

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5725
5726 025210 016702 154756      ; -      MOV      ACTLNS,R2      ;GET THE ACTIVE (TX) LINE BIT MAP.
5727 025214 005102              COM      R2      ;GENERATE A BIT MAP OF INACTIVE LINES.
5728 025216 040102              BIC      R1,R2      ;CLEAR ANY NONE EXISTANT INACTIVE LINES.
5729 025220 046702 155012      BIC      LGRP1M,R2      ;ONLY PASS LGRP2 ASSOCIATED LINE BIT MAP.
5730 025224 010267 155704      MOV      R2,CBMAPA      ;SET-UP BIT MAP IN CONTROL BLOCK.
5731 025230 016767 000036 155664 MOV      70$,CBLPRA      ;SET-UP COMPLEMENTARY LPR PARAM FOR LGRP1.
5732 025236 004767 000534      JSR      PC,TXRINI      ;INITIALISE INACTIVE LINES IN LGRP1.
5733
5734      ; -
5735      ; *
5736      ; DISABLE RECEIVERS ON ALL LINES TO ENSURE THAT ONLY THE RECEIVERS OF THE
5737      ; ASSOCIATED ACTIVE (TX) LINES ARE ENABLED.(STAGGARED LOOPBACK)
5738      ; RE-ENABLE RECEPTION ON THE CORRECT ASSOCIATED LINES.
5739 025242 012705 177777      ; -      MOV      $MAPLNS,R5      ;SET-UP BIT MAP FOR ALL LINES.
5740 025246 004767 177124      JSR      PC,RXDSBL      ;DISABLE RX ON ALL LINES.
5741
5742      ; *
5743      ; ENABLE RECEIVERS ON ASSOCIATED (RX) LINES.
5744 025252 016705 154714      ; -      MOV      ACTLNS,R5      ;GET ACTIVE (TX) LINE BIT MAP.
5745 025256 004767 172212      JSR      PC,CONMAP      ;GENERATE AN ASSOCIATED (RX) LINE BIT MAP.
5746 025262 004767 177204      JSR      PC,RXENBL      ;ENABLE RECEIVERS ON ASSOCIATED LINES.
5747 025266
5748 025270 000207              60$: PASS                      ;RESTORE GRP'S.
5749 025272 000000              RTS      PC      JSR      PC,$(SP)+      ;RETURN TO PREG05 SUBRT.
5750 025274 000000              70$: .WORD 0
5751 025276 000000              72$: .WORD 0
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5752 .SBTTL GLOBAL SUBROUTINE - STPSW -
5753 ;* *****
5754 ;* - SET PROCESSOR STATUS WORD -
5755 ;* THIS ROUTINE SETS THE PSW TO THE CONTENTS OF R1.
5756 ;*
5757 ;* INPUTS: R1 - CONTAINS THE NEW PSW SETTINGS
5758 ;*
5759 ;* OUTPUTS: PSW - SET TO THE CONTENTS OF R1
5760 ;*
5761 ;* CALLING SEQUENCE: JSR PC,STPSW
5762 ;*
5763 ;* COMMENTS: USED IN THE DMA ADDRESS TEST TO SET THE PROCESSOR
5764 ;* PRIORITY WITHOUT MAKING A CALL TO THE DRS.
5765 ;*
5766 ;* SUBROUTINES CALLED: NONE.
5767 ;* *****
5768
5769 025276 STPSW:: SAVE
5770 025276 004567 160022 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5771 025302 010146 MOV R1,-(SP) ;PUSH THE NEW PSW CONTENTS ONTO THE STACK
5772 025304 012746 025312 MOV #ADDR,-(SP) ;PUSH THE NEW PC VALUE ONTO THE STACK
5773 025310 000002 RTI ;LOAD THE NEW PC AND PSW
5774 025312 004736 ADDR: PASS ;
5775 025314 000207 RTS PC JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
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5801 025316 004567 160002
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5808 025322 016703 154644
5809 025326 016702 155150
5810 025332 040203
5811 025334 005703
5812 025336 001427
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5819 025340 005004
5820 025342 012702 000001
5821 025346 030203
5822 025350 001003
5823 025352 006102
5824 025354 005204
5825 025356 000773
5826 025360 006304
5827 025362 016401 003442
5828 025366 016702 154654
5829 025372 004767 174134
5830 025376 006301
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5835 025400 016702 154566

.SBTTL GLOBAL SUBROUTINE - TXDONE -
;+ *****
;+ - TRANSMISSION DONE -
;+ THIS SUBROUTINE IS USED IN THE TRANSMISSION/RECEPTION TESTS TO ALLOW
;+ TIME FOR TRANSMISSION TO COMPLETE ON OUTSTANDING LINES.
;+
;+ INPUTS: ACTLNS - CONTAINS BIT MAP OF ALL ACTIVE LINES.
;+ TXDNF - TX DONE FLAGS, SET FOR LINES THAT HAVE SENT ALL CHARS.
;+ CHCNT - TABLE CONTAINING THE NUMBER OF CHARS TO BE TX'D.
;+
;+ OUTPUTS: TRANSMISSION INTERRUPTS ARE DISABLED.
;+
;+ CALLING SEQUENCE: JSR PC,TXDONE
;+
;+ COMMENTS:
;+
;+ SUBORDINATE ROUTINES CALLED: MSLOOP,MUL16U.
;+ *****
TXDONE:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
;+ JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
;+
;+ CHECK IF ALL ACTIVE LINES HAVE COMPLETED TRANSMISSION.
;+ IF ANY HAVE NOT YET COMPLETED, DETERMINE THE TX CHAR COUNT FOR A
;+ LINE THAT HAS OUTSTANDING CHARACTERS TO TRANSMIT. USING THIS VALUE,
;+ CALCULATE THE TIME-OUT VALUE NEEDED AT THE CURRENTLY SELECTED BAUD RATE.
;+
;+ MOV ACTLNS,R3 ;GET THE ACTIVE LINE BIT MAP.
;+ MOV TXDNF,R2 ;GET THE BIT MAP OF LINES THAT HAVE COMPLETED.
;+ BIC R2,R3 ;GENERATE A BIT MAP OF LINES THAT ARE STILL TX.
;+ TST R3 ;CHECK IF ALL LINES HAVE COMPLETED TX.
;+ BEQ 64 ;GO DISABLE TX INTERRUPTS IF ALL DONE.
;+
;+ FIND A LINE THAT HAS NOT COMPLETED TRANSMISSION.
;+ OBTAIN THE EXPECTED CHARACTER COUNT FOR THAT LINE (WHICH IS THE SAME FOR
;+ ALL OTHER LINES WITH OUTSTANDING TX'S).
;+ CALCULATE TIME-OUT VALUE.
;+
;+ CLR R4 ;CLEAR LINE NUMBER COUNTER.
;+ MOV #1,R2 ;SELECT BIT MAP FOR THE FIRST LINE.
24: BIT R2,R3 ;SEE IF THIS LINE HAS COMPLETED.
;+ BNE 44 ;BRANCH IF THIS LINE HAS NOT COMPLETED TX.
;+ ROL R2 ;SHIFT THE LINE BIT MAP FOR THE NEXT LINE.
;+ INC R4 ;INCREMENT THE LINE NUMBER COUNTER.
;+ BR 24 ;LOOP TO CHECK THE NEXT LINE.
44: ASL R4 ;LINE NUMBER X 2 TO OBTAIN OFFSET INTO TABLE.
;+ MOV CHCNTB(R4),R1 ;GET THE EXPECTED NUMBER OF CHARS TO BE TX'D.
;+ MOV RXTOUT,R2 ;GET THE CURRENT TIME-OUT VALUE FOR ONE CHAR.
;+ JSR PC,MUL16U ;(NUMBER OF CHARS TO TX) X (TIME-OUT OF 1 CHAR)
;+ ASL R1 ;MULTIPLY DELAY TIME BY 2 TO GIVE A SAFE VALUE.
;+
;+ WAIT FOR ALL OUTSTANDING TRANSMISSIONS TO COMPLETE OR TIME-OUT.
;+ DISABLE ALL TRANSMISSION INTERRUPTS.
;+
;+ MOV ACTLNS,R2 ;PASS A BIT MAP OF THE BITS TO TEST.

```

```
5836 025404 010203          MOV    R2,R3          ;PASS THE EXPECTED STATE OF THE TXDNF.
5837 025406 012704 002502    MOV    #TXDNF,R4      ;PASS THE ADDRESS OF THE WORD TO TEST.
5838 025412 004767 173664    JSR     PC,MSLOOP      ;WAIT FOR TIME-OUT OF TX COMPLETION.
5839 025416 004767 000270    64:    JSR     PC,TXIE0    ;DISABLE ALL TX INTERRUPTS.
5840
5841 025422          604:    PASS
      025422 004736          ;RESTORE GPRS.
5842 025424 000207          RTS    PC              JSR     PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
```

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5867			
5868	025426		
	025426	004567	157672
5869	025432	010500	
5870	025434	012701	000001
5871	025440	016702	154550
5872	025444	005202	
5873	025446	012703	000020
5874	025452	016704	154556
5875	025456	005005	
5876			
5877			
5878			
5879	025460	014477	154514
5880	025464	105712	
5881	025466	100001	
5882	025470	050105	
5883			
5884			
5885			
5886			
5887	025472	030100	
5888	025474	001402	
5889	025476	142712	000200
5890	025502	005204	
5891	025504	006301	
5892	025506	005303	
5893	025510	001363	
5894			
5895	025512		
	025512	010566	000014
	025516	004736	
5896			
5897	025520	000207	

```

.SBTTL  GLOBAL SUBROUTINE                                - TXDSBL -
;+ *****
;+ - TRANSMITTER DISABLE -
;+ THIS SUBROUTINE IS USED TO DISABLE TRANSMISSION ON SELECTED LINES BY,
;+ CLEARING THE ASSOCIATED TX.ENABLE BIT ON THE DUT.
;+
;+ INPUTS:      R5 - BIT'S SET CORRESPOND TO LINES ON WHICH TO CLEAR TX.ENABLE.
;+              CSRA - CONTAINS THE ADDRESS OF THE DUT CSR.
;+              IESTAT - CONTAINS THE STATE OF TXIE AND RXIE BITS IN THE CSR.
;+              NUMLNS - EQUATED TO BE THE MAXIMUM NUMBER OF LINES AVAILABLE.
;+              TXAD2A - CONTAINS THE ADDRESS OF THE TBUFFAD2 REGISTER.
;+
;+ OUTPUTS:     R5 - BIT'S SET INDICATE THE INITIAL STATES OF ALL TX.ENABLE BITS.
;+              TBUFFAD2 - THE STATE OF THE TX.ENABLE BIT MAY BE ALTERED.
;+              THE CONTENTS OF THE IND.ADD.REG FIELD IN THE CSR ARE DESTROYED.
;+
;+ CALLING SEQUENCE:  JSR      PC,TXDSBL
;+
;+ COMMENTS:
;+
;+ SUBORDINATE ROUTINES CALLED:  NONE.
;+ *****
TXDSBL::  SAVE                                ;SAVE CONTENTS OF GPRS R0 THRU R5.
;+
;+              JSR      R5,PREG05          ;CALL REGISTER SAVE SUBRT.
;+              MOV      R5,R0              ;COPY BIT MAP OF LINES TO DISABLE TRANSMISSION.
;+              MOV      #BIT0,R1          ;INITIALIZE THE SELECTED LINE BIT MASK.
;+              MOV      TXAD2A,R2         ;GET THE ADDRESS OF THE TBUFFAD2 REGISTER.
;+              INC      R2                 ;GET THE ADDRESS OF THE MSBYTE OF TBUFFAD2 REG.
;+              MOV      #NUMLNS,R3        ;GET MAXIMUM LINE NUMBER PLUS ONE.
;+              MOV      IESTAT,R4         ;GET THE STATES OF THE INT ENABLE BITS.
;+              CLR      R5                ;LOG POSSIBLE TX DISABLED ON ALL LINES.
;+
;+ SELECT EVERY LINE IN TURN, AND LOG THE STATE OF EACH TX.ENABLE BIT.
;+
20:      MOV      R4,BCSRA                ;WRITE TO DUT CSR TO SELECT LINE REGISTERS.
;+              TSTB     (R2)              ;CHECK STATE OF TX.ENABLE BIT ON SELECTED LINE.
;+              BPL      40                ;SKIP NEXT INSTRUCTION IF TX.ENABLE CLEAR.
;+              BIS      R1,R5             ;LOG TX ENABLE BIT SET FOR SELECTED LINE.
;+
;+ CLEAR TX.ENABLE ON LINES THAT HAVE A CORRESPONDING BIT SET IN THE TX DISABLE
;+ LINE BIT MAP.
;+
40:      BIT      R1,R0                    ;CHECK STATE OF DISABLE LINE BIT MAP.
;+              BEQ      60                ;BRANCH IF THIS LINE TO REMAIN UNALTERED.
;+              BICB     #BIT7,(R2)       ;CLEAR TX.ENABLE BIT ON SELECTED LINE.
;+              INC      R4                ;PREPARE TO SELECT REGISTERS FOR NEXT LINE.
;+              ASL      R1                 ;SHIFT BIT MAP FOR NEXT LINE.
;+              DEC      R3                ;DECREMENT LINE NUMBER.
;+              BNE      2'                ;LOOP TO CHECK NEXT LINE.
;+
60:      PASS      R5                      ;RESTORE GPRS,EXCEPT
;+              MOV      R5,R5SLOT(SP)    ;PUT R5 IN STACK SLOT.
;+              JSR      PC,@(SP)+         ;RETURN TO PREG05 SUBRT.
;+              ;R5 - PREVIOUS STATES OF ALL TX.ENABLE BITS.
;+
RTS      PC

```

```

5899 .SBTTL GLOBAL SUBROUTINE - TXENBL -
5900 ;+ *****
5901 ;* - TRANSMITTER ENABLE -
5902 ;* THIS SUBROUTINE IS USED TO ENABLE TRANSMISSION ON SELECTED LINES BY
5903 ;* SETTING THE ASSOCIATED TX.ENABLE BIT ON THE OUT.
5904 ;*
5905 ;* INPUTS: R5 - BIT'S SET CORRESPOND TO LINES ON WHICH TO SET TX.ENABLE.
5906 ;* CSRA - CONTAINS THE ADDRESS OF THE OUT CSR.
5907 ;* IESTAT - CONTAINS THE STATE OF TXIE AND RXIE BITS IN THE CSR.
5908 ;* NUMLNS - EQUATED TO BE THE MAXIMUM NUMBER OF LINES AVAILABLE.
5909 ;* TXAD2A - CONTAINS THE ADDRESS OF THE TBUFAD2 REGISTER.
5910 ;*
5911 ;* OUTPUTS: R5 - BIT'S SET INDICATE PREVIOUSLY DISABLED LINES.
5912 ;* TBUFAD2 - THE STATE OF THE TX.ENABLE BIT MAY BE ALTERED.
5913 ;* THE CONTENTS OF THE IND.ADD.REG FIELD IN THE CSR ARE DESTROYED.
5914 ;*
5915 ;* CALLING SEQUENCE: JSR PC,TXENBL
5916 ;*
5917 ;* COMMENTS:
5918 ;*
5919 ;* SUBORDINATE ROUTINES CALLED: NONE.
5920 ;-- *****
5921
5922 TXENBL:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5923 025522 004567 157576 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5924 025526 010500 MOV R5,R0 ;COPY BIT MAP OF LINES TO ENABLE.
5925 025530 012701 000001 MOV #BIT0,R1 ;INITIALIZE THE SELECTED LINE BIT MASK.
5926 025534 016702 154454 MOV TXAD2A,R2 ;GET THE ADDRESS OF THE TBUFAD2 REGISTER.
5927 025540 005202 INC R2 ;GET THE ADDRESS OF THE MSBYTE OF TBUFAD2 REG.
5928 025542 012703 000020 MOV #NUMLNS,R3 ;GET MAXIMUM LINE NUMBER.
5929 025546 016704 154462 MOV IESTAT,R4 ;GET THE STATES OF THE INT ENABLE BITS.
5930 025552 005005 CLR R5 ;CLEAR TX.ENABLE BIT LOG OF DISABLED LINES.
5931 ;+
5932 ; SELECT EVERY LINE IN TURN,AND LOG ANY TX.ENABLE BIT THAT IS CLEAR.
5933 025554 010477 154420 2+: MOV R4,BCSRA ;WRITE TO OUT CSR TO SELECT LINE REGISTERS.
5934 025560 105712 TSTB (R2) ;CHECK STATE OF TX.ENABLE BIT ON SELECTED LINE.
5935 025562 100401 BMI 4+ ;SKIP NEXT INSTRUCTION IF TX.ENABLE SET.
5936 025564 050105 BIS R1,R5 ;LOG TX ENABLE BIT CLEAR FOR SELECTED LINE.
5937 ;+
5938 ; SET TX.ENABLE ON LINES THAT HAVE A CORRESPONDING BIT SET IN THE TX ENABLE
5939 ; LINE BIT MAP.
5940 ;+
5941 025566 030100 4+: BIT R1,R0 ;CHECK STATE OF TX.ENABLE LINE BIT MAP.
5942 025570 001402 BEQ 6+ ;BRANCH IF THIS LINE TO REMAIN UNALTERED.
5943 025572 152712 000200 BISB #BIT7,(R2) ;ENABLE TRANSMISSION ON SELECTED LINE.
5944 025576 005204 6+: INC R4 ;PREPARE TO SELECT REGISTERS FOR NEXT LINE.
5945 025600 006301 ASL R1 ;SHIFT BIT MAP FOR NEXT LINE.
5946 025602 005303 DEC R3 ;DECREMENT LINE NUMBER.
5947 025604 001363 BNE 2+ ;LOOP TO CHECK NEXT LINE.
5948
5949 025606 60+: PASS R5 ;RESTORE GPRS,EXCEPT
5950 025606 010566 000014 MOV R5,R5SLOT(SP) ;PUT R5 IN STACK SLOT.
5951 025612 004736 JSR PC,@(SP)+ ;RETURN TO PREG05 SUBRT.
5952 025614 000207 RTS PC ;R5 - LINE BIT MAP CORRESPONDING TO THE
; PREVIOUS LINES THAT WERE DISABLED.

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5954 .SBTTL GLOBAL SUBROUTINE - TXFRPR -
5955 ;* *****
5956 ;* - TRANSMIT FRAMMING ERROR DATA ROUTINE -
5957 ;* THIS ROUTINE IS USED TO INITIATE DMA MODE TRANSMISSION
5958 ;* IN THE FRAMMING ERROR TEST. IT SENDS A SINGLE CHARACTER DMA BUFFER ON
5959 ;* EACH ACTIVE LINE IN THE BIT MAP, TO CAUSE FUTURE TX INTERRUPTS WHICH
5960 ;* WILL CONTINUE THE TRANSMISSION IF MORE THAN ONE BUFFER IS TO BE SENT.
5961 ;*
5962 ;* INPUTS: R4 - CONTAINS THE LINES ON WHICH TX IS TO TAKE PLACE.
5963 ;* ACTLNS - ACTIVE LINES BIT MAP.
5964 ;* BITTBL - LABEL OF TABLE OF WORDS EACH WITH A BIT SET.
5965 ;* CSRA - CONTAINS THE ADDRESS OF THE DUT CSR.
5966 ;* DPENDB - BASE OF THE DATA PATTERN END TABLE (ENTRY PER LINE).
5967 ;* DPLENB - BASE OF THE DATA PATTERN LENGTH TABLE.
5968 ;* IESTAT - PRESERVED STATES OF THE DUT INTERRUPT ENABLE BITS.
5969 ;* NUMLNS - EQUATED TO NUMBER OF LINES ON A DUT.
5970 ;* TXCNTB - LABEL AT BASE OF THE TX CHARACTER COUNTER TABLE.
5971 ;* TXPTRB - LABEL AT BASE OF THE TX DATA PATTERN POINTERS TABLE.
5972 ;*
5973 ;* OUTPUTS: CSR - DUT CSR IND.ADR.REG FIELD IS DESTROYED.
5974 ;* TXCNTX - COUNTERS INCREMENTED FOR LINES ON WHICH CHARS SENT.
5975 ;* TXINTF - TX INT FLAGS (BIT SET IF DMA.HO FOUND SET ON LINE).
5976 ;*
5977 ;* CALLING SEQUENCE: JSR PC,TXFRPR
5978 ;*
5979 ;* COMMENTS: THIS ROUTINE ASSUMES THAT AT LEAST ONE DATA PATTERN SHOULD BE
5980 ;* TRANSMITTED ON EACH ACTIVE LINE.
5981 ;* INTERRUPTS MUST BE DISABLED WHEN CALLING THIS ROUTINE.
5982 ;*
5983 ;* SUBORDINATE ROUTINES CALLED: DODMA.
5984 ;* *****
5985 TXFRPR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
5986 ; JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
5987 ; MOV ACTLNS,R5 ;GET THE ACTIVE LINE BIT MAP.
5988 ; COM R4 ;GET BIT MAP OF LINES THAT WILL RECEIVE DATA.
5989 ; BIC R4,R5 ;CLEAR LINES THAT WILL RX FROM TX LINE BIT MAP.
5990 ;*
5991 ; SET UP LOOP WHICH HANDLES ONE LINE PER ITERATION.
5992 ;*
5993 ; CLR R1 ;CLEAR THE LINE NUMBER COUNTER.
5994 ;*
5995 ; IF THE LINE IS INACTIVE SKIP TO SELECT THE NEXT LINE.
5996 ;*
5997 2: CLC ;CLEAR BOOLEAN REGISTER.
5998 ; ROR R5 ;SHIFT BIT MAP OF LINES TO TX ON INTO BOOL.REG.
5999 ; BCC 6: ;DON'T TX ON THIS LINE IF IT IS NOT ACTIVE.
6000 ;*
6001 ; LINE IS ACTIVE.
6002 ; INITIATE DMA ON THIS LINE.
6003 ; GET THE DATA PATTERN LENGTH FOR THIS LINE.
6004 ;*
6005 ; MOV R1,R4 ;COPY LINE NUMBER.
6006 ; ASL R4 ;CALCULATE WORD OFFSET FOR THIS LINE.
6007 ; MOV DPLENB(R4),R3 ;GET DATA PATTERN LENGTH FOR THIS LINE.
6008 ; MOV TXPTRB(R4),R2 ;PREPARE TO PASS DATA PATTERN ADR TO DODMA RTN.
6009 ;*
; WRITE DMA PARAMETERS TO THE DUT.

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5985 025616
5986 025616 004567 157502
5987 025622 016705 154344
5988 025626 005104
5989 025630 040405
5992 025632 005001
5996 025634 000241
5997 025636 006005
5998 025640 103017
6004 025642 010104
6005 025644 006304
6006 025646 016403 003202
6007 025652 016402 003342

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6010
6011 025656 004767 172122      JSR    PC,DODMA
6012 025662 103404              BCS    4:      ;SKIP ERROR IF DODMA WAS SUCCESSFUL.
6013
6014                          ; SET THE PROPER BIT OF THE TX INTERRUPT FLAGS TO INDICATE THE LINE ERROR.
6015                          ;
6016 025664 056467 002364 154372  BIS    BITBL(R4),TXINTF      ;INDICATE THE ERROR.
6017 025672 000402              BR     6:      ;SKIP UPDATING POINTERS AND COUNTERS.
6018
6019                          ; UPDATE THE TX CHARACTER COUNT FOR THIS LINE.
6020                          ;
6021 025674 060364 003502 4:      ADD    R3,TXCNTB(R4)      ;ADD THE DATA PATTERN LENGTH TO TX CHAR COUNT.
6022                          ;
6023                          ; INCREMENT LINE COUNTER,GOTO NEXT LINE IF NOT DONE.
6024                          ;
6025 025700 005201 6:      INC    R1              ;INCREMENT THE LINE COUNTER.
6026 025702 005705          TST    R5              ;TEST THE TX LINE BIT MAP.
6027 025704 001353          BNE    2:              ;LOOP TO SEND CHAR TO ANOTHER LINE IF NOT DONE.
6028
6029 025706 60:      PASS              ;RESTORE GPRS.
        025706 004736          JSR    PC,8(SP).      ;RETURN TO PREG05 SUBRT.
6030 025710 000207          RTS    PC
  
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6032 .SBTTL GLOBAL SUBROUTINE - TXIEO -
6033 ;* *****
6034 ;* - TRANSMITTER INTERRUPT DISABLE -
6035 ;* THIS ROUTINE IS USED TO DISABLE TRANSMITTER INTERRUPTS IN THE DHU11.
6036 ;*
6037 ;* INPUTS: NONE.
6038 ;*
6039 ;* OUTPUTS: THE TX.INT.ENBL BIT IS CLEARED IN THE DUT CSR.
6040 ;* IESTST -CONTAINS THE UPDATED STATUS OF THE TX AND RX INTERRUPT
6041 ;* ENABLE BITS.
6042 ;*
6043 ;* CALLING SEQUENCE: JSR PC,TXIEO
6044 ;*
6045 ;* COMMENTS: THE CONTENTS OF THE INDIRECT ADDRESS REGISTER FIELD IN
6046 ;* THE DUT CSR ARE DESTROYED.
6047 ;*
6048 ;* SUBORDINATE ROUTINES CALLED: NONE.
6049 ;* *****
6050 TXIEO: MOV R0,-(SP) ;SAVE CONTENTS OF R0 ON THE STACK.
6051 GETPRI -(SP) ;SAVE CURRENT PROCESSOR PRIORITY ON THE STACK.
        TRAP C$GPRI
        MOV R0,-(SP)
6052 SETPRI $PRI07 ;IGNORE ANY INTERRUPTS THAT MAY BE GENERATED.
        MOV $PRI07,R0
        TRAP C$SPRI
        025712 010046
        025714 104440
        025716 010046
        025720 012700 000340
        025724 104441
6053 025726 042767 177677 154300 BIC #177677,IESTAT ;CLEAR TX.INT.ENBL BIT IN IESTAT.
6054 025734 016777 154274 154236 MOV IESTAT,$CSRA ;DISABLE TX INTERRUPTS.
6055 025742 SETPRI (SP)+ ;ENABLE INTERRUPTS TO THE PROCESSOR AGAIN.
        MOV (SP)+,R0
        TRAP C$SPRI
        025742 012600
        025744 104441
6056 025746 012600 MOV (SP)+,R0 ;RESTORE R0.
6057 025750 000207 RTS PC

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6059      .SBTTL GLOBAL SUBROUTINE                      - TXIE1 -
6060      ;* *****
6061      ;* - TRANSMITTER INTERRUPT ENABLE -
6062      ;* THIS ROUTINE IS USED TO ENABLE TRANSMITTER INTERRUPTS IN THE DHU11.
6063      ;*
6064      ;* INPUTS:      NONE.
6065      ;*
6066      ;* OUTPUTS:     THE TX.INT.ENBL BIT IS SET IN THE DUT CSR.
6067      ;*               IESTST -CONTAINS THE UPDATED STATUS OF THE TX AND RX INTERRUPT
6068      ;*               ENABLE BITS.
6069      ;*
6070      ;* CALLING SEQUENCE:  JSR      PC,TXIE1
6071      ;*
6072      ;* COMMENTS:     THE CONTENTS OF THE INDIRECT ADDRESS REGISTER FIELD IN
6073      ;*               THE DUT CSR ARE DESTROYED.
6074      ;*
6075      ;* SUBORDINATE ROUTINES CALLED: NONE.
6076      ;* -- *****
6077
6078 025752 052767 040000 154254 TXIE1:: BIS      #BIT14,IESTAT ;SET TX.INT.ENBL BIT IN IESTAT.
6079 025760 042767 137677 154246      BIC      #137677,IESTAT ;CLEAR ALL BITS EXCEPT TX RX I.E BITS.
6080 025766 016777 154242 154204      MOV      IESTAT,#CSRA ;ENABLE TX INTERRUPTS.
6081 025774 000207                      RTS      PC

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6083 .SBTTL GLOBAL SUBROUTINE - TXRINI -
6084 ;* *****
6085 ;* - TRANSMIT AND RECEIVE INITIALIZATION ROUTINE -
6086 ;* THIS SUBROUTINE PERFORMS THE INITIALIZATION OF THE VARIOUS POINTERS,
6087 ;* COUNTERS, AND FLAGS WHICH ARE USED DURING THE TRANSMISSION AND
6088 ;* RECEPTION PORTION OF A TEST. THIS INITIALIZATION IS PERFORMED ON
6089 ;* THE SPECIFIED LINES ONLY, OTHER LINE VARIABLES REMAIN UNCHANGED.
6090 ;*
6091 ;* INPUTS:
6092 ;* CHCNTB - LABEL AT BASE OF LINE CHARACTER COUNT TABLE.
6093 ;* CHRTOT - MAX # OF CHARS TO RX ON LINES ALREADY INITIALIZED.
6094 ;* DPENDB - LABEL AT BASE OF LINE DATA PATTERN END TABLE.
6095 ;* DPLENB - LABEL AT BASE OF LINE DATA PATTERN LENGTH TABLE.
6096 ;* EXCNTB - LABEL AT BASE ADDRESS OF EXTRA CHAR COUNTERS TABLE.
6097 ;* IESTAT - PRESENT STATE OF THE RX.IE AND TX.IE BITS.
6098 ;* NUMLNS - EQUATED TO NUMBER OF LINES ON THE DUT.
6099 ;* RXCNTB - LABEL AT BASE ADDRESS OF RX CHARACTER COUNTERS TABLE.
6100 ;* RXPTRB - LABEL AT BASE ADR OF "NEXT RX CHAR" POINTERS TABLE.
6101 ;* TXCNTB - LABEL AT BASE ADDRESS OF TX CHARACTER COUNTERS TABLE.
6102 ;* TXPTRB - LABEL AT BASE ADR OF "NEXT TX CHAR" POINTERS TABLE.
6103 ;* CBB - LABEL AT BASE OF TX/RX CONTROL BLOCK.
6104 ;* CB CONTENTS - TX/RX CONTROL BLOCK CONTAINS THE FOLLOWING:
6105 ;* CBLPRA - DUT LPR CONTENTS.
6106 ;* CBLNCA - DUT LNCTRL CONTENTS.
6107 ;* CBDPAA - ADDRESS OF BEGINNING OF DATA PATTERN.
6108 ;* CBDPLA - LENGTH IN BYTES OF DATA PATTERN.
6109 ;* CBDPNA - NUMBER OF DATA PATTERNS TO TRANSMIT.
6110 ;* CBMAPA - BIT MAP OF LINES TO BE INITIALIZED.
6111 ;* CBLPBA - TYPE OF LOOPBACK TO BE USED FOR TEST.
6112 ;* CBOFSA - AMOUNT TO OFFSET EACH TX START IN THE DATA PAT.
6113 ;* TXRXLB - LABEL AT BASE OF TX/RX LINE ASSOCIATION TABLE.
6114 ;*
6115 ;* OUTPUTS:
6116 ;* CHCNT - TABLE OF NUMBER OF LINE TX CHARACTERS (INITIALIZED).
6117 ;* CHRTOT - MAXIMUM NUMBER OF CHARS TO RECEIVE (2 * PAT LENGTH).
6118 ;* DPEND - TABLE OF DATA PATTERN ENDS (INITIALIZED).
6119 ;* DPLEN - TABLE OF DATA PATTERN LENGTHS (INITIALIZED).
6120 ;* DUT LNCTRL - LINE CONTROL REGISTERS (INITIALIZED).
6121 ;* DUT LPR - LINE PARAMETER REGISTERS (INITIALIZED).
6122 ;* EXCNT - TABLE OF EXTRA RX CHAR COUNTS (Cleared, SELECTED LINES).
6123 ;* RXCNT - TABLE OF RX CHARACTER COUNTS (Cleared, SELECTED LINES).
6124 ;* RXDONF - "RECEPTION DONE" FLAGS (Cleared FOR SELECTED LINES).
6125 ;* RXPTR - TABLE OF RECEIVE POINTERS (INITIALIZED).
6126 ;* TXCNT - TABLE OF TX CHARACTER COUNTERS (Cleared, SELECTED LINES).
6127 ;* TXDONF - "TRANSMISSION DONE" FLAGS (Cleared FOR SELECTED LINES).
6128 ;* TXPTR - TABLE OF TRANSMIT POINTERS (INITIALIZED).
6129 ;* TXRXL - TX/RX LINE ASSOCIATION TABLE (INITIALIZED).
6130 ;*
6131 ;* CALLING SEQUENCE: JSR PC, TXRINI
6132 ;*
6133 ;* COMMENTS: IF THE CALCULATION OF THE CHRTOT VALUE (2 TIMES THE DATA
6134 ;* PATTERN LENGTH) RESULTS IN A NUMBER GREATER THAN 64K THEN
6135 ;* CHRTOT IS INITIALIZED TO 64K - 1.
6136 ;* THIS ROUTINE WILL NOT FORCE INTERNAL LOOPBACK BASED ON THE
6137 ;* LOOPBACK TYPE IN CBLPBA. THE USER MUST SET UP CBLNCA CORRECTLY
6138 ;* TO GET INTERNAL LOOPBACK.
6139 ;*
6140 ;* SUBORDINATE ROUTINES CALLED: WTLNLC, WTLNPR,
6141 ;* *****

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6140 025776      TXRINI:: SAVE
        025776 004567 157322      JSR      R5,PREG05      ;SAVE CONTENTS OF GPRS R0 THRU R5.
                                      ;CALL REGISTER SAVE SUBRT.
6141
6142      ; SET UP THE LPR AND LNCTRL REGISTERS AS SPECIFIED IN THE TX/RX CONTROL BLOCK.
6143      ; -
6144 026002 016705 155126      MOV      CBMAPA,R5      ;GET THE BIT MAP OF SELECTED LINES.
6145 026006 016700 155112      MOV      CBLNCA,R0      ;GET THE NEW LNCTRL CONTENTS.
6146 026012 026727 155120 000001      CMP      CBLPBA,#1      ;CHECK IF INTERNAL LOOPBACK HAS BEEN SELECTED.
6147 026020 001002      BNE      2#      ;SKIP SETTING INT. LOPBCK IN MAINTENANCE FIELD.
6148 026022 052700 000200      BIS      #200,R0      ;SET INTERNAL LOOPBACK IN MAINTENANCE FIELD.
6149 026026 004767 001126      2#: JSR      PC,WTWLNC      ;SET UP THE LNCTRL REGS FOR SELECTED LINES.
6150 026032 016700 155064      MOV      CBLPRA,R0      ;GET THE NEW LPR CONTENTS.
6151 026036 004767 001146      JSR      PC,WTWLPB      ;SET UP THE LPR REGISTERS FOR SELECTED LINES.
6152 026042 004767 177454      JSR      PC,TXENBL      ;ENABLE TX FOR ALL SELECTED LINES.
6153
6154      ; SET UP AND BEGIN LOOP WHICH HANDLES ONE LINE PER ITERATION.
6155      ; -
6156 026046 005004      CLR      R4      ;CLEAR THE LINE OFFSET.
6157 026050 016705 155052      MOV      CBDPAA,R5      ;INITIALIZE THE TX START ADDRESS VALUE.
6158 026054 016703 155050      MOV      CBDPLA,R3      ;GET THE LENGTH OF THE DATA PATTERN.
6159 026060 060503      ADD      R5,R3      ;CALCULATE END ADDRESS OF THE DATA PATTERN.
6160 026062 036467 002364 155044 4#: BIT      BITTB(R4),CBMAPA      ;CHECK IF THIS LINE IS SELECTED FOR INIT.
6161 026070 001452      BEQ      12#      ;SKIP SET UP IF LINE IS NOT SELECTED.
6162
6163      ; THIS LINE IS SELECTED FOR INITIALIZATION.
6164      ; SET UP PROPER ENTRY IN NUMBER OF CHARS TO TX AND RX TABLE.
6165      ; INCLUDE CHAR COUNT ON THIS LINE IN MAX ALLOWABLE CHAR TOTAL FOR ALL LINES.
6166      ; -
6167 026072 016701 155032      MOV      CBDPLA,R1      ;GET THE LENGTH OF THIS LINE'S DATA PATTERN.
6168 026076 016702 155030      MOV      CBDPNA,R2      ;GET THE NUMBER OF PATTERNS TO TX AND RX.
6169 026102 004767 173424      JSR      PC,MUL16U      ;CALCULATE THE TOTAL NUMBER OF CHARS TO TX/RX.
6170 026106 010164 003442      MOV      R1,CHCNTB(R4)      ;SET UP THE NUMBER OF TX/RX CHARS FOR LINE.
6171 026112 060167 154360      ADD      R1,CHRTOT      ;ADD TWICE THE NUMBER OF CHARACTERS TO TX/PX
6172 026116 103403      BCS      6#      ; ON THIS LINE TO THE TOTAL NUMBER OF CHARS
6173 026120 060167 154352      ADD      R1,CHRTOT      ; WHICH WE WILL ALLOW TO BE RECEIVED ON
6174 026124 103003      BCC      8#      ; ALL LINES.
6175 026126 012767 177777 154342 6#: MOV      #-1,CHRTOT      ; SET MAX CHAR TOTAL TO -1 IF OVERFLOW.
6176 026134      8#:
6177
6178      ; SET UP THE DATA PATTERN END AND LENGTH FOR THIS LINE.
6179      ; -
6180 026134 016764 154770 003202      MOV      CBDPLA,DPLENB(R4)      ;SET UP TX DATA PATTERN LENGTH FOR THIS LINE.
6181 026142 010364 003142      MOV      R3,DPENDB(R4)      ;SET UP TX DATA PAT END ADDRESS FOR THIS LINE.
6182
6183      ; SET UP THE TX COUNTER AND CHARACTER POINTER FOR THIS LINE.
6184      ; -
6185 026146 005064 003502      CLR      TXCNTB(R4)      ;CLEAR THE TX COUNTER FOR THIS LINE.
6186 026152 010564 003342      MOV      R5,TXPTRB(R4)      ;SET UP THE TX CHAR POINTER FOR THIS LINE.
6187
6188      ; SET UP THE TX/RX LINE ASSOCIATION OFFSET TABLE ENTRY FOR THIS LINE.
6189      ; -
6190 026156 010402      MOV      R4,R2      ;SELECT LINE OFFSET FOR NON-STAGGERED LPBK.
6191 026160 026727 154752 000002      CMP      CBLPBA,#2      ;TEST FOR STAGGERED LOOPBACK.
6192 026166 001003      BNE      10#      ;SKIP SETTING STAGGERED LPBK IF NOT.
6193 026170 006202      ASR      R2      ;FORM BYTE OFFSET INTO TABLE FROM TX LINE #.
6194 026172 116202 005274      MOVB     STGTRB(R2),R2      ;GET THE RX LINE CORRESPONDING WITH TX LINE.
6195 026176 010264 005234      10#: MOV      R2,TXRXLB(R4)      ;LOAD TX TABLE ENTRY WITH RX LINE OFFSET.

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6200 026202 005062 003542
6201 026206 005062 003242
6202 026212 010562 003402
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6206 026216 066705 154716
6207 026222 020503
6208 026224 103403
6209 026226 166705 154676
6210 026232 000773
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6214 026234 005204
6215 026236 005204
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6217
6218
6219 026240 020427 000040
6220 026244 002706
6221
6222 026246
6223 026246 004736
6223 026250 000207

;+
; SET UP THE RX COUNTERS AND CHARACTER POINTER FOR THE RX LINE WHICH
; IS ASSOCIATED WITH THIS TX LINE.
;-
CLR RXCNTB(R2) ;CLEAR THE RX COUNTER FOR THIS RX LINE.
CLR EXCNTB(R2) ;CLEAR THE EXTRA CHAR COUNTER FOR THIS RX LINE.
MOV R5,RXPTRB(R2) ;SET UP THE RX CHAR POINTER FOR THIS RX LINE.
;+
; UPDATE THE TX START POINTER IN PREPARATION FOR THE NEXT LINE.
;-
12$: ADD CBOFSA,R5 ;ADD THE TX OFFSET TO THE TX START POINTER.
14$: CMP R5,R3 ;COMPARE TX START WITH END OF DATA PATTERN.
BLO 16$ ;SKIP WRAPAROUND IF START IS BEFORE PAT END.
SUB CBOFSA,R5 ;SUBTRACT DATA PATTERN LENGTH FROM START.
BR 14$ ;LOOP UNTIL START IS WITHIN DATA PATTERN.
;+
; UPDATE THE TX LINE NUMBER OFFSET TO THE NEXT LINE.
;-
16$: INC R4
INC R4
;+
; TEST FOR DONE HANDLING ALL POSSIBLE LINES ON THE DEVICE.
;-
CMP R4,#NUMLNS*2 ;COMPARE OFFSET WITH 2 TIMES MAX # OF LINES.
BLT 4$ ;LOOP IF NOT ALL LINES DONE.
60$: PASS ;RESTORE GPRS.
JSR PC,0(SP), ;RETURN TO PREGOS SUBRT.
RTS PC

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6247 026252
026252 004567 157046
6248 026256
026256 104440
026260 010067 153764
6249 026264
026264 012700 000300
026270 104441
6250 026272 012705 177777
6251 026276 004767 177124
6252 026302 010567 153744
6253 026306
026306 004736
6254 026310 000207

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.SBTTL GLOBAL SUBROUTINE - TXROFF
; ** *****
; * - TURN TX AND RX OFF ROUTINE -
; * THIS SUBROUTINE IS USED TO TURN OFF DUT TRANSMISSION AND RECEPTION.
; * THIS ROUTINE ACHIEVES THIS BY BOOSTING PROCESSOR PRIORITY TO 5 TO
; * AVOID RX INTERRUPTS AND BY CLEARING ALL THE DUT TX.ENABLE BITS TO
; * HALT TX (EITHER DMA OR SINGLE CHARACTER TX). THE STATES OF THE
; * TX.ENABLE BITS AND THE PROCESSOR PRIORITY ARE SAVED FOR RESTORATION
; * WHEN TX AND RX ARE RE-ENABLED.
; *
; * INPUTS: MAPLNS - BIT MAP OF ALL POSSIBLE LINES ON THE DUT.
; *
; * OUTPUTS: SAVPRI - SAVED PROCESSOR PRIORITY.
; * SAVTEN - BIT MAP OF TX.ENBL BITS (BIT SET IF TX.ENBL WAS SET).
; *
; * CALLING SEQUENCE: JSR PC,TXROFF
; *
; * COMMENTS:
; *
; * SUBORDINATE ROUTINES CALLED: TXDSBL.
; -- *****

TXROFF:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
; R5,PREG05 ;CALL REGISTER SAVE SUBRT.
GETPRI SAVPRI JSR ;GET THE PRESENT PROCESSOR PRIORITY.
; TRAP C:GPRI
; MOV RO,SAVPRI
SETPRI #PRI06 ;DISABLE DUT INTERRUPTS.
; MOV #PRI06,RO
; TRAP C:SPRI
MOV #MAPLNS,R5 ;PREPARE TO DISABLE TX ON ALL DUT LINES.
JSR PC,TXDSBL ;CLEAR ALL DUT TX.ENABLE BITS.
MOV R5,SAVTEN ;PRESERVE THE PREVIOUS TX.ENABLE BIT STATES.
60: PASS ;RESTORE GPRS.
; PC,0(SP)+ ;RETURN TO PREG05 SUBRT.
RTS PC JSR
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6256 .SBTTL GLOBAL SUBROUTINE - TXRON -
6257 ;* *****
6258 ;* - TURN TX AND RX ON ROUTINE -
6259 ;* THIS SUBROUTINE IS USED TO TURN ON DUT TRANSMISSION AND RECEPTION.
6260 ;* THIS ROUTINE RESTORES THE DUT TX.ENABLE BITS AND THE PROCESSOR PRIORITY
6261 ;* TO THE STATES SAVED BY THE TXROFF ROUTINE.
6262 ;*
6263 ;* INPUTS: SAVPRI - SAVED PROCESSOR PRIORITY.
6264 ;* SAVTEN - BIT MAP OF TX.ENBL BITS (BIT SETIF TX.ENBL WAS SET).
6265 ;*
6266 ;* OUTPUTS: DUT TX.ENABLE BITS - SET TO SPECIFIED STATES.
6267 ;* PROCESSOR PRIORITY - SET TO SPECIFIED PRIORITY.
6268 ;*
6269 ;* CALLING SEQUENCE: JSR PC,TXRON
6270 ;*
6271 ;* COMMENTS:
6272 ;*
6273 ;* SUBORDINATE ROUTINES CALLED: TXENBL.
6274 ;* -- *****
6275
6276 026312 TXRON:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
        026312 004567 157006 R5,PREG05 ;CALL REGISTER SAVE SUBRT.
6277 026316 016705 153730 MOV SAVTEN,R5 ;GET THE SAVED STATES OF THE TX.ENABLE BITS.
6278 026322 004767 177174 JSR PC,TXENBL ;SET THE SPECIFIED TX.ENABLE BITS.
6279 026326 SETPRI SAVPRI ;RESTORE THE PROCESSOR PRIORITY.
        026326 016700 153716 MOV SAVPRI,R0
        026332 104441 TRAP C$SPRI
6280 026334 601: PASS ;RESTORE GPRS.
        026334 004736 JSR PC,0(SP)+ ;RETURN TO PREG05 SUBRT.
6281 026336 000207 RTS PC

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6283 .SBTTL GLOBAL SUBROUTINE - TXRREP -
6284 ;* *****
6285 ;* - REPORT FINAL TX/RX ERRORS ROUTINE -
6286 ;* THIS SUBROUTINE REPORTS ERRORS WHICH ARE FOUND AFTER THE COMPLETION
6287 ;* OF THE X, RX, AND VERIFICATION OF DATA PATTERNS. IT REPORTS ERRORS
6288 ;* DEALING WITH INCOMPLETE TX OR RX AND WITH DMA_START BITS.
6289 ;*
6290 ;* INPUTS: ACTLNS - BIT MAP OF ACTIVE OUT LINES.
6291 ;* DPLENB - LABEL AT BASE OF THE DATA PATTERN LENGTHS TABLE.
6292 ;* ERRMSG - ADDRESS OF PRIMARY ERROR MESSAGE FOR THIS ROUTINE.
6293 ;* ERRNBR - ERROR NUMBER OF ERROR REPORTED IN THIS ROUTINE.
6294 ;* RXCNTB - LABEL AT BASE OF THE RX CHARACTER COUNTERS TABLE.
6295 ;* RXDNF - RECEPTION DONE FLAGS.
6296 ;* TXCNTB - LABEL AT BASE OF THE TX CHARACTER COUNTERS TABLE.
6297 ;* TXDNF - TRANSMISSION DONE FLAGS.
6298 ;* TXINTF - CONTAINS BIT MAP OF LINES WITH DMA_START BIT ERRORS.
6299 ;*
6300 ;* OUTPUTS: CARRY FLAG - RESTORED TO ITS ENTERING VALUE.
6301 ;* ERRBLK - ADDRESS OF THE ERROR REPORTING ROUTINE (DESTROYED).
6302 ;* MESSAGES MAY BE PRINTED AT THE OPERATOR CONSOLE.
6303 ;*
6304 ;* CALLING SEQUENCE: JSR PC,TXRREP
6305 ;*
6306 ;* COMMENTS: THIS ROUTINE REPORTS ERRORS AT INITIAL ERRNBR THRU
6307 ;* INITIAL ERRNBR+2.
6308 ;* IF NO LINES FAILED TO COMPLETE THEIR RECEPTION OR FAILED TO
6309 ;* COMPLETE THEIR TRANSMISSION OR HAD DMA_START BIT ERRORS
6310 ;* THEN NO MESSAGES ARE PRINTED.
6311 ;*
6312 ;* SUBORDINATE ROUTINES CALLED: CONMAP,ER9005,ER9102,RDMAST,RRXNDN,RTXNDN.
6313 ;* -- *****
6314
6315 026340 TXRREP:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
6316 026340 004567 156760 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
6317 026344 006003 ROR R3 ;ROTATE CARRY INTO GPR TO SAVE CARRY STATE.
6318 026346 016704 156744 MOV ERRNBR,R4 ;SAVE THE INITIAL ERROR NUMBER VALUE.
6319 026352 016705 153614 MOV ACTLNS,R5 ;GET THE ACTIVE LINES BIT MAP.
6320 026356 004767 175340 JSR PC,RDMAST ;REPORT ANY DMA_START BIT ERRORS.
6321 026362 032767 000100 153572 BIT #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
6322 026370 001003 BNE 24 ;YES, THEN BRANCH.
6323 026372 005767 153626 TST FERROR ;HAS AN ERROR BEEN DETECTED ?
6324 026376 001024 BNE 604 ;BRANCH AND EXIT IF IT HAS.
6325
6326 026400 005267 156712 24: INC ERRNBR ;SELECT INITIAL ERROR NUMBER + 1.
6327 026404 004767 175720 JSR PC,RTXNDN ;REPORT TX NOT COMPLETE IF NECESSARY.
6328
6329 026410 032767 000100 153544 BIT #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
6330 026416 001003 BNE 44 ;YES, THEN BRANCH.
6331 026420 005767 153600 TST FERROR ;HAS AN ERROR BEEN DETECTED ?
6332 026424 001011 BNE 604 ;BRANCH AND EXIT IF IT HAS.
6333
6334 026426 005267 156664 44: INC ERRNBR ;SELECT INITIAL ERROR NUMBER + 2.
6335 026432 004767 171036 JSR PC,CONMAP ;GENERATE AN ASSOCIATED LINE BIT MAP.
6336 026436 004767 175620 JSR PC,RRXNDN ;REPORT RX NOT COMPLETE IF NECESSARY.
6337 026442 010467 156650 MOV R4,ERRNBR ;RESTORE THE INITIAL ERROR NUMBER VALUE.
6338

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6339 026446 006103
6340 026450
026450 004736
6341 026452 000207

604: ROL R3
PASS
RTS PC

JSR

; ROTATE SAVED CARRY STATE BACK INTO CARRY.
; RESTORE GPRS, THIS ROUTINE PRESERVES THE
PC, 0(SP). ; RETURN TO PREG05 SUBRT.
; INITIAL CARRY STATE.

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6343 .SBTTL GLOBAL SUBROUTINE - UNSDIV -
6344 ;+ *****
6345 ;* - UNSIGNED DIVIDE ROUTINE -
6346 ;* THIS SUBROUTINE IS USED TO DIVIDE A 32 BIT UNSIGNED DIVIDEND BY A
6347 ;* 16 BIT UNSIGNED DIVISOR GIVING A 16 BIT QUOTIENT. ALL NUMBERS ARE
6348 ;* CONSIDERED TO BE UNSIGNED. A SUCCESS FLAG IS NOT SET ON RETURN IF
6349 ;* THE QUOTIENT WAS TOO BIG TO BE CONTAINED IN 16 BITS.
6350 ;*
6351 ;* INPUTS: R1 - THE DIVISOR, UNSIGNED, 16 BITS.
6352 ;* R2 - MOST SIGNIFICANT WORD OF THE DIVIDEND, UNSIGNED, 16 BITS.
6353 ;* R3 - LEAST SIGNIFICANT WORD OF THE DIVIDEND, UNSIGNED, 16 BITS.
6354 ;*
6355 ;* OUTPUTS: R1 - QUOTIENT, UNSIGNED, 16 BITS (177777 IF OVERFLOW).
6356 ;* CARRY - SUCCESS FLAG, SET IF COMPLETE QUOTIENT FITS IN 16 BITS.
6357 ;*
6358 ;* CALLING SEQUENCE: JSR PC,UNSDIV
6359 ;*
6360 ;* COMMENTS: IF THE DIVISOR IS 0 THE QUOTIENT IS RETURNED AS ALL ONES
6361 ;* (177777) AND THE CARRY IS CLEAR REGARDLESS OF THE DIVIDEND.
6362 ;*
6363 ;* SUBORDINATE ROUTINES CALLED: NONE.
6364 ;-- *****
6365
6366 026454 UNSDIV:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
026454 004567 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
156644
6367 ;+
6368 ; CHECK FOR QUOTIENT GREATER THAN 16 BITS CONDITION.
6369 ; -
6370 026460 010204 MOV R2,R4 ;GET MSW OF DIVIDEND FOR SUBTRACT.
6371 026462 160104 SUB R1,R4 ;SUBTRACT DIVISOR FROM MSW OF DIVIDEND.
6372 026464 103403 BCS 2$ ;IF IT DIDN'T GO, WE HAVE QUOTIENT < 16 BITS.
6373 026466 012701 177777 MOV 0-1,R1 ;SET QUOTIENT TO ALL ONES (177777).
6374 026472 000442 BR 60$ ;EXIT WITH CARRY CLEAR.
6375
6376 ;+
6377 ; SET UP COUNTERS AND VARIOUS WORKING GPRS.
6378 026474 005004 2$: CLR R4 ;CLEAR THE LSW OF THE DIVISOR.
6379 026476 000241 CLC ;CLEAR CARRY FOR THE SHIFT OF THE DIVISOR.
6380 026500 006001 ROR R1 ; DIVISOR BY
6381 026502 006004 ROR R4 ; 2(UNSIGNED)
6382 026504 012700 000020 MOV 016.,R0 ;SET UP INITIAL SHIFT COUNT TO 16.
6383
6384 ;+
6385 ; THE SUBTRACT AND SHIFT LOOP.
6386 026510 010246 4$: MOV R2,-(SP) ;SAVE MSWORD OF DIVIDEND.
6387 026512 010346 MOV R3,-(SP) ;SAVE LSWORD OF DIVIDEND.
6388 026514 160403 SUB R4,R3 ;LSWORD DIVIDEND - LSWORD OF DIVISOR.
6389 026516 005602 SBC R2 ;MSWORD DIVIDEND - BORROW
6390 026520 103402 BCS 6$ ;IF BORROW FROM BORROW SUBTRACT, IT DIDN'T GO.
6391 026522 160102 SUB R1,R2 ;MSWORD DIVIDEND - MSWORD OF DIVISOR.
6392 026524 103003 BCC 8$ ;IF NO BORROW, IT WENT, CARRY IS CLEAR.
6393
6394 ;+
6395 ; IT DIDN'T GO, SO WE SHIFT A 1 INTO THE QUOTIENT (COMPLEMENTED LATER).
6396 ; CARRY IS SET.
6397 026526 012603 6$: MOV (SP)+,R3 ;RESTORE LSWORD OF DIVIDEND.
6398 026530 012602 MOV (SP)+,R2 ;RESTORE MSWORD OF DIVIDEND.

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6399 026532 000401          BR      10$      ;GOTO SHIFT 1 INTO THE QUOTIENT.
6400
6401          ;+
6402          ; IT WENT, SO WE RESTORE THE STACK AND SHIFT A 0 INTO QUOTIENT (WILL BE
6403          ; COMPLEMENTED LATER).  CARRY IS CLEAR.
6404 026534 012626      8$:  MOV      (SP)+,(SP)+      ;POP THE SAVED DIVIDEND OFF OF THE STACK.
6405          ;+
6406          ; SHIFT THE RESULT OF THE SUBTRACT ATTEMPT INTO THE QUOTIENT SHIFT REG.
6407          ;-
6408 026536 006105      10$:  ROL      R5              ;SHIFT NEXT BIT INTO THE INVERTED QUOTIENT.
6409 026540 000241          CLC                      ;DIVIDE THE
6410 026542 006001          ROR      R1              ; DIVISOR BY
6411 026544 006004          ROR      R4              ; 2 (UNSIGNED).
6412 026546 005300          DEC      R0              ;COUNT THIS SHIFT AND SUBTRACT.
6413 026550 001357          BNE      4$              ;LOOP FOR ANOTHER SHIFT & SUB IF NOT DONE.
6414 026552 005105          COM      R5              ;GET QUOTIENT FROM INVERTED QUOTIENT.
6415          ;+
6416          ; NOW WE EITHER ROUND UP OR LEAVE QUOTIENT ALONE.
6417          ;-
6418 026554 000241          CLC                      ;CLEAR THE CARRY FOR THE SHIFT OF THE DIVIDEND.
6419 026556 006103          ROL      R3              ;MULTIPLY LWORD OF DIVIDEND BY 2, MSWORD IS 0.
6420 026560 103402          BCS      12$              ;IF CARRY FROM SHIFT, ROUND UP.
6421 026562 160403          SUB      R4,R3              ;SUBTRACT DIVISOR FROM DIVIDEND.
6422 026564 103403          BCS      14$              ;IF BORROW, DON'T ROUND UP.
6423          ;+
6424          ; ROUND UP, EXTRA SUBTRACT WENT.
6425          ;-
6426 026566 005205      12$:  INC      R5              ;INCREMENT THE QUOTIENT BY ONE.
6427 026570 001001          BNE      14$              ;IF NO OVERFLOW, WE LEAVE THE ROUND UP.
6428 026572 005305          DEC      R5              ;DON'T LET ROUNDING CAUSE OVERFLOW.
6429          ;+
6430          ; ALL DONE, PASS QUOTIENT AND EXIT.
6431          ;-
6432 026574 010501      14$:  MOV      R5,R1              ;PASS QUOTIENT BACK IN R1.
6433 026576 000261          SEC                      ;INDICATE NO OVERFLOW.
6434
6435 026600          60$:  PASS      R1              ;RESTORE GPRS, LEAVE THE FOLLOWING INTACT:
6436          026600 010166 000004          MOV      R1,R1SLOT(SP)      ;PUT R1 IN STACK SLOT.
6437          026604 004736          JSR      PC,0(SP)+      ;RETURN TO PREG05 SUBRT.
6438          ;R1 - 16 BIT, UNSIGNED QUOTIENT,
6439          ;CARRY - SET INDICATES NO OVERFLOW (SUCCESS).
6437 026606 000207          RTS      PC

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6439 .SBTTL GLOBAL SUBROUTINE - UPDCHR -
6440 ;* *****
6441 ;* - UPDATE CHARACTER POINTERS AND COUNTERS ROUTINE -
6442 ;* THIS SUBROUTINE UPDATES THE POINTERS AND COUNTERS ASSOCIATED WITH
6443 ;* THE RECEPTION OF A CHARACTER ON A SPECIFIED LINE. THE RECEIVE CHAR,
6444 ;* POINTER IS SET TO THE NEXT EXPECTED CHARACTER, THE RECEIVE CHAR COUNT
6445 ;* IS INCREMENTED, AND THE COUNT IS CHECKED TO DETERMINE IF THE RECEPTION
6446 ;* IS COMPLETE. IF THE RECEPTION IS COMPLETE THE RECEPTION DONE FLAG
6447 ;* IS SET FOR THE SPECIFIED LINE.
6448 ;*
6449 ;* INPUTS: R3 - LINE NUMBER TIMES 2 OF LINE ON WHICH CHAR WAS RECEIVED.
6450 ;* BITTBL - LABEL OF TABLE OF WORDS USED TO FORM SINGLE BIT MAPS.
6451 ;* CHCNTB - BASE OF NUMBER OF CHARS TO TX ON EACH LINE TABLE.
6452 ;* DPENDB - BASE OF DATA PATTERN END ADDRESSES TABLE.
6453 ;* DPLENB - BASE OF DATA PATTERN LENGTHS TABLE.
6454 ;* RXCNTB - BASE OF THE RX CHARACTER COUNTERS TABLE.
6455 ;* RXPTRB - BASE OF THE RX CHARACTER POINTERS TABLE.
6456 ;* TXRXLB - BASE OF TX/RX LINE NUMBER ASSOCIATION TABLE.
6457 ;*
6458 ;* OUTPUTS: FOLLOWING VARIABLES UPDATED FOR LINE ON WHICH CHAR WAS RECEIVED:
6459 ;* RXCNT - COUNT OF THE NUMBER OF CHARACTERS RECEIVED ON LINE.
6460 ;* RXDNF - RX DONE FLAGS WITH BIT0 FOR LINE 0 ... (UPDATED).
6461 ;* RXPTR - UPDATED TO POINT TO THE NEXT EXPECTED CHAR ON LINE.
6462 ;*
6463 ;* CALLING SEQUENCE: JSR PC,UPDCHR
6464 ;*
6465 ;* COMMENTS:
6466 ;*
6467 ;* SUBORDINATE ROUTINES CALLED: NONE.
6468 ;* *****
6469 UPDCHR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
6470 026610 004567 156510 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
6471 026614 016302 005234 MOV TXRXLB(R3),R2 ;GET TX LINE NUMBER OFFSET FOR THIS RX LINE.
6472 ;*
6473 ;* UPDATE THE RX DATA POINTER WITH WRAPAROUND AT THE END OF THE DATA PATTERN.
6474 026620 016301 003402 MOV RXPTRB(R3),R1 ;GET THE RX DATA POINTER FROM THE RX PTR TABLE.
6475 026624 005201 INC R1 ;INCREMENT THE RX POINTER VALUE BY 1.
6476 026626 020162 003142 CMP R1,DPENDB(R2) ;CMP RX PTR VALUE WITH ADR OF END OF DATA PAT.
6477 026632 103402 BLO 2$ ;SKIP WRAPPING RX PTR AROUND IF NOT AT END.
6478 026634 166201 003202 SUB DPLENB(R2),R1 ;WRAP RX PTR AROUND TO START OF DATA PATTERN.
6479 026640 010163 003402 2$: MOV R1,RXPTRB(R3) ;UPDATE THE RX POINTER WITH THE NEW VALUE.
6480 ;*
6481 ;* UPDATE THE RX CHARACTER COUNT WITH OVERFLOW DETECTION.
6482 ;*
6483 026644 016301 003542 MOV RXCNTB(R3),R1 ;GET THE RX CHARACTER COUNT.
6484 026650 005201 INC R1 ;INCREMENT THE RX CHAR COUNT VALUE BY 1.
6485 026652 001002 BNE 4$ ;SKIP SETTING COUNT TO MAX IF NO OVERFLOW.
6486 026654 012701 177777 MOV #-1,R1 ;SET RX CHAR COUNT VALUE TO MAX VALUE.
6487 026660 010163 003542 4$: MOV R1,RXCNTB(R3) ;UPDATE THE RX CHAR COUNT WITH NEW VALUE.
6488 ;*
6489 ;* CHECK FOR RX COMPLETION ON THIS LINE.
6490 ;* IF RX IS COMPLETE ON THIS LINE, SET THE CORRECT RX DONE FLAG.
6491 ;*
6492 026664 016204 003442 MOV CHCNTB(R2),R4 ;GET THE NUMBER OF TX CHARS IN COMPLETE TX.
6493 026671 020104 CMP R1,R4 ;COMPARE RX CHAR COUNT WITH NUMBER OF TX CHARS.
6494 026677 103403 BLO 60$ ;EXIT ROUTINE IF NOT ALL CHARS RECEIVED.

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6495 026674 056367 002364 153602      BIS    BIT1BL(R3),RXDNF      ;SET THE RX DONE FLAG FOR THIS LINE.
6496
6497 026702                                50$:  PASS      ;RESTORE GPRS.
        026702 004736                                JSR      PC,0(SP).
6498 026704 000207                        RTS    PC      ;RETURN TO PREG05 SUBRT.
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6500 .SBTTL GLOBAL SUBROUTINE VANSUP -
6501 ;* *****
6502 ;* - TRANSMISSION / RECEPTION SET UP ROUTINE -
6503 ;*
6504 ;* THIS ROUTINE IS USED TO INITIALISE BOTH THE DUT AND THE
6505 ;* TRANSMISSION/RECEPTION CONTROL PARAMETERS TO THE CORRECT
6506 ;* STATE, PRIOR TO A SINGLE CHARACTER OR DMA TRANSMISSION,
6507 ;* RECEPTION TEST.
6508 ;*
6509 ;* INPUTS: R1 - TX, RX LPR CONTENTS.
6510 ;* R2 - START ADDRESS OF DATA PATTERN TO TRANSMIT.
6511 ;* R3 - LENGTH OF DATA PATTERN.
6512 ;* R4 - NUMBER OF PATTERNS TO TRANSMIT.
6513 ;* ACTLNS - CONTAINS A BIT MAP OF ALL CURRENTLY ACTIVE LINES.
6514 ;* LOPBCK - CONTAINS THE TYPE OF LOOPBACK MODE SELECTED.
6515 ;* CBB - LABEL AT BASE OF TX/RX CONTROL BLOCK.
6516 ;*
6517 ;* OUTPUTS: THE CONTENTS OF THE TX/RX CONTROL BLOCK (CCB) ARE DESTROYED.
6518 ;* THE INDIRECT ADDRESS FIELD OF THE DUT CSR MAY BE DESTROYED.
6519 ;* THE DUT'S LPR'S AND LNC'S MAY BE MODIFIED.
6520 ;* THE FOLLOWING POINTERS AND COUNTERS ARE INITIALISED;
6521 ;* CHCNT,CHRTOT,DPEND,DPLEN,EXCNT,RXCNT,RXPTR,TXCNT,
6522 ;* TXPTR,TXRXL.
6523 ;* CHRTOT, RXDNF, TXDNF AND TXINTF ARE CLEARED.
6524 ;*
6525 ;* CALLING SEQUENCE: JSR PC,VANSUP
6526 ;*
6527 ;* COMMENTS: MODEM LOOPBACK MODE IS INHIBITED IF IT HAS BEEN SELECTED
6528 ;* VIA HARDWARE P-TABLE QUESTIONS, AND INTERNAL LOOPBACK MODE
6529 ;* IS FORCED TO TAKE PLACE.
6530 ;*
6531 ;*
6532 ;* SUBORDINATE ROUTINES CALLED: CONMAP,RXENBL,TXRINI.
6533 ;* - *****
6534
6535 026706 VANSUP:: SAVE ;SAVE CONTENTS OF THE GPR'S R0 THRU R5.
6536 026706 004567 156412 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
6537 026712 005067 153560 CLR CHRTOT ;CLEAR TOTAL RECEIVED CHAR COUNTER.
6538 026716 005067 153342 CLR TXINTF ;CLEAR FLAGS USED TO LOG DMA H.OVER ERRORS.
6539 026722 005067 153554 CLR TXDNF ;CLEAR THE TX DONE FLAGS.
6540 026726 005067 153552 CLR RXDNF ;CLEAR THE RX DONE FLAGS.
6541 ;*
6542 ;* SET UP THE TRANSMISSION/RECEPTION CONTROL BLOCK TO THE DESIRED STATE.
6543 ;* -
6543 026732 010167 154164 MOV R1,CBB ;SET CONTENTS OF LPR PARAMS IN TX/RX C.BLK.
6544 026736 012701 003122 MOV #CBB,R1 ;GET BASE ADDRESS OF CONTROL BLOCK.
6545 026742 005201 INC R1 ;INCREMENT ADDRESS FOR NEXT WORD
6546 026744 005201 INC R1 ;INITIALISE THE FOLLOWING IN THE CNTRL.BLK:
6547 026746 012721 000004 MOV #4,(R1). ; LNCtrl PARAMETER, ENABLE RECEIVERS.
6548 026752 010221 MOV R2,(R1). ; START ADDRESS OF DATA PATTERN.
6549 026754 010321 MOV R3,(R1). ; DATA PATTERN LENGTH.
6550 026756 010421 MOV R4,(R1). ; NUMBER OF DATA PATTERNS TO TRANSMIT.
6551 026760 016721 153206 MOV ACTLNS,(R1). ; BIT MAP OF LINES TO INITIALISE.
6552 026764 032767 000004 153202 BIT #BIT2,LOPBCK ;TEST IF MODEM LOOPBACK MODE HAS BEEN SELECTED.
6553 026772 001404 BEQ 24 ;DONT SELECT INTERNAL LOPBCK IF STAGRD OR LOCAL.
6554 026774 012702 000001 MOV #1,R2 ;FORCE INTERNAL LOOPBACK MODE TO BE SELECTED.
6555 027000 110221 MOVB R2,(R1). ;INITIALISE LOOPBACK MODE IN CONTROL BLOCK.

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6556 027002 000402
6557 027004 116721 153164
6558 027010 005201
6559 027012 012711 000002
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6564 027016 004767 176754
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6568 027022 012701 177777
6569 027026 016702 153140
6570 027032 005101
6571 027034 005102
6572 027036 040102
6573 027040 010267 154070
6574 027044 005067 154062
6575 027050 004767 176722
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6580 027054 012705 177777
6581 027060 004767 175312
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6585 027064 016705 153102
6586 027070 004767 170400
6587 027074 004767 175372
6588 027100
        027100 004736
6589 027102 000207

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BR      4:      ;SKIP NEXT INSTRUCTION IF IN MODEM LOOPBACK.
24:     MOV8    LOPBCK,(R1)+ ;SET LOOPBACK MODE.
44:     INC     R1          ;INCREMENT ADDRESS FOR THE NEXT WORD.
        MOV     02,(R1)     ;SET AMOUNT OF OFFSET EACH TX STARTS AT TO 2.
;+
; INITIALISE THE DUT AND THE ASSOCIATED POINTERS AND COUNTERS, TO THE STATE
; DICTATED BY THE CONTENTS OF THE TX/RX CONTROL BLOCK.
;-
        JSR     PC,TXRINI    ;INITIALISE DUT.
;+
; INITIALISE POINTERS AND COUNTERS FOR INACTIVE LINES TO ZERO.
;-
        MOV     #MAPLNS,R1   ;GET THE LINE BIT MAP FOR ALL LINES.
        MOV     ACTLNS,R2    ;GET THE ACTIVE LINE BIT MAP.
        COM     R1           ;
        COM     R2           ;
        BIC     R1,R2        ;GENERATE AN IN-ACTIVE LINE BIT MAP.
        MOV     R2,CBMAPA    ;MOVE BIT MAP TO THE CONTROL BLOCK.
        CLR     CBOPNA       ;CLEAR THE REPEAT TX COUNT IN CNTRL BLCK.
        JSR     PC,TXRINI    ;SET UP PARAMETERS FOR INACTIVE LINES.
;+
; DISABLE RECEIVERS ON ALL LINES TO ENSURE CORRECT INITIALISATION OF ONLY THE
; LINES THAT ARE SELECTED.
;-
        MOV     #MAPLNS,R5   ;SET-UP BIT MAP FOR ALL LINES.
        JSR     PC,RXDSBL    ;DISABLE RX ON ALL LINES.
;+
; ENABLE RECEIVERS ON ASSOCIATED (RX) LINES.
;-
        MOV     ACTLNS,R5    ;GET THE ACTIVE LINE BIT MAP.
        JSR     PC,CONMAP    ;GENERATE AN ASSOCIATED LINE BIT MAP.
        JSR     PC,RXENBL    ;ENABLE RECEIVERS ON ASSOCIATED LINES.
604:    PASS              ;RESTORE GPR'S.
        JSR     PC,0(SP)+    ;RETURN TO PREG05 SUBRT.
        RTS     PC

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6591 .SBTTL GLOBAL SUBROUTINE - WAIBIS -
6592 ;** *****
6593 ;* - WAIT FOR BIT SET ROUTINE -
6594 ;* THIS SUBROUTINE WAITS FOR THE SPECIFIED BIT TO BECOME SET. IF THE
6595 ;* SPECIFIED BIT GOES TO A SET STATE WITHIN THE SPECIFIED TIME-OUT
6596 ;* PERIOD A SUCCESS INDICATION IS RETURNED BY THIS ROUTINE.
6597 ;* THE LAST VALUE WHICH IS READ LOOKING FOR THE CONDITION IS RETURNED TO
6598 ;* ALLOW THE USE OF THIS ROUTINE TO LOOK FOR DESTRUCTIVE READ CONDITIONS.
6599 ;*
6600 ;* INPUTS: R1 - TIME-OUT VALUE AND BIT NUMBER INDICATION:
6601 ;*          BITS 15 THRU 12 - NUMBER OF BIT TO TEST (RANGE 0 THRU 15).
6602 ;*          BITS 11 THRU 0 - TIME-OUT VALUE IN MILLI-SECONDS (4095 MAX).
6603 ;*          R2 - ADDRESS OF WORD CONTAINING THE BIT TO TEST.
6604 ;*          MSLCNT.
6605 ;*
6606 ;* OUTPUTS: R2 - THE LAST WORD WHICH WAS READ TO CHECK FOR THE CONDITION.
6607 ;*          CARRY - SUCCESS FLAG (CARRY SET IF BIT SET BEFORE TIME-OUT).
6608 ;*
6609 ;* CALLING SEQUENCE: MOV #130040,R1 ;PASS BIT 11 (13 OCTAL) AND
6610 ;*                   ; 32 (40 OCTAL) MS DELAY.
6611 ;*                   MOV #LABEL,R2 ;TEST BIT IN WORD AT "LABEL".
6612 ;*                   JSR PC,WAIBIS ;WAIT 32 MS FOR BIT 11 TO SET.
6613 ;*
6614 ;* COMMENTS:
6615 ;*
6616 ;* SUBORDINATE ROUTINES CALLED: MSLGET.
6617 ;-- *****
6618
6619 027104 WAIBIS:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
6620 027104 004567 156214 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
6621 027110 010204 MOV R2,R4 ;SET UP THE ADDRESS PARAMETER FOR MSLGET.
6622 027112 010102 MOV R1,R2
6623 027114 042701 170000 BIC #170000,R1 ;SEPERATE DELAY COUNT OUT OF PASSED PARAMETER.
6624 027120 042702 007777 BIC #7777,R2 ;SEPERATE LINE NUMBER FIELD OF PASSED PARAM.
6625 027126 006202 SWAB R2 ;PUT LINE NUMBER FIELD IN LSBYTE.
6626 027130 006202 ASR R2 ;SHIFT THE LINE NUMBER FIELD INTO THE PROPER
6627 027132 006202 ASR R2 ; POSITION TO USE IT AS A WORD TABLE OFFSET
6628 027134 016202 002364 ASR R2 ; FOR THE TABLE LOOKUP OF THE LINE BIT MAP.
6629 027140 010203 MOV BITTBL(R2),R2 ;GET BIT MAP OF LINE TO TEST FROM TABLE.
6630 027142 004767 172020 MOV R2,R3 ;INDICATE THAT THE BIT SHOULD BE SET.
6631 JSR PC,MSLGET ;WAIT FOR THE BIT TO BE SET WITHIN TIME-OUT.
6632 027146 010002 ; CARRY IS CORRECT UPON MSLGET RETURN.
6633 027150 010266 000006 604: MOV R0,R2 ;PASS LAST VALUE READ AS OUTPUT PARAMETER.
6634 027154 004736 ;RESTORE GPRS, EXCEPT THE FOLLOWING:
6635 027156 000207 RTS PC ; R2 - LAST VALUE READ LOOKING FOR CONDIITION.
; CARRY - SUCCESS FLAG (SET IF BIT FOUND SET).

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6660 027160
027160 004567 156140
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6664 027164 016701 153020
6665 027170 010002
6666 027172 010503
6667 027174 012704 177777
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6671 027200 004767 166706
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6673 027204
027204 004736
6674 027206 000207

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.SBTTL GLOBAL SUBROUTINE - WTWLNC -
;+ *****
;+ - LINE CONTROL REGISTER SETUP ROUTINE -
;+ THIS SUBROUTINE IS USED TO SET THE DEVICE UNDER TEST (DUT) LINE
;+ CONTROL REGISTERS (LNCTRL) TO THE SPECIFIED STATE. ONLY THE LNCTRLS
;+ FOR THE SPECIFIED LINES ARE ALTERED.
;+
;+ INPUTS:      R0 - NEW LINE PARAMETERS.
;+              R5 - BIT MAP OF LINES TO BE ALTERED.
;+              CSRA - CONTAINS ADDRESS OF THE DUT CSR.
;+              IESTAT - CONTAINS THE CURRENT STATE OF THE TX AND RX INTERRUPT
;+                  ENABLE BITS IN THE CSR.
;+              LNCTRA - CONTAINS ADDRESS OF THE DUT LNCTRL REGISTERS.
;+
;+ OUTPUTS:     LNCTRL - SPECIFIED DUT LINE CONTROL REGISTERS ARE ALTERED.
;+
;+ CALLING SEQUENCE:  JSR      PC,WTWLNC
;+
;+ COMMENTS:
;+
;+ SUBORDINATE ROUTINES CALLED:  ALTFD.
;+ *****
WTWLNC:: SAVE
;+              JSR      R5,PREG05 ;SAVE CONTENTS OF GPRS R0 THRU R5.
;+              ;CALL REGISTER SAVE SUBRT.
;+
;+ SET UP THE PARAMETERS FOR THE CALL TO ALTFD.
;+
;+              MOV      LNCTRA,R1 ;SET UP THE REGISTER ADDRESS PARAMETER.
;+              MOV      R0,R2     ;SET UP THE DESIRED REGISTER CONTENTS.
;+              MOV      R5,R3     ;SET UP THE BIT MAP OF LINES TO ALTER.
;+              MOV      #-1,R4    ;SELECT ALL REGISTER BITS TO BE ALTERED.
;+
;+ CALL THE SUBROUTINE WHICH ALTERS THE REGISTER CONTENTS.
;+
;+              JSR      PC,ALTFD ;ALTER THE REGISTER CONTENTS.
;+
600:  PASS
;+              JSR      PC,0(SP) ;RESTORE GPRS.
;+              ;RETURN TO PREG05 SUBRT.
RTS      PC
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6676 .SBTTL GLOBAL SUBROUTINE - WTWLPR -
6677 ;* *****
6678 ;* - LINE PARAMETER REGISTER SETUP ROUTINE -
6679 ;* THIS SUBROUTINE IS USED TO SET THE DEVICE UNDER TEST (DUT) LINE
6680 ;* PARAMETER REGISTERS (LPR) TO THE SPECIFIED STATE. ONLY THE LPRS FOR
6681 ;* THE SPECIFIED LINES ARE ALTERED.
6682 ;*
6683 ;* INPUTS: RO - NEW LINE PARAMETERS.
6684 ;* RS - BIT MAP OF LINES TO BE ALTERED.
6685 ;* CSRA - CONTAINS ADDRESS OF THE DUT CSR.
6686 ;* IESTAT - CONTAINS THE CURRENT STATE OF THE TX AND RX INTERRUPT
6687 ;* ENABLE BITS IN THE CSR.
6688 ;* LPRA - CONTAINS ADDRESS OF THE DUT LPR.
6689 ;*
6690 ;* OUTPUTS: LPR - SPECIFIED DUT LINE PARAMETER REGISTERS ARE ALTERED.
6691 ;*
6692 ;* CALLING SEQUENCE: JSR PC,WTWLPR
6693 ;*
6694 ;* COMMENTS:
6695 ;*
6696 ;* SUBORDINATE ROUTINES CALLED: ALTFLO.
6697 ;* - *****
6698
6699 027210 WTWLPR:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
027210 004567 156110 JSR R5,PREG05 ;CALL REGISTER SAVE SUBRT.
6700 ;*
6701 ; SET UP THE PARAMETERS FOR THE CALL TO ALTFLO.
6702 ; -
6703 027214 016701 152764 MOV LPRA,R1 ;SET UP THE REGISTER ADDRESS PARAMETER.
6704 027220 010002 MOV R0,R2 ;SET UP THE DESIRED REGISTER CONTENTS.
6705 027222 010503 MOV R5,R3 ;SET UP THE BIT MAP OF LINES TO ALTER.
6706 027224 012704 177777 MOV #-1,R4 ;SELECT ALL REGISTER BITS TO BE ALTERED.
6707 ;*
6708 ; CALL THE SUBROUTINE WHICH ALTERS THE REGISTER CONTENTS.
6709 ; -
6710 027230 004767 166656 JSR PC,ALTFLO ;ALTER THE REGISTER CONTENTS.
6711
6712 027234 004736 601: PASS ;RESTORE GPRS.
027234 000207 JSR PC,0(SP)+ ;RETURN TO PREG05 SUBRT.
6713 027236
RTS PC

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6715 .SBTTL INTERRUPT SERVICE ROUTINE - CLKINT -
6716 ;** *****
6717 ;* THIS ROUTINE IS EXECUTED CLKHRZ TIMES PER SECOND. IT DECREASES THE
6718 ;* TWO TIMER COUNTERS DOWN TO ZERO.
6719 ;*
6720 ;* INPUTS: TIMER1 - TIMER COUNTER #1.
6721 ;* TIMER2 - TIMER COUNTER #2.
6722 ;* TIMER3 - TIMER COUNTER FOR CALL OF BREAK MACRO.
6723 ;*
6724 ;* OUTPUTS: THE 2 TIMER COUNTERS ARE DECREMENTED IF THEY ARE NOT ZERO.
6725 ;*
6726 ;* CALLING SEQUENCE: PUT #CLKINT IN THE CLOCK INTERRUPT VECTOR SLOT.
6727 ;* PUT THE DESIRED TIME PERIOD (SECONDS TIMES CLKHRZ) IN
6728 ;* EITHER TIMER1 OR TIMER2 AND POLL THE RESPECTIVE TIMER
6729 ;* COUNTER TO DETECT ITS GOING TO 0 ON TIME-OUT.
6730 ;*
6731 ;* COMMENTS: THE 2 COUNTERS WILL NOT WRAPAROUND BUT WILL STOP AT 0. THIS
6732 ;* ALLOWS THE DETECTION OF A TIME-OUT ANY TIME AFTER THE TIME-OUT
6733 ;* HAS OCCURRED UNTIL THE TIMER COUNTER IS SET TO ANOTHER VALUE.
6734 ;*
6735 ;* SUBORDINATE ROUTINES CALLED: NONE.
6736 ;-- *****
6737
6738 027240 005767 153034 CLKINT:: TST TIMER1 ;CHECK FOR TIMER1 AT ZERO.
6739 027244 001402 BEQ 2$ ;BRANCH TO LEAVE IT AT ZERO IF IT IS ZERO.
6740 027246 005367 153026 DEC TIMER1 ;DECREMENT TIME COUNT.
6741 027252 005767 153024 2$: TST TIMER2 ;CHECK FOR TIMER2 AT ZERO.
6742 027256 001402 BEQ 4$ ;BRANCH TO LEAVE IT ALONE IF IT'S ALREADY ZERO.
6743 027260 005367 153016 DEC TIMER2 ;DECREMENT TIME COUNT.
6744 027264 005367 153014 4$: DEC TIMER3 ;DECREMENT THE BREAK COUNT.
6745 027270 001006 BNE 60$ ;EXIT IF NOT TIME TO CALL BREAK.
6746 027272 016767 153010 153004 MOV BCOUNT,TIMER3 ;SET UP TIME TILL NEXT BREAK.
6747 027300 010046 MOV RO,-(SP) ;SAVE CONTENTS OF RO FROM BREAK MACRO.
6748 027302 BREAK ;CHECK FOR OPERATCH CONTROL/C.
6749 027304 012600 MOV (SP)+,RO TRAP C$BRK
6750 027306 000002 60$: RTI ;RESTORE CONTENTS OF RO.

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6752 .SBTTL INTERRUPT SERVICE ROUTINE - RXCHRS -
6753 ;* *****
6754 ;* - DMA RECEIVE INTERRUPT SERVICE ROUTINE -
6755 ;* THIS ROUTINE EXECUTES IN RESPONSE TO AN INTERRUPT CAUSED BY THE DUT
6756 ;* RX.DATA.AVAIL BIT BECOMING ACTIVE. THIS ROUTINE READS CHARACTERS FROM
6757 ;* THE DUT RECEIVE CHARACTER FIFO AND DEPOSITS THEM INTO THE RECEIVE
6758 ;* BUFFER IN MEMORY. IF THE NUMBER OF CHARACTERS IN THE RECEIVE BUFFER
6759 ;* EXCEEDS A SPECIFIED THRESHOLD, TRANSMISSION IS HALTED (BY CLEARING ALL
6760 ;* DUT TX.ENABLE BITS) AND IF THE RECEIVE BUFFER IS FULL RECEPTION IS
6761 ;* HALTED (BY DISABLING RX INTERRUPTS). THE ROUTINE EXITS IF THE RECEIVE
6762 ;* BUFFER BECOMES FULL OR IF A CHARACTER IS READ FROM THE FIFO WITH THE
6763 ;* DATA.VALID BIT CLEAR.
6764 ;*
6765 ;* INPUTS: RBUFA - CONTAINS ADDRESS OF THE DUT RX CHARACTER FIFO.
6766 ;* RXBCNT - RX BUFFER CHARACTER COUNT.
6767 ;* RXBDTX - EQUATED TO RX BUFFER LEVEL AT WHICH TO DISABLE TX.
6768 ;* RXBEND - LABEL AFTER END OF THE RX BUFFER AREA IN MEMORY.
6769 ;* RXBFUL - EQUATED TO THE CAPACITY OF THE RX BUFFER.
6770 ;* RXBIPT - POINTER TO NEXT AVAILABLE INPUT SLOT OF RX BUFFER.
6771 ;* RXBSTA - LABEL AT START OF RX BUFFER AREA IN MEMORY.
6772 ;*
6773 ;* OUTPUTS: RXBIPT - UPDATED TO POINT TO NEXT INPUT SLOT OF RX BUFFER.
6774 ;* RXBCNT - RX BUFFER CHARACTER COUNT (INCREMENTED).
6775 ;* TXENBM - MAP OF PREVIOUS DUT TX.ENABLE STATES.
6776 ;* CARRY - "SUCCESS" FLAG (SET IF BUFFER IS NOT FULL).
6777 ;*
6778 ;* CALLING SEQUENCE: PUT THE ADDRESS OF THE LABEL RXCHRS IN THE VECTOR
6779 ;* LOCATION.
6780 ;*
6781 ;* COMMENTS: IF THE RX BUFFER IS FULL UPON ENTRY, THIS ROUTINE ABORTS THE
6782 ;* PROGRAM.
6783 ;*
6784 ;* SUBORDINATE ROUTINES CALLED: RXIE0,TXDSBL.
6785 ;* -- *****
6786
6787 027310 010246 RXCHRS:: MOV R2, -(SP) ;SAVE CONTENTS OF GPR R2.
6788 027312 017702 152664 2$: MOV BRBUFA, R2 ;READ A CHARACTER FROM THE DUT RX FIFO.
6789 027316 100054 BPL 60$ ;EXIT THE ROUTINE IF THE DATA.VALID BIT IS CLR.
6790
6791 027320 026727 153372 000100 CMP RXBCNT, #RXBFUL ;COMPARE BUFFER COUNT WITH BUFFER CAPACITY.
6792 027326 103402 BLO 4$ ;SKIP ABORT IF BUFFER IS NOT FULL.
6793 027330 004767 172562 JSR PC, OOPS ;ABORT, MUST BE A PROGRAM BUG.
6794 027334 010277 153354 4$: MOV R2, #RXBIPT ;PUT THE CHAR IN THE BUFFER.
6795 027340 062767 000002 153346 ADD #2, RXBIPT ;UPDATE POINTER TO THE NEXT BUFFER SLOT.
6796 027346 026727 153342 003120 CMP RXBIPT, #RXBEND ;CHECK IF POINTER SHOULD WRAP AROUND.
6797 027354 103403 BLO 6$ ;SKIP WRAPAROUND IF POINTER IS NOT AT END.
6798 027356 012767 002720 153330 MOV #RXBSTA, RXBIPT ;WRAP INPUT POINTER AROUND.
6799
6800 027364 005267 153326 6$: INC RXBCNT ;COUNT THIS CHARACTER AS BEING IN THE BUFFER.
6801 027370 026727 153322 000030 CMP RXBCNT, #RXBDTX ;CHECK FOR BUFFER AT DISABLE TX LEVEL.
6802 027376 002745 BLT 2$ ;SKIP DISABLING TX IF BUFFER LEVEL NOT CORRECT.
6803 027400 005767 153102 TST TXDBLF ;CHECK STATE OF TX DISABLE FLAG.
6804 027404 100413 BMI 8$ ;BRANCH IF TRANSMISSION ALREADY DISABLED.
6805 027406 010546 MOV R5, -(SP) ;SAVE THE VALUE OF GPR R5.
6806 027410 012705 177777 MOV #MAPLNS, R5 ;SPECIFY THAT ALL LINES SHOULD BE AFFECTED.
6807 027414 004767 176006 JSR PC, TXDSBL ;CLEAR THE TX ENABLES FOR ALL LINES.
6808 027420 010567 152636 MOV R5, TXENBM ;SAVE PREVIOUS TX ENABLE STATES IN STORAGE.

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6809 027424 012605          MOV    (SP)+,R5      ;RESTORE GPR R5.
6810 027426 012767 100000 153052    MOV    08IT15,TXDBLF ;PREVENT TX FROM BEING DISABLED AGAIN.
6811                                     ;
6812 027434 026727 153256 000100 8$:  CMP    RXBCNT,0RXBFUL ;CHECK FOR BUFFER FULL CONDITION.
6813 027442 103723                                     ;LOOP TO READ ANOTHER CHAR IF BUFFER NOT FULL.
6814                                     ;
6815 027444 004767 175116          JSR    PC,RXIE0      ;BUFFER IS FULL, DISABLE RX INTERRUPTS.
6816                                     ;
6817 027450 012602          60$:  MOV    (SP)+,R2      ;RESTORE R2 TO ITS SAVED VALUE.
6818 027452 000002          RTI
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6820 .SBTTL TRAP SERVICE ROUTINE - TP4BRT -
6821 ;*****
6822 ;* BUS TIME-OUT TRAP (004 TRAP) SERVICE ROUTINE -
6823 ;* THIS ROUTINE IS USED DURING THE DMA ADDRESS TEST.
6824 ;* IT DETERMINES IF THE 004 TRAP WAS CAUSED BY AN "EXPECTED" ERROR OR
6825 ;* NOT BY EXAMINING THE RETURN PC VALUE ON THE STACK. IF THE TRAP IS
6826 ;* UNEXPECTED, THIS ROUTINE JUMPS TO THE NORMAL DIAGNOSTIC SUPERVISOR
6827 ;* 004 TRAP HANDLING ROUTINE.
6828 ;*
6829 ;* INPUTS: SP - POINTS TO THE PC WHERE THE TRAP OCCURED.
6830 ;* TRPAD2 - LABEL AT THE ADDRESS WHERE "EXPECTED" TRAPS OCCUR.
6831 ;* TP4FLG - 004 TRAP FLAGS.
6832 ;*
6833 ;* OUTPUTS: TP4FLG - BIT 15 IS SET IF "EXPECTED" TRAP OCCURED.
6834 ;*
6835 ;* CALLING SEQUENCE: PUT ADDRESS POINTED TO BY TP4BRT IN 004 VECTOR.
6836 ;* OCCURENCE OF 004 TRAP VECTORS TO THIS ROUTINE.
6837 ;*
6838 ;* COMMENTS: ANY 004 TRAP WHICH OCCURS AT AN ADDRESS OTHER THAN THAT LABELED
6839 ;* TRPAD2 WILL BE HANDLED BY THE NORMAL 004 TRAP SERVICE ROUTINE.
6840 ;* THIS ROUTINE IS USED IN CONJUNCTION WITH CKTRPB SUBROUTINE.
6841 ;*
6842 ;* SUBORDINATE ROUTINES CALLED: NONE.
6843 ;*****
6844
6845 027454 021627 017412 TP4BRT:: CMP (SP),#TRPAD2 ;COMPARE EXPECTED ADDR WITH TRAP RET PC.
6846 027460 001402 BEQ 2$ ;IF THEY MATCH, CONTINUE THIS ROUTINE.
6847 027462 000177 152570 JMP @TP4VEC ;IF NOT, JUMP TO NORMAL 004 TRAP SERVICE RTN.
6848 027466 052767 100000 152560 2$: BIS @BIT15,TP4FLG ;SET THE 004 TRAP OCCURED FLAG.
6849 027474 000002 RTI ;ALL DONE, GO BACK TO THE TEST.

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6851 .SBTTL GLOBAL TRAP SERVICE ROUTINE - TP4RTN -
6852 ;*****
6853 ;* BUS TIME-OUT TRAP (004 TRAP) SERVICE ROUTINE -
6854 ;* THIS ROUTINE DETERMINES IF THE 004 TRAP WAS CAUSED BY
6855 ;* AN "EXPECTED" ERROR OR NOT BY EXAMINING THE RETURN PC VALUE ON THE
6856 ;* STACK. IF THE TRAP IS UNEXPECTED, THIS ROUTINE JUMPS TO THE NORMAL
6857 ;* DIAGNOSTIC SUPERVISOR 004 TRAP HANDLING ROUTINE.
6858 ;*
6859 ;*
6860 ;* INPUTS: SP - POINTS TO THE PC WHERE THE TRAP OCCURED.
6861 ;* ADRPTR - LABEL AT THE ADDRESS WHERE "EXPECTED" TRAPS OCCUR.
6862 ;* TP4FLG - 004 TRAP FLAGS.
6863 ;*
6864 ;* OUTPUTS: TP4FLG - BIT 15 IS SET IF "EXPECTED" TRAP OCCURED.
6865 ;*
6866 ;* CALLING SEQUENCE: PUT ADDRESS POINTED TO BY TP4RTN IN 004 VECTOR.
6867 ;* OCCURENCE OF 004 TRAP VECTORS TO THIS ROUTINE.
6868 ;*
6869 ;* COMMENTS: ANY 004 TRAP WHICH OCCURS AT AN ADDRESS OTHER THAN THAT LABELED
6870 ;* ADRPTR WILL BE HANDLED BY THE NORMAL 004 TRAP SERVICE ROUTINE.
6871 ;*
6872 ;* SUBORDINATE ROUTINES CALLED: NONE.
6873 ;*****
6874
6875 027476 021627 017362 TP4RTN:: CMP (SP),ADRPTR ;COMPARE EXPECTED ADR AGAINST TRAP RET PC.
6876 027502 001402 BEQ 2: ;IF THEY MATCH, CONTINUE THIS ROUTINE.
6877 027504 000177 152546 JMP @TP4VEC ;IF NOT, JUMP TO NORMAL 004 TRAP SERVICE RTN.
6878 027510 052767 100000 152536 2: BIS @BIT15,TP4FLG ;SET THE 004 TRAP OCCURED FLAG.
6879 027516 000002 RTI ;ALL DONE, GO BACK TO THE TEST.

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6881 .SBTTL INTERRUPT SERVICE ROUTINE - TXDMA -
6882 ;* *****
6883 ;* - DMA TRANSMIT INTERRUPT SERVICE ROUTINE -
6884 ;* THIS ROUTINE EXECUTES IN RESPONSE TO AN INTERRUPT CAUSED BY THE DUT
6885 ;* TX.ACTION BIT BECOMING ACTIVE. THIS ROUTINE INITIATES THE TX OF A
6886 ;* NEW DMA BUFFER OF CHARACTERS OR SETS THE TX DONE FLAG FOR THE CORRECT
6887 ;* LINE IF TX IS COMPLETE ON THAT LINE.
6888 ;*
6889 ;* INPUTS: BITTBL - LABEL OF TABLE OF WORDS EACH WITH A BIT SET.
6890 ;* CNCNTB - BASE OF # OF CHARS TO TX/RX TABLE.
6891 ;* CSRA - CONTAINS THE ADDRESS OF THE DUT CSR.
6892 ;* DPENDB - BASE OF THE DATA PATTERN END TABLE (ENTRY PER LINE).
6893 ;* DPLENB - BASE OF THE DATA PATTERN LENGTH TABLE.
6894 ;* IESTAT - PRESERVED STATES OF THE DUT INTERRUPT ENABLE BITS.
6895 ;* TXCNTB - LABEL AT BASE OF THE TX CHARACTER COUNTER TABLE.
6896 ;* TXPTRB - LABEL AT BASE OF THE TX DATA PATTERN POINTERS TABLE.
6897 ;*
6898 ;* OUTPUTS: TXCNTX - COUNTERS INCREMENTED FOR LINES ON WHICH CHARS SENT.
6899 ;* TXDNFB - TX DONE FLAGS SET FOR LINES WHICH HAVE SENT ALL CHARS.
6900 ;* TXINTF - TX INT FLAGS (BIT SET IF DMA.HO FOUND SET ON LINE).
6901 ;*
6902 ;* CALLING SEQUENCE: PUT THE ADDRESS OF THE LABEL TXDMA IN THE VECTOR
6903 ;* LOCATION.
6904 ;*
6905 ;* COMMENTS:
6906 ;*
6907 ;* SUBORDINATE ROUTINES CALLED: DODMA.
6908 ;* -- *****
6909
6910 027520 TXDMA:: SAVE ;SAVE CONTENTS OF GPRS R0 THRU R5.
6911 027520 004567 155600 ;R5,PREG05 ;CALL REGISTER SAVE SUBRT.
6912 027524 017701 152450 MOV @CSRA,R1 ;READ THE CONTENTS OF THE DUT CSR.
6913 027530 010100 MOV R1,R0 ;SAVE INITIAL CONTENTS OF IND.ADR.REG FIELD.
6914 027532 000402 BR 4$ ;BRANCH TO SKIP DOUBL READING OF DUT CSR.
6915 ;*
6916 ; READ THE CONTENTS OF THE DUT CSR. THIS WILL CLEAR THE TX.ACTION CSR BIT.
6917 ; IF TX.ACTION IS NOT SET, EXIT THIS ROUTINE.
6918 ; DETERMINE THE LINE FOR WHICH THE TX.ACTION WAS SET.
6919 ; CALCULATE AN OFFSET FOR USE IN ACCESSING TABLES (2 TIMES THE LINE NUMBER).
6920 ; GET THE BIT MAP OF THIS LINE.
6921 027534 017701 152440 2$: MOV @CSRA,R1 ;READ THE CONTENTS OF THE DUT CSR.
6922 027540 100033 4$: BPL 60$ ;EXIT ROUTINE IF TX.ACTION IS CLEAR.
6923 027542 000301 SWAB R1 ;CALCULATE THE LINE NUMBER OF THE LINE WHICH IS
6924 027544 042701 177760 BIC @177760,R1 ; ASSOCIATED WITH THE TX.ACTION.
6925 027550 010104 MOV R1,R4 ;CALCULATE AN OFFSET FOR USE IN ACCESSING
6926 027552 006304 ASL R4 ; LINE COUNTER AND POINTER IN TABLES.
6927 027554 016405 002364 MOV BITTBL(R4),R5 ;GET THE BIT MAP OF THIS LINE.
6928 ;*
6929 ; GET THE TX CHARACTER COUNTER FOR THIS LINE.
6930 ; IF ALL THE CHARACTERS HAVE BEEN SENT FOR THIS LINE:
6931 ; SET THE TX DONE FLAG FOR THIS LINE.
6932 ; DON'T SEND A CHAR TO THE LINE (NO MORE TX.ACTIONS ON THIS LINE).
6933 ; LOOP TO CHECK THE TX.ACTION FOR ANOTHER LINE.
6934 ;*
6935 027560 026464 003502 003442 CMP TXCNTB(R4),CHCNTB(R4) ;COMPARE # CHARS SENT AND TX COUNT.
6936 027566 103403 BLO 6$ ;GO TO SEND A CHAR IF NOT ALL CHARS SENT.

```

6937	027570	050567	152706	BIS	R5, TXDNF	;SET THIS LINE'S TX DONE FLAG.
6938	027574	000757		BR	2\$;LOOP TO CHECK TX.ACTION AGAIN.
6939				;*		
6940				; START THE DMA OF THE NEXT BUFFER (DATA PATTERN) ON THIS LINE.		
6941				; GET THE DATA PATTERN LENGTH FOR THIS LINE.		
6942				; GET THE START ADDRESS OF THE DATA PATTERN.		
6943				;-		
6944	027576	016403	003202	6\$:	MOV	DLENB(R4),R3 ;PASS DATA PATTERN LENGTH FOR LINE TO DODMA.
6945	027602	016402	003342		MOV	TXPTRB(R4),R2 ;PASS THE TX START ADR TO DODMA.
6946				;*		
6947				; WRITE DMA PARAMETERS TO THE DUT.		
6948				;		
6949	027606	004767	170172		JSR	PC,DODMA
6950	027612	103403			BCS	8\$;SKIP ERROR IF DODMA WAS SUCCESSFUL.
6951				;*		
6952				; SET THE PROPER BIT OF THE TX INTERRUPT FLAGS TO INDICATE THE LINE ERROR.		
6953				;-		
6954	027614	050567	152444		BIS	R5, TXINTF ;INDICATE THE ERROR.
6955	027620	000402			BR	10\$;SKIP UPDATING POINTERS AND COUNTERS.
6956				;*		
6957				; UPDATE THE TX CHARACTER FOR THIS LINE.		
6958				; UPDATE THE TX BUFFER POINTER FOR THIS LINE.		
6959				;-		
6960	027622	060364	003502	8\$:	ADD	R3, TXCNTB(R4) ;ADD THE DATA PAT LENGTH TO THE TX COUNT.
6961				;*		
6962				; LOOP TO CHECK THE TX.ACTION BIT FOR ANOTHER LINE.		
6963				;-		
6964	027626	000742		10\$:	BR	2\$;LOOP BACK TO CHECK TX.ACTION BIT AGAIN.
6965				;		
6966	027630	016701	152400	60\$:	MOV	IESTAT,R1 ;GET THE PRESENT STATES OF TX.IE & RX.IE BITS.
6967	027634	042700	177760		BIC	#177760,R0 ;GET SAVED IND.ADR.REG FIELD BITS.
6968	027640	050001			BIS	R0,R1 ;COMBINE IND.ADR.REG FIELD BITS WITH IE BITS.
6969	027642	010177	152332		MOV	R1, &CSRA ;RESTORE THE DUT CSR IND.ADR.REG FIELD.
6970	027646				PASS	;RESTORE GPRS.
	027646	004736				
6971	027650	000002			JSR	PC,&(SP)+ ;RETURN TO PREG05 SUBRT.
				RTI		

```

6973
6974 ;*****
6975 ;
6976 ;           FVTA.RPT
6977 ;
6978 ;*****
6979
6980
6981
6982 .SBTTL  REPORT CODING SECTION
6983
6984 ;**
6985 ; THE REPORT CODING SECTION CONTAINS THE
6986 ; "PRINTS" CALLS THAT GENERATE STATISTICAL REPORTS.
6987 ;--
6988
6989 027652      BGNRPT
6990 027652
6991 027652      EXIT      RPT
6992 027652      000167
6993 027654      000000
6994
6995 027656      .EVEN
6996 027656      ENDRPT
6997 027656      L10016:
6998 027656      TRAP      C$RPT
6999
7000

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6997
6998
6999

.SBTTL PROTECTION TABLE

7000
7001
7002
7003
7004
7005
7006
7007
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7009
7010
7011

;
; FVTSKL4.P11
;

; THIS TABLE IS USED BY THE RUNTIME SERVICES
; TO PROTECT THE LOAD MEDIA.

7012 027660
027660

BGNPROT

L\$PROT::

7013

7014 027660 177777
7015 027662 177777
7016 027664 177777

1 ;OFFSET INTO P-TABLE FOR CSR ADDRESS
-1 ;OFFSET INTO P-TABLE FOR MASSBUS ADDRESS
-1 ;OFFSET INTO P-TABLE FOR DRIVE NUMBER

7017

7018 027666

ENDPROT

7019

```

7034 ;*****
7035 ;
7036 ;               FVTC.INI
7037 ;
7038 ;*****
7039 ;*****
7040 ;*****
7041 ;*****
7042 ;*****
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7068 ;*****
7069 ;*****
7070 ;*****
7071 ;*****
7072 ;*****
7073 ;*****
7074 ;*****

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027742 010001
7075 027744 012167 152320
7076 027750 012167 152316
7077 027754 012167 152314
7078 027760 012167 152312
7079 027764 026727 152306 000062
7080 027772 001004
7081 027774 012767 000024 152306
7082 030002 000403
7083 030004 012767 000021 152276 2$:
7084 030012 012767 000021 152276 4$:
030012 016746 150262
030016 012746 027240
030022 016746 152246
030026 012746 000003
030032 104437
030034 062706 000010
7085 030040 016700 152232
7086 030044 006300
7087 030046 010067 152234
7088 030052
030052 012700 000240
030056 104441

MOV (R1)+,CLKCSR ;STORE CLOCK CSR ADDRESS.
MOV (R1)+,CLKBRL ;STORE CLOCK BUS REQ INT LEVEL.
MOV (R1)+,CLKVEC ;STORE CLOCK INTERRUPT VECTOR.
MOV (R1)+,CLKHRZ ;STORE CLOCK FREQUENCY.
CMP CLKHRZ,#50. ;TEST FOR 50HZ LINE FREQUENCY.
BNE 2$ ;BRANCH IF CLOCK IS NOT 50HZ.
MOV #20.,MSTICK ;INDICATE 20MS PER CLOCK TICK.
BR 4$
MOV #17.,MSTICK ;INDICATE 17 MS PER CLOCK TICK.
SETVEC CLKVEC,#CLKINT,PRI06 ;INITIALIZE CLOCK INTERRUPT VECTOR.
MOV PRI06,-(SP)
MOV #CLKINT,-(SP)
MOV CLKVEC,-(SP)
MOV #3,-(SP)
TRAP C$SVEC
ADD #10,SP

MOV CLKHRZ,R0 ;INITIALIZE THE BREAK COUNT
ASL R0 ; TO CAUSE A BREAK
MOV R0,BCOUNT ; EVERY 2 SECONDS.
SETPRI #PRI05 ;ALLOW CLOCK INTERRUPTS DISABLE OTHERS.
MOV #PRI05,R0
TRAP C$SPRI

;
; ENABLE THE LINE TIME CLOCK (LTC) CHECKING TO MAKE SURE THAT THE CSR
; IS ACCESSABLE.
; FIRST SET UP TO CATCH ANY 004 TRAPS WHICH OCCUR:
;
MOV 4,TP4VEC ;SAVE THE EXISTING 004 TRAP VECTOR.
MOV #TP4RTN,4 ;SET 004 TRAP VECTOR TO OUR SERVICE RTN ADR.
;
; ENABLE LTC CHECKING FOR 004 TRAP IN CASE CSR IS NOT THERE.
;
CLR TP4FLG ;CLEAR THE 004 TRAP FLAG.
MOV #BIT6,WORD1 ;SET UP TO SET BIT6 OF THE LTC CSR.
MOV #WORD1,R0 ;SET UP WORD1 AS THE CKTRAP MOVE SOURCE.
MOV CLKCSR,R1 ;SET UP LTC CSR AS DESTINATION FOR CKTRAP MOVE.
JSR PC,CKTRAP ;MOVE AND CHECK FOR TRAP.
MOV TP4VEC,4 ;RESTORE THE NORMAL 004 TRAP VECTOR.
BCS 6$ ;IF NO TRAP, LTC IS THERE SO CONTINUE.
CLR CLKHRZ ;CLEAR LTC FREQUENCY WORD TO INDICATE NO LTC.
BR 8$ ;BYPASS THE FOLLOWING CALIBRATION PROCEDURES.

;
; CALIBRATE THE DELAY ROUTINE MILLI-SECOND DELAY COUNT VALUE.
;
6$: JSR PC,CALMSL
;
; CHECK FOR MEMORY MANAGEMENT PRESENT ON THIS MACHINE.
; IF MEM MGT IS PRESENT, DISABLE IT.
;
8$: MOV 4,TP4VEC ;SAVE THE EXISTING 004 TRAP VECTOR.
MOV #TP4RTN,4 ;SET 004 TRAP VECTOR TO OUR SERVICE RTN ADR.
CLR TP4FLG ;CLEAR THE 004 TRAP FLAG.
CLR WORD1 ;PREPARE TO CLEAR THE MEM MGT SRO REGISTER.
MOV #WORD1,R0 ;SELECT CLEARED WORD AS CKTRAP RTN SOURCE.
MOV MMSRO,R1 ;SELECT MEM MGT SRO REGISTER AS DESTINATION.
CLR MMPRES ;INDICATE NO MEM MGT PRESENT IN CASE IT ISN'T.

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7123 030204 005067 152112          CLR    MMENAB      ;INDICATE MEM MGT IS NOT ENABLED.
7124 030210 004767 167134          JSR    PC,CKTRAP    ;CLEAR THE MEM MGT SRO REG AND CHECK FOR TRAP.
7125 030214 016767 152036 147562    MOV    TP4VEC,4      ;RESTORE THE NORMAL 004 TRAP VECTOR.
7126 030222 103003                BCC    10$           ;SKIP INDICATING MEM MGT PRESENT IF IT ISN'T.
7127 030224 012767 000001 152066    MOV    #1,MMPRES    ;INDICATE THAT MEM MGT IS PRESENT.
7128 030232 005067 152004          CLR    PASCNT        ;CLR COUNTER USED IN REPORTING ROM VERSION #.
7129 030236 000167 000006          JMP    NEWPAS        ;SKIP AROUND THE BUS RESET, IT'S BEEN DONE.
7130
7131 030242          NEWRES: BRESET      ;RESET THE BUS TO PREVENT ILLEGAL INTERRUPTS.
      030242 104433          CLR    PASCNT        TRAP    C$RESET
7132 030244 005067 151772          ; LR COUNTER USED IN REPORTING ROM VERSION #.
7133
7134 030250          NEWPAS:
7135 030250 012767 177777 151720    MOV    #-1,UNITN      ;RESET LOGICAL DEVICE TO -1
7136
7137          ;*
7138          ; INCREMENT THE PASS COUNTER, CORRECT FOR ANY OVERFLOW.
7139          ; THIS COUNTER IS USED IN THE ROM VERSION TEST.
7140          ;*
7140 030256 005267 151760          INC    PASCNT        ;INCREMENT THE PASS COUNTER.
7141 030262 001002                BNE    GETPRM        ;BRANCH IF WE HAVE NOT YET! OVERFLOWED.
7142 030264 005367 151752          DEC    PASCNT        ;SET PASS COUNT TO 177777 OCTAL.
7143
7144          ; GET THE HARDWARE PARAMETERS FOR THIS UNIT.
7145          GETPRM:
7146 030270 005267 151702          INC    UNITN        ;INCREMENT LOGICAL DEVICE NUMBER
7147 030274 026767 151676 151510    CMP    UNITN,L$UNIT    ;SEE IF MAXIMUM UNIT NO. EXCEEDED
7148 030302 002362                BGE    NEWPAS        ;BR IF YES
7149
7150 030304          GPWARD UNITN,R1      ;GET P-TABLE POINTER INTO R1
      030304 016700 151666          MOV    UNITN,R0
      030310 104442          TRAP    C$GPWARD
      030312 010001          MOV    R0,R1
7151 030314          BCOMPLETE 30$      ;BR IF DEVICE AVAILABLE
      030314 103401          BCS    30$
7152 030316 000764          BR    GETPRM      ;SKIP THIS DEVICE
7153
7154
7155          ;***** HARDWARE PARAMETER MOVING CODE *****
7156 030320 012167 151654 30$: MOV    (R1)+,CSRA      ;STORE DMU-11 CSR ADDRESS IN DEV.REG.ADDRESS TABLE
7157 030324 012102          MOV    (R1)+,R2      ;GET THE RX INTERRUPT VECTOR ADDRESS.
7158 030326 010267 151634          MOV    R2,RXVECA    ;STORE RX INT VECTOR ADDRESS.
7159 030332 062702 000004          ADD    #4,R2      ;CALCULATE TX INTERRUPT VECTOR ADDRESS.
7160 030336 010267 151626          MOV    R2,TXVECA    ;STORE TX INT VECTOR ADDRESS.
7161 030342 012167 151624          MOV    (R1)+,ACTLNS    ;STORE DMU-11 ACTIVE LINE BIT MAP
7162 030346 112167 151622          MOV    (R1)+,LOPBCK    ;STORE DMU-11 LOOPBACK MODE
7163 030352 111167 151617          MOV    (R1),BRLEVL    ;STORE DMU-11 INTERRUPT BUS REQUEST LEVEL
7164
7165          ;*
7166          ; CALCULATE DEVICE REGISTER ADDRESSES, AND PUT THEM IN THE
7167          ; DEVICE REGISTER ADDRESS TABLE.
7168          ;*
7168 030356 016701 151616          MOV    CSRA,R1      ;COPY CSR ADDRESS
7169 030362 005201          INC    R1              ;INCREMENT CSR ADDRESS
7170 030364 005201          INC    R1              ; COPY BY 2.
7171 030366 012703 000007          MOV    #7,R3      ;SET UP REGISTER COUNT
7172 030372 012702 002202          MOV    #RBUFA,R2    ;GET LOCATION WHERE RBUF ADDRESS GOES IN TABLE
7173 030376 010122          12$: MOV    R1,(R2)+      ;STORE REGISTER ADDRESS IN TABLE
7174 030400 005201          INC    R1              ;INCREMENT REGISTER ADDRESS

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7175 030402 005201          INC    R1          ; BY 2, FOR THE NEXT DEVICE REGISTER.
7176 030404 005303          DEC    R3          ; DECREMENT REGISTER COUNT
7177 030406 001373          BNE    12$         ; LOOP IF NOT DONE
7178
7179
7180          ;+
7181          ; INITIALISE THE BMP CODE QUEUE.
7182          ; -
7182 030410 012700 002512    MOV    #BMPQCB,R0    ; GET THE START ADDRESS OF THE QUEUE.
7183 030414 012701 002712    MOV    #BMPQCE,R1    ; GET THE END ADDRESS OF THE QUEUE.
7184 030420 010067 152064    MOV    R0,BMPCQP    ; SET THE POINTER TO THE START OF THE QUEUE.
7185 030424 005020          14$: CLR    (R0)+      ; CLEAR OUT THE CONTENTS OF THE QUEUE.
7186 030426 020001          CMP    R0,R1          ; CHECK IF END OF QUEUE HAS BEEN REACHED.
7187 030430 103775          BLO    14$           ; LOOP IF NOT ALL DONE.
7188
7189          ;+
7190          ; REPORT THE UNIT NUMBER IF THE SOFTWARE P-TABLE QUESTION WAS ANSWERED YES,
7191          ; AND THE MAXIMUM UNIT NUMBER IS GREATER THAN 1.
7192          ; -
7192 030432 032767 000020 151522 BIT    #BIT4,OPTION ; CHECK IF THE QUESTION WAS ANSWERED YES.
7193 030440 001416          BEQ    16$           ; SKIP REPORTING UNIT NUMBER IF IT IS DISABLED.
7194 030442 026727 151344 000001 CMP    L$UNIT,#1 ; CHECK MAXIMUM NUMBER OF UNITS SELECTED.
7195 030450 003412          BLE    16$           ; DO NOT REPORT UNIT NUMBER IF MAX NUMBER < 1.
7196 030452          PRINTF  #MFUNIT,UNITN ; REPORT UNIT NUMBER.
7196 030452 016746 151520          MOV    UNITN,-(SP)
7196 030456 012746 005422          MOV    #MFUNIT,-(SP)
7196 030462 012746 000002          MOV    #2,-(SP)
7196 030466 010600          MOV    SP,R0
7196 030470 104417          TRAP   C$PNTF
7196 030472 062706 000006          ADD    #6,SP
7197 030476          16$:
7198
7199 030476          ENDIT:
7200          ;+
7201          ; SET THE PROCESSOR PRIORITY TO DISABLE ALL INTERRUPTS.
7202          ; -
7203 030476          SETPRI  #PRI07              ; SET PROCESSOR PRIORITY TO 7.
7203 030476 012700 000340          MOV    #PRI07,R0
7203 030502 104441          TRAP   C$SPRI
7204
7205 030504          ENDINIT
7205 030504          L10020:
7205 030504 104411          TRAP   C$INIT
7206
7207          TNUM == 0          ; INITIALIZE THE ASSEMBLER TEST NUMBER VARIABLE.

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```

7210 ;*****
7211 ;
7212 ;          FVTA.ATD
7213 ;
7214 ;*****
7215
7216
7217
7218 .SBTTL AUTODROP SECTION
7219
7220
7221 ;**
7222 ; THIS CODE IS EXECUTED IMMEDIATELY AFTER THE INITIALIZE CODE IF
7223 ; THE "ADR" FLAG WAS SET. THE UNIT(S) UNDER TEST ARE CHECKED TO
7224 ; SEE IF THEY WILL RESPOND. THOSE THAT DON'T ARE IMMEDIATELY
7225 ; DROPPED FROM TESTING.
7226 ;--
7227
7228 030506          BGNAUTO
7229 030506
7230
7231
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7237 030506          ENDAUTO
7238 030506
7239 030506 104461
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7239
7240 ;*****
7241 ;
7242 ;           FVT.CUC
7243 ;
7244 ;*****
7245
7246
7247
7248 .SBTTL  CLEANUP CODING SECTION
7249
7250 ;**
7251 ; THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS PERFORMED
7252 ; AFTER THE HARDWARE TESTS HAVE BEEN PERFORMED.
7253 ;--
7254
7255 030510      BGNCLN
7256 030510
7257
7258                                     L$CLEAN::
7259
7260 030510 005767 151504      TST      CTRLCF      ;DID WE GET HERE BY CTRL-C FROM TEST?
7261 030514 001401      BEQ      2$      ;CTRL-C FROM TEST? NO, SKIP BUS RESET.
7262 030516      BRESET      ;YES, CLR ANY DMAS OR OUTSTANDING INTERRUPTS.
7263 030516 104433      TRAP      C$RESET
7264 030520
7265 030520 2$:      EXIT      CLN
7266 030520 104432      TRAP      C$EXIT
7267 030522 000002      .WORD      L10022-.
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7286 030524      .EVEN
7287 030524      ENDCLN
7288 030524 104412      L10022:      TRAP      C$CLEAN

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7304 030526
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7312 030526
      030526 010046
      030530 012746 030552
      030534 012746 000002
      030540 010600
      030542 104417
      030544 062706 000006
7313 030550 000427
7314
7315 030552 045 101 040 DROP: .ASCIZ/%A UNIT%D6%A DROPPED FROM FURTHER TESTING.%N/
      030555 125 116 111
      030560 124 045 104
      030563 066 045 101
      030566 040 104 122
      030571 117 120 120
      030574 105 104 040
      030577 106 122 117
      030602 115 040 106
      030605 125 122 124
      030610 110 105 122
      030613 040 124 105
      030616 123 124 111
      030621 116 107 056
      030624 045 116 000
7316
7317 030630
7318
7319 030630
      030630 000167
      030632 000000
7320
7321

```

```

;*****
;
;          FVTA.DRP
;
;*****

.SBTTL  DROP UNIT SECTION

; **
; THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
; TO NO LONGER BE TESTED.
; --

      BGNDU

                                L$DU:
;*****
;          INSERT DROP CODE HERE.  THIS CODE WILL BE EXECUTED AFTER
;          A "DROP" COMMAND OR A "DODU" MACRO EXECUTION.  THE PURPOSE
;          OF THIS CODE IS TO DO ANY NECESSARY HOUSEKEEPING AFTER A
;          UNIT HAS BEEN DROPPED.  THIS SECTION IS OPTIONAL.
;*****
      PRINTF #DROP,RO          ;REPORT UNIT THAT HAS BEEN DROPPED.
                                MOV      RO,-(SP)
                                MOV      #DROP,-(SP)
                                MOV      #2,-(SP)
                                MOV      SP,RO
                                TRAP     C$PNTF
                                ADD      #6,SP

      BR      EDROP            ;BRANCH AROUND THE MESSAGE.

                                .EVEN
EDROP:
      EXIT      DU

                                .WORD    J$JMP
                                .WORD    L10023-2-.

```

K15

DHU 11 FUNCTIONAL VERIFICATION MACRO M1200 12-DEC-83 16:16 PAGE 127-1
DROP UNIT SECTION

SEQ 192

7322 030634
030634
030634 104453

ENDDU

L10023: TRAP C\$DU

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7324
7325 ;*****
7326 ;
7327 ;          FVTA.ADD
7328 ;
7329 ;*****
7330
7331
7332
7333 .SBTTL  ADD UNIT SECTION
7334
7335 ;**
7336 ; THE ADD-UNIT SECTION CONTAINS ANY CODE THE PROGRAMMER WISHES
7337 ; TO BE EXECUTED IN CONJUNCTION WITH THE ADDING OF A UNIT BACK
7338 ; TO THE TEST CYCLE.
7339 ;--
7340
7341 030636          BGNAU
7342 030636
7343
7344 ;*****
7345 ;          INSERT ADD CODE HERE.  THIS CODE WILL BE EXECUTED AFTER
7346 ;          AN "ADD" COMMAND.  THE PURPOSE OF THIS CODE IS TO DO ANY
7347 ;          HOUSEKEEPING THAT MAY BE NECESSARY AFTER A UNIT HAS BEEN ADDED.
7348 ;          THIS SECTION IS OPTIONAL.
7349 ;*****
7350 030636          EXIT    AU
7351 030636 000167
7352 030640 000000
7353
7354
7355 030642          .EVEN
7356 030642          ENDAU
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7358 .SBTTL  HARDWARE TEST          - ADRA -
7359 ;**
7360 ;*****
7361 ;*                                - REGISTER ADDRESS TEST -
7362 ;*
7363 ;*      THIS TEST VERIFIES THAT THE DEVICE REGISTERS WILL RESPOND TO THE PROPER
7364 ;*      UNIBUS HANDSHAKING SIGNALS WHEN ACCESSED. IF THE DHU11 DOES NOT RESPOND
7365 ;*      TO THE ACCESS ATTEMPTS (IF THE DHU11 IS AT THE WRONG ADDRESS, FOR EXAMPLE)
7366 ;*      THE 004 BUS TIME-OUT TRAP IS DETECTED BY THIS ROUTINE AND AN ERROR
7367 ;*      IS REPORTED. THIS TEST IS PERFORMED ON LINE 0 ONLY.
7368 ;*
7369 ;*****
7370 ;--
7371
7372 030644      BGNTST
7373 030644
7373      TNUM == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
7374 030644 000001 012767 000001 151406      MOV  #TNUM,TSTNUM      ;SET UP THE TEST NUMBER. (1)
7375 030652 012767 177777 151340      MOV  #-1,CTRLCF      ;INDICATE THAT WE ARE IN A TEST.
7376 030660 012767 000145 154430      MOV  #101,ERRNBR      ;SET THE TEST ERROR NUMBER IN THE TABLE.
7377 030666 012767 010041 154424      MOV  #EM0103,ERRMSG      ;SET UP THE TEST FAILURE MESSAGE IN THE TABLE.
7378 030674 012767 013572 154420      MOV  #ER0101,ERRBLK      ;SET-UP THE ERROR ROUTINE IN THE ERROR TABLE.
7379
7380 ; SET UP TO CATCH ANY 004 TRAPS WHICH OCCUR:
7381 ;--
7382 030702 016767 147076 151346      MOV  4,TP4VEC      ;SAVE THE EXISTING 004 TRAP VECTOR.
7383 030710 012767 027476 147066      MOV  #TP4RTN,4      ;SET 004 TRAP VECTOR TO OUR SERVICE RTN ADR.
7384 030716 005005                    CLR  R5      ;CLEAR THE ERROR FLAGS.
7385
7386 ;*
7387 ; HERE BEGINS THE LOOP TO TEST THE REGISTERS FOR A LINE.
7388 ; FIRST TEST THE CSR AND SET THE IND.ADR.REG (I.A.R) FIELD.
7389 ;--
7390 030720 016700 151254      MOV  CSRA,R0      ;SET UP CSR AS THE CKTRAP MOVE SOURCE.
7391 030724 012701 031116      MOV  #52,R1      ;SET UP DESTINATION LOCATION FOR CKTRAP MOVE.
7392 030730 004767 166414      JSR  PC,CKTRAP      ;MOVE AND CHECK FOR TRAP.
7393 030734 103402            BCS  4#            ;IF NO TRAP, BYPASS ERROR.
7394 030736 052705 100001      BIS  #100001,R5      ;SET FATAL READ ERROR FLAGS.
7395 030742 042767 000017 000146 4# :      BIC  #17,52#      ;CLEAR THE I.A.R FIELD OF THE CSR DATA.
7396 030750 010100            MOV  R1,R0      ;USE OLD DESTINATION FOR SOURCE OF CKTRAP MOVE.
7397 030752 016701 151222      MOV  CSRA,R1      ;SET UP CSR AS THE CKTRAP MOVE DESTINATION.
7398 030756 004767 166366      JSR  PC,CKTRAP      ;MOVE AND CHECK FOR TRAP.
7399 030762 103403            BCS  6#            ;IF NO TRAP, BYPASS ERROR.
7400 030764 052705 100002      BIS  #100002,R5      ;SET FATAL WRITE ERROR FLAGS.
7401 030770 000434            BR   40#            ;EXIT AND REPORT FATAL ERROR.
7402
7403 ;*
7404 ; NOW, WE TEST EACH REGISTER FOR THIS LINE.
7405 ;--
7405 030772 012702 000010      6# :      MOV  #8,R2      ;INIT REGISTER COUNTER TO 8.
7406 030776 016767 151176 000110      MOV  CSRA,50#      ;INITIALIZE THE REGISTER POINTER.
7407 031004 012700 031114      8# :      MOV  #50,R0      ;SET UP REGISTER AS THE SOURCE FOR CKTRAP MOVE.
7408 031010 012701 031116      MOV  #52,R1      ;SET UP LOCAL STORAGE AS THE DES FOR CKTRAP.
7409 031014 004767 166330      JSR  PC,CKTRAP      ;PERFORM THE MOVE, CHECK FOR TRAP.
7410 031020 103402            BCS  10#           ;IF NO TRAP, BYPASS THE SETTING OF ERROR FLAGS.
7411 031022 052705 100001      BIS  #100001,R5      ;SET FATAL READ ERROR FLAGS.
7412 031026 010100            MOV  R1,R0      ;USE OLD DEST AS SRC FOR CKTRAP MOVE.
7413 031030 012701 031114      10# :      MOV  #50,R1      ;SET UP REGISTER AS THE DEST FOR CKTRAP MOVE

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7414 031034 004767 166310      JSR      PC,CKTRAP      ;PERFORM THE MOVE, CHECK FOR TRAP.
7415 031040 103402              BCS      12$          ;IF NO TRAP, BYPASS THE SETTING OF ERROR FLAGS.
7416 031042 052705 100002      BIS      #100002,R5      ;SET FATAL WRITE ERROR FLAGS.
7417 031046 005267 000042      12$: INC      50$          ;INCREMENT THE REGISTER
7418 031052 005267 000036      INC      50$          ; POINTER BY 2.
7419 031056 005302              DEC      R2            ;COUNT THE REGISTER.
7420 031060 001351              BNE      8$            ;LOOP TO TEST THE NEXT REGISTER ADDRESS.
7421
7422
7423      ;+
7424      ; DONE CHECKING DEVICE REGISTER ADDRESSES.
7425      ; REPORT ANY ERRORS AND EXIT.
7426      ;-
7427 031062 016767 151170 146714 40$: MOV      TP4VEC,4      ;RESTORE THE NORMAL 004 TRAP VECTOR.
7428 031070 005705              TST      R5            ;CHECK THE ERROR FLAGS.
7429 031072 100012              BPL      60$          ;EXIT ROUTINE IF NO ERRORS.
7430
7431      ;+
7432      ; REPORT "DEVICE REGISTER ACCESS TEST FAILED"
7433      ;-
7433 031074              ERROR
7434 031074 104460
7435
7436      DODU      UNITN      ;DROP THIS UNIT FROM FUTHER TESTING.
7437 031076 016700 151074              MOV      UNITN,R0      UNITN,R0
7438 031102 104451              TRAP      C$DODU
7437 031104 005067 151110      CLR      CTRLCF      ;INDICATE NO CTRL-C ABORT FROM TEST.
7438 031110 104444              DOCLN      ;ABORT THIS SUB PASS.
7439 031112 000402              BR      60$          TRAP      C$DCLN
7440
7441      ;***** LOCAL STORAGE. *****
7442 031114 000000 50$: .WORD 0      ;STORAGE FOR THE SOURCE OR DEST OF THE CKTRAP MOVE.
7443 031116 000000 52$: .WORD 0      ;STORAGE FOR THE SOURCE OR DEST OF THE CKTRAP MOVE.
7444      ;***** END *****
7445
7446 031120 005067 151074 60$: CLR      CTRLCF      ;INDICATE THAT WE ARE NOT WITHIN A TEST.
7447 031124 031124 104401      ENDTST
7448 031124 104401
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7449          SBTTL  HARDWARE TEST          - KBECHO
7450          ;... *****
7451          ;*
7452          ;*      - KEYBOARD ECHO TEST -
7453          ;*      THIS IS A TEST WHICH PUTS UARTS FOR THE ACTIVE LINES INTO REMOTE
7454          ;*      LOOPBACK MODE.  THE ACTIVE LINE UARTS ARE SET UP WITH A BAUDRATE
7455          ;*      WHICH IS SPECIFIED BY THE OPERATOR.  THIS TEST SETS UP THE LINES
7456          ;*      FOR: 1 STOP BIT, NO PARITY AND 8 BITS/CHARACTER.
7457          ;*      THE TEST EXECUTES INDEFINITELY UNTIL TERMINATED BY THE OPERATOR.
7458          ;*
7459          ;*      THIS TEST CAN BE USED FOR LOOPING BACK TERMINAL KEYBOARD INPUT ONTO
7460          ;*      A TERMINAL CRT OR IT CAN BE USED AS A GENERAL LOOPBACK METHOD FOR
7461          ;*      TESTING COMMUNICATIONS LINKS TO THE DUT FROM THE OTHER END OF THE
7462          ;*      CHANNEL.  DTR AND RTS ARE SET ON THE SELECTED LINES DURING THIS
7463          ;*      TEST TO ALLOW THE TESTING OF MODEM LINKS.
7464          ;*
7465          ;-- *****
7465 031126      BGNST
7466 031126      SETPRI  #PRI05              ;ALLOW LTC INTERRUPTS.          T2::
7466 031126      012700  000240              MOV      #PRI05,R0
7466 031132      104441                      TRAP      C#SPRI
7467          000002                      TNUM  == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
7468 031134      012767  000002  151116      MOV      #TNUM,TSTNUM ;SET UP THE TEST NUMBER.          (94)
7469          ;*
7470          ;*  VERIFY THAT THE TEST SHOULD BE PERFORMED.  MUST HAVE THE FOLLOWING:
7471          ;*  KEYBOARD ECHO LOOPBACK SELECTED.
7472          ;*  MANUAL INTERVENTION ALLOWED.
7473          ;*
7474 031142      126727  151026  000005      CMPB     LOPBCK,#5      ;TEST THE LOOPBACK TYPE INDICATOR.
7475 031150      001402                      BEQ      2#          ;KBD ECHO LPBCK SELECTED? YES, CONTINUE TEST.
7476 031152      104432                      EXIT     TST          ;NO. ABORT THE TEST.
7476 031154      000212                      TRAP      C#EXIT
7477 031156      104450      2#::            .WORD     L10026 .
7478 031160      103402                      TRAP      C#MANI
7479 031162      104432                      BCS      4#          ;CHECK FOR MANUAL INTERVENTION ALLOWED.
7479 031164      000202                      BCS      4#          ;MANUAL INTERVENTION ALLOWED? YES, DO TEST.
7480          4#::            EXIT     TST          ;NO. ABORT THE TEST.
7481 031166      012767  177777  151024      4#::    MOV      #-1,CTRLCF ;INDICATE THAT WE ARE IN A TEST.
7482 031174      012767  000001  154112      MOV      #1,ERRTYP  ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
7483 031202      012767  022271  154106      MOV      #9401,ERRNBR ;SET THE FIRST ERROR NUMBER IN ERROR TABLE.
7484 031210      012767  013065  154102      MOV      #EM9401,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERRTABL.
7485 031216      005067  151256      CLR      ERSRNF          ;INITIALIZE THE "REPORT ERROR SUMMARY" FLAGS.
7486          ;*
7487          ;*  RESET THE DUT TO A KNOWN STATE, REMOVE THE STATUS CODES FROM THE FIFO.
7488          ;*  CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
7489          ;*  THIS SUBROUTINE REPORTS ERROR >>>> 9401 <<<<.
7490          ;*
7491 031222      004767  166202      JSR      PC,CLNRST ;RESET THE DMU-11, REPORT ANY ERRORS FOUND.
7492 031226      103402                      BCS      .+6        ;SKIP EXIT OF TEST IF NO FATAL ERROR FOUND.
7493 031230      000167  000120      JMP      60#        ;RESET FAILURE, ABORT THIS TEST.
7494          ;*
7495          ;*  PRINT THE TEST NAME.
7496          ;*

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7497 031234          PRINTF  #EF0503,#EM9401
      031234 012746 013065
      031240 012746 005453
      031244 012746 000002
      031250 010600
      031252 104417
      031254 062706 000006
7498
7499
7500
7501
7502
7503
7504
7505
7506
7507 031260 004767 167134
7508 031264 010100
7509 031266 006301
7510 031270 006301
7511 031272 006301
7512 031274 006301
7513 031276 050100
7514 031300 000300
7515 031302 042700 000377
7516 031306 052700 000030
7517
7518 031312 016705 150654
7519 031316 004767 175666
7520
7521 031322 012700 011304
7522 031326 004767 175626
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7528 031332 012767 000001 150670
7529 031340
      031340 104443
      031342 000404
      031344 002230
      031346 000130
      031350 013531
      031352 000001
      031354
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7534 031354
      031354 012700 000340
      031360 104441
7535 031362 005067 150632
7536 031366
      031366 104401

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      JSR      PC,GETBDR
      MOV      R1,R0
      ASL      R1
      ASL      R1
      ASL      R1
      ASL      R1
      BIS      R1,R0
      SWAB     R0
      BIC      #377,R0
      BIS      #30,R0
      MOV      ACTLNS,R5
      JSR      PC,WTWLPR
      MOV      #11304,R0
      JSR      PC,WTWLNC

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; SET UP THE DUT UARTS WITH THE PROPER LINE PARAMETERS.
; GET THE DESIRED BAUDRATE FROM THE OPERATOR.
; CALCULATE PROPER DUT LPR CONTENTS.
; SET UP THE DUT LPR REGISTERS.
; GET THE PROPER DUT LNCRTL REGISTER CONTENTS.
; SET UP THE DUT LNCRTL REGISTERS.
; GET DUPLICATE COPIES OF BAUDRATE CODE
; IN THE UPPER BYTE OF THE NEW
; LPR CONTENTS.
; SET UP 1 STOP BIT, NO PARITY, 8 BITS/CHAR
; IN THE LPR CONTENTS.
; GET THE ACTIVE LINES BIT MAP.
; SET UP THE DUT LPR REGISTERS FOR ACTIVE LINES.
; SET UP DTR, RTS, REMOTE LPBK, AND RX ENABLE.
; SET UP THE DUT LNCRTL REGS FOR ACTIVE LINES.

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```

; WAIT FOR THE OPERATOR TO TERMINATE THE TEST.
; PROMPT "TYPE <CR> TO TERMINATE THE TEST:"
; SET UP DEFAULT ANSWER TO YES.
      MOV      #1,GMANWD
      GMAIL    TERMSG,GMANWD,1,YES

```

```

      TRAP     C$GMAN
      BR       10000$
      .WORD    GMANWD
      .WORD    T$CODE
      .WORD    TERMSG
      .WORD    1
10000$:

```

```

; WE GOT A RESPONSE FROM THE OPERATOR, SO TERMINATE THE TEST.
; DISABLE ALL INTERRUPTS.
      SETPRI   #PRI07
      MOV      #PRI07,R0
      TRAP     C$SPRI
      CLR      CTRLCF
      ENDTST
; INDICATE THAT WE ARE NOT WITHIN A TEST.
L10026:
      TRAP     C$ETST

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7538 .SBTTL  HARDWARE TEST          - MODLPB -
7539 ;* *****
7540 ;* - MODEM LOOPBACK TEST -
7541 ;* THIS TEST IS USED TO MOVE DATA THROUGH A MODEM WHICH IS CONNECTED TO
7542 ;* ONE OF THE DEVICE SERIAL PORTS. THIS TEST IS RUN ONLY IF MODEM
7543 ;* LOOPBACK IS SPECIFIED. THIS TEST UTILIZES THE FOLLOWING OPERATOR
7544 ;* DIALOGUE:
7545 ;* MODEM BAUDRATE IN BPS: (D) 1200 ?
7546 ;* TYPE <CR> WHEN MODEM LINK ESTABLISHED: (L) Y ?
7547 ;* MODEM STATUS SIGNAL REPORT:
7548 ;* LINE #N: DSR=N, RI=N, DCD=N, CTS=N
7549 ;* ... REPEATED FOR EACH ACTIVE LINE
7550 ;* NUMBER OF 256 BYTE DATA PATTERNS TO SEND ON EACH SELECTED LINE
7551 ;* (1-255, 0=SEND UNTIL ^C): (D) 1 ?
7552 ;* PRINT MODEM STATUS SIGNAL REPORT AFTER EACH PATTERN: (L) Y ?
7553 ;*
7554 ;* AT THE COMPLETION OF SENDING THE SPECIFIED NUMBER OF DATA PATTERNS THE
7555 ;* TEST ISSUES THE FOLLOWING PROMPT:
7556 ;* EXIT THE TEST (N = LOOP BACK TO SEND MORE DATA): (L) Y ?
7557 ;*
7558 ;* IF EXTENDED ERROR REPORTING IS ALLOWED, A REPORT IS PRINTED AT THE END
7559 ;* OF EACH DATA PATTERN WITH THE FOLLOWING FORMAT:
7560 ;* MODEM LOOPBACK TEST STATUS REPORT: PATTERN #NNN (D) COMPLETED.
7561 ;*
7562 ;* THIS TEST IS PERFORMED USING 8 BITS PER CHARACTER, 1 STOP BIT, AND NO
7563 ;* PARITY. THIS TEST DOES NOT SUPPORT SPLIT SPEED. ALL SELECTED LINES
7564 ;* ARE TESTED AT THE SELECTED BAUDRATE. AN ERROR SUMMARY IS REPORTED AT
7565 ;* THE END OF THE TEST IF ANY LINES HAVE EXCEEDED THE NUMBER OF INDIVIDUAL
7566 ;* DATA ERRORS TO REPORT AS SELECTED IN THE SOFTWARE P-TABLE DIALOGUE.
7567 ;*
7568 ;* - - - - -
7569 ;* BGNTEST
7570 ;*
7571 ;* SETPRI #PRI05          ;ALLOW LTC INTERRUPTS.          T3::
7572 ;* MOV #PRI05,R0
7573 ;* TRAP C$SPRI
7574 ;* TNUM == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
7575 ;* MOV #TNUM,TSTNUM      ;SET UP THE TEST NUMBER.          (89)
7576 ;*
7577 ;* VERIFY THAT THE TEST SHOULD BE PERFORMED. MUST HAVE THE FOLLOWING:
7578 ;* MODEM LOOPBACK SELECTED.
7579 ;* MANUAL INTERVENTION ALLOWED.
7580 ;*
7581 ;* CMPB LOPBCK,#4        ;TEST THE LOOPBACK TYPE INDICATOR.
7582 ;* BEQ 2$               ;MODEM LOOPBACK SELECTED? YES, CONTINUE TEST.
7583 ;* EXIT TST              ;NO, ABORT THE TEST.
7584 ;*
7585 ;* TRAP C$EXIT
7586 ;* .WORD L10027-
7587 ;*
7588 ;* 2$: MANUAL            ;CHECK FOR MANUAL INTERVENTION ALLOWED.
7589 ;* BCOMPLETE 4$          ;MANUAL INTERVENTION ALLOWED? YES, DO TEST.
7590 ;* TRAP C$MANI
7591 ;* BCS 4$
7592 ;*
7593 ;* EXIT TST              ;NO, ABORT THE TEST.
7594 ;*
7595 ;* TRAP C$EXIT
7596 ;* .WORD L10027
7597 ;*
7598 ;* MOV #-1,CTRLCF        ;INDICATE THAT WE ARE IN A TEST.

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7586 031436 012767 000001 153650      MOV    #1,ERRTYP      ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
7587 031444 012767 021305 153644      MOV    #8901,ERRNBR    ;SET THE FIRST ERROR NUMBER IN ERROR TABLE.
7588 031452 012767 011211 153640      MOV    #EM8901,ERRMSG  ;SET ERROR MESSAGE ADDRESS IN ERRTABL.
7589 031460 005067 151014              CLR    ERSRWF          ;INITIALIZE THE "REPORT ERROR SUMMARY" FLAGS.
7590
7591
7592      ;*
7593      ; RESET THE DUT TO A KNOWN STATE, REMOVE THE STATUS CODES FROM THE FIFO.
7594      ; CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
7595      ; THIS SUBROUTINE REPORTS ERROR >>>> 8901 <<<<.
7596      ;*
7596 031464 004767 165740      JSR    PC,CLNRST      ;RESET THE DHU-11, REPORT ANY ERRORS FOUND.
7597 031470 103402              BCS    .+6              ;SKIP EXIT OF TEST IF NO FATAL ERROR FOUND.
7598 031472 000167 000610      JMP    60$          ;RESET FAILURE, ABORT THIS TEST.
7599
7600      ;*
7601      ; SET UP FOR TRANSMIT AND RECEIVE INTERRUPTS.
7602      ;*
7603 031476              SETPRI  #PRI07          ;DISABLE ALL INTERRUPTS.
7603 031476 012700 000340              MOV    #PRI07,R0
7603 031502 104441              TRAP    C$SPRI
7604 031504              SETVEC  TXVECA,#TXDMA,#PRI06    ;SELECT DMA TX INT SERVICE RTN.
7604 031504 012746 000300              MOV    #PRI06,-(SP)
7604 031510 012746 027520              MOV    #TXDMA,-(SP)
7604 031514 016746 150450              MOV    TXVECA,-(SP)
7604 031520 012746 000003              MOV    #3,-(SP)
7604 031524 104437              TRAP    C$SVEC
7604 031526 062706 000010              ADD    #10,SP
7605 031532              SETVEC  RXVECA,#RXCHRS,#PRI06    ;SELECT RX INT SERVICE RTN.
7605 031532 012746 000300              MOV    #PRI06,-(SP)
7605 031536 012746 027310              MOV    #RXCHRS,-(SP)
7605 031542 016746 150420              MOV    RXVECA,-(SP)
7605 031546 012746 000003              MOV    #3,-(SP)
7605 031552 104437              TRAP    C$SVEC
7605 031554 062706 000010              ADD    #10,SP
7606 031560              SETPRI  #PRI04          ;ALLOW INTERRUPTS.
7606 031560 012700 000200              MOV    #PRI04,R0
7606 031564 104441              TRAP    C$SPRI
7607
7608      ;*
7609      ; CLEAR THE CUMULATIVE ERROR COUNTERS (ONE FOR EACH LINE).
7610      ;*
7610 031566 012700 003302      MOV    #ERCNTB,R0
7611 031572 004767 165654      JSR    PC,CLR16W      ;CLEAR THE RX ERROR COUNTERS TABLE.
7612
7613      ;*
7614      ; PRINT THE THE TEST NAME.
7615      ;*
7615 031576              PRINTF  #EF0503,#EM8901
7615 031576 012746 011211              MOV    #EM8901,-(SP)
7615 031602 012746 005453              MOV    #EF0503,-(SP)
7615 031606 012746 000002              MOV    #2,-(SP)
7615 031612 010600              MOV    SP,R0
7615 031614 104417              TRAP    C$PNTF
7615 031616 062706 000006              ADD    #6,SP
7616
7617      ;*
7618      ; PREPARE TO CALL THE SET UP ROUTINE.
7619      ; GET THE DESIRED BAUDRATE FROM THE OPERATOR.
7620      ; CALCULATE PROPER DUT LPR CONTENTS.
7620      ; CALCULATE THE PROPER RX TIME-OUT VALUE FOR THIS SPEED.

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7621      ; SET UP THE BIT MAP OF UNUSED TX/RX BITS.
7622      ; -
7623 031622 004767 166572      JSR    PC,GETBDR
7624 031626 010100      MOV    R1,R0
7625 031630 006301      ASL    R1
7626 031632 006301      ASL    R1
7627 031634 006301      ASL    R1      ;GET DUPLICATE COPIES OF BAUDRATE CODE
7628 031636 006301      ASL    R1      ; IN THE UPPER BYTE OF THE NEW
7629 031640 050001      BIS    R0,R1      ; LPR CONTENTS.
7630 031642 000301      SWAB   R1
7631 031644 042701 000377      BIC    #377,R1      ;SET UP 1 STOP BIT, NO PARITY, 8 BITS/CHAR
7632 031650 052701 000030      BIS    #30,R1      ; IN THE LPR CONTENTS.
7633
7634 031654 004767 166750      JSR    PC,GETTIM      ;GET TIME-OUT BASED ON MINIMUM BAUDRATE IN USE.
7635 031660 012767 177400 150344      MOV    #177400,IBM      ;FORM BIT MAP OF UNUSED TX/RX BITS.
7636
7637      ; SET UP A 256 BYTE DATA PATTERN.
7638      ; -
7639 031666 005003      CLR    R3      ;PREPARE TO START DATA PATTERN AT 255.
7640 031670 012702 003602      MOV    #BUFBA5,R2      ;GET THE BASE OF THE DATA PATTERN BUFFER.
7641 031674 010204      MOV    R2,R4
7642 031676 105303      64:      DECB   R3      ;GET THE NEXT BYTE OF THE DATA PATTERN.
7643 031700 110324      MOV    R3,(R4)+      ;WRITE A BYTE OF THE DATA PATTERN.
7644 031702 105703      TSTB   R3      ;CHECK FOR DONE WRITING DATA PATTERN.
7645 031704 001374      BNE    64      ;DATA PATTERN DONE? NO, LOOP TO DO NEXT BYTE.
7646      ;YES, WRITE 32 BYTE OVERFLOW REGION.
7647 031706 010205      MOV    R2,R5      ;PREPARE SOURCE POINTER.
7648 031710 012700 000020      MOV    #16,,R0      ;PREPARE LOOP COUNTER.
7649 031714 012524      84:      MOV    (R5)+,(R4)+      ;WRITE 2 BYTES OF THE OVERFLOW PATTERN.
7650 031716 005300      DEC    R0      ;COUNT THESE 2 BYTES.
7651 031720 001375      BNE    84      ;16 WORDS WRITTEN? NO, LOOP TO WRITE ANOTHER.
7652      ;YES, COMPLETE DATA PATTERN IS DONE.
7653 031722 012703 000400      MOV    #256,,R3      ;SET DATA PATTERN LENGTH TO 256.
7654
7655      ; SET THE DUT RTS AND DTR BITS FOR THE ACTIVE LINES.
7656      ; -
7657 031726 012700 011000      MOV    #11000,R0      ;SPECIFY TO SET RTS AND DTR.
7658 031732 016705 150234      MOV    ACTLNS,R5      ;SPECIFY ACTIVE LINES.
7659 031736 004767 175216      JSR    PC,WTMLNC      ;SET DUT RTS AND DTR ON ALL ACTIVE LINES.
7660
7661      ; WAIT FOR THE OPERATOR TO ESTABLISH THE MODEM CONNECTION.
7662      ; PROMPT "TYPE <CR> WHEN MODEM LINK ESTABLISHED:"
7663      ; -
7664 031742 012767 000001 150260      MOV    #1,GMANWD      ;SET UP DEFAULT ANSWER TO YES.
7665 031750      GMANIL    EMLMSG,GMANWD,1,YES
7666      ;
7667      ; TRAP C$GMAN
7668      ; BR 10000$
7669      ; .WORD GMANWD
7670 031764 004767 167326      ; .WORD T$CODE
7671      ; .WORD EMLMSG
7672      ; .WORD 1
7673      ; 10000$:
7674
7675      ; REPORT THE STATE OF THE MODEM STATUS SIGNALS.
7676      ; SET DEFAULT OF PRINTING MODEM STATUS AFTER EVERY DATA PATTERN.
7677      ; -
7678      JSR    PC,MSSRPT

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7671 031770 012767 000001 150246      MOV    #1,PMSFLG
7672
7673      ;*
7674      ; ASK OPERATOR FOR THE NUMBER OF DATA PATTERNS TO SEND.
7675      ; PROMPT: "NUMBER OF 256 BYTE DATA PATTERNS TO SEND ON EACH SELECTED LINE
7676      ;          (1-255, 0=SEND UNTIL ^C): (D) 1 ?"
7677 031776 012767 000001 150224 10$:      MOV    #1,GMANWD      ;SET DEFAULT NUMBER OF PATTERNS TO 1.
7678 032004      GMANID  NDPMSG,GMANWD,D,377,0,255,YES
      032004 104443
      032006 000406
      032010 002230
      032012 000052
      032014 013316
      032016 000377
      032020 000000
      032022 000255
      032024
      TRAP    C$GMAN
      BR      10001$
      .WORD   GMANWD
      .WORD   T$CODE
      .WORD   NDPMSG
      .WORD   377
      .WORD   T$LOLIM
      .WORD   T$HILIM
      10001$:
7679 032024 016704 150200      MOV    GMANWD,R4
7680 032030 005005      CLR    R5      ;CLEAR THE DATA PATTERN COUNTER.
7681 032032 005067 150166      CLR    FERROR    ;CLEAR THE "AT LEAST ONE ERROR" FLAG
7682
7683      ;*
7684      ; ASK IF MODEM STATUS SIGNALS SHOULD BE REPORTED AFTER EACH DATA PATTERN.
7685      ; PROMPT: "PRINT MODEM STATUS SIGNAL REPORT AFTER EACH PATTERN: (L) Y ?"
7686      ; USE LAST RESPONSE AS DEFAULT (DEFAULT OF YES THE FIRST TIME).
7687      ;-
      GMANIL  PMSMSG,PMSFLG,1,YES
      TRAP    C$GMAN
      BR      10002$
      .WORD   PMSFLG
      .WORD   T$CODE
      .WORD   PMSMSG
      .WORD   1
      10002$:
7688
7689
7690      ;*
7691      ; SET UP THE DUT AND TX/RX VARIABLES.
7692      ; R1 - TX, RX LPR CONTENTS.
7693      ; R2 - START ADDRESS OF DATA PATTERN TO TX/RX.
7694      ; R3 - LENGTH OF DATA PATTERN.
7695      ; SEND THE DATA.
7696 032052 005205 12$:      INC    R5      ;COUNT THIS DATA PATTERN.
7697 032054 004767 166746      JSR    PC,MODSUP    ;SET UP THE DUT AND TX/RX VARIABLES.
7698
7699 032060 004767 170656      JSR    PC,PUFIFO    ;PURGE THE DUT RECEIVE CHARACTER FIFO.
7700 032064 103110      BCC     60$      ;ABORT THIS TEST IF FIFO WOULD NOT PURGE.
7701
7702 032066 004767 171134      JSR    PC,PURRXB    ;PURGE THE RX CHAR BUFFER IN MEMORY.
7703 032072 004767 166610      JSR    PC,INIDMA    ;SEND THE FIRST BATCH OF DATA PATTERNS.
7704 032076 012767 021306 153212      MOV    #8902.,ERRNBR ;SET ERROR NUMBER TO 8905.
7705
7706      ;*
7707      ; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 8902 THRU 8907 <<<<<.
7708 032104 004767 171152      JSR    PC,RDCHRS    ;READ AND VERIFY THE RX CHARACTERS.
7709
7710 032110 005767 150110      TST    FERROR
7711 032114 001404      BEQ    13$      ;HAS AN ERROR BEEN DETECTED ?
                        ;BRANCH IF IT HASN'T.

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7712 032116 032767 000100 150036      BIT    #BIT06,OPTION    ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
7713 032124 001430                      BEQ     16$              ;BRANCH TO THE "EXIT TEST ?" QUESTION IF NOT.
7714
7715 032126 012767 021314 153162 13$:  MOV     #8908.,ERRNBR    ;SET ERROR NUMBER TO 8908.
7716
7717                      ;*
7718                      ; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 8908 THRU 8911 <<<<<.
7719 032134 004767 174200              JSR     PC,TXRREP        ;REPORT FINAL ERRORS FROM RX/RX.
7720
7721                      ;*
7722                      ; REPORT END OF DATA PATTERN IF ALLOWED.
7723                      ; "MODEM LOOPBACK TEST STATUS REPORT: PATTERN #NNN (D) COMPLETED."
7724                      ; REPORT THE MODEM STATUS SIGNAL STATES IF REQUESTED.
7725 032140                      ;*
032140 010546                      PRINTX  #EDPFMT,R5
032142 012746 007733                      MOV     R5,-(SP)
032146 012746 000002                      MOV     #EDPFMT,-(SP)
032152 010600                      MOV     #2,-(SP)
032154 104415                      MOV     SP,R0
032156 062706 000006                      TRAP    C$PNTX
7726 032162 005767 150056                      ADD     #6,SP
7727 032166 001402                      TST     PMSFLG        ;CHECK THE "PRINT MODEM STATUS" FLAG.
7728 032170 004767 167122                      BEQ     14$              ;PRINT MODEM STATUS? NO, SKIP PRINTING.
7729                      JSR     PC,MSSRPT        ;REPORT THE MODEM STATUS.
7730
7731                      ;*
7732                      ; IF THERE ARE MORE DATA PATTERNS TO SEND, LOOP BACK TO SEND AGAIN.
7733 032174 005304 14$: DEC     R4              ;COUNT THIS DATA PATTERN.
7734 032176 001403                      BEQ     16$              ;LAST DATA PAT SENT? YES, PROMPT FOR EXIT.
7735 032200 100324                      BPL     12$              ;NO, CONTINUOUS SENDING? NO, SEND NEXT PAT.
7736 032202 005204                      INC     R4              ;YES, RESTORE PATTERN COUNTER.
7737 032204 000722                      BR      12$              ;GO TO SEND NEXT DATA PATTERN.
7738
7739                      ;*
7740                      ; PROMPT FOR EXIT OF THE TEST OR SENDING OF MORE DATA PATTERNS.
7741                      ; PROMPT: "EXIT THE TEST (N = LOOP BACK TO SEND MORE DATA): (L) Y ?"
7742 032206 012767 000001 150014 16$: MOV     #1,GMANWD        ;SET DEFAULT ANSWER TO YES.
032214 104443                      GMANIL  EXTMSG,GMANWD,1,YES
032216 000404                      TRAP    C$GMAN
032220 002230                      BR      10003$
032222 000130                      .WORD   GMANWD
032224 013235                      .WORD   T$CODE
032226 000001                      .WORD   EXTMSG
032230                      .WORD   1
7743 032230 026727 147774 000001                      CMP     GMANWD,#1        ;CHECK OPERATOR RESPONSE.
7744 032236 001257                      BNE     10$              ;EXIT RESPONSE? NO, LOOP TO SEND MORE DATA.
7745
7746                      ;NO, EXIT ROUTINE.
7747
7748                      ;*
7749                      ; ALL DONE. HAVE BEEN TOLD TO EXIT.
7750                      ; CLEAR DEVICE DTR AND RTS SIGNALS.
7751                      ; DISABLE INTERRUPTS.
7752                      ; CLEAR THE INTERRUPT VECTORS.
7753                      ; REPORT ANY NECESSARY ERROR SUMMARIES.
7754 032240 005000                      ;*
7755 032242 012705 177777                      CLR     R0              ;INDICATE TO CLEAR ALL LNCTRL BITS.
032246 004767 174706                      MOV     #MAPLNS,R5      ;INDICATE TO CLEAR FOR ALL LINES.
7755                      JSR     PC,WTWLNLC        ;CLEAR ALL THE RTS AND DTR SIGNALS

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7756
7757 032252          SETPRI  #PRI07          ;DISABLE ALL INTERRUPTS.
      032252 012700 000340
      032256 104441
7758 032260          CLRVEC  TXVECA          ;RETURN TX INT VECTOR TO UNUSED POOL.
      032260 016700 147704
      032264 104436
7759 032266          CLRVEC  RXVECA          ;RETURN RX INT VECTOR TO UNUSED POOL.
      032266 016700 147674
      032272 104436
7760
7761 032274 012767 021320 153014          MOV  #8912.,ERRNBR ;SELECT NUMBER 8912 FOR THE NEXT ERROR REPORT.
7762 032302 004767 171614          JSR   PC,REPSMR ;REPORT ERROR SUMMARIES IF CALLED FOR.
7763 032306          SETPRI  #PRI07          ;DISABLE ALL INTERRUPTS.
      032306 012700 000340
      032312 104441
7764 032314 005067 147700          CLR   CTRLCF          ;INDICATE THAT WE ARE NOT WITHIN A TEST.
7765 032320          ENDTST
      032320
      032320 104401

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L10027: TRAP C#ETST


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7767 .SBTTL  HARDWARE TEST - DMAADR -
7768 ;* *****
7769 ;* - DMA ADDRESSING TEST -
7770 ;* THIS TEST VERIFIES , AS FAR AS POSSIBLE , THAT THE DUT CAN PERFORM A
7771 ;* DMA FROM A FULL 18 BIT OR 16 BIT ADDRESS. THE TEST RELIES ON FINDING A
7772 ;* COMPLEMENTARY PAIR OF ADDRESSES BETWEEN THE TOP OF PHYSICAL MEMORY AND
7773 ;* THE START OF THE TOP OF THE DIAGNOSTIC PROGRAM .
7774 ;* THIS MAY INVOLVE REMOVING PART OF THE DIAGNOSTIC RUNTIME SERVICES AND
7775 ;* THEN RESTORING. THE NUMBER OF BITS THAT HAVE BEEN SUCCESSFULLY TESTED
7776 ;* WILL BE PRINTED AT THE CONSOLE AT THE END OF THE TEST, IF REQUESTED.
7777 ;*
7778 ;*
7779 ;* *****
7780 032322 BGNTST
7781 032322
7782 032322 012700 000240
7783 032326 104441
7784 000004
7785 032330 012767 000004 147722 TNUM -- TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER
7786 032336 012767 177777 147654 MOV #TNUM,TSTNUM ;SET UP THE TEST NUMBER
7787 032344 012767 000001 152742 MOV #-1,CTRLCF ;INDICATE THAT WE ARE IN A TEST
7788 032352 012767 010461 152736 MOV #1,ERRTYP ;SET ERROR TYPE AS FATAL IN ERROR TABLE
7789 032360 012767 010166 152732 MOV #4401,ERRNBR ;SET ERROR NUMBER TO 4401
7790 032366 012767 014124 152726 MOV #EM4401,ERRMSG ;SET ERROR MESSAGE ADDRESS IN TABLE
7791 MOV #ER0503,ERRBLK ;SELECT THE CORRECT ERROR REPORTING ROUTINE
7792
7793 ;*
7794 ;* CLEAR THE SUCCESS FLAG TO INDICATE TEST FAILURE IN CASE IT DOES
7795 ;*
7796 032374 005067 001374 CLR SUCCS ;INDICATE FAILURE , IN CASE THE DUT FAILS
7797
7798 ;*
7799 ;* RESET THE DUT TO A KNOWN STATE,REMOVE THE STATUS CODES FROM THE FIFO.
7800 ;* CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR
7801 ;*
7802
7803 032400 004767 165024 JSR PC,CLNRST ;RESET THE DHU , REPORT ANY ERRORS
7804 032404 103402 BCS .+6 ;SKIP EXIT OF TEST IF NO FATAL ERROR FOUND.
7805 032406 000167 001324 JMP 60$ ;EXIT THE TEST, FATAL ERROR WAS FOUND.
7806
7807 ;*
7808 ;* SET UP THE 004 TRAP VECTOR TO POINT TO OUR TRAP SERVICE ROUTINE.
7809 ;*
7810 032412 016767 145366 147636 MOV 4,TP4VEC ;SAVE THE EXISTING 004 TRAP VECTOR
7811 032420 012767 027476 145356 MOV #TP4RTN,4 ;POINT THE VECTOR AT OUR SERVICE ROUTINE.
7812
7813 ;*
7814 ;* DETERMINE WHETHER MEMORY MANAGEMENT IS PRESENT
7815 ;*
7816
7817 032426 005767 147666 TST MMPRES ;IF MEM MGT IS PRESENT THEN
7818 032432 001007 BNE 1$ ;AVOID SETTING THE DMA TEST ADDR FOR
7819 ;* A 16 BIT MACHINE.
7820 032434 012767 001252 147560 MOV #1252,DMTSTA ;SET UP THE FIRST DMA TEST ADDR FOR

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Address	Op-Code	Source	Target	Comment
7821				; A 16 BIT MACHINE
7822	032442	012767	000021 001316	MOV #17.,BITSTD ;SET THE BITS TESTED TO 16 + 1
7823	032450	000442		BR 10\$;SINCE MEM MGT ISN'T PRESENT
7824				;THERE'S NO NEED TO SET UP THE MEM MGT RLGS.
7825				;
7826				; SET UP THE HIGHEST POSSIBLE TEST ADDRESS IN DMTSTA
7827				;
7828	032452	012767	005252 147542	1\$: MOV #5252,DMTSTA ;SET UP THE FIRST DMA TEST ADDRESS FOR THE
7829				; 18 BIT MACHINE.
7830	032460	012767	000023 001300	MOV #19.,BITSTD ;SET THE BITS TESTED TO 18 BITS + 1.
7831				;
7832				; SET UP THE PARS 0 THROUGH 6 TO RELOCATE TO THE SAME ADDRESS
7833				;
7834	032466	005000		CLR R0 ;SET THE PAGE BASE ADDRESS TO ZERO
7835	032470	012701	002324	MOV #PARATB,R1 ;POINT AT THE START OF THE PAR ADDRESS TABLE
7836				
7837	032474	010031		2\$: MOV R0,B(R1)+ ;LOAD THE PAR
7838	032476	062700	000200	ADD #200,R0 ;CALCULATE THE NEXT PAGE ADDRESS
7839	032502	022701	002342	CMP #PAR7A,R1 ;LOOP UNTIL PARS 0 THROUGH 6
7840	032506	001372		BNE 2\$;ARE LOADED.
7841				
7842				;
7843				; SET UP THE INPUT/OUTPUT PAGE PAR TO THE TOP 4KW OF PHYSICAL MEMORY
7844				;
7845				
7846	032510	012777	177600 147624	MOV #177600,#PAR7A ;SET PAR #7 TO POINT AT THE TOP 4KW
7847				
7848				;
7849				; SET UP THE PDRS FOR , NO ABORT/TRAP,UPWARD EXPANSION,128 BLOCKS PER PAGE
7850				;
7851				
7852	032516	012700	077406	MOV #77406,R0 ;BIT PATTERN FOR THE PDRS
7853	032522	012701	002344	MOV #PDRATB,R1 ;POINT AT START OF PDR ADDR TABLE
7854	032526	010031		4\$: MOV R0,B(R1)+ ;
7855	032530	022701	002364	CMP #PDRATE,R1 ;LOOP UNTIL ALL PDRS HAVE
7856	032534	001374		BNE 4\$;BEEN SET UP.
7857				
7858				;
7859				; SET THE MEM MGT STATUS REG #3 FOR, 18 BIT ADDRESSING,
7860				; NO UNIBUS MAPPING, NO D SPACE, IN CASE THIS REG EXISTS.
7861				; NOT ALL UNIBUS MACHINES HAVE MEM MGT STATUS REG #3, SO THE CKTRAP ROUTINE
7862				; MUST BE USED TO CATCH AN 004 TRAPS THAT OCCUR IF THE REG DOES NOT EXIST.
7863				;
7864	032536	005067	001220	CLR 70\$;SET UP THE SOURCE OPERAND
7865	032542	012700	033762	MOV #70\$,R0 ;SET UP THE SOURCE ADDR FOR THE MOVE.
7866	032546	016701	147544	MOV MMSR3,R1 ;SET UP THE DESTINATION ADDR FOR THE MOVE.
7867	032552	004767	164572	JSR PC,CKTRAP ;PERFORM THE MOVE AND CATCH ANY 004 TRAPS.
7868				
7869				;
7870				; TRY AND FIND A COMPLEMENTARY PAIR OF ADDRESSES WITHIN THE MEMORY AND SAVE
7871				; THE CONTENTS OF THE TWO AREAS. THE TEST IS ABANDONED IF A COMPLEMENTARY
7872				; PAIR HAS NOT BEEN FOUND BEFORE THE AREA OF MEMORY CONTAINING THE
7873				; DIAGNOSTIC IS ENCOUNTERED.
7874				;
7875	032556	012767	027454 145220	10\$: MOV #TP48RT,4 ;CHANGE THE 004 TRAP VECTOR TO POINT TO
7876				;TP48RT SINCE THIS IS THE ROUTINE ASSOCIATED
7877				;WITH THE BYTE SUBROUTINE CKTRPB.

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7878
7879 032564          MEMORY FFREM          ;GET THE ADDRESS OF THE FIRST FREE WORD
      032564 104431          TRAP          C$MEM
      032566 010067 147434          MOV          RO,FFREM
7880
7881
7882 032572 012701 003602          MOV          #8UFBAS,R1          ;POINT AT THE BUFFER WHERE THE CONTENTS OF
7883
7884 032576 005004          CLR          R4          ;THE MEMORY BEING READ ARE TO BE SAVED.
7885
7886
7887 032600 005204          12$: INC          R4          ;INCREMENT THE CPI
7888 032602 005005          CLR          R5          ;INDICATE THAT A SAVE OF THE DATA AT
7889
7890 032604 012703 000020          MOV          #16.,R3          ;(DMTSTA) IS REQUIRED
7891 032610 004767 165046          JSR          PC,DMRW          ;SET THE NUMBER OF BYTES TO BE READ
7892 032614 012701 004202          MOV          #8UFMID,R1          ;SAVE THE DATA CONTAINED AT ADDRESS DMTSTA.
7893 032620 005767 147430          TST          TP4FLG          ;POINT AT SECOND STORAGE AREA
7894 032624 001403          BEQ          14$          ;IF WE HAVE VALID MEMORY THEN AVOID CLEARING
7895
7896 032626 005004          CLR          R4          ;THE CPI AND RESETTING THE SAVE AREA ADDR
7897 032630 012701 003602          MOV          #8UFBAS,R1          ;CLEAR THE CPI.
7898 032634 022704 000002          14$: CMP          #2,R4          ;RESET THE ADDR FOR THE SAVED DATA STORE
7899
7900 032640 001447          BEQ          17$          ;IF A PAIR OF COMPLEMENTARY ADDRESSES HAVE
7901 032642 016767 147354 001122          MOV          DMTSTA,ODTSTA          ;BEEN FOUND THEN
7902 032650 000241          CLC          ;GO AND WRITE THE TEST DATA TO THESE ADDRS.
7903 032652 006067 147344          ROR          DMTSTA          ;SAVE THE OLD DMTSTA
7904
7905 032656 005367 001104          DEC          BITSTD          ;CLEAR CARRY READY FOR THE ROTATION
7906
7907
7908
7909
7910
7911 032662 032767 176000 147332          BIT          #176000,DMTSTA          ;COMPLEMENT THE DMTSTA TO PRODUCE THE NEXT
7912
7913 032670 001343          BNE          12$          ;DMA TEST ADDR.
7914 032672 004767 164712          JSR          PC,DM16B          ;DECREMENT THE NUMBER OF BITS TESTED COUNT
7915 032676 020067 147324          CMP          RO,FFREM
7916 032702 103336          BHS          12$
7917
7918
7919
7920
7921
7922
7923
7924
7925 032704 022767 000252 147310          CMP          #252,DMTSTA          ;CHECK THAT THE NEW DMTSTA IS NOT INSIDE THE DIAGNOSTIC PROGRAM
7926 032712 001014          BNE          15$
7927
7928 032714 012767 000652 147300          MOV          #652,DMTSTA          ;IS THE DMTSTA > 1252 . IF IT IS THEN WE'RE
7929 032722 062700 040000          ADD          #40000,RO          ;SAFE SO.
7930 032726 020067 147274          CMP          RO,FFREM          ;BRANCH AND CONTINUE WITH THE SEARCH
7931 032732 103404          BLO          15$          ;CONVERT THE DMTSTA TO A PHYSICAL ADDR.
7932 032734 012767 000016 001024          MOV          #14.,BITSTD          ;ARE WE INSIDE THE DIAGNOSTIC REGION ?
                                     ;NO , THEN BRANCH AND CONTINUE WITH THE SEARCH
                                     ;+
                                     ;SINCE WE ARE NOW INSIDE THE DIAGNOSTIC, WE INCREMENT BIT #14 OF THE DMTSTA
                                     ;PHYSICAL ADDRESS AND IF WE'RE STILL INSIDE THE DIAGNOSTIC WE ABANDON THE
                                     ;TEST. ONCE WE ARE IN THIS REGION WE ARE ONLY ABLE TO TEST THE LOWEST 14 BITS.
                                     ;-
                                     ;+
                                     ;IF THE BIT HAS ALREADY BEEN SET THEN
                                     ;ABANDON THE TEST,AFTER REPORTING THE ERROR ,
                                     ;BECAUSE NO SUITABLE MEMORY HAS BEEN FOUND.
                                     ;SET THE BIT
                                     ;ADD THE BIT INTO THE PHYSICAL ADDR
                                     ;IF WE'RE NOW STILL INSIDE THE DIAGNOSTIC THEN
                                     ;REPORT ERROR AND ABANDON THE TEST.
                                     ;OTHERWISE SET THE BITS TESTED TO 14 BITS.

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7933 032742 000716          BR      12$          ;CONTINUE WITH THE SEARCH.
7934
7935
7936 032744 005267 152346    15$:   INC      ERRNBR          ;SET THE ERROR NUMBER TO 4402
7937 032750 012701 010216    MOV      #EM4402,R1          ;SELECT MESSAGE TO BE REPORTED.
7938                                     ; " NO SUITABLE ADDR FOUND. DMA TEST ABORTED "
7939
7940
7941 032754 000167 000754    16$:   JMP      34$          ;JUMP TO THE ERROR.
7942
7943
7944                                     ;+
7945                                     ; WRITE THE TEST DATA INTO THE TWO AREAS JUST FOUND. IF A TRAP OCCURS WHILE
7946                                     ; WE ARE WRITING DATA INTO THESE AREAS THEN THE HOST MACHINE IS AT FAULT.
7947                                     ; -
7948
7949 032760 012700 005130    17$:   MOV      #SDPBAS,R0          ;SET UP THE SOURCE ADDR FOR THE MOVE AS OUR
7950                                     ;TEST DATA PATTERN.
7951 032764 016767 147232 000776    MOV      DMTSTA,DUMY          ;SAVE THE LOWER DMTSTA
7952 032772 016767 000774 147222    MOV      ODTSTA,DMTSTA          ;START WITH THE HIGHER OF THE TWO
7953                                     ; COMPLEMENTARY ADDRESSES.
7954 033000 012703 000020    MOV      #16.,R3          ;SET THE NUMBER OF DATA BYTES TO BE WRITTEN
7955 033004 012705 000001    MOV      #1,P5          ;INDICATE TO WRITE TO DMTSTA
7956
7957
7958 033010 012701 000340    MOV      #340,R1          ;SET PRIORITY 7 TO DISABLE THE CLOCK
7959 033014 004767 172256    JSR      PC,STPSW          ;
7960
7961 033020 005267 152272    INC      ERRNBR          ;SET THE ERROR NUMBER TO 4403
7962 033024 012701 010264    MOV      #EM4403,R1          ;SELECT THE MESSAGE.
7963                                     ; "HOST FAILURE. WRITE FAILED TO AN ADDR WHICH
7964                                     ;HAD BEEN SUCCESSFULLY READ, TEST ABANDONED "
7965
7966 033030 004767 164626    JSR      PC,DMRW          ;PERFORM THE TRANSFER
7967 033034 005767 147214    TST      TP4FLG          ;EXIT IF HOST FAILURE
7968 033040 001345          BNE      16$          ;AND REPORT ERROR.
7969 033042 016767 000722 147152    MOV      DUMY,DMTSTA          ;SELECT THE LOWER DMA TEST ADDR.
7970 033050 012700 005154    MOV      #SDP2B,R0          ;SELECT THE NEXT DATA PATTERN
7971 033054 004767 164602    JSR      PC,DMRW          ;PERFORM THE TRANSFER
7972 033060 005767 147170    TST      TP4FLG          ;EXIT IF HOST FAILURE
7973 033064 001333          BNE      16$          ;
7974
7975                                     ;+
7976                                     ; SET UP THE DHU TO PERFORM THE DMA.
7977                                     ; -
7978
7979                                     ;+
7980                                     ; SET INTERNAL LOOPBACK, ENABLE THE RECIEVER FUNCTION ON THE LINE.
7981                                     ; SET THE LPR ON THE LINE TO 38.4K BAUD, 8 BITS PER CHARACTER, ODD PARITY,
7982                                     ; 2 STOP BITS. ENABLE THE TRANSMITTER ON THE LINE.
7983                                     ; -
7984
7985 033066 005267 152224    INC      ERRNBR          ;SET THE ERRNBR TO 4404
7986
7987
7988 033072 004767 164776    JSR      PC,FINACT          ;FIRST FIND AN ACTIVE LINE ON WHICH TO PERFORM
7989                                     ; THE DMA.

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7990	033076	010102			MOV	R1,R2	;SAVE THE LINE NUMBER ON WHICH THE DMA WILL OCCUR
7991	033100	012701	010361		MOV	#EM4404,R1	;SELECT THE MESSAGE,
7992							; "NO ACTIVE LINES , TEST ABANDONED"
7993	033104	103402			BCS	,+6	;EXIT IF A LINE COULD NOT BE FOUND ,AFTER FIRST
7994	033106	000167	000424		JMP	301	;RESTORING THE CONTENTS OF MEMORY.
7995	033112	010201			MOV	R2,R1	;RESTORE THE ACTIVE LINE NUMBER.
7996							
7997							
7998							
7999							
8000	033114	012700	000204		MOV	#204,R0	;PASS THE LNCRTL CONTENTS
8001	033120	004767	174034		JSR	PC,WTWLC	;INITIALISE THE LNCRTL REGISTER
8002	033124	012700	177670		MOV	#177670,R0	;PASS THE LPR CONTENTS
8003	033130	004767	174054		JSR	PC,WTWLP	;INITIALISE THE LPR REGISTER
8004	033134	004767	172362		JSR	PC,TXENBL	;ENABLE TRANSMITTER ON THE LINE
8005							
8006							
8007							
8008							
8009							
8010	033140	016 05	000626		MOV	ODTSTA,R5	;START FROM THE HIGHER OF THE PAIR OF ADDR.
8011	033144	012704	005130		MOV	#SDPBAS,R4	;SET UP THE ADDR OF THE DATA PATTERN
8012	033150	010167	000610		MOV	R1,801	;SAVE THE LINE NUMBER FOR THE DMA
8013	033154	012767	000002	000600	MOV	#2,701	;INITIALISE THE LOOP COUNT
8014							
8015	033162	012700	000052		MOV	#52,R0	;SET UP THE LSB'S
8016	033166	005003		181:	CLR	R3	;CLEAR THE REG THAT WILL HOLD THE 6 MSB'S
8017	033170	012702	000006		MOV	#6,R2	;CONVERT THE DMTSTA INTO
8018	033174	006305		201:	ASL	R5	;A PHYSICAL ADDRESS WITH
8019	033176	006103			ROL	R3	;THE MSB'S IN REG #3.
8020	033200	005302			DEC	R2	
8021	033202	001374			BNE	201	
8022	033204	032705	000100		BIT	#100,R5	;TEST BIT #6 OF THE DMTSTA
8023	033210	001402			BEQ	221	
8024	033212	012700	000025		MOV	#25,R0	;ALTER THE LSB'S IF BIT #6 WAS SET.
8025	033216	060005		221:	ADD	R0,R5	;ADD IN THE LSB'S
8026	033220	052703	000200		BIS	#200,R3	;SET BIT #7.
8027							
8028	033224	016777	000534	146746	MOV	801,8CSRA	;SELECT THE LINE ON WHICH TO PERFORM THE DMA.
8029							
8030	033232	012767	010465	152056	MOV	#4405,ERRNBR	;SET ERROR NUMBER 4405
8031	033240	012701	010420		MOV	#EM4405,R1	;SELECT THE MESSAGE,
8032							; "DMA_START BIT FOUND SET BEFORE DMA INIT.
8033							;TEST ABANDONED"
8034	033244	105777	146744		TSTB	#TXAD2A	;TEST THE OUT DMA-START BIT
8035	033250	100532			BMI	301	;EXIT WITH ERROR IF SET ,AFTER FIRST RESTORING
8036							;THE CONTENTS OF MEMORY.
8037	033252	012777	000020	146736	MOV	#16,8TXBFCA	;SET UP CHARACTER COUNT
8038	033260	010577	146726		MOV	R5,8TXAD1A	;SET UP BITS 0 TO 15 OF THE PHYSICAL ADDR.
8039	033264	110377	146724		MOVB	R3,8TXAD2A	;SET UP BITS 16 TP 21 , AND INITIATE THE DMA.
8040							
8041							
8042							
8043							
8044							
8045	033270	012701	170144		MOV	#170144,R1	;TEST BIT 15, TIME-OUT OF 100 MS.
8046	033274	016702	146700		MOV	CSRA,R2	;PASS THE ADDR OF THE REG TO TEST.

```

8047
8048 033300 005267 152012          INC      ERRNBR          ;SET ERROR NUMBER TO 4406
8049
8050 033304 004767 173574          JSR      PC,WAIBIS        ;WAIT FOR BIT TO SET
8051 033310 012701 010514          MOV      @EM4406,R1      ;SELECT THE MESSAGE
8052
8053                                ; " TIME-OUT OCCURED WAITING FOR DMA TO
8054 033314 103110          BCC      30$          ;COMPLETE. TEST ABANDONED"
8055                                ;EXIT IF TIME-OUT OCCURED, AFTER FIRST
8056 033316 010402          MOV      R4,R2          ;RESTORING THE CONTENTS OF MEMORY.
8057 033320 012704 000005          MOV      @5,R4          ;SAVE R4
8058 033324 004767 164220          JSR      PC,DELAY        ;SET 5 MS DELAY
8059 033330 010204          MOV      R2,R4          ;DELAY TO ALLOW LAST CHARACTER TO BE RECIEVED
8060                                ;RESTORE R4
8061
8062                                ;*
8063                                ; READ THE CONTENTS OF THE RXFIFO AND COMPARE THEM WITH THE CORRECT DATA
8064                                ;*
8065 033332 005003          CLR      R3          ;CLEAR THE READ DATA COUNTER
8066 033334 012705 000200          MOV      @128.,R5      ;SET THE MAX BMP CODE READ COUNT
8067
8068 033340 012767 010467 151750 24$: MOV      @4407.,ERRNBR      ;SET THE ERRNBR TO 4407
8069 033346 012701 010570          MOV      @EM4407,R1      ;SELECT THE MESSAGE
8070                                ; " RXFIFO EMPTY TOO SOON, DMA FAILED
8071                                ;TEST ABANDONED"
8072
8073 033352 017702 146624          MOV      @RBUFA,R2      ;READ THE CHARACTER FROM THE FIFO
8074 033356 100067          BPL      30$          ;BRANCH TO REPORT ERROR IF FIFO EMPTY TOO SOON,
8075                                ;AFTER FIRST RESTORING THE CONTENTS OF MEMORY.
8076 033360 012700 170301          MOV      @170301,R0      ;SET UP BIT MASK OF A BMP CODE
8077 033364 040200          BTC      R2,R0          ;TRY TO CLEAR THE BMP CODE MASK
8078 033366 001011          BNC      28$          ;BRANCH IF NOT A BMP CODE
8079 033370 004767 171256          JSR      PC,SAVBMP      ;SAVE THE BMP CODE ON THE QUEUE
8080
8081 033374 005267 151716          INC      ERRNBR          ;SET THE ERRNBR TO 4408
8082 033400 012701 010652          MOV      @EM4408,R1      ;SELECT THE MESSAGE
8083                                ; " TOO MANY BMP CODES FOUND IN THE RXFIFO.
8084                                ;TEST ABANDONED"
8085
8086 033404 005305          DEC      R5          ;DEC THE MAX BMP CODE READ COUNT
8087 033406 001453          BEQ      30$          ;GO REPORT ERROR IF TOO MANY BMP CODES FOUND
8088                                ;AFTER FIRST RESTORING THE CONTENTS OF MEMORY.
8089 033410 000753          BR       24$          ;DON'T COUNT THE BMP CODE AS A VALID CHARACTER
8090
8091
8092 033412 012767 010471 151676 28$: MOV      @4409.,ERRNBR      ;SET THE ERRNBR TO 4409
8093 033420 010201          MOV      R2,R1          ;SAVE THE CHARACTER FROM THE FIFO
8094 033422 012702 010715          MOV      @EM4409,R2      ;SELECT THE MESSAGE
8095                                ; " BAD BIT BETWEEN BITS 0 AND "
8096 033426 012767 015536 151666          MOV      @ER9101,ERRBLK      ;SELECT THE ERROR ROUTINE.
8097 033434 012767 177777 000332          MOV      @-1,SUCSS      ;INDICATE 'BAD BITS' FAILURE
8098
8099 033442 122401          CMPB     (R4)+,R1      ;COMPARE CHAR FROM FIFO WITH THE CORRECT DATA.
8100 033444 001034          BNE      30$          ;BRANCH IF INCORRECT AND RESTORE MEM CONT'S.
8101 033446 005067 000322          CLR      SUCSS          ;INDICATE NON TEST SPECIFIC FAILURE E.G. TIME-OUTS
8102 033452 005203          INC      R3          ;COUNT THIS CHARACTER.
8103 033454 022703 000020          CMP      @16.,R3      ;HAVE WE RECIEVED ALL THE CHARACTERS ?

```

8104	033460	001327		BNE	24:	; LOOP UNTIL ALL CHARACTERS (NON-BMP) ARE READ.
8105	033462	005367	000274	DEC	70:	; DECREMENT THE LOOP COUNT
8106	033466	001420		BEQ	29:	; BRANCH IF BOTH DMA'S ARE COMPLETED
8107	033470	012704	005154	MOV	#SDP2B,R4	; SET UP THE SECOND DATA PATTERN
8108	033474	016705	146522	MOV	DMTSTA,R5	; SET UP THE OTHER DMA TEST ADDRESS
8109						
8110	033500	012767	010472	MOV	#4410.,ERRNBR	; SET ERRNBR TO 4410
8111	033506	012701	010752	MOV	#EM4410,R1	; SELECT THE MESSAGE ,
8112						; " RXFIFO FAILED TO PURGE. TEST ABANDONED "
8113	033512	012767	014124	MOV	#ERC503,ERRBLK	; SELECT THE ERROR ROUTINE
8114						
8115	033520	004767	167216	JSR	PC,PURFIFO	; PURGE THE RXFIFO
8116	033524	103004		BCC	30:	; EXIT WITH ERROR IF FIFO WOULD NOT PURGE ,
8117						; AFTER FIRST RESTORING THE CONTENTS OF MEMORY.
8118	033526	000615		BR	18:	; OTHERWISE REPEAT.
8119						
8120	033530	012767	000001	MOV	#1,SUCSS	; INDICATE THAT WE HAVE BEEN ABLE TO TEST,
8121						; SOME OF THE BITS.
8122						
8123						
8124						
8125						;+ ; RESTORE THE ORIGINAL DATA IN THE MEMORY ;-
8126						
8127						
8128	033536	016767	146460	MOV	DMTSTA,DUMY	; START WITH THE HIGHER OF THE PAIR OF DMTSTA
8129	033544	016767	000222	MOV	OOTSTA,DMTSTA	;
8130	033552	012700	003602	MOV	#BUFBA,R0	; POINT AT THE START OF THE SAVED DATA AREA
8131	033556	012705	000001	MOV	#1,R5	; SELECT WRITE TO (DMTSTA)
8132	033562	012703	000020	MOV	#16.,R3	; PASS NUMBER OF BYTES TO BE WRITTEN
8133	033566	004767	164070	JSR	PC,DNRW	; RESTORE THE DATA
8134	033572	005767	146456	TST	TP4FLG	; GO REPORT ERROR IF A TRAP OCCURED
8135	033576	001012		BNE	31:	;
8136	033600	016767	000164	MOV	DUMY,DMTSTA	; NOW RESTORE THE DATA FROM THE LOWER
8137						; OF THE PAIR OF TEST ADDRESSES.
8138	033606	012700	004202	MOV	#BUFMID,R0	; POINT AT THE START OF THE SAVED DATA AREA
8139	033612	004767	164044	JSR	PC,DNRW	; RESTORE THE DATA
8140						
8141	033616	005767	146432	TST	TP4FLG	; GO REPORT ANY ERRORS IF A NO TRAP
8142	033622	001411		BEQ	32:	; OCCURED DURING THE RESTORE.
8143						
8144	033624	012767	010473	MOV	#4411.,ERRNBR	; SET THE ERROR NUMBER TO 4411
8145	033632	012701	011001	MOV	#EM4411,R1	; SELECT THE MESSAGE ,
8146						; " MOST FAILURE. WRITE FAILURE TO AN ADDR
8147						; WHICH HAD PREVIOUSLY BEEN SUCCESSFULLY
8148						; WRITTEN TO. "
8149	033636	012767	014124	MOV	#ER0503,ERRBLK	; SELECT THE ERROR ROUTINE
8150	033644	000433		BR	34:	; REPORT THE ERROR
8151						
8152						
8153						;+ ; HAS THE TEST BEEN SUCCESSFUL. PRINT THE BITS TESTED IF IT WAS, ; REPORT THE ERRORS OTHERWISE. ;-
8154						
8155						
8156						
8157						
8158	033646	005767	000122	TST	SUCSS	; IF THE ERROR IS NON TEST SPECIFIC THEN
8159	033652	001430		BEQ	34:	; BRANCH TO REPORT ERRORS
8160	033654	016701	000106	MOV	BITSND,R1	; LOAD THE NUMBER OF BITS TESTED

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8161 033660 005301          DEC    R1          ;DEC TO GIVE THE BIT POSITION OF THE MSB TESTED.
8162 033662 022767 000001 000104      CMP    #1,SUCSS ;IF THE BITS TESTED ARE BAD THEN
8163 033670 001021          BNE    34$        ;BRANCH AND REPORT ERRORS.
8164
8165          ;*
8166          ; OTHERWISE DETERMINE IF PRINTING OF THE SUCCESSFULLY TESTED BITS WAS REQUESTED.
8167          ;
8168
8169
8170 033672 032767 000040 146262      BIT    #BIT05,OPTION ; PRINT THE BITS TESTED IF THE SOFTWARE
8171 033700 001416          BEQ    60$        ;OPTION HAS REQUESTED IT
8172 033702 010102          MOV    R1,R2      ;CALCULATE THE NUMBER OF BITS WHICH HAVE
8173 033704 005202          INC    R2        ; BEEN TESTED SUCCESSFULLY.
8174 033706          PRINTB  #EF4401,R1,R2 ;PRINT THE NUMBER OF BITS TESTED MESSAGE.
      033706 010246          MOV    R2,-(SP)
      033710 010146          MOV    R1,-(SP)
      033712 012746 005554          MOV    #EF4401,-(SP)
      033716 012746 000003          MOV    #3,-(SP)
      033722 010600          MOV    SP,R0
      033724 104414          TRAP   C$PNTB
      033726 062706 000010          ADD    #10,SP
8175 033732 000401          BR     60$        ;EXIT THE TEST
8176
8177
8178 033734          34$:  ERROR          ; REPORT ERRORS
      033734 104460          TRAP   C$ERROR
8179
8180
8181 033736          60$:  SETPRI  #PRI05      ;ENABLE THE CLOCK
      033736 012700 000240          MOV    #PRI05,R0
      033742 104441          TRAP   C$SPRI
8182 033744 016767 146306 144032      MOV    TP4VEC,4 ;RESTORE THE NORMAL 004 TRAP VECTOR
8183 033752 005067 146242          CLR    CTRLCF ;INDICATE THAT WE ARE NOT WITHIN A TEST
8184
8185 033756          EXIT    TST
      033756 104432          TRAP   C$EXIT
      033760 000016          .WORD  L10030-.
8186
8187
8188
8189          ;*
8190          ; ***** LOCAL VARIABLE AREA *****
8191          ; -
8192 033762 000000      70$:  .WORD  0          ;COUNTER FOR THE NUMBER OF DMA'S COMPLETED
8193 033764 000000      80$:  .WORD  0          ;SAVE AREA FOR THE ACTIVE LINE NUMBER
8194 033766 000000      BITSTD: .WORD  0        ;NUMBER OF BITS TESTED
8195 033770 000000      DUMY:  .WORD  0        ;DUMMY VARIABLE
8196 033772 000000      ODTSTA: .WORD  0        ;HIGHER OF THE PAIR OF 'OMPLEMENTARY ADDR.
8197 033774 000000      SUCSS:  .WORD  0        ;SUCCESS INDICATOR, -1 ERROR DUE TO BAD BITS
8198                                     ;          1 - SUCCESSFUL TEST
8199                                     ;          0 OTHER ERRORS
8200
8201          ;*
8202          ; ***** END *****
8203          ; -
8204
8205

```


F1

8206
8207 033776
033776
033776 104401

ENDTST

L10030: TRAP C8ETST

```

8209 .SBTTL  HARDWARE TEST          - FRMERR -
8210 ;*****
8211 ;                                FRAMING ERROR GENERATION TEST
8212 ;
8213 ;                                THIS TEST IS USED TO VERIFY THE FRAMING ERROR DETECTION CAPABILITIES
8214 ;                                OF THE DHU11.
8215 ;                                WHEN IN STAGGARED LOOPBACK MODE, CHARACTERS ARE TRANSMITTED FROM
8216 ;                                ONE GROUP OF LINES AT 8 BITS/CHAR, AND RECEIVED BY THE OTHER GROUP
8217 ;                                AT 5 BITS/CHAR. THIS WILL GENERATE A FRAMING ERROR FOR EACH CHARACTER.
8218 ;                                THIS TEST WILL ONLY EXECUTE IF THE STAGGARED LOOPBACK MODE IS SELECTED.
8219 ;                                THE SPECIAL STAGGARED LOOPBACK BERG CONNECTOR MUST BE FITTED.
8220 ;                                THE ACTIVE LINES BIT MASK IS USED TO INDICATE WHICH LINES HAVE BEEN
8221 ;                                REMOVED FROM FURTHER TESTING.
8222 ;
8223 ;-----*****
8224 034000 BGNTST
8225 034000
8226 ;
8227 ; EXECUTE THIS TEST IN STAGGARED LOOPBACK MODE ONLY.
8228 034000 126727 146170 000002
8229 034006 001402
8230
8231 034010 000167 000372
8232 034014
8233 034014 012700 000240
8234 034020 104441
8235 034022 012767 177777 146170
8236 034030 000005
8237 034036 012767 000005 146222
8238 034044 012767 000001 151250
8239 034052 012767 014071 151244
8240 034060 012767 011117 151240
8241 034066 005067 146414
8242 034064 005067 146134
8243
8244 ;
8245 ; RESET THE DUT TO A KNOWN STATE, REMOVE STATUS CODES FROM THE FIFO.
8246 ; CLEAR TX AND RX INTERRUPT ENABLE BITS.
8247 ; THIS SUBROUTINE REPORTS ERROR >>>> 6201 <<<<.
8248 ;
8249 JSR PC,CLRST ;RESET THE DUT.
8250 BCC 60$ ;ABORT THE TEST IF FATAL ERROR FOUND IN RESET.
8251
8252 ;
8253 ; DISABLE ALL INTERRUPTS.
8254 ; SET UP DMA TX AND RX INTERRUPT SERVICE ROUTINES.
8255
8256 034076 012700 000340
8257 034102 104441
8258 034104 012746 000300
8259 034110 012746 027520
8260 034114 016746 146050
8261 034120 012746 000003
8262 034124 104437
8263 034126 062706 000010
8264 034132

```

```

;*****
;                                FRAMING ERROR GENERATION TEST
;
;                                THIS TEST IS USED TO VERIFY THE FRAMING ERROR DETECTION CAPABILITIES
;                                OF THE DHU11.
;                                WHEN IN STAGGARED LOOPBACK MODE, CHARACTERS ARE TRANSMITTED FROM
;                                ONE GROUP OF LINES AT 8 BITS/CHAR, AND RECEIVED BY THE OTHER GROUP
;                                AT 5 BITS/CHAR. THIS WILL GENERATE A FRAMING ERROR FOR EACH CHARACTER.
;                                THIS TEST WILL ONLY EXECUTE IF THE STAGGARED LOOPBACK MODE IS SELECTED.
;                                THE SPECIAL STAGGARED LOOPBACK BERG CONNECTOR MUST BE FITTED.
;                                THE ACTIVE LINES BIT MASK IS USED TO INDICATE WHICH LINES HAVE BEEN
;                                REMOVED FROM FURTHER TESTING.
;-----*****
BGNTST
;
; EXECUTE THIS TEST IN STAGGARED LOOPBACK MODE ONLY.
;
;                                CMPB  LOPBCK, #2 ;CHECK MODE SELECTED.
;                                BEQ    .+6 ;AVOID EXITING THE TEST IF STAGGERED LOOPBACK
;                                ;MODE IS SELECTED.
;                                JMP     60$ ; EXIT THE TEST.
;                                SETPRI #PRI05 ;ALLOW LTC INTERRUPTS.
;
;                                MOV     #1, CTRLCF ;INDICATE THAT WE ARE IN A TEST.
;                                TNUM -- TNUM + 1 ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
;                                MOV     #TNUM, TSTNUM ;SET UP THE TEST NUMBER. (62)
;                                MOV     #1, ERRTP ;SET ERROR TYPE IN ERROR TABLE.
;                                MOV     #6201, ERRNBR ;SET THE FIRST ERROR NUMBER IN ERROR TABLE.
;                                MOV     #EM6201, ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
;                                CLR     ERSRFR ;INITIALIZE THE "REPORT ERROR SUMMARY" FLAGS.
;                                CLR     FERROR ;CLEAR THE "AT LEAST ONE ERROR" INDICATOR.
;
; RESET THE DUT TO A KNOWN STATE, REMOVE STATUS CODES FROM THE FIFO.
; CLEAR TX AND RX INTERRUPT ENABLE BITS.
; THIS SUBROUTINE REPORTS ERROR >>>> 6201 <<<<.
;
; JSR PC,CLRST ;RESET THE DUT.
; BCC 60$ ;ABORT THE TEST IF FATAL ERROR FOUND IN RESET.
;
;
; DISABLE ALL INTERRUPTS.
; SET UP DMA TX AND RX INTERRUPT SERVICE ROUTINES.
;
; SETPRI #PRI07 ;DISABLE ALL INTERRUPTS.
;
;                                MOV     #PRI07, RC
;                                TRAP    C$SPRI
;                                SETVEC TXVECA, #TXDMA, #PRI06 ;SELECT DMA TX INT SERVICE RTN.
;                                MOV     #PRI06, (SP)
;                                MOV     #TXDMA, -(SP)
;                                MOV     TXVECA, -(SP)
;                                MOV     #3, -(SP)
;                                TRAP    C$SVEC
;                                ADD     #10, SP
;
; SETPRI #PRI04 ;ALLOW INTERRUPTS.

```

Address	Hex	Hex	Hex	Hex	Assembly	Comments
8255	034132	012700	000200		MOV	#PRIORITY, R0
8256	034136	104441			TRAP	C\$SPRI
8257					;	CLEAR TX, RX, AND DMA_START ERROR FLAGS.
8258	034140	005067	146336		CLR	TXDNF ;CLEAR TX DONE FLAGS FOR ALL LINES.
8259	034144	005067	146334		CLR	RXDNF ;CLEAR RX DONE FLAGS FOR ALL LINES.
8260	034150	005067	146110		CLR	TXINTF ;CLEAR TX ERROR FLAGS FOR ALL LINES.
8261					;	SET UP ERROR TABLE AND DATA PATTERN TABLE.
8262					;	THE NUMERICAL VALUE OF THE CHARACTER INDICATES THE NUMBER OF THE LINE
8263					;	THAT TRANSMITTED IT.
8264					;	
8265					;	
8266	034154	012700	003302		MOV	#ERRCNTB, R0 ;PASS THE ADDRESS OF THE TABLE TO BE CLEARED.
8267	034160	004767	163266		JSR	PC, CLR16W ;CLEAR THE RX ERROR COUNTERS TABLE.
8268	034164	005067	147412		CLR	BUFBAS ;SET SINGLE CHAR DATA TO BE A NULL.
8269					;	INITIALISE DMA PARAMETERS IN THE CONTROL BLOCK.
8270					;	TRANSMISSION ON LINE GROUP 1 AT 8 BITS/CHAR, 1 STOP BITS, ODD PARITY.
8271					;	RECEPTION ON LINE GROUP 2 AT 5 BITS/CHAR, 1 STOP, ODD PARITY.
8272					;	
8273					;	
8274	034170	012700	156470		MOV	#156470, R0 ;PASS LPR PARAMETER FOR 8 BITS/CHAR.
8275	034174	012701	156440		MOV	#156440, R1 ;PASS LPR PARAMETER FOR 5 BITS/CHAR.
8276	034200	004767	164424		JSR	PC, GETTIM ;GET TIME-OUT BASED ON MINIMUM BAUDRATE IN USE.
8277	034204	012702	003602		MOV	#BUFBAS, R2 ;PASS START ADDRESS OF DATA PATTERN.
8278	034210	012703	000001		MOV	#1, R3 ;PASS LENGTH OF DATA PATTERN.
8279	034214	016704	146016		MOV	LGRP1M, R4 ;PASS LINE GROUP OF LINES THAT ARE TO TX.
8280	034220	004767	163730		JSR	PC, FRPSUP ;SET UP DUT FOR TRANSMISSION AND RECEPTION.
8281					;	
8282					;	PURGE THE FIFO OF ANY UN-WANTED CHARACTERS. THIS ROUTINE REPORTS ERRORS
8283					;	WITH WITH ERROR NUMBERS FROM >>>> 6202 THRU 6204 <<<<.
8284					;	PERFORM TRANSMISSION AND RECEPTION AT 9600 BAUD.
8285					;	REPORT ANY ERRORS FOUND, IE. FRAMING ERROR BIT CLEAR OR PARITY ERROR SET.
8286					;	
8287					;	
8288	034224	005267	151066		INC	ERRNBR ;SET THE ERROR REPORT NUMBER TO 6202.
8289	034230	004767	166570		JSR	PC, PUFIFR ;CLEAN OUT THE FIFO.
8290	034234	103064			BCC	60H ;ABORT THIS TEST IF FIFO WOULD NOT PURGE.
8291	034236	012767	014075	151052	MOV	#6205, ERRNBR ;SET THE ERROR NUMBER TO 6205.
8292	034244	004767	171346		JSR	PC, TXFRPR ;TX DATA PATTERN ON SELECTED ACTIVE LINES.
8293	034250	012705	100000		MOV	#100000, R5 ;PASS FRAMING ERROR TEST FLAG.
8294					;	
8295					;	THIS SUBROUTINE REPORTS ERROR NUMBER >>>> 6205 <<<<.
8296					;	
8297	034254	004767	162550		JSR	PC, CKFRPR ;READ CHARACTERS, REPORT ANY ERRORS FOUND.
8298					;	
8299	034260	005767	145740		TST	FERROR ;HAS AN ERROR BEEN DETECTED?
8300	034264	001404			BEQ	2H ;BRANCH IF NO ERROR
8301	034266	032767	000100	145666	BIT	#BIT06, OPTION ;HAS EXTENDED ERROR REPORTING BEEN ENBL'D?
8302	034274	001436			BEQ	54H ;BRANCH IF IT HASN'T AND EXIT THE TEST. THE
8303					;	TEST FAILURE MESSAGE HAS BEEN PRINTED.
8304					;	
8305					;	REVERSE TRANSMISSION/RECEPTION ROLES ON ALL ACTIVE LINES, AND REPEAT TEST.
8306					;	
8307	034276	005104			COM	R4 ;REVERSE ROLES FOR TRANSMISSION AND RECEPTION.
8308	034300	004767	163650		JSR	PC, FRPSUP ;SET UP DUT FOR TRANSMISSION AND RECEPTION.
8309	034304	005267	151006		INC	ERRNBR ;SET ERROR NUMBER TO 6206.

I 1

```

8310
8311
8312
8313 034310 004767 166510
8314 034314 103034
8315 034316 012767 014101 150772
8316 034324 004767 171266
8317 034330 012705 100000
8318
8319
8320
8321 034334 004767 162470
8322
8323 034340 005767 145660
8324 034344 001404
8325 034346 032767 000100 145606
8326 034354 001406
8327
8328
8329 034356 005267 150734
8330
8331
8332
8333
8334
8335
8336 034362 004767 171324
8337 034366 004767 171746
8338
8339 034372
      034372 012700 000340
      034376 104441
8340 034400
      034400 016700 145564
      034404 104436
8341
8342 034406 005067 145606
8343
8344 034412
      034412
      034412 104401

;+
; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>> 6206 THRU 6208 <<<<.
;-
      JSR      PC,PUFIFR      ;CLEAN OUT THE FIFO.
      BCC      60$           ;ABORT THIS TEST IF FIFO WOULD NOT PURGE.
      MOV      #6209,ERRNBR   ;SET ERROR NUMBER TO 6209.
      JSR      PC,TXFRPR      ;TX DATA PATTERN ON SELECTED ACTIVE LINES.
      MOV      #100000,R5     ;PASS FRAMING ERROR TEST FLAG.

;+
; THIS SUBROUTINE REPORTS ERRORS >>>> 6209 <<<<.
;-
      JSR      PC,CKFRPR      ;READ CHARACTERS, REPORT ANY ERRORS FOUND.

      TST      FERROR         ;HAS AN ERROR BEEN DETECTED?
      BEQ      4$             ;BRANCH IF NO ERROR
      BIT      #BIT06,OPTION   ;HAS EXTENDED ERROR REPORTING BEEN ENBL'D?
      BEQ      54$            ;BRANCH IF IT HASN'T AND EXIT THE TEST. THE
                                ;TEST FAILURE MESSAGE HAS BEEN PRINTED.

4$:      INC      ERRNBR      ;SET ERROR NUMBER TO 6210.

;+
; DISABLE INTERRUPTS.
; CLEAR THE INTERRUPT VECTORS.
; UPDATE THE ACTIVE LINES BIT MAP TO REFLECT LINES REMOVED FROM TESTING.
; THIS SUBROUTINE REPORTS ERRORS >>>> 6210 THRU 6212 <<<<.
;-
      JSR      PC,TXIEO       ;DISABLE ALL TX INTERRUPTS.
      JSR      PC,TXRREP      ;REPORT FINAL ERRORS FROM TX/RX.

54$:     SETPRI  #PRI07       ;DISABLE ALL INTERRUPTS.

                                MOV      #PRI07,R0
                                TRAP     C$SPRI
                                MOV      TXVECA,R0
                                TRAP     C$CVEC

      CLRVEC  TXVECA         ;RETURN TX INT VECTOR TO UNUSED POOL.

60$:     CLR      CTRLCF      ;INDICATE THAT WE ARE NOT WITHIN A TEST.

      ENDTST

                                L10031:
                                TRAP     C$ETST

```

J1

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8346 .SBTTL  HARDWARE TEST          - PARERR
8347 ;+*****
8348 ;*                                - PARITY ERROR GENERATION TEST -
8349 ;*
8350 ;*      THIS TEST IS USED TO VERIFY THE PARITY ERROR DETECTION AND REPORT
8351 ;*      CAPABILITIES OF THE DUT.
8352 ;*      WHEN STAGGARED LOOPBACK MODE IS SELECTED, DATA IS TRANSMITTED
8353 ;*      ON ALL ACTIVE LINES IN LINE GROUP 1 WITH ODD PARITY SELECTED.
8354 ;*      AND RECEIVED ON LINES IN GROUP 2 WITH EVEN PARITY SELECTED.
8355 ;*      THIS WILL GENERATE A PARITY ERROR FOR EACH CHARACTER RECEIVED.
8356 ;*      THE PARITY SELECTION IS THEN REVERSED ON THE LINES IN EACH GROUP
8357 ;*      AND THE TEST IS REPEATED.
8358 ;*      THIS TEST WILL ONLY EXECUTE IF THE STAGGARED LOOPBACK MODE IS SELECTED.
8359 ;*      THE SPECIAL STAGGARED LOOPBACK BERG CONNECTOR MUST BE FITTED.
8360 ;*
8361 ;-*****
8362 034414 BGNTST
      034414
8363
8364 ;+
8365 ; EXECUTE THIS TEST IN  STAGGARED LOOPBACK MODE ONLY.
8366 ;-
8366 034414 126727 145554 000002      CMPB  LOPBCK, #2      ;CHECK MODE SELECTED.
8367 034422 001402                      BEQ    .+6          ;AVOID EXITING THE TEST IF STAGGERED LOOPBACK
8368                                ;MODE IS SELECTED.
8369 034424 000167 000434      JMP    60$          ; EXIT THE TEST.
8370
8371 034430      SETPRI  #PRI05          ;ALLOW LTC INTERRUPTS.
      034430 012700 000240
      034434 104441
8372 034436 012767 177777 145554      MOV    #-1, CTRLCF      ;INDICATE THAT WE ARE IN A TEST.
8373 000006      TNUM  == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
8374 034444 012767 000006 145606      MOV    #TNUM, TSTNUM      ;SET UP THE TEST NUMBER. (63)
8375 034452 012767 000001 150634      MOV    #1, ERRTP        ;SET ERROR TYPE IN ERROR TABLE.
8376 034460 012767 014235 150630      MOV    #6301, ERRNBR      ;SET THE FIRST ERROR NUMBER IN ERROR TABLE.
8377 034466 012767 011160 150624      MOV    #EM6301, ERRMSG      ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
8378 034474 005067 146000      CLR    ERSRFR      ;INITIALIZE THE "REPORT ERROR SUMMARY" FLAGS.
8379 034500 005067 145520      CLR    FERROR      ;CLEAR THE "AT LEAST ONE ERROR" INDICATOR
8380
8381 ;+
8382 ; RESET THE DUT TO A KNOWN STATE, REMOVE STATUS CODES FROM THE FIFO.
8383 ; CLEAR TX AND RX INTERRUPT ENABLE BITS.
8384 ; THIS SUBROUTINE REPORTS ERROR >>>> 6301 <<<<<.
8385 ;-
8385 034504 004767 162720      JSR    PC, CLNRST      ;RESET THE DUT.
8386 034510 103165      BCC    60$          ;ABORT THE TEST IF FATAL ERROR FOUND IN RESET.
8387
8388 ;+
8389 ; DISABLE ALL INTERRUPTS.
8390 ; SET UP DMA TX AND RX INTERRUPT SERVICE ROUTINES.
8391 ;-
8391 034512      SETPRI  #PRI07          ;DISABLE ALL INTERRUPTS.
      034512 012700 000340
      034516 104441
8392 034520      SETVEC  TXVECA, #TXDMA, #PRI06      ;SELECT DMA TX INT SERVICE RTN.
      034520 012746 000300
      034524 012746 027520
      034530 016746 145434
      034534 012746 000003
      034540 104437
      MOV    #PRI07, R0
      TRAP   C$SPRI
      MOV    #PRI06, -(SP)
      MOV    #TXDMA, -(SP)
      MOV    TXVECA, -(SP)
      MOV    #3, -(SP)
      TRAP   C$SVEC
  
```

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8393 034542 062706 000010          SETPRI  #PRI04          ;ALLOW INTERRUPTS.          ADD    #10,SP
      034546                                MOV    #PRI04,R0
      034546 012700 000200                                TRAP   C$SPRI
      034552 104441
8394
8395
8396
8397 034554 005067 145722          CLR    TXDNF          ;CLEAR TX DONE FLAGS FOR ALL LINES.
8398 034560 005067 145720          CLR    RXDNF          ;CLEAR RX DONE FLAGS FOR ALL LINES.
8399 034564 005067 145474          CLR    TXINTF        ;CLEAR TX ERROR FLAGS FOR ALL LINES.
8400
8401
8402
8403 034570 012700 003302          MOV    #ERCNTB,R0          ;PASS THE ADDRESS OF THE TABLE TO BE CLEARED.
8404 034574 004767 162652          JSR    PC,CLR16W        ;CLEAR THE RX ERROR COUNTERS TABLE.
8405
8406
8407
8408
8409 034600 012700 156470          MOV    #156470,R0          ;PASS LPR PARAMETER WITH ODD PARITY.
8410 034604 012701 156570          MOV    #156570,R1          ;PASS LPR PARAMETER WITH EVEN PARITY.
8411 034610 004767 164014          JSR    PC,GETTIM        ;GET TIME-OUT BASED ON MINIMUM BAUDRATE IN USE.
8412 034614 012702 005154          MOV    #SDP2B,R2          ;PASS START ADDRESS OF DATA PATTERN.
8413 034620 012703 000020          MOV    #16.,R3           ;PASS LENGTH OF DATA PATTERN.
8414 034624 016704 145406          MOV    LGRP1M,R4          ;PASS BIT MAP OF LINES TO BE SET WITH ODD PAR.
8415 034630 004767 163320          JSR    PC,FRPSUP        ;SET UP DUT FOR TRANSMISSION AND RECEPTION.
8416
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8427
8428 034634 004767 166164          JSR    PC,PUFIFR        ;CLEAN OUT THE FIFO.
8429 034640 103111 150446          BCC    60$              ;ABORT THIS TEST IF FIFO WOULD NOT PURGE.
8430 034642 012767 014241          MOV    #6305.,ERRNBR    ;SET ERROR NUMBER TO 6305
8431 034650 004767 164032          JSR    PC,INIDMA        ;TX DATA PATTERN ON ALL ACTIVE LINES.
8432 034654 005005                    CLR    R5              ;PASS PARITY ERROR TEST FLAG.
8433
8434
8435
8436 034656 004767 162146          JSR    PC,CKFRPR        ;READ CHARACTERS, REPORT ANY ERRORS FOUND.
8437
8438 034662 005767 145336          TST    FERROR          ;HAS AN ERROR BEEN FOUND ?
8439 034666 001404                    BEQ    2$              ;BRANCH TO CONTINUE IF IT HASN'T.
8440 034670 032767 000100 145264    BIT    #BIT06,OPTION    ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
8441 034676 001457                    BEQ    54$              ;EXIT THE TEST IF IT HASN'T. THE TEST FAILURE
8442
8443
8444 034700 005267 150412          2$: INC    ERRNBR          ;SET ERROR NUMBER TO 6306.
8445
8446

```

; THIS SUBROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 6306 THRU 6309 <<<<<

```

8447
8448 034704 004767 171430      ; JSR    PC, TXRREP      ;REPORT FINAL ERRORS FROM TX/RX.
8449 034710 005767 145310      ; TST    FERROR        ;HAS AN ERROR BEEN FOUND ?
8450 034714 001404              ; BEQ    4$             ;BRANCH TO CONTINUE IF IT HASN'T.
8451 034716 032767 000100 145236 ; BIT    #BIT06,OPTION  ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
8452 034724 001457              ; BEQ    60$           ;EXIT THE TEST IF IT HASN'T. THE TEST FAILURE
8453                                ; MESSAGE HAS ALREADY BEEN REPORTED.
8454
8455 034726 012767 014246 150362 4$: MOV    #6310.,ERRNBR  ;SET ERROR NUMBER TO 6310.
8456 034734 005067 145542      ; CLR    TXDNF         ;CLEAR TX DONE FLAGS FOR ALL LINES.
8457 034740 005067 145540      ; CLR    RXDNF         ;CLEAR RX DONE FLAGS FOR ALL LINES.
8458 034744 005067 145314      ; CLR    TXINTF        ;CLEAR TX DMA HANDOVER ERROR FLAGS.
8459
8460      ;+
8461      ; REVERSE TRANSMISSION/RECEPTION ROLES ON ALL ACTIVE LINES, AND REPEAT TEST.
8462 034750 005104              ; COM     R4
8463 034752 004767 163176      ; JSR    PC,FRPSUP     ;REVERSE ROLES FOR TRANSMISSION AND RECEPTION.
8464                                ; SET UP DUT FOR TRANSMISSION AND RECEPTION.
8465      ;+
8466      ; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>> 6310 THRU 6311 <<<<.
8467 034756 004767 166042      ; JSR    PC,PUFIFR     ;CLEAN OUT THE FIFO.
8468 034762 103040              ; BCC    60$           ;ABORT THIS TEST IF FIFO WOULD NOT PURGE.
8469 034764 012767 014250 150324 ; MOV    #6312.,ERRNBR  ;SET ERROR NUMBER TO 6312.
8470 034772 004767 163710      ; JSR    PC,INIDMA     ;TX DATA PATTERN ON SELECTED ACTIVE LINES.
8471
8472      ;+
8473      ; THIS SUBROUTINE REPORTS ERRORS WITH NUMBERS >>>> 6312 THRU 6316 <<<<.
8474 034776 004767 162026      ; JSR    PC,CKFRPR     ;READ CHARACTERS, REPORT ANY ERRORS FOUND.
8475 035002 005767 145216      ; TST    FERROR        ;HAS AN ERROR BEEN FOUND ?
8476 035006 001404              ; BEQ    6$             ;BRANCH TO CONTINUE IF IT HASN'T.
8477 035010 032767 000100 145144 ; BIT    #BIT06,OPTION  ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
8478 035016 001407              ; BEQ    54$           ;EXIT THE TEST IF IT HASN'T. THE TEST FAILURE
8479                                ; MESSAGE HAS ALREADY BEEN REPORTED.
8480
8481 035020 012767 014255 150270 6$: MOV    #6317.,ERRNBR  ;SET ERROR NUMBER TO 6317.
8482
8483      ;+
8484      ; DISABLE INTERRUPTS.
8485      ; CLEAR THE INTERRUPT VECTORS.
8486      ; UPDATE THE ACTIVE LINES BIT MAP TO REFLECT LINES REMOVED FROM TESTING.
8487 035026 004767 170660      ; JSR    PC, TXIEO     ;DISABLE ALL TX INTERRUPTS.
8488
8489      ;+
8490      ; THIS SUBROUTINE REPORTS ERRORS >>>> 6317 THRU 6320 <<<<.
8491 035032 004767 171302      ; JSR    PC, TXRREP     ;REPORT FINAL ERRORS FROM TX/RX.
8492
8493 035036 012700 000340 54$: SETPRI  #PRI07      ;DISABLE ALL INTERRUPTS.
8494 035042 104441              ;
8495      ;
8496      ;
8497      ;
8498      ;
8499 035044 016700 145120      ; CLRVEC TXVECA        ;RETURN TX INT VECTOR TO UNUSED POOL.
8500 035050 104436              ;
8501      ;
8502      ;+
8503      ; THIS SUBROUTINE REPORTS ERRORS WITH NUMBERS >>>> 6321 <<<<.
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M1

DHU 11 FUNCTIONAL VERIFICATION MACRO M1200 12 DEC 83 16:16 PAGE 134-3
HARDWARE TEST PARERR

SEQ 219

8500 035060 004767 167036

JSR PC,REPSMR

;REPORT ERROR SUMMARIES IF CALLED FOR.

8501

8502 035064 005067 145130

60\$:

CLR CTRLCF

;INDICATE THAT WE ARE NOT WITHIN A TEST.

8503 035070

ENDTST

L10032:

035070 104401

TRAP C\$ETST


```

8505 .SBTTL  HARDWARE TEST          - DMA -
8506 ;** *****
8507 ;*          - DMA MODE TEST -
8508 ;*      THIS TEST VERIFIES THAT THE DEVICE UNDER TEST (DUT) WILL PERFORM
8509 ;*      TRANSMISSION AND RECEPTION CORRECTLY USING THE DMA MODE TRANSMISSION.
8510 ;*      THE TEST IS PERFORMED AT ALL BAUDRATES (EXCEPT 50 BAUD), 8 BITS PER
8511 ;*      CHARACTER, 1 STOP BIT, AND WITH PARITY CHECKING (BOTH ODD AND EVEN).
8512 ;*      A HIGH SPEED TEST IS ALSO PERFORMED AT THE HIGHEST 3 BAUDRATES AT
8513 ;*      BOTH 5 AND 8 BITS PER CHARACTER, 1 STOP BIT, AND NO PARITY CHECKING.
8514 ;*      THIS TEST IS PERFORMED WITH THE TYPE OF LOOPBACK WHICH WAS SPECIFIED
8515 ;*      IN THE DUT HARDWARE P TABLE ON ALL ACTIVE LINES.
8516 ;*
8517 ;*****
8518 035072 BGNTST
8519 035072          SETPRI  #PRI05          ;ALLOW LTC INTERRUPTS.
8520 035072          MOV      #PRI05,R0
8521 035076          TRAP    C$SPRI
8522 035100          TNUM  == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
8523 035106          MOV     #TNUM,TSTNUM   ;SET UP THE TEST NUMBER. (91)
8524 035114          MOV     #-1,CTRLCF    ;INDICATE THAT WE ARE IN A TEST.
8525 035122          MOV     #1,ERRTYP     ;SET ERROR TYPE AS FATAL IN ERROR TABLE.
8526 035130          MOV     #9101,ERRNBR  ;SET THE FIRST ERROR NUMBER IN ERROR TABLE.
8527 035136          MOV     #EM9101,ERRMSG ;SET ERROR MESSAGE ADDRESS IN ERRIBL.
8528 035142          CLR     ERSRFR        ;INITIALIZE THE "REPORT ERROR SUMMARY" FLAGS.
8529          CLR     FERROR              ;CLEAR THE "AT LEAST ONE ERROR" INDICATOR.
8530 ;*
8531 ;* RESET THE DUT TO A KNOWN STATE, REMOVE THE STATUS CODES FROM THE FIFO.
8532 ;* CLEAR TX AND RX INTERRUPT ENABLE BITS IN THE CSR.
8533 ;* THIS SUBROUTINE REPORTS ERROR >>>> 9101 <<<<.
8534 ;*
8535 035146          JSR     PC,CLNRST      ;RESET THE DHU-11, REPORT ANY ERRORS FOUND.
8536 035152          BCS     2$             ;SKIP AROUND TEST EXIT IF NO FATAL ERROR FOUND.
8537 035154          JMP     60$            ;RESET FAILURE, ABORT THIS TEST.
8538 ;*
8539 2$: SETUP FOR TRANSMIT INTERRUPTS.
8540          SETPRI  #PRI07              ;DISABLE ALL INTERRUPTS.
8541          MOV     #PRI07,R0
8542          TRAP    C$SPRI
8543          SETVEC  TXVECA,#TXDMA,#PRI06 ;SELECT DMA TX INT SERVICE RTN.
8544          MOV     #PRI06,-(SP)
8545          MOV     #TXDMA,-(SP)
8546          MOV     TXVECA,-(SP)
8547          MOV     #3,-(SP)
8548          TRAP    C$SVEC
8549          ADD     #10,SP
8550          SETVEC  RXVECA,#RXCHRS,#PRI06 ;SELECT RX INT SERVICE RTN.
8551          MOV     #PRI06,-(SP)
8552          MOV     #RXCHRS,-(SP)
8553          MOV     RXVECA,-(SP)
8554          MOV     #3,-(SP)
8555          TRAP    C$SVEC
8556          ADD     #10,SP
8557          SETPRI  #PRI04              ;ALLOW INTERRUPTS.
8558          MOV     #PRI04,R0
8559          TRAP    C$SPRI

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8547
8548 035250 012700 003302
8549 035254 004767 162172
8550 035260 012701 010470
8551 035264 004767 163340
8552 035270 012702 005154
8553 035274 012703 000020
8554 035300 012704 000001
8555 035304 004767 171376
8556 035310 012767 177400 144714
8557 035316 012767 021616 147772
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8561 035324 004767 165474
8562 035330 103402
8563 035332 000167 000452
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8565 035336 004767 165664
8566 035342 004767 163340
8567 035346 012767 021621 147742
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8571 035354 004767 165702
8572 035360 005767 144640
8573 035364 001406
8574 035366 032767 000100 144566
8575 035374 001002
8576 035376 000167 000406
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8578 035402 012767 021627 147706 54:
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8582 035410 004767 170724
8583 035414 005767 144604
8584 035420 001404
8585 035422 032767 000100 144532
8586 035430 001567
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8591 035432 010100
8592 035434 042701 000100
8593 035440 005100
8594 035442 042700 177677
8595 035446 050001
8596 035450 062701 010400
8597 035454 103303
8598
8599

; *
; TRANSMIT AND RECEIVE SHORT DATA PATTERN AT ALL BAUDRATES,
; WITH 8 BITS PER CHARACTER, 1 STOP BIT, AND BOTH TYPES OF PARITY.
; BOTH LINE GROUPS (LGPRS) TX AND RX WITH THE SAME PARAMETERS.
; -
      MOV     #ERCNTB,R0
      JSR     PC,CLR16W      ;CLEAR THE RX ERROR COUNTERS TABLE.
      MOV     #10470,R1      ;SET UP LPR CONTENTS FOR TX/RX AT 75 BAUD.
44:    JSR     PC,GETTIM      ;GET TIME-OUT BASED ON MINIMUM BAUDRATE IN USE.
      MOV     #SDP2B,R2      ;SET UP THE START ADR OF THE DATA PATTERN.
      MOV     #SDP2E-SDP2B,R3 ;SET UP THE DATA PATTERN LENGTH.
      MOV     #1,R4          ;SPECIFY TO SEND 1 DATA PATTERN TO EACH LINE.
      JSR     PC,VANSUP      ;SET UP "VANILLA FLAVORED" TX/RX.
      MOV     #177400,IBM    ;FORM BIT MAP OF UNUSED TX/RX BITS.
      MOV     #9102,,ERRNBR  ;SET THE ERROR REPORT NUMBER TO 9102.
; *
; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 9102 THRU 9104 <<<<<.
; -
      JSR     PC,PUFIFR      ;PURGE THE DUT RECEIVE CHARACTER FIFO.
      BCS     .+6
      JMP     504            ;ABORT THIS TEST IF FIFO WOULD NOT PURGE.
      JSR     PC,PURRXB      ;PURGE THE RX CHAR BUFFER IN MEMORY.
      JSR     PC,INIDMA      ;SEND THE FIRST BATCH OF DATA PATTERNS.
      MOV     #9105,,ERRNBR  ;SET ERROR NUMBER TO 9105.
; *
; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 9105 THRU 9110 <<<<<.
; -
      JSR     PC,RDCHRS      ;READ AND VERIFY THE RX CHARACTERS.
      TST     FERROR         ;HAS AN ERROR BEEN DETECTED ?
      BEQ     54             ;NO, THEN BRANCH.
      BIT     #BIT06,OPTION   ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
      BNE     .+6
      JMP     504            ;NO, THEN EXIT THE TEST.
54:    MOV     #9111,,ERRNBR  ;SET ERROR NUMBER TO 9111.
; *
; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 9111 THRU 9114 <<<<<.
; -
      JSR     PC,TXRREP      ;REPORT FINAL ERRORS FROM RX/RX.
      TST     FERROR         ;HAS AN ERROR BEEN DETECTED ?
      BEQ     64             ;NO, THEN BRANCH.
      BIT     #BIT06,OPTION   ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
      BEQ     504            ;NO, THEN EXIT THE TEST.
; *
; TOGGLE THE PARITY TYPE BIT SPECIFIER IN THE TX/RX SETUP PARAMETERS.
; SELECT THE NEXT BAUDRATE AND PERFORM THE TEST AGAIN IF NOT DONE.
; -
64:    MOV     R1,R0          ;COMPLEMENT THE PARITY TYPE
      BIC     #100,R1        ; BIT IN THE TX/RX LPR SETUP
      COM     R0              ; PARAMETER LEAVING THE
      BIC     #177677,R0     ; OTHER LPR PARAMETER
      BIS     R0,R1          ; BITS UNCHANGED.
      ADD     #10400,R1      ;SELECT THE NEXT BAUDRATE.
      BCC     44             ;LOOP TO TX/RX AGAIN IF NOT PAST LAST BAUDRATE.
; *
; PERFORM WIDE OPEN DMA TEST.

```

```

8600      ; TRANSMIT AND RECEIVE 512 BYTE DATA PATTERNS AT ALL COMBINATIONS OF 9.6K,
8601      ; 19.2K, AND 38.4K BUADRATES AND 5 AND 8 BITS PER CHARACTER.  USE 1 STOP BIT
8602      ; AND NO PARITY GENERATION OR DETECTION.
8603      ;
8604      ;
8605      ; INITIALIZE THE 512 BYTE PATTERN AND THE VARIOUS DATA PATTERN POINTERS.
8606      ;
8607 035456 005001      CLR      R1      ;CLEAR THE DATA BYTE COUNTER.
8608 035460 012702 003602  MOV     #BUFBAS,R2  ;GET THE BASE OF THE DATA PATTERN BUFFER.
8609 035464 110122      MOV     R1,(R2)+  ;WRITE A BYTE OF THE DATA PATTERN.
8610 035466 105201      INCB     R1      ;GET THE NEXT BYTE FOR THE DATA PATTERN.
8611 035470 001375      BNE      7$      ;LOOP UNTIL FIRST 1/2 OF PATTERN IS DONE.
8612 035472 105301      8$: DEC     R1      ;GET THE NEXT BYTE FOR THE DATA PATTERN.
8613 035474 110122      MOV     R1,(R2)+  ;WRITE A BYTE OF THE DATA PATTERN.
8614 035476 105701      TSTB     R1      ;CHECK FOR DONE WRITING DATA PATTERN.
8615 035500 001374      BNE      8$      ;LOOP IF DATA PATTERN IS NOT DONE.
8616 035502 110122      10$: MOV    R1,(R2)+  ;WRITE A BYTE OF THE 32 BYTE OVERFLOW REGION.
8617 035504 005201      INC      R1      ;COUNT THIS BYTE.
8618 035506 020127 000040  CMP     R1,#32.  ;TEST FOR 32 BYTES WRITTEN.
8619 035512 001373      BNE      10$      ;LOOP UNTIL 32 BYTES ARE WRITTEN.
8620      ;
8621      ; PREPARE TO LOOP ON THE 3 DIFFERENT BAUDRATES (9.6K, 19.2K, AND 38.4K).
8622      ;
8623 035514 012705 005072  MOV     #DLPRTB,R5  ;GET THE BASE ADR OF THE DMA BAUDRATE TABLE.
8624      ;
8625      ; SPECIFY THE PROPER BAUDRATE.
8626      ; SPECIFY 8 BITS PER CHARACTER.
8627      ; PERFORM DMA TRANSMISSION AND RECEPTION OF 512 BYTE DATA PATTERN.
8628      ;
8629      ;
8630      ; THE FOLLOWING ROUTINE REPORTS THE ERROR WITH NUMBERS 914 THRU 921.
8631      ; LPR CHANGE BIT ERROR FLAGS MAY BE SET BY THIS SUBROUTINE.
8632      ;
8633 035520 012501      12$: MOV     (R5)+,R1  ;SET UP LPR PARAM AT NEXT BAUD, 8 BITS/CHAR.
8634 035522 004767 163102  JSR     PC,GETTIM  ;GET TIME-OUT BASED ON MINIMUM BAUDRATE IN USE.
8635 035526 012702 003602  MOV     #BUFBAS,R2  ;SET UP THE START ADR OF THE DATA PATTERN.
8636 035532 012703 001000  MOV     #512.,R3  ;SET UP THE DATA PATTERN LENGTH.
8637 035536 012704 000001  MOV     #1,R4  ;SPECIFY TO SEND 1 DATA PATTERN TO EACH LINE.
8638 035542 004767 171140  JSR     PC,VANSUP  ;SET UP "VANILLA FLAVORED" TX/RX.
8639 035546 012767 177400 144456  MOV     #177400,IBM  ;FORM BIT MAP OF UNUSED BITS FOR 8 BITS/CHAR.
8640 035554 012767 021633 147534  MOV     #9115.,ERRNBR  ;SET ERROR NUMBER TO 9115.
8641      ;
8642      ; THIS ROUTINE REPORTS ERROS WITH NUMBERS >>>>> 9115 THRU 9117 <<<<<.
8643      ;
8644 035562 004767 165236  JSR     PC,PUFIFR  ;PURGE THE OUT RECEIVE CHARACTER FIFO.
8645 035566 103126      BCC     60$      ;ABORT THIS TEST IF FIFO WOULD NOT PURGE.
8646 035570 012767 021636 147520  MOV     #9118.,ERRNBR  ;SET ERROR NUMBER TO 9118.
8647      ;
8648 035576 004767 165424  JSR     PC,PURRXB  ;PURGE THE RX CHAR BUFFER IN MEMORY.
8649 035602 004767 163100  JSR     PC,INIDMA  ;SEND THE FIRST BATCH OF DATA PATTERNS.
8650      ;
8651      ; THIS ROUTINE REPORTS THE ERROR WITH NUMBERS >>>>> 9118 THRU 9123 <<<<<.
8652      ;
8653 035606 004767 165450  JSR     PC,RDCHRS  ;READ AND VERIFY THE RX CHARACTERS.
8654      ;
8655 035612 005767 144406  TST     FERROR  ;HAS AN ERROR BEEN DETECTED ?
8656 035616 001407      BEQ     14$      ;NO, THEN BRANCH.

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```

8657 035620 032767 000100 144334      BIT    #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
8658 035626 001470                      BEQ     50$           ;NO, THEN EXIT THE TEST.
8659
8660 035630 012767 021644 147460      MOV     #9124.,ERRNBR ;SET ERROR NUMBER TO 9124.
8661
8662                                     ;*
8663                                     ; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>> 9124 THRU 9127 <<<<.
8664 035636 004767 170476      14$:    JSR     PC,TXRREP      ;REPORT FINAL ERRORS FROM RX/RX.
8665 035642 005767 144356      TST     FERROR          ;HAS AN ERROR BEEN DETECTED ?
8666 035646 001404                      BEQ     16$           ;NO, THEN BRANCH.
8667 035650 032767 000100 144304      BIT     #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
8668 035656 001454                      BEQ     50$           ;NO, THEN EXIT THE TEST.
8669
8670 035660 012767 021650 147430 16$:    MOV     #9128.,ERRNBR ;SET ERROR NUMBER TO 9128.
8671
8672                                     ;*
8673                                     ; SPECIFY 5 BITS PER CHARACTER.
8674                                     ; PERFORM DMA TRANSMISSION AND RECEPTION OF 512 BYTE DATA PATTERN.
8675 035666 042701 000030      BIC     #30,R1          ;SET UP CHAR LENGTH PARAM TO 5 BITS/CHAR.
8676 035672 004767 171010      JSR     PC,VANSUP        ;SET UP "VANILLA FLAVORED" TX/RX.
8677 035676 012767 177740 144326      MOV     #177740,IBM      ;FORM BIT MAP OF UNUSED BITS FOR 5 BITS/CHAR.
8678
8679                                     ;*
8680                                     ; THIS ROUTINE REPORTS THE ERROR WITH NUMBERS >>> 9128 THRU 9131 <<<.
8681 035704 004767 165114      JSR     PC,PUFIFR        ;PURGE THE DUT RECEIVE CHARACTER FIFO.
8682 035710 103055                      BCC     60$           ;ABORT THIS TEST IF FIFO WOULD NOT PURGE.
8683 035712 012767 021654 147376      MOV     #9132.,ERRNBR ;SET THE ERROR REPORT NUMBER TO 9132.
8684
8685 035720 004767 165302      JSR     PC,PURRXB        ;PURGE THE RX CHAR BUFFER IN MEMORY.
8686 035724 004767 162756      JSR     PC,INIDMA       ;SEND THE FIRST BATCH OF DATA PATTERNS.
8687
8688                                     ;*
8689                                     ; THIS ROUTINE REPORTS THE ERROR WITH NUMBERS >>>> 9132 THRU 9137 <<<<.
8690 035730 004767 165326      JSR     PC,RDCHRS        ;READ AND VERIFY THE RX CHARACTERS.
8691 035734 005767 144264      TST     FERROR          ;HAS AN ERROR BEEN DETECTED ?
8692 035740 001404                      BEQ     18$           ;NO, THEN BRANCH.
8693 035742 032767 000100 144212      BIT     #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
8694 035750 001417                      BEQ     50$           ;NO, THEN EXIT THE TEST.
8695
8696 035752 012767 021662 147336 18$:    MOV     #9138.,ERRNBR ;SET ERROR NUMBER TO 9138.
8697
8698                                     ;*
8699                                     ; THIS ROUTINE REPORTS THE ERROR WITH NUMBERS >>>> 9138 THRU 9141 <<<<.
8700 035760 004767 170354      JSR     PC,TXRREP        ;REPORT FINAL ERRORS FROM RX/RX.
8701 035764 005767 144234      TST     FERROR          ;HAS AN ERROR BEEN DETECTED ?
8702 035770 001404                      BEQ     20$           ;NO, THEN BRANCH.
8703 035772 032767 000100 144162      BIT     #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
8704 036000 001403                      BEQ     50$           ;NO, THEN EXIT THE TEST.
8705
8706 036002 020527 005100      20$:    CMP     R5,#DLPRTE      ;COMPARE DMA BAUDRATE TABLE PTR WITH TABLE END.
8707 036006 103644                      BLO     12$           ;LOOP IF NOT ALL BAUDRATES DONE YET.
8708
8709                                     ;*
8710                                     ; ALL DONE. HAVE EITHER RUN OUT OF ACTIVE LINES, OR COMPLETED THE TEST.
8711                                     ; DISABLE INTERRUPTS.
8712                                     ; CLEAR THE INTERRUPT VECTORS.
8713 036010      50$:    SETPRI #PRI07              ;DISABLE ALL INTERRUPTS.

```

Address	Op Code	Op 1	Op 2	Op 3	Op 4	Op 5	Op 6	Op 7	Op 8	Op 9	Op 10	Op 11	Op 12	Op 13	Op 14	Op 15	Op 16	Op 17	Op 18	Op 19	Op 20	Op 21	Op 22	Op 23	Op 24	Op 25	Op 26	Op 27	Op 28	Op 29	Op 30	Op 31	Op 32	Op 33	Op 34	Op 35	Op 36	Op 37	Op 38	Op 39	Op 40	Op 41	Op 42	Op 43	Op 44	Op 45	Op 46	Op 47	Op 48	Op 49	Op 50	Op 51	Op 52	Op 53	Op 54	Op 55	Op 56	Op 57	Op 58	Op 59	Op 60	Op 61	Op 62	Op 63	Op 64	Op 65	Op 66	Op 67	Op 68	Op 69	Op 70	Op 71	Op 72	Op 73	Op 74	Op 75	Op 76	Op 77	Op 78	Op 79	Op 80	Op 81	Op 82	Op 83	Op 84	Op 85	Op 86	Op 87	Op 88	Op 89	Op 90	Op 91	Op 92	Op 93	Op 94	Op 95	Op 96	Op 97	Op 98	Op 99	Op 100	Op 101	Op 102	Op 103	Op 104	Op 105	Op 106	Op 107	Op 108	Op 109	Op 110	Op 111	Op 112	Op 113	Op 114	Op 115	Op 116	Op 117	Op 118	Op 119	Op 120	Op 121	Op 122	Op 123	Op 124	Op 125	Op 126	Op 127	Op 128	Op 129	Op 130	Op 131	Op 132	Op 133	Op 134	Op 135	Op 136	Op 137	Op 138	Op 139	Op 140	Op 141	Op 142	Op 143	Op 144	Op 145	Op 146	Op 147	Op 148	Op 149	Op 150	Op 151	Op 152	Op 153	Op 154	Op 155	Op 156	Op 157	Op 158	Op 159	Op 160	Op 161	Op 162	Op 163	Op 164	Op 165	Op 166	Op 167	Op 168	Op 169	Op 170	Op 171	Op 172	Op 173	Op 174	Op 175	Op 176	Op 177	Op 178	Op 179	Op 180	Op 181	Op 182	Op 183	Op 184	Op 185	Op 186	Op 187	Op 188	Op 189	Op 190	Op 191	Op 192	Op 193	Op 194	Op 195	Op 196	Op 197	Op 198	Op 199	Op 200	Op 201	Op 202	Op 203	Op 204	Op 205	Op 206	Op 207	Op 208	Op 209	Op 210	Op 211	Op 212	Op 213	Op 214	Op 215	Op 216	Op 217	Op 218	Op 219	Op 220	Op 221	Op 222	Op 223	Op 224	Op 225	Op 226	Op 227	Op 228	Op 229	Op 230	Op 231	Op 232	Op 233	Op 234	Op 235	Op 236	Op 237	Op 238	Op 239	Op 240	Op 241	Op 242	Op 243	Op 244	Op 245	Op 246	Op 247	Op 248	Op 249	Op 250	Op 251	Op 252	Op 253	Op 254	Op 255	Op 256	Op 257	Op 258	Op 259	Op 260	Op 261	Op 262	Op 263	Op 264	Op 265	Op 266	Op 267	Op 268	Op 269	Op 270	Op 271	Op 272	Op 273	Op 274	Op 275	Op 276	Op 277	Op 278	Op 279	Op 280	Op 281	Op 282	Op 283	Op 284	Op 285	Op 286	Op 287	Op 288	Op 289	Op 290	Op 291	Op 292	Op 293	Op 294	Op 295	Op 296	Op 297	Op 298	Op 299	Op 300	Op 301	Op 302	Op 303	Op 304	Op 305	Op 306	Op 307	Op 308	Op 309	Op 310	Op 311	Op 312	Op 313	Op 314	Op 315	Op 316	Op 317	Op 318	Op 319	Op 320	Op 321	Op 322	Op 323	Op 324	Op 325	Op 326	Op 327	Op 328	Op 329	Op 330	Op 331	Op 332	Op 333	Op 334	Op 335	Op 336	Op 337	Op 338	Op 339	Op 340	Op 341	Op 342	Op 343	Op 344	Op 345	Op 346	Op 347	Op 348	Op 349	Op 350	Op 351	Op 352	Op 353	Op 354	Op 355	Op 356	Op 357	Op 358	Op 359	Op 360	Op 361	Op 362	Op 363	Op 364	Op 365	Op 366	Op 367	Op 368	Op 369	Op 370	Op 371	Op 372	Op 373	Op 374	Op 375	Op 376	Op 377	Op 378	Op 379	Op 380</
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8723 .SBTTL  HARDWARE TEST          - SPLSPD -
8724 ;*****
8725 ;*
8726 ;*          - SPLIT SPEED TEST -
8727 ;*          THIS TEST IS USED TO VERIFY THE SPLIT SPEED CAPABILITIES OF THE DMU11,
8728 ;*          AND THE CORRECT OPERATION OF THE A & B BAUD RATE GROUP SELECTION.
8729 ;*          THE TEST USES THREE SETS OF BAUD RATES (38.4,50; 1200,75; 2000,2400).
8730 ;*          THIS TEST WILL ONLY EXECUTE IF THE STAGGERED LOOPBACK MODE IS SELECTED.
8731 ;*          THE SPECIAL STAGGERED LOOPBACK BERG CONNECTOR MUST BE FITTED.
8732 ;*
8733 ;*****
8733 036060 BGNTST
8734 036060                                T8::
8735 036066 126727 144110 000002          CMPB  LOPBCK,#2          ;CHECK MODE SELECTED.
8736 036070 001402                      BEQ    2$              ;DO NOT EXIT IF STAGGERD LOPBCK MODE SELECTED.
8737 036074 000167 000516                JMP    60$              ;EXIT THIS TEST.
8737 036074 012700 000240                2$:  SETPRI #PRI05        ;ALLOW LTC INTERRUPTS.
8738 036100 104441                      MOV    #PRI05,R0
8738 000010                      TRAP    C$SPRI
8739 036102 012767 000010 144150          TNUM == TNUM + 1      ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
8740 036110 012767 177777 144102          MOV    #TNUM,TSTNUM    ;SET UP THE TEST NUMBER. (92)
8741 036116 012767 000001 147170          MOV    #-1,CTRLCF      ;INDICATE THAT WE ARE IN A TEST.
8742 036124 012767 021761 147164          MOV    #1,ERRTYP      ;SET ERROR TYPE IN ERROR TABLE.
8743 036132 012767 012605 147160          MOV    #9201,ERRNBR     ;SET THE FIRST ERROR NUMBER IN ERROR TABLE.
8744 036140 005067 144334                MOV    #EM9201,ERRMSG    ;SET ERROR MESSAGE ADDRESS IN ERROR TABLE.
8745 036144 005067 144054                CLR    ERSRFR          ;INITIALIZE THE "REPORT ERROR SUMMARY" FLAGS.
8746                                CLR    FERROR              ;CLEAR THE "AT LEAST ONE ERROR" FLAG.
8747 ;*
8748 ; RESET THE DUT TO A KNOWN STATE, REMOVE STATUS CODES FROM THE FIFO.
8749 ; CLEAR TX AND RX INTERRUPT ENABLE BITS.
8750 ; THIS SUBROUTINE REPORTS ERROR >>>> 9201 <<<<.
8751 036150 004767 161254                JSR    PC,CLNRST        ;RESET THE DUT.
8752 036154 103402                      BCS    .+6
8753 036156 000167 000430                JMP    60$              ;ABORT THE TEST IF FATAL ERROR FOUND IN RESET.
8754 ;*
8755 ; DISABLE ALL INTERRUPTS.
8756 ; SET UP DMA TX AND RX INTERRUPT SERVICE ROUTINES.
8757 ;
8758 036162 SETPRI #PRI07                  ;DISABLE ALL INTERRUPTS.
8758 036162 012700 000340                                MOV    #PRI07,R0
8758 036166 104441                                TRAP    C$SPRI
8759 036170 SETVEC TXVECA,#TXDMA,#PRI06    ;SELECT DMA TX INT SERVICE RTN.
8759 036170 012746 000300                                MOV    #PRI06,-(SP)
8759 036174 012746 027520                                MOV    #TXDMA,-(SP)
8759 036200 016746 143764                                MOV    TXVECA,(SP)
8759 036204 012746 000003                                MOV    #3,-(SP)
8759 036210 104437                                TRAP    C$SVEC
8759 036212 062706 000010                                ADD    #10,SP
8760 036216 SETVEC RXVECA,#RXCHRS,#PRI06  ;SELECT RX INT SERVICE RTN.
8760 036216 012746 000300                                MOV    #PRI06,-(SP)
8760 036222 012746 027310                                MOV    #RXCHRS,-(SP)
8760 036226 016746 143734                                MOV    RXVECA,-(SP)
8760 036232 012746 000003                                MOV    #3,-(SP)
8760 036236 104437                                TRAP    C$SVEC
8760 036240 062706 000010                                ADD    #10,SP
8761 036244 SETPRI #PRI04                  ;ALLOW INTERRUPTS.
8761 036244 012700 000200                                MOV    #PRI04,R0

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036250 104441
8762
8763
8764
8765 036252 012705 177777
8766 036256 004767 167240
8767
8768
8769
8770
8771 036262 012700 003302
8772 036266 004767 161160
8773
8774
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8777
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8780
8781
8782
8783
8784 036272 012705 005100
8785 036276 012500
8786 036300 012501
8787 036302 004767 162322
8788 036306 012702 005154
8789 036312 012503
8790 036314 012504
8791 036316 004767 166454
8792 036322 012767 021762 146766
8793
8794
8795
8796 036330 004767 164470
8797 036334 103126
8798 036336 012767 021765 146752
8799
8800 036344 004767 164656
8801 036350 004767 162332
8802
8803
8804
8805 036354 004767 164702
8806 036360 005767 143640
8807 036364 001404
8808 036366 032767 000100 143566
8809 036374 001473
8810
8811 036376 012767 021773 146712 64:
8812
8813
8814
8815 036404 004767 167730
8816 036410 005767 143610
8817 036414 001404

;+
; ENABLE TRANSMITTERS ON ALL LINES.
;
; MOV #MAPLNS,R5 ;PASS ACTIVE LINE BIT MAP.
; JSR PC,TXENBL ;ENABLE TRANSMISSIONS ON ALL LINES.

;+
; CLEAR ERROR TABLE PRIOR TO PERFORMING TX/RX TEST.
;-
; MOV #ERCNTB,R0 ;GET THE BASE ADDRESS OF THE ERROR COUNTER TBL.
; JSR PC,CLR16W ;CLEAR THE RX ERROR COUNTERS TABLE.

;+
; PERFORM SPLIT SPEED DMA TX AND RX ON ALL SELECTED LINES AT THE FOLLOWING
; BAUD RATES.
; 38.4K, 50 ; 1200, 75 ; 2000, 2400.
;-
;+
; INITIALISE DMA TX/RX PARAMETERS IN THE CONTROL BLOCK FR EACH OF THE BAUD
; RATES MENTIONED ABOVE.
; 8 BITS/CHAR,1 STOP BITS,ODD PARITY.
;-
; MOV #SPLPRB,R5 ;GET BASE ADDRESS OF LPR PARAMETER TABLE.
44: ; MOV (R5)+,R0 ;GET LPR CONTENTS FOR LINGRP II.
; MOV (R5)+,R1 ;GET LPR CONTENTS FOR LINGRP I.
; JSR PC,GETTIM ;GET TIME-OUT BASED ON MINIMUM BAUDRATE IN USE.
; MOV #SDP2B,R2 ;SET UP THE START ADR OF THE DATA PATTERN.
; MOV (R5)+,R3 ;GET NUMBER OF REPEAT TRANSMISSION ON LINGRP II.
; MOV (R5)+,R4 ;GET NUMBER OF REPEAT TRANSMISSION ON LINGRP I.
; JSR PC,SPLSUP ;SET UP CONTROL BLOCK ETC. FOR TX/RX.
; MOV #9202,,ERRNBR ;SET THE ERROR NUMBER TO 9202.

;+
; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 9202 THRU 9204 <<<<<.
;-
; JSR PC,PURIFR ;PURGE THE DUT RECEIVE CHARACTER FIFO.
; BCC 60$ ;ABORT THIS TEST IF FIFO WOULD NOT PURGE.
; MOV #9205,,ERRNBR ;SET ERROR NUMBER TO 9205.

; JSR PC,PURRXB ;PURGE THE RX CHAR BUFFER IN MEMORY.
; JSR PC,INIDMA ;SEND THE FIRST BATCH OF DATA PATTERNS.

;+
; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 9205 THRU 9210 <<<<<.
;-
; JSR PC,RDCHRS ;READ AND VERIFY THE RX CHARACTERS.
; TST FERROR ;HAS AN ERROR BEEN DETECTED ?
; BEQ 6$ ;NO, THEN BRANCH.
; BIT #BIT06,OPTION ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
; BEQ 50$ ;NO, THEN EXIT THE TEST.

; MOV #9211,,ERRNBR ;SET THE ERROR NUMBER TO 9211.

;+
; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 9211 THRU 9214 <<<<<.
;-
; JSR PC,TXRREP ;REPORT FINAL ERRORS FROM RX/RX.
; TST FERROR ;HAS AN ERROR BEEN DETECTED ?
; BEQ 8$ ;NO, THEN BRANCH.

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8818 036416 032767 000100 143536      BIT    #BIT06.OPTION    ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
8819 036424 001457                      BEQ    50$              ;NO, THEN EXIT THE TEST.
8820
8821 036426 012767 021777 146662 8$:    MOV    #9215.,ERRNBR    ;SET ERROR NUMBER TO 9215.
8822
8823      ;+
8824      ; SWAP PARAMETERS TO ALLOW FOR BOTH CHANNELS TO BE EXERCISED.
8825 036434 010246                      MOV    R2,(SP)          ;PUSH THE START ADDRESS ONTO THE STACK.
8826 036436 010002                      MOV    R0,R2
8827 036440 010100                      MOV    R1,R0
8828 036442 010201                      MOV    R2,R1
8829 036444 010302                      MOV    R3,R2
8830 036446 010403                      MOV    R4,R3
8831 036450 010204                      MOV    R2,R4
8832 036452 012602                      MOV    (SP)+,R2
8833 036454 004767 166316              JSR    PC,SPLSUP      ;RESTORE THE START ADDRESS.
8834
8835      ;+
8836      ; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 9215 THRU 9217 <<<<<.
8837      ;-
8838 036460 004767 164340              JSR    PC,PUFIFR      ;PURGE THE DUT RECEIVE CHARACTER FIFO.
8839 036464 103052                      BCC    60$              ;ABORT THIS TEST IF FIFO WOULD NOT PURGE.
8840 036466 012767 022002 146622      MOV    #9218.,ERRNBR    ;SET ERROR NUMBER TO 9218.
8841
8842 036474 004767 164526              JSR    PC,PURRXB      ;PURGE THE RX CHAR BUFFER IN MEMORY.
8843 036500 004767 162202              JSR    PC,INIDMA     ;SEND THE FIRST BATCH OF DATA PATTERNS.
8844
8845      ;+
8846      ; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 9218 THRU 9223 <<<<<.
8847      ;-
8847 036504 004767 164552              JSR    PC,RDCHRS      ;READ AND VERIFY THE RX CHARACTERS.
8848 036510 005767 143510              TST    FERROR        ;HAS AN ERROR BEEN DETECTED ?
8849 036514 001404                      BEQ    10$             ;NO, THEN BRANCH.
8850 036516 032767 000100 143436      BIT    #BIT06.OPTION    ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
8851 036524 001417                      BEQ    50$              ;NO, THEN EXIT THE TEST.
8852
8853 036526 012767 022010 146562 10$:    MOV    #9224.,ERRNBR    ;SET ERROR NUMBER TO 9224.
8854
8855      ;+
8856      ; THIS ROUTINE REPORTS ERRORS WITH NUMBERS >>>>> 9224 THRU 9227 <<<<<.
8857      ;-
8857 036534 004767 167600              JSR    PC,TXRREP      ;REPORT FINAL ERRORS FROM RX/RX.
8858 036540 005767 143460              TST    FERROR        ;HAS AN ERROR BEEN DETECTED ?
8859 036544 001404                      BEQ    12$             ;NO, THEN BRANCH.
8860 036546 032767 000100 143406      BIT    #BIT06.OPTION    ;HAS EXTENDED ERROR REPORTING BEEN REQUESTED ?
8861 036554 001403                      BEQ    50$              ;NO, THEN EXIT THE TEST.
8862
8863 036556 020527 005130 12$:          CMP    R5,#SPLPRE      ;CHECK IF ALL PARAMETERS HAVE BEEN DONE.
8864 036562 103645                      BLO    4$              ;IF NOT DONE LOOP TO SELECT THE NEXT PARAMETER.
8865
8866      ;+
8867      ; DISABLE INTERRUPTS.
8868      ; CLEAR THE INTERRUPT VECTORS.
8869      ;-
8869 036564 012700 000340 50$:          SETPRI #PRI07          ;DISABLE ALL INTERRUPTS.
8870 036572 016700 143372              CLRVEC TXVECA      ;RETURN TX INT VECTOR TO UNUSED
                                MOV    #PRI07,R0
                                TRAP   C$SPRI
                                MOV    TXVECA,R0
                                TRAP   C$CVEC

```


I2

```
8871
8872 036600 012767 022014 146510      MOV    #9228.,ERRNBR ;SELECT NUMBER 9228 FOR THE NEXT ERROR REPORT.
8873 036606 004767 165310      JSR     PC,REPSMR  ;REPORT ERROR SUMMARIES IF CALLED FOR.
8874 036612 005067 143402      60$:   CLR     CTRLCF    ;INDICATE THAT WE ARE NOT WITHIN A TEST.
8875 036616                                     L10034:
      036616 104401                                     TRAP    C$ETST
```

```

8877 .SBTTL  HARDWARE TEST          - REPBM -
8878 ;+ *****
8879 ;*          - REPORT ANY BMP CODES IN THE QUEUE -
8880 ;*          THIS IS A PSEUDO-TEST USED TO REPORT ANY BMP CODES THAT WERE FOUND
8881 ;*          IN THE DUT'S FIFO DURING PREVIOUS TEST, AND LOGGED IN THE BMP CODE
8882 ;*          QUEUE.
8883 ;*          IT IS UNLIKELY THAT RUNNING THIS PSEUDO-TEST ALONE WILL PRODUCE ANY
8884 ;*          ERROR REPORTS.
8885 ;*
8886 ;-- *****
8887 036620 BGNTST
      036620
8888      TNUM == TNUM + 1          ;INCREMENT THE ASSEMBLY TIME TEST COUNTER.
8889 036620 012767 000011 143432  MOV  #TNUM,TSTNUM          ;SET UP THE TEST NUMBER. (93)
8890 036626 012767 177777 143364  MOV  #-1,CTRLCF          ;INDICATE THAT WE ARE IN A TEST.
8891 036634 016702 143650          MOV  BMPCQP,R2          ;GET THE CONTENTS OF THE POINTER.
8892 036640 012703 002512          MOV  #BMPCQB,R3          ;GET THE START ADDRESS OF THE QUEUE.
8893 036644 020203          CMP    R2,R3          ;SEE IF THE POINTER HAS MOVED FROM THE BASE.
8894 036646 001411          BEQ    60$          ;EXIT NO CODES IN THE QUEUE.
8895
8896 ;+
8897 ; THERE IS AT LEAST ONE BMP CODE IN THE QUEUE. REPORT THE ERROR.
8898 ;--
8899 ;REPORT ERROR BMP CODE FOUND IN TEST NN. BMP CODE:NNNNNN"
8900 036650 012701 013011          MOV  #EM9304,R1          ;PASS THE FIRST MESSAGE TO BE REORTED.
8901 036654          ERRDF  9301,EM9301,ER9301 ;
      036654 104455          >>>>> ERROR #9301 <<<<<.
      036656 022125          TRAP  C$ERDF
      036660 012635          .WORD  9301
      036662 015720          .WORD  EM9301
      .WORD  ER9301
8902
8903 036664 012767 002512 143616  MOV  #BMPCQB,BMPCQP          ;SET POINTER BACK TO THE BEGINING OF THE QUE.
8904
8905 036672 005067 143322 60$:    CLR  CTRLCF          ;INDICATE THAT WE ARE NOT WITHIN A TEST.
8906 036676          ENDTST
      036676 104401          L10035:
      TRAP  C$ETST
  
```

```

8909 ;*****
8910 ;
8911 ;           FVTC.HWQ
8912 ;
8913 ;*****
8915
8916
8917 .SBTTL  HARDWARE PARAMETER CODING SECTION
8918
8919
8920
8921 ;**
8922 ; THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
8923 ; THAT ARE USED BY THE SUPERVISOR TO BUILD P TABLES.  THE
8924 ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
8925 ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
8926 ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
8927 ; WITH THE OPERATOR.
8928 ;--
8929
8930          BGNHRD
8931
8932          036700      000027
8933          036700
8934          036702
8935
8936          ;DEVICE CSR ADDRESS QUESTION:
8937          GPRMA  HWPTQ1,0,0,160000,177776,YES
8938
8939          .WORD  L10036-L$HARD/2
8940          L$HARD::
8941
8942          036702      000031
8943          036704      036760
8944          036706      160000
8945          036710      177776
8946
8947          ;DEVICE INTERRUPT VECTOR QUESTION:
8948          GPRMA  HWPTQ2,2,0,40,776,YES
8949
8950          .WORD  T$CODE
8951          .WORD  HWPTQ1
8952          .WORD  T$LOLIM
8953          .WORD  T$HILIM
8954
8955          ;ACTIVE LINES BIT MAP QUESTION:
8956          GPRMD  HWPTQ3,4,0,MAPLNS,0,MAPLNS,YES
8957
8958          .WORD  T$CODE
8959          .WORD  HWPTQ2
8960          .WORD  T$LOLIM
8961          .WORD  T$HILIM
8962
8963          ;TYPE OF LOOPBACK QUESTION:
8964          GPRMD  HWPTQ4,6,0,377,1,5,YES
8965
8966          .WORD  T$CODE
8967          .WORD  HWPTQ3
8968          .WORD  MAPLNS
8969          .WORD  T$LOLIM
8970          .WORD  T$HILIM
8971
8972          ;INTERRUPT BR LEVEL QUESTION:
8973          GPRMD  HWPTQ5,6,0,177400,0,6,YES
8974
8975          .WORD  T$CODE
8976          .WORD  HWPTQ4
8977          .WORD  377
8978          .WORD  T$LOLIM
8979          .WORD  T$HILIM
8980
8981          036722      002032
8982          036724      037031
8983          036726      177777
8984          036730      000000
8985          036732      177777
8986
8987          036734      003032
8988          036736      037057
8989          036740      000377
8990          036742      000001
8991          036744      000005
8992
8993          036746      003032
8994          036750      037216
8995          036752      177400
8996          036754      000000
8997          036756      000006

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8951
8952
8953 036760                                ENDHRD

                                036760                                .EVEN
                                L10036:

8954
8961
8962 036760      103      123      122      HWPTQ1: .ASCIZ /CSR ADDRESS: /
      036763      040      101      104
      036766      104      122      105
      036771      123      123      072
      036774      040      000
8963 036776      111      116      124      HWPTQ2: .ASCIZ /INTERRUPT VECTOR ADDRESS: /
      037001      105      122      122
      037004      125      120      124
      037007      040      126      105
      037012      103      124      117
      037015      122      040      101
      037020      104      104      122
      037023      105      123      123
      037026      072      040      000
8964 037031      101      103      124      HWPTQ3: .ASCIZ /ACTIVE LINE BIT MAP: /
      037034      111      126      105
      037037      040      114      111
      037042      116      105      040
      037045      102      111      124
      037050      040      115      101
      037053      120      072      040
      037056      000
8965 037057      124      131      120      HWPTQ4: .ASCII /TYPE OF LOOPBACK (1=INTERNAL, 2=M3277, 3=M325/<15><12>
      037062      105      040      117
      037065      106      040      114
      037070      117      117      120
      037073      102      101      103
      037076      113      040      050
      037101      061      075      111
      037104      116      124      105
      037107      122      116      101
      037112      114      054      040
      037115      062      075      110
      037120      063      062      067
      037123      067      054      040
      037126      063      075      110
      037131      063      062      065
      037134      015      012
8966 037136      040      040      040      .ASCIZ /
      037141      040      040      040      4=MODEM, 5=KEYBOARD ECHO): /
      037144      040      040      040
      037147      040      040      040
      037152      040      040      040
      037155      040      040      040
      037160      040      040      064
      037163      075      115      117
      037166      104      105      115
      037171      054      040      065
      037174      075      113      105
      037177      131      102      117

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	037202	101	122	104
	037205	040	105	103
	037210	110	117	051
	037213	072	040	000
8967	037216	111	116	124
	037221	105	122	122
	037224	125	120	124
	037227	040	102	122
	037232	040	114	105
	037235	126	105	114
	037240	072	040	000

8968
8969

HWPTQ5: .ASCIZ /INTERRUPT BR LEVEL: /

.EVEN

```

8972      ;*****
8973      ;
8974      ;           FVTA.SWQ
8975      ;
8976      ;*****

8978
8979
8980      .SBTTL  SOFTWARE PARAMETER CODING SECTION
8981
8982      ;**
8983      ; THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
8984      ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
8985      ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
8986      ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
8987      ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
8988      ; WITH THE OPERATOR.
8989      ; -
8990
8991      BGNSFT
8992      037244      000017
8992      037244      000017
8992      037246
8992      L$SOFT:      .WORD L1003, _+SOFT/2
9001
9002      ;UNIT NUMBER PRINTOUT QUESTION:
9002      GPRML      SWPTQ1,0,20,YES
9002      037246      000130
9002      037250      037304
9002      037252      000020
9002      .WORD      T$CODE
9002      .WORD      SWPTQ1
9002      .WORD      20
9003
9004      ;REPORT NUMB OF BITS TESTED IN DMA ADDR TEST QUESTION:
9004      GPRML      SWPTQ2,0,40,YES
9004      037254      000130
9004      037256      037360
9004      037260      000040
9004      .WORD      T$CODE
9004      .WORD      SWPTQ2
9004      .WORD      40
9005
9006      ;EXTENDED ERROR REPORTING QUESTION:
9006      GPRML      SWPTQ3,0,100,YES
9006      037262      000130
9006      037264      037440
9006      037266      000100
9006      .WORD      T$CODE
9006      .WORD      SWPTQ3
9006      .WORD      100
9007
9008      ;*
9008      ; IF EXTENDED ERROR REPORTING IS NOT REQUIRED THEN SKIP THE NEXT QUESTION.
9009      ;*
9010      XFERF      ENDD
9010      037270
9010      037270      006044
9010      .WORD      T$CODE
9011
9012      ;NUMBER OF INDIVIDUAL DATA ERRORS TO REPORT ON A LINE QUESTION:
9012      GPRMD      SWPTQ4,2,D,177777,0,177777,YES
9012      037272      001052
9012      037274      037473
9012      037276      177777
9012      037300      000000
9012      037302      177777
9012      .WORD      T$CODE
9012      .WORD      SWPTQ4
9012      .WORD      177777
9012      .WORD      T$LOLIM
9012      .WORD      T$HILIM
9014
9015      .EVEN
9016      ENDD:      ENDSFT
9016      037304
9016      .EVEN
9016      L10037:
9017
9018

```

SOFTWARE PARAMETER CODING SECTION

SPU 234

9025	037304	122	105	120
	037307	117	122	124
	037312	040	125	116
	037315	111	124	040
	037320	116	125	115
	037323	102	105	122
	037326	040	101	123
	037331	040	105	101
	037334	103	110	040
	037337	125	116	111
	037342	124	040	111
	037345	123	040	124
	037350	105	123	124
	037353	105	104	072
	037356	040	000	
9026	037360	122	105	120
	037363	117	122	124
	037366	040	116	125
	037371	115	102	105
	037374	122	040	117
	037377	106	040	102
	037402	111	124	123
	037405	040	124	105
	037410	123	124	105
	037413	104	040	111
	037416	116	040	104
	037421	115	101	040
	037424	101	104	104
	037427	122	040	124
	037432	105	123	124
	037435	072	040	000
9027	037440	105	130	124
	037443	105	116	104
	037446	105	104	040
	037451	105	122	122
	037454	117	122	040
	037457	122	105	120
	037462	117	122	124
	037465	111	116	107
	037470	072	040	000
9028	037473	116	125	115
	037476	102	105	122
	037501	040	117	106
	037504	040	111	116
	037507	104	111	126
	037512	111	104	125
	037515	101	114	040
	037520	104	101	124
	037523	101	040	105
	037526	122	122	117
	037531	122	123	040
	037534	124	117	040
	037537	122	105	120
	037542	117	122	124
	037545	040	117	116
	037550	040	101	040
	037553	114	111	116

SWPTQ1: .ASCIIZ /REPORT UNIT NUMBER AS EACH UNIT IS TESTED: /

SWPTQ2: .ASCIIZ /REPORT NUMBER OF BITS TESTED IN DMA ADDR TEST: /

SWPTQ3: .ASCIIZ /EXTENDED ERROR REPORTING: /

SWPTQ4: .ASCIIZ /NUMBER OF INDIVIDUAL DATA ERRORS TO REPORT ON A LINE: /

037556	105	072	040
037561	000		

9029
9030

.EVEN


```

9032
9033      ;*****
9034      ;
9035      ;           FVTSKL6.P11
9036      ;
9037      ;*****
9038
9039
9040
9041 037562      $PATCH::
9042 037562      .BLKW    24
9043
9050
9051
9052
9053
9054 037632      LASTAD
9055      037632 000000      .EVEN
9056      037634 000000      .WORD    0
9057      037636      L$LAST::
9058      037636      ENDMOD      .WORD    0
9059
9060
9061
9062
9063      000001      .END

```

ACTLNS	002172	G	CHCNTB	003442	G	C#OPEN=	000034	EF9012	007001	G	EM9401	013065	G
ADDR	025312		CHKEXT	016410	G	C#PNTB=	000014	EF9013	007115	G	ENDD	037304	
ADR	000020	G	CHKLOS	016510	G	C#PNTF=	000017	EF9019	007162	G	ENDETB	004602	G
ADRPTR	017362	G	CHRTOT	002476	G	C#PNTS=	000016	EF9020	007201	G	ENDIT	030476	
ALTFLD	016112	G	CKCHR	016612	G	C#PNTX=	000015	EF9101	007262	G	ERCNTB	003302	G
ASSEMB=	000010		CKFRPR	017030	G	C#QIO =	000377	EF9103	007265	G	ERLTBL	003602	G
BCOUNT	002306	G	CKINAC	017242	G	C#RDBU=	000007	EF9301	007333	G	ERRBLK	005322	G
BDRMSG	013137	G	CKTRAP	017350	G	C#REFG=	000047	EF9302	007401	G	ERRMSG	005320	G
BITSTD	033766		CKTRPB	017400	G	C#RESE=	000033	EMLMSG	013166	G	ERRNBR	005316	G
BITTBL	002364	G	CLKBRL	002272	G	C#REVI=	000003	EM0101	022162	G	ERRTYP	005314	G
BIT0	000001	G	CLKCSR	002270	G	C#RFLA=	000021	EM0102	022246	G	ERSHWF	002500	G
BIT00	000001	G	CLKHRZ	002276	G	C#RPT =	000025	EM0103	010041	G	ER0101	013572	G
BIT01	000002	G	CLKINT	027240	G	C#SEFG=	000046	EM0509	010077	G	ER0503	014124	G
BIT02	000004	G	CLKVEC	002274	G	C#SPRI=	000041	EM1601	010103	G	ER1603	014162	G
BIT03	000010	G	CLNRST	017430	G	C#SVEC=	000037	EM4401	010166	G	ER6201	014254	G
BIT04	000020	G	CLR16W	017452	G	C#TPRI=	000013	EM4402	010216	G	ER9001	014512	G
BIT05	000040	G	CONMAP	017474	G	DELAY	017550	EM4403	010264	G	ER9002	014612	G
BIT06	000100	G	CSRA	002200	G	DFPTBL	002150	EM4404	010361	G	ER9003	014770	G
BIT07	000200	G	CSRO	000000	G	DIAGMC=	000000	EM4405	010420	G	ER9004	015162	G
BIT08	000400	G	CTRLCF	002220	G	DLPRTB	005072	EM4406	010514	G	ER9005	015276	G
BIT09	001000	G	C#AU =	000052		DLP RTE	005100	EM4407	010570	G	ER9101	015536	G
BIT1	000002	G	C#AUTO=	000061		DMRW	017662	EM4408	010652	G	ER9102	015576	G
BIT10	002000	G	C#BRK =	000022		DMTSTA	002222	EM4409	010715	G	ER9301	015720	G
BIT11	004000	G	C#BSEG=	000004		DM168	017610	EM4410	010752	G	EVL	000004	G
BIT12	010000	G	C#BSUB=	000002		DODMA	020004	EM4411	011001	G	EXCNTB	003242	G
BIT13	020000	G	C#CEFG=	000045		DPENDB	003142	EM5303	011046	G	EXTMSG	013235	G
BIT14	040000	G	C#CLCK=	000062		DPLENB	003202	EM6201	011117	G	E#END =	002100	
BIT15	100000	G	C#CLEA=	000012		DPRSQB	004642	EM6202	011151	G	E#LOAD=	000035	
BIT2	000004	G	C#CLOS=	000035		DPRSQE	005042	EM6301	011160	G	FDATA	002206	G
BIT3	000010	G	C#CLP1=	000006		DRADRT	002200	EM8901	011211	G	FDATA	000006	G
BIT4	000020	G	C#CVEC=	000036		DROP	030552	EM9003	011236	G	FERROR	002224	G
BIT5	000040	G	C#DCLN=	000044		DUMY	033770	EM9004	011260	G	FFREM	002226	G
BIT6	000100	G	C#DODU=	000051		EDPFMT	007733	EM9006	011276	G	FINACT	020074	G
BIT7	000200	G	C#DRPT=	000024		EDROP	003630	EM9007	011351	G	FRPSUP	020154	G
BIT8	000400	G	C#DU =	000053		EF.CON=	000036	EM9008	011434	G	FSLSA	002206	G
BIT9	001000	G	C#EDIT=	000003		EF.NEW=	000035	EM9009	011515	G	FSLSO =	000006	G
BMPQCB	002512	G	C#ERDF=	000055		EF.PWR=	000034	EM9010	011541	G	F#AU =	000015	
BMPQCE	002712	G	C#ERMR=	000056		EF.RES=	000037	EM9011	011565	G	F#AUTO=	000020	
BMPQCP	002510	G	C#ERRR=	000060		EF.STA=	000040	EM9012	011575	G	F#BGN =	000040	
BOE	000400	G	C#ERSF=	000054		EF0503	005453	EM9013	011605	G	F#CLEA=	000007	
BRLEVL	002175	G	C#ERSO=	000057		EF1601	005460	EM9014	011614	G	F#DU =	000016	
BRTBLB	002424	G	C#ESCA=	000010		EF1603	005512	EM9015	011710	G	F#END =	000041	
BRTBLE	002464	G	C#ESEG=	000005		EF4401	005554	EM9016	011724	G	F#HARD=	000004	
BUFBAS	003602	G	C#ESUB=	000003		EF6201	005671	EM9017	011733	G	F#HW =	000013	
BUFEND	004602	G	C#ETST=	000001		EF6202	006004	EM9025	012044	G	F#INIT=	000006	
BUF MID	004202	G	C#EXIT=	000032		EF6203	006102	EM9026	012140	G	F#JMP =	000050	
BUF3QT	004402	G	C#GETB=	000026		EF7801	006177	EM9027	012164	G	F#MOD =	000000	
CALMSL	016164	G	C#GETW=	000027		EF9001	006235	EM9028	012244	G	F#MSG =	000011	
CBB	003122	G	C#GMAN=	000043		EF9002	006317	EM9030	012323	G	F#PROT=	000021	
CBDPAA	003126	G	C#GPHR=	000042		EF9003	006371	EM9101	012400	G	F#PWR =	000017	
CBDPLA	003130	G	C#GPLO=	000030		EF9004	006420	EM9102	012442	G	F#RPT =	000012	
CBDPNA	003132	G	C#GPRI=	000040		EF9005	006450	EM9104	012531	G	F#SEG =	000003	
CBLNCA	003124	G	C#INIT=	000011		EF9006	006501	EM9201	012605	G	F#SOFT=	000005	
CBLPBA	003136	G	C#INLP=	000020		EF9007	006520	EM9301	012635	G	F#SRV =	000010	
CBLPRA	003122	G	C#MANI=	000050		EF9008	006614	EM9302	012714	G	F#SUB =	000002	
CBMAPA	003134	G	C#MEM =	000031		EF9009	006653	EM9303	012744	G	F#SW =	000014	
CBOFSA	003140	G	C#MSG =	000023		EF9010	006712	EM9304	013011	G	F#TEST=	000001	

GETBDR 020420 G	I\$TST = 000041	L\$RPT 027652 G	MSTICK 002310 G	PRI07 = 000340 G
GETCHR 020546 G	J\$JMP = 000167	L\$SOFT 037246 G	MUL16U 021532 G	PROTBL 005214 G
GETPRM 030270	LGRP1M 002236 G	L\$SPC 002056 G	NDERPT 002164 G	PRPARE 022450 G
GETTIM 020630 G	LGRP2M 002240 G	L\$SPCP 002020 G	NDPMMSG 013316 G	PRTLPR 022660 G
GMANWD 002230 G	LINBIT 021000 G	L\$SPTP 002024 G	NEWCHR 021606 G	PUFIFO 022742 G
GPRSOB 002464 G	LNCTRA 002210 G	L\$STA 002030 G	NEWPAS 030250	PUFIFR 023024 G
G\$CNT0= 000200	LNCTRO= 000010 G	L\$SW 002162 G	NEWRES 030242	PURRXB 023226 G
G\$DELM= 000372	LOE = 040000 G	L\$TEST 002114 G	NEWSTA 027732	RBUFA 002202 G
G\$DISP= 000003	LOPBC 002174 G	L\$TIML 002014 G	NUMLNS= 000020 G	RBUFO = 000002 G
G\$EXCP= 000400	LOT = 000010 G	L\$UNIT 002012 G	ODTSTA 033772	RDCHRS 023262 G
G\$HILI= 000002	LPCSLT= 000036 G	L10000 002160	OOPS 022116 G	RDMAST 023722 G
G\$LOLI= 000001	LPRA = 002204 G	L10001 002166	OPTION 002162 G	REPCOD 023762 G
G\$NO = 000000	LPRO = 000004 G	L10002 013706	O\$APTS= 000000	REPSMR 024122 G
G\$OFFS= 000400	L\$ACP 002110 G	L10003 014160	O\$AU = 000000	RESETT 024150 G
G\$OFSI= 000376	L\$APT 002036 G	L10004 014252	O\$BGNR= 000001	RRXNDN 024262 G
G\$PRMA= 000001	L\$AU 030636 G	L10005 014510	O\$BGNS= 000001	RTXNDN 024330 G
G\$PRMD= 000002	L\$AUT 002070 G	L10006 014610	O\$DU = 000001	RXCNT 002716 G
G\$PRML= 000000	L\$AUTO 030506 G	L10007 014766	O\$ERRT= 000001	RXBCTX= 000030 G
G\$RADA= 000140	L\$CCP 002106 G	L10010 015160	O\$GNSW= 000001	RXBEND 003120 G
G\$RAD8= 000000	L\$CLEA 030510 G	L10011 015274	O\$POIN= 000001	RXBETX= 000020 G
G\$RADD= 000040	L\$CO 002032 G	L10012 015534	O\$SETU= 000000	RXBFUL= 000100 G
G\$RADL= 000120	L\$DEPO 002011 G	L10013 015574	PARATB 002324 G	RXBPT 002714 G
G\$RADO= 000020	L\$DESC 005374 G	L10014 015716	PARATE 002344 G	RXBPT 002712 G
G\$XFER= 000004	L\$DESP 002076 G	L10015 016110	PAROA 002324 G	RXBSTA 002720 G
G\$YES = 000010	L\$DEVP 002060 G	L10016 027656	PAR1A 002326 G	RXCHRS 027310 G
HELP = 000000	L\$DISP 002124 G	L10020 030504	PAR2A 002330 G	RXCNTB 003542 G
HOE = 100000 G	L\$DLY 002116 G	L10021 030506	PAR3A 002332 G	RXDONF 002504 G
H\$PTQ1 036760	L\$DTP 002040 G	L10022 030524	PAR4A 002334 G	RXDSBL 024376 G
H\$PTQ2 036776	L\$DTYP 002034 G	L10023 030634	PAR5A 002336 G	RXENBL 024472 G
H\$PTQ3 037031	L\$DU 030526 G	L10024 030642	PAR6A 002340 G	RXIE0 024566 G
H\$PTQ4 037057	L\$DUT 002072 G	L10025 031124	PAR7A 002342 G	RXIE1 024626 G
H\$PTQ5 037216	L\$DVTY 005364 G	L10026 031366	PASCNT 002242 G	RXPTRB 003402 G
IBE = 010000 G	L\$EF 002052 G	L10027 032320	PCSLT = 000016 G	RXTIMO= 000002 G
IBM 002232 G	L\$ENVI 002044 G	L10030 033776	PDRATB 002344 G	RXTMA 002202 G
IDU = 000040 G	L\$ERRT 005314 G	L10031 034412	PDRATE 002364 G	RXTOUT 002246 G
IER = 020000 G	L\$ETP 002102 G	L10032 035070	PDR0A 002344 G	RXVECA 002166 G
IESTAT 002234 G	L\$EXP1 002046 G	L10033 036056	PDR1A 002346 G	R0SLOT= 000002 G
INIDMA 020706 G	L\$EXP4 002064 G	L10034 036616	PDR2A 002350 G	R1SLOT= 000004 G
ISR = 000100 G	L\$EXP5 002066 G	L10035 036676	PDR3A 002352 G	R2SLOT= 000006 G
IXE = 004000 G	L\$HARD 036702 G	L10036 036760	PDR4A 002354 G	R3SLOT= 000010 G
I\$AU = 000041	L\$HIME 002120 G	L10037 037304	PDR5A 002356 G	R4SLOT= 000012 G
I\$AUTO= 000041	L\$HPCP 002016 G	MAPLNS= 177777 G	PDR6A 002360 G	R5SLOT= 000014 G
I\$CLN = 000041	L\$HPTP 002022 G	MFUNIT 005422 G	PDR7A 002362 G	SAVBMP 024652 G
I\$DU = 000041	L\$HM 002150 G	MMENAB 002322 G	PMSFLG 002244 G	SAVPRI 002250 G
I\$HRD = 000041	L\$ICP 002104 G	MPRES 002320 G	PMSMSG 013444 G	SAVTEN 002252 G
I\$INIT= 000041	L\$INIT 027666 G	MMSRO 002314 G	PNT = 001000 G	SCBCTB 005042 G
I\$MOD = 000041	L\$LADP 002026 G	MMSR3 002316 G	PREGRT 005346 G	SCBCTE 005052 G
I\$MSG = 000041	L\$LAST 037636 G	MODSUP 021026 G	PREG05 005324	SCBRTB 005052 G
I\$PROT= 000040	L\$LOAD 002100 G	MSFMT1 007577 G	PRFRME 022344 G	SCBRTB 005060 G
I\$PTAB= 000041	L\$LUN 002074 G	MSFMT2 007637 G	PRI = 002000 G	SCNSTB 005060 G
I\$PWR = 000041	L\$MREV 002050 G	MSG1 013710 G	PRI00 = 000000 G	SCNSTE 005064 G
I\$RPT = 000041	L\$NAME 002000 G	MSG2 013766 G	PRI01 = 000040 G	SCTPTB 005064 G
I\$SEG = 000041	L\$PRIO 002042 G	MSG3 014045 G	PRI02 = 000100 G	SCTPTE 005072 G
I\$SETU= 000041	L\$PROT 027660 G	MSLCNT 002312 G	PRI03 = 000140 G	SDPBAS 005130 G
I\$SFT = 000041	L\$PRT 002112 G	MSLGET 021166 G	PRI04 = 000200 G	SDPEND 005150 G
I\$SRV = 000041	L\$REPP 002062 G	MSLOOP 021302 G	PRI05 = 000240 G	SDP2B 005154 G
I\$SUB = 000041	L\$REV 002010 G	MSSRPT 021316 G	PRI06 = 000300 G	SDP2E 005174 G

SFPTBL 002162 G	TP4FLG 002254 G	TXPTRB 003342 G	T\$SAVL = 177777	T2 031126 G
SKPSTS 024720 G	TP4RTN 027476 G	TXRINI 025776 G	T\$SEGL = 177777	T3 031370 G
SPLPRB 005100 G	TP4VEC 002256 G	TXROFF 026252 G	T\$SUBN = 000000	T4 032322 G
SPLPRE 005130 G	TRPAD2 017412 G	TXRON 026312 G	T\$TAGL = 177777	T5 034000 G
SPLSUP 024776 G	TSTNUM 002260 G	TXRREP 026340 G	T\$TAGN = 010040	T6 034414 G
STGTRB 005274 G	TXA1J1A 002212 G	TXRXLB 005234 G	T\$TEMP = 000000	T7 035072 G
STPSW 025276 G	TXA1J10 = 000012 G	TXRXLE 005274 G	T\$TEST = 000011	T8 036060 G
SUCSS 033774	TXAD2A 002214 G	TXVECA 002170 G	T\$TSTM = 177777	T9 036620 G
SVCGBL = 000000	TXAD20 = 000014 G	T\$ARGC = 000003	T\$TSTS = 000001	UAM = 000200 G
SVCINS = 000001	TXBFCA 002216 G	T\$CODE = 001052	T\$AU = 010024	UBRFMT 007501 G
SVCSUB = 000001	TXBFCA = 000016 G	T\$ERRN = 022125	T\$AUT = 010021	UNITN 002176 G
SVCTAG = 000001	TXCNTB 003502 G	T\$EXCP = 000000	T\$CLE = 010022	UNSDIV 026454 G
SVCTST = 000001	TXDBLF 002506 G	T\$FLAG = 000040	T\$DU = 010023	UPDCHR 026610 G
SWPTQ1 037304	TXDMA 027520 G	T\$GMAN = 000000	T\$HAR = 010036	VANSUP 026706 G
SWPTQ2 037360	TXDONE 025316 G	T\$HILI = 177777	T\$HW = 010000	WAIBIS 027104 G
SWPTQ3 037440	TXDONF 002502 G	T\$LAST = 000001	T\$INI = 010020	WORD1 002266 G
SWPTQ4 037473	TXDSBL 025426 G	T\$LOLI = 000000	T\$MSG = 010015	WTWLNC 027160 G
S\$LSYM = 010000	TXENBL 025522 G	T\$LSYM = 010000	T\$PRO = 010017	WTWLPR 027210 G
TERMSG 013531 G	TXENBM 002262 G	T\$LTNO = 000011	T\$RPT = 010016	X\$ALWA = 000000
TIMER1 002300 G	TXFRPR 025616 G	T\$NEST = 177777	T\$SOF = 010037	X\$FALS = 000040
TIMER2 002302 G	TXIE0 025712 G	T\$NS0 = 000000	T\$SW = 010001	X\$OFFS = 000400
TIMER3 002304 G	TXIE1 025752 G	T\$NS1 = 000005	T\$TES = 010035	X\$TRUE = 000020
TNUM = 000011 G	TXINTF 002264 G	T\$PTNU = 000000	T1 030644 G	\$PATCH 037562 G
TP4BRT 027454 G				

. ABS. 037636 000
000000 001

ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 30240 WORDS (119 PAGES)

DYNAMIC MEMORY: 20060 WORDS (77 PAGES)

ELAPSED TIME: 00:03:59

PARTC.BIN,PARTC.LST/-SP=SVC34R/ML,PARTC.P11