

11/21+
TSV05

TSV05 CTRL LT1
CNTSRA0

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JUL 1984
digital
Made In USA

11/21+
TSV05

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IDENTIFICATION

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TABLE OF CONTENTS

1.0	GENERAL INFORMATION
1.1	PROGRAM ABSTRACT
1.2	SYSTEM REQUIREMENTS
1.3	RELATED DOCUMENTS AND STANDARDS
1.4	DIAGNOSTIC HIERARCHY PREREQUISITES
1.5	ASSUMPTIONS
2.0	OPERATING INSTRUCTIONS
2.1	COMMANDS
2.2	SWITCHES
2.3	FLAGS
2.4	HARDWARE QUESTIONS
2.5	SOFTWARE QUESTIONS
2.6	EXTENDED P-TABLE DIALOGUE
2.7	QUICK STARTUP PROCEDURE
3.0	ERROR INFORMATION
4.0	PERFORMANCE AND PROGRESS REPORTS
5.0	DEVICE INFORMATION TABLES
6.0	TEST SUMMARIES
7.0	MAINTENANCE HISTORY

1.0 GENERAL INFORMATION

1.1 PROGRAM ABSTRACT

THIS IS A SBC-11/21+ RESIDENT DIAGNOSTIC WHICH CHECKS THE FUNCTIONALITY OF A TSV05 MAGTAPE SUBSYSTEM WHILE CONNECTED TO A SBC-11/21+ SYSTEM (Q BUS). THE PROGRAM PROVIDES ERROR MESSAGES WHICH IDENTIFY FAILING FUNCTIONS THAT AID IN THE REPAIR OF THE DEVICE. THIS DIAGNOSTIC CONSISTS OF ELEVEN TEST WHICH ARE EXECUTED IN SEQUENCE.

THIS DIAGNOSTIC HAS BEEN WRITTEN FOR USE WITH THE DIAGNOSTIC RUNTIME SERVICES SOFTWARE (SUPERVISOR). THESE SERVICES PROVIDE THE INTERFACE TO THE OPERATOR AND TO THE SOFTWARE ENVIRONMENT. THIS PROGRAM CAN BE USED WITH XXDP+, ACT, APT, SLIDE AND PAPER TAPE. FOR A COMPLETE DESCRIPTION OF THE RUNTIME SERVICES, REFER TO THE XXDP+ USER'S MANUAL. THERE IS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES IN SECTION 2 OF THIS DOCUMENT.

1.2 SYSTEM REQUIREMENTS

SBC-11/21+ PROCESSOR AND MEMORY
CAUTION: DIAGNOSTIC REQUIRES 32K WORDS OF MEMORY
(28K USEABLE I.E. 4K FOR I/O PAGE)
TSV05 MAGTAPE SUBSYSTEM (DRIVE AND CONTROLLER)
CONSOLE TERMINAL
PDP 11 DIAGNOSTIC SUPERVISOR (MSAAA.SYS VERSION 34 OR LATER)
PDP 11 DIAGNOSTIC LOADER/MONITOR (XXDP+)

1.3 RELATED DOCUMENTS AND STANDARDS

DIGITAL EQUIPMENT CORPORATION DOCUMENTS:

1. XXDP+ USERS GUIDE
2. TSV05 TRANSPORT SUBSYSTEM USER'S GUIDE
3. TSV05 TRANSPORT SUBSYSTEM TECHNICAL MANUAL
4. TSV05 TRANSPORT SUBSYSTEM INSTALLATION MANUAL

1.4 DIAGNOSTIC HIERARCHY PREREQUISITES

FUNCTIONAL SBC-11/21+ CENTRAL PROCESSOR AND MEMORY
FUNCTIONAL CONSOLE TERMINAL
FUNCTIONAL STANDALONE DIAGNOSTIC SUPERVISOR
FUNCTIONAL DIAGNOSTIC LOADER/MONITOR (XXDP+)

1.5 ASSUMPTIONS

ALL HARDWARE EXCEPT THE HARDWARE UNDER TEST IS ASSUMED TO WORK

PROPERLY OR FALSE ERRORS CAN BE REPORTED.
THE TAPE BEING USED ON THE TSV05 TRANSPORT IS A KNOWN GOOD REEL
OF TAPE.

2.0 OPERATING INSTRUCTIONS

THIS SECTION CONTAINS A BRIEF DESCRIPTION OF THE RUNTIME SERVICES.
FOR DETAILED INFORMATION, REFER TO THE XXDP+ USER'S MANUAL.

2.1 COMMANDS

THERE ARE ELEVEN LEGAL COMMANDS FOR THE DIAGNOSTIC RUNTIME SERVICES
(SUPERVISOR). THIS SECTION LISTS THE COMMANDS AND GIVES A VERY
BRIEF DESCRIPTION OF THEM. THE XXDP+ USER'S MANUAL HAS MORE DETAILS.

COMMAND	EFFECT
-----	-----
START	START THE DIAGNOSTIC FROM AN INITIAL STATE
RESTART	START THE DIAGNOSTIC WITHOUT INITIALIZING
CONTINUE	CONTINUE AT TEST THAT WAS INTERRUPTED (AFTER ↑C)
PROCEED	CONTINUE FROM AN ERROR HALT
EXIT	RETURN TO XXDP+ MONITOR (XXDP+ OPERATION ONLY!)
ADD	ACTIVATE A UNIT FOR TESTING (ALL UNITS ARE CONSIDERED TO BE ACTIVE AT START TIME)
DROP	DEACTIVATE A UNIT
PRINT	PRINT STATISTICAL INFORMATION (IF IMPLEMENTED BY THE DIAGNOSTIC - SECTION 4.0)
DISPLAY	TYPE A LIST OF ALL DEVICE INFORMATION
FLAGS	TYPE THE STATE OF ALL FLAGS (SEE SECTION 2.3)
ZFLAGS	CLEAR ALL FLAGS (SEE SECTION 2.3)

A COMMAND CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. SO
YOU MAY, FOR EXAMPLE, TYPE "STA" INSTEAD OF "START".

2.1.1 OPERATOR COMMANDS

THE TSV05 DIAGNOSTIC IS A SBC-11/21+ DIAGNOSTIC SUPERVISOR COMPATIBLE
PROGRAM. ALL LOADING AND RUNTIME INSTRUCTIONS CAN BE REFERENCED IN THE
XXDP+ USERS GUIDE. THE USER ENTRY IS IN QUOTES.

BOOT THE DIAGNOSTIC MEDIA

```
.R NTSA??
DIAG. RUN-TIME SERVICES REV D. APR 79
CNTSA-A-0
****TSV05 LOGIC DIAGNOSTIC****
UNIT IS TSV05
>DR
```

2.2 SWITCHES

THERE ARE SEVERAL SWITCHES WHICH ARE USED TO MODIFY SUPERVISOR OPERATION.

THESE SWITCHES ARE APPENDED TO THE LEGAL COMMANDS. ALL OF THE LEGAL SWITCHES ARE TABULATED BELOW WITH A BRIEF DESCRIPTION OF EACH. IN THE DESCRIPTIONS BELOW, A DECIMAL NUMBER IS DESIGNATED BY "DDDDD".

SWITCH	EFFECT
/TESTS:LIST	EXECUTE ONLY THOSE TESTS SPECIFIED IN THE LIST. LIST IS A STRING OF TEST NUMBERS, FOR EXAMPLE - /TESTS:1:5:7 10. THIS LIST WILL CAUSE TESTS 1,5,7,8,9,10 TO BE RUN. ALL OTHER TESTS WILL NOT BE RUN.
/PASS:DDDDD	EXECUTE DDDDD PASSES (DDDDD = 1 TO 64000)
/FLAGS:FLGS	SET SPECIFIED FLAGS. FLAGS ARE DESCRIBED IN SECTION 2.3.
/EOP:DDDDD	REPORT END OF PASS MESSAGE AFTER EVERY DDDDD PASSES ONLY. (DDDDD = 1 TO 64000)
/UNITS:LIST	TEST/ADD/DROP ONLY THOSE UNITS SPECIFIED IN THE LIST. LIST EXAMPLE - /UNITS:0:5:10-12 USE UNITS 0,5,10,11,12 (UNIT NUMBERS = 0-63)

EXAMPLE OF SWITCH USAGE:

START/TESTS:1-5/PASS:1000/EOP:100

THE EFFECT OF THIS COMMAND WILL BE: 1) TESTS 1 THROUGH 5 WILL BE EXECUTED, 2) ALL UNITS WILL TESTED 1000 TIMES AND 3) THE END OF PASS MESSAGES WILL BE PRINTED AFTER EACH 100 PASSES ONLY. A SWITCH CAN BE RECOGNIZED BY THE FIRST THREE CHARACTERS. YOU MAY, FOR EXAMPLE, TYPE "/TES:1-5" INSTEAD OF "/TESTS:1-5".

BELOW IS A TABLE THAT SPECIFIES WHICH SWITCHES CAN BE USED BY EACH COMMAND.

	TESTS	PASS	FLAGS	EOP	UNITS
START	X	X	X	X	X
RESTART	X	X	X	X	X
CONTINUE		X	X	X	
PROCEED			X		
DROP					X
ADD					X
PRINT					
DISPLAY					X
FLAGS					
ZFLAGS					
EXIT					

2.3 FLAGS

FLAGS ARE USED TO SET UP CERTAIN OPERATIONAL PARAMETERS SUCH AS LOOPING ON ERROR. ALL FLAGS ARE CLEARED AT STARTUP AND REMAIN CLEARED UNTIL EXPLICITLY SET USING THE FLAGS SWITCH. FLAGS ARE ALSO CLEARED AFTER A START COMMAND UNLESS SET USING THE FLAG SWITCH. THE ZFLAGS COMMAND MAY ALSO BE USED TO CLEAR ALL FLAGS. WITH THE EXCEPTION OF THE START AND ZFLAGS COMMANDS, NO COMMANDS AFFECT THE STATE OF THE FLAGS; THEY REMAIN SET OR

CLEARED AS SPECIFIED BY THE LAST FLAG SWITCH.

FLAG	EFFECT
MOE	HALT ON ERROR - CONTROL IS RETURNED TO RUNTIME SERVICES COMMAND MODE
LOE	LOOP ON ERROR
IER*	INHIBIT ALL ERROR REPORTS
IBR*	INHIBIT ALL ERROR REPORTS EXCEPT FIRST LEVEL (FIRST LEVEL CONTAINS ERROR TYPE, NUMBER, PC, TEST AND UNIT)
IXE*	INHIBIT EXTENDED ERROR REPORTS (THOSE CALLED BY PRINTX MACRO'S)
PRI	DIRECT MESSAGES TO LINE PRINTER
PNT	PRINT TEST NUMBER AS TEST EXECUTES
BOE	"BELL" ON ERROR
UAM	UNATTENDED MODE (NO MANUAL INTERVENTION)
ISR	INHIBIT STATISTICAL REPORTS (DOES NOT APPLY TO DIAGNOSTICS WHICH DO NOT SUPPORT STATISTICAL REPORTING)
IDR	INHIBIT PROGRAM DROPPING OF UNITS
ADR	EXECUTE AUTODROP CODE
LOT	LOOP ON TEST

*ERROR MESSAGES ARE DESCRIBED IN SECTION 3.1

SEE THE XXDP* USER'S MANUAL FOR MORE DETAILS ON FLAGS. YOU MAY SPECIFY MORE THAN ONE FLAG WITH THE FLAG SWITCH. FOR EXAMPLE, TO CAUSE THE PROGRAM TO LOOP ON ERROR, INHIBIT ERROR REPORTS AND TYPE A "BELL" ON ERROR, YOU MAY USE THE FOLLOWING STRING:

```
/FLAGS:LOE:IER:BOE
```

2.4 HARDWARE QUESTIONS

WHEN A DIAGNOSTIC IS STARTED, THE RUNTIME SERVICES WILL PROMPT THE USER FOR HARDWARE INFORMATION BY TYPING "CHANGE HW (L) ?" YOU MUST ANSWER "Y" AFTER A START COMMAND UNLESS THE HARDWARE INFORMATION HAS BEEN "PRELOADED" USING THE SETUP UTILITY (SEE CHAPTER 14 OF THE XXDP* USER'S MANUAL). WHEN YOU ANSWER THIS QUESTION WITH A "Y", THE RUNTIME SERVICES WILL ASK FOR THE NUMBER OF UNITS (IN DECIMAL).

AFTER INITIAL STARTING OF THE PROGRAM (START COMMAND TO THE DIAGNOSTIC SUPERVISOR), THE PROGRAM WILL ISSUE THE "CHANGE HW?" QUESTION TO ASK IF THE HARDWARE PARAMETERS ARE TO BE CHANGED (BY THE OPERATOR).

ON A "N" (NO) RESPONSE TO THE "CHANGE HW?" QUESTION, THE DIAGNOSTIC WILL RUN USING THE DEFAULT VALUES FOR ALL QUESTIONS. THE DEFAULT ADDRESS AND VECTOR ARE:

```
TSBA/TSDB = 176000, VECTOR = 224
```

ON A "Y" (YES) RESPONSE TO THE QUESTION, THE FOLLOWING QUESTIONS WILL THEN BE ASKED TO ALLOW THE OPERATOR TO SELECT THE UNITS TO BE TESTED. A VALUE, IF PRESENT, LOCATED TO THE LEFT OF THE QUESTION MARK IS THE DEFAULT VALUE THAT WILL BE TAKEN IF ONLY A CARRIAGE RETURN IS TYPED AS A RESPONSE. A "(D)" IN A QUESTION INDICATES THAT A DECIMAL NUMBER IS REQUIRED AS A RESPONSE. AN "(O)" INDICATES AN OCTAL NUMBER IS BEING SOLICITED. AN "(L)" INDICATES THAT A LOGICAL RESPONSE IS TO BE MADE: "Y" FOR YES, "N" FOR NO.

* UNITS (D) ? <ENTER THE NUMBER OF M7196 CONTROLLERS
PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (O) 176000 ? <ENTER THE ADDRESS OF THE
TSBA/TSDB REGISTER>

VECTOR (O) 224 ? <ENTER ADDRESS OF INTERRUPT
VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "* UNITS?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER, BEGINNING AT 0. UP TO FOUR UNITS CAN BE SELECTED FOR TESTING AS FOLLOWS:
UP TO 4 TSV05 CONTROLLERS PER 11/23 AND UP TO 2 DRIVES PER CONTROLLER

2.5 SOFTWARE QUESTIONS

AFTER YOU HAVE ANSWERED THE HARDWARE QUESTIONS OR AFTER A RESTART OR CONTINUE COMMAND, THE RUNTIME SERVICES WILL ASK FOR SOFTWARE PARAMETERS. THESE PARAMETERS WILL GOVERN SOME DIAGNOSTIC SPECIFIC OPERATION MODES. YOU WILL BE PROMPTED BY "CHANGE SW (L) ?" IF YOU WISH TO CHANGE ANY PARAMETERS, ANSWER BY TYPING "Y". THE SOFTWARE QUESTIONS AND THE DEFAULT VALUES ARE DESCRIBED IN THE NEXT PARAGRAPH(S).

THE FOLLOWING QUESTIONS ARE ASKED ON A START, RESTART, OR CONTINUE. THEY ALLOW FLEXIBILITY IN THE WAY THE PROGRAM BEHAVES.

CHANGE SW (L) ? <TYPE Y TO CAUSE THE FOLLOWING
QUESTIONS TO BE ASKED>

INHIBIT ITERATIONS (L) N ? <TYPE "Y" TO PREVENT MULTIPLE
ITERATIONS OF CERTAIN TESTS.
THIS CAUSES EACH TEST PASS TO
RUN AS QUICKLY AS POSSIBLE.
ONLY QUICK-RUNNING LOGIC
TESTS USE MULTIPLE
ITERATIONS.>

2.6 EXTENDED P-TABLE DIALOGUE

WHEN YOU ANSWER THE HARDWARE QUESTIONS, YOU ARE BUILDING ENTRIES IN A TABLE THAT DESCRIBES THE DEVICES UNDER TEST. THE SIMPLEST WAY TO BUILD THIS TABLE IS TO ANSWER ALL QUESTIONS FOR EACH UNIT TO BE TESTED. IF YOU HAVE A MULTIPLEXED DEVICE SUCH AS A MASS STORAGE CONTROLLER WITH SEVERAL DRIVES OR A COMMUNICATION

DEVICE WITH SEVERAL LINES, THIS BECOMES TEDIOUS SINCE MOST OF THE ANSWERS ARE REPETITIOUS.

TO ILLUSTRATE A MORE EFFICIENT METHOD, SUPPOSE YOU ARE TESTING A DEVICE, THE XY11. SUPPOSE THIS DEVICE CONSISTS OF A CONTROL MODULE WITH EIGHT UNITS (SUB-DEVICES) ATTACHED TO IT. THESE UNITS ARE DESCRIBED BY THE OCTAL NUMBERS 0 THROUGH 7. THERE IS ONE HARDWARE PARAMETER THAT CAN VARY AMONG UNITS CALLED THE Q-FACTOR. THIS Q-FACTOR MAY BE 0 OR 1. BELOW IS A SIMPLE WAY TO BUILD A TABLE FOR ONE XY11 WITH EIGHT UNITS.

* UNITS (0) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0<CR>
Q-FACTOR (0) 0 ? 1<CR>

UNIT 2
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 1<CR>
Q-FACTOR (0) 1 ? 0<CR>

UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 4
CSR ADDRESS (0) ? 160000<CR>
SUB DEVICE # (0) ? 3<CR>
Q FACTOR (0) 0 ? <CR>

UNIT 5
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 4<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 6
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 5<CR>
Q-FACTOR (0) 0 ? <CR>

UNIT 7
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 6<CR>
Q FACTOR (0) 0 ? 1<CR>

UNIT 8
CSR ADDRESS (0) 160000<CR>
SUB DEVICE # (0) ? 7<CR>
Q FACTOR (0) 1 ? <CR>

NOTICE THAT THE DEFAULT VALUE FOR THE Q-FACTOR CHANGES WHEN A NON DEFAULT RESPONSE IS GIVEN. BE CAREFUL WHEN SPECIFYING MULTIPLE UNITS!

AS YOU CAN SEE FROM THE ABOVE EXAMPLE, THE HARDWARE PARAMETERS DO NOT VARY SIGNIFICANTLY FROM UNIT TO UNIT. THE PROCEDURE SHOWN IS NOT VERY EFFICIENT.

THE RUNTIME SERVICES CAN TAKE MULTIPLE UNIT SPECIFICATIONS HOWEVER. LET'S BUILD THE SAME TABLE USING THE MULTIPLE SPECIFICATION FEATURE.

```
* UNITS (D) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0,1<CR>
Q-FACTOR (0) 0 ? 1,0<CR>

UNIT 3
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 2-5<CR>
Q FACTOR (0) 0 ? 0<CR>

UNIT 7
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 6,7<CR>
Q-FACTOR (0) 0 ? 1<CR>
```

AS YOU CAN SEE IN THE ABOVE DIALOGUE, THE RUNTIME SERVICES WILL BUILD AS MANY ENTRIES AS IT CAN WITH THE INFORMATION GIVEN IN ANY ONE PASS THROUGH THE QUESTIONS. IN THE FIRST PASS, TWO ENTRIES ARE BUILT SINCE TWO SUB-DEVICES AND Q-FACTORS WERE SPECIFIED. THE SERVICES ASSUME THAT THE CSR ADDRESS IS 160000 FOR BOTH SINCE IT WAS SPECIFIED ONLY ONCE. IN THE SECOND PASS, FOUR ENTRIES WERE BUILT. THIS IS BECAUSE FOUR SUB-DEVICES WERE SPECIFIED. THE "-" CONSTRUCT TELLS THE RUNTIME SERVICES TO INCREMENT THE DATA FROM THE FIRST NUMBER TO THE SECOND. IN THIS CASE, SUB-DEVICES 2, 3, 4 AND 5 WERE SPECIFIED. (IF THE SUB-DEVICE WERE SPECIFIED BY ADDRESSES, THE INCREMENT WOULD BE BY 2 SINCE ADDRESSES MUST BE ON AN EVEN BOUNDARY.) THE CSR ADDRESSES AND Q-FACTORS FOR THE FOUR ENTRIES ARE ASSUMED TO BE 160000 AND 0 RESPECTIVELY SINCE THEY WERE ONLY SPECIFIED ONCE. THE LAST TWO UNITS ARE SPECIFIED IN THE THIRD PASS.

THE WHOLE PROCESS COULD HAVE BEEN ACCOMPLISHED IN ONE PASS AS SHOWN BELOW.

```
* UNITS (D) ? 8<CR>

UNIT 1
CSR ADDRESS (0) ? 160000<CR>
SUB-DEVICE # (0) ? 0 7<CR>
Q-FACTOR (0) 0 ? 0,1,0,...,1,1<CR>
```

AS YOU CAN SEE FROM THIS EXAMPLE, NULL REPLIES (COMMAS ENCLOSING A NULL FIELD) TELL THE RUNTIME SERVICES TO REPEAT THE LAST REPLY.

2.7 QUICK START-UP PROCEDURE (XXDP+)

TO START UP THIS PROGRAM:

1. BOOT XXDP.
2. TYPE "R NAME", WHERE NAME IS THE NAME OF THE BIN OR BIC FILE FOR THIS PROGRAM
3. TYPE "START"
4. ANSWER THE "CHANGE HW" QUESTION WITH 'Y'
5. ANSWER ALL THE HARDWARE QUESTIONS
6. ANSWER THE "CHANGE SW" QUESTION WITH "N"

WHEN YOU FOLLOW THIS PROCEDURE YOU WILL BE USING ONLY THE DEFAULTS FOR FLAGS AND SOFTWARE PARAMETERS. THESE DEFAULTS ARE DESCRIBED IN SECTIONS 2.3 AND 2.5.

3.0 ERROR INFORMATION

3.1 TYPES OF ERROR MESSAGES

THERE ARE THREE LEVELS OF ERROR MESSAGES THAT MAY BE ISSUED BY A DIAGNOSTIC: GENERAL, BASIC AND EXTENDED. GENERAL ERROR MESSAGES ARE ALWAYS PRINTED UNLESS THE "IER" FLAG IS SET (SECTION 2.3). THE GENERAL ERROR MESSAGE IS OF THE FORM:

NAME TYPE NUMBER ON UNIT NUMBER TST NUMBER PC:XXXXXX
ERROR MESSAGE

WHERE: NAME = DIAGNOSTIC NAME
TYPE = ERROR TYPE (SYS FATAL, DEV FATAL, HARD OR SOFT)
NUMBER = ERROR NUMBER
UNIT NUMBER = 0 N (N IS LAST UNIT IN PTABLE)
TST NUMBER = TEST AND SUBTEST WHERE ERROR OCCURRED
PC:XXXXXX = ADDRESS OF ERROR MESSAGE CALL

BASIC ERROR MESSAGES ARE MESSAGES THAT CONTAIN SOME ADDITIONAL INFORMATION ABOUT THE ERROR. THESE ARE ALWAYS PRINTED UNLESS THE "IER" OR "IBR" FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL MESSAGE.

EXTENDED ERROR MESSAGES CONTAIN SUPPLEMENTARY ERROR INFORMATION SUCH AS REGISTER CONTENTS OR GOOD/BAD DATA. THESE ARE ALWAYS PRINTED UNLESS THE "IER", "IBR" OR "IXE" FLAGS ARE SET (SECTION 2.3). THESE MESSAGES ARE PRINTED AFTER THE ASSOCIATED GENERAL ERROR MESSAGE AND ANY ASSOCIATED BASIC ERROR MESSAGES.

3.2 SPECIFIC ERROR MESSAGES

BELOW ARE SAMPLE ERROR MESSAGES. EACH ERROR MESSAGE REPRESENTS DIFFERENT TYPES OF ERRORS DETECTED BY THIS DIAGNOSTIC.

ERROR MESSAGE EXAMPLE 1

THIS ERROR IS INDICATIVE OF AN INCORRECT REGISTER OR STATUS WORD RETURNED TO THE DIAGNOSTIC. THE FIRST PART DEFINES THE TEST FUNCTION AND UNIT THAT FAILED. THE SECOND PART PROVIDES THE REGISTER BITS AND THEIR MNEMONICS FOR THE INCORRECT REGISTER OR STATUS WORDS. THE THIRD PART IS THE EXPECTED AND RECEIVED DATA.

TST: 016 FIFO EXERCISER TEST
CNTSA HRD ERR 01610 ON UNIT 00 TST 016 SUB 002 PC: 040624
FIFO STATUS (IN WORD 9) INCORRECT AFTER WRITE FIFO

TAPE BUS SIGNALS IN WORD #8: - DESIGNATOR <BIT #>
PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>
IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>
IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>

TAPE BUS SIGNALS IN WORD #9:
DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>

MESSAGE BUFFER ADDRESS = 047352

MESSAGE BUFFER CONTENTS:

WORD #0	EXPD: 100020	RECV: 100020	XOR: 000000
WORD #1	EXPD: 000012	RECV: 000012	XOR: 000000
WORD #2	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #3	EXPD: 000010	RECV: 000010	XOR: 000000
WORD #4	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #5	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #6	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #7	EXPD: 000000	RECV: 000000	XOR: 000000
WORD #8	EXPD: 070217	RECV: 070217	XOR: 000000
WORD #9	EXPD: 000074	RECV: 000034	XOR: 000040

ERROR MESSAGE EXAMPLE 2

THIS ERROR SHOWS A FATAL FUNCTION ERROR FROM THE TAPE DRIVE. IN THIS INSTANCE A UNRECOVERABLE ERROR OCCURED WHICH INDICATES THAT THE CONTROLLER MAY BE DEFECTIVE.

CNTSA HRD ERR 00159 ON UNIT 00 TST 001 SUB 005 PC: 026202
TSSR NOT CORRECT AFTER SPACE RECORDS COMMAND

TSSR = 100214

TSSR BITS SET: SC,SSR

TERMINATION CLASS CODE = UNRECOVERABLE ERROR

PACKET ADDRESS = 026420

PACKET WORD # = 140010

PACKET WORD # = 000010

PACKET WORD # = 000000

PACKET WORD # = 000024

ERROR MESSAGE EXAMPLE 3

THIS ERROR SHOWS THAT THE MOTION BIT DID NOT GET SET WHILE DOING A REWIND WITH EXTENDED FEATURES MODE ENABLED.

CNTS HRD ERR 00121 ON UNIT 00 TST 001 SUB 002 PC: 023306

MOT BIT (XSTO) NOT SET DURING REWIND (EXTENDED FEATURES MODE)
 EXPD: 000312 RECV: 000112 XOR: 000200

4.0 PERFORMANCE AND PROGRESS REPORTS

AT THE END OF EACH PASS, THE PASS COUNT IS GIVEN ALONG WITH THE TOTAL NUMBER OF ERRORS REPORTED SINCE THE DIAGNOSTIC WAS STARTED. THE "EOP" SWITCH CAN BE USED TO CONTROL HOW OFTEN THE END OF PASS MESSAGE IS PRINTED. SECTION 2.2 DESCRIBES SWITCHES.

SUCCESSFUL RUN EXAMPLE (PDP 11/23)

DR>STA/FLA:PNT:HOE

UNITS (D) ? 1

UNIT 0

DEVICE ADDRESS (0) 176000 ? <CR>

VECTOR (0) 224 ? <CR>

CHANGE SW (L) ? N<CR>

THE ABOVE COMMAND WILL START THE DIAGNOSTIC. THE COMMAND HAS TWO SWITCHES ON WHICH ARE "PRINT EACH TEST NBR AS EXECUTED" AND "HALT ON ERROR".

TST: 001 INITIALIZE #1
 TST: 002 WRAP DATA HIGH BYTE TEST
 TST: 003 WRAP DATA LOW BYTE TEST
 TST: 004 RAM TEST
 TST: 005 INITIALIZE 2 TEST
 TST: 006 COMMAND REJECT TEST
 TST: 007 WRITE CHARACTERISTICS TEST
 TST: 008 VOLUME CHECK
 TST: 009 COMPLETION INTERRUPT TEST
 TST: 010 BASIC PACKET PROTOCOL TEST
 TST: 011 NON-TAPE MOTION COMMANDS TEST

0 ERRORS

NOTE: THE DIAGNOSTIC WILL RUN CONTINUOUSLY UNLESS A PASS NUMBER LIMIT HAS BEEN SPECIFIED WITH THE "/PASS:" SWITCH.

PROGRAM RUN TIMES

THE AVERAGE RUN TIMES OF THE PROGRAM ARE LISTED BELOW. THESE FIGURES ARE TO BE USED AS A GUIDE. THE TIMING WAS DONE ON THE FALCON PROCESSOR.

THE PROGRAM RUNS IN TWO MODES; NO ITERATIONS AND DEFAULT MODE. IN THE NO ITERATIONS MODE, EACH TEST IS RUN ONCE, WITH NO ITERATIONS. IN THE DEFAULT MODE EACH TEST IS REPEATED BY THE NUMBER OF TIMES INDICATED BY

THE ITERATION COUNT. NO ITERATIONS MODE IS SELECTED BY ANSWERING THE INHIBIT ITERATIONS QUESTION WITH A "Y" (YES).

TIMES REQUIRED TO RUN TESTS 1 THROUGH 12:

Q.V. 2 MIN 15 SECONDS
DEFAULT 16 MINS

MORE EXHAUSTIVE CHECKS ARE AVAILABLE BY ALLOWING THE DIAGNOSTIC PROGRAMS TO RUN FOR MORE THAN ONE PASS. THE SECOND PASS OF THE PROGRAM IS MORE COMPREHENSIVE THAN THE FIRST PASS. ALL ITERATIONS AFTER THE FIRST PASS ARE THE SAME, HOWEVER, THEY ARE SUBSTANTIALLY LONGER.

5.0 DEVICE INFORMATION TABLES

WHENEVER THE PROGRAM IS STARTED, VIA THE STA(RT) COMMAND, THE SUPERVISOR REQUESTS THE FOLLOWING P-TABLES PARAMETER CHANGES:

CHANGE HW (L) ?

UNITS (D) ? <ENTER THE NUMBER OF M7196 CONTROLLERS
PRESENT TO BE TESTED>

UNIT 0

DEVICE ADDRESS (O) 176000 ? <ENTER THE ADDRESS OF THE
TSBA/TSDB REGISTER>

VECTOR (O) 224 ? <ENTER ADDRESS OF INTERRUPT
VECTOR>

THE ADDRESS AND VECTOR QUESTIONS WILL BE ASKED FOR EACH OF THE NUMBER OF UNITS (CONTROLLERS) SPECIFIED IN THE "# UNITS?" QUESTION. LOGICAL UNIT NUMBERS ARE ASSIGNED IN ORDER, BEGINNING AT 0. UP TO FOUR UNITS CAN BE SELECTED FOR TESTING.

IN ADDITION, ON A START, RESTART OR CONTINUE THE SUPERVISOR REQUESTS CHANGES TO THE SOFTWARE OPERATING PARAMETERS, AS FOLLOWS:

CHANGE SW (L) ?

INHIBIT ITERATIONS (L) N ?

6.0 TEST SUMMARIES

TEST 1: BUS RESET TEST

THIS TEST VERIFIES THAT THE M7196 MODULE'S DEVICE REGISTERS ARE ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND ANY BASIC PROBLEMS IN THE MODULE. AREAS OF LOGIC TESTED BY THE SELF-TEST SEQUENCE ARE AS FOLLOWS: ROM AND PIPELINE REGISTER, SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM. THIS TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER, WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA) BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0). IF THE CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES. THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNs AND REPORTS ONE OF THREE POSSIBILITIES:

1. TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET, OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE APPARENT ERROR CODE IN BITS 0-5): INDICATES THAT THE TSSR CONTENT CANNOT BE TRUSTED. INDICATES A CATASTROPHIC CONTROLLER MALFUNCTION. THIS IS A FATAL ERROR (EXECUTION IS ABORTED). FIELD ACTION WOULD BE TO REPLACE THE M7196. IF THE M7196 ITSELF IS BEING DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
2. SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN THE RANGE 17-13: THIS IS A FATAL ERROR. THE ERROR CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN. INDICATES THAT A SERIOUS PROBLEM EXISTS.

TEST 2: WRAP DATA - HIGH BYTE

THIS TEST VERIFIES OPERATION OF:

1. PART OF THE 11/21+ BUS INTERFACE SECTION OF THE M7196 MODULE: PART OF THE INPUT FILE (TSDB HIGH BYTE), PART OF THE OUTPUT FILE (TSSR HIGH BYTE AND TSBA, BOTH BYTES), PART OF THE DCO05 TRANSCEIVER CIRCUITS (ADDRESS DECODER, BDAL DRIVERS, HIGH BYTE OF INTERNAL DAL BUS DRIVERS), AND BASIC PROGRAMMED I/O CONTROL SEQUENCES AND LOGIC;
2. PART OF 2901 MICROPROCESSOR ELEMENTS (Q REGISTER, REGISTER 0, ROTATE AND NEGATE FUNCTIONS
3. Y AND SOURCE BUSES;
4. BASIC MICROPROGRAM SEQUENCES.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE HIGH BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

TEST 3: WRAP DATA - LOW BYTE

THIS TEST FURTHER VERIFIES OPERATION OF MANY OF THE SAME ELEMENTS TESTED IN TEST 2, AND ADDITIONALLY VERIFIES:

1. LOW BYTE OF THE TSDB INPUT FILE REGISTER.
2. LOW BYTE OF INTERNAL DAL BUS DRIVERS ON THE DC005 TRANSCEIVER CIRCUITS.
3. BASIC FUNCTIONING OF PARTS OF THE RAM.

THE PROGRAM WRITES A TEST DATA BYTE INTO THE LOW BYTE OF TSDB, WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA BYTES (0-377 OCTAL).

TEST 4: RAM TEST

THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE M7196 CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT EACH RAM LOCATION IS UNIQUELY ADDRESSED (I.E., THAT ONE AND ONLY ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THE BYPRODUCT OF THESE TESTS IS A VERIFICATION OF TWO REGISTERS IN THE 2901 AND THE CAPABILITY OF THE 2901 TO CORRECTLY PERFORM AN ADD.

TEST 5: SECOND INITIALIZATION TEST

THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO

AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO. THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE IS PERFORMED:

1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW BYTE AND MAINTENANCE MODE WORD WRITES).
2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE PERFORMED.
3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB, WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0. DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM OR A TIMING PROBLEM.

TEST 6: COMMAND REJECT

THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC COMMAND DECODING AND DATA DMA HANDLING. THIS TEST CONTAINS TWO SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED.

TEST 7: WRITE CHARACTERISTICS

THIS TEST VERIFIES BASIC OPERATION OF THE WRITE CHARACTERISTICS COMMAND. IT VERIFIES THAT THE COMMAND BLOCK AND CHARACTERISTICS DATA BLOCK ARE FETCHED PROPERLY FROM CPU MEMORY, THE NEED BUFFER ADDRESS (NBA) BIT IN TSSR IS HANDLED PROPERLY, AND THAT A PROPER MESSAGE PACKET IS STORED, WHERE APPROPRIATE. THIS TEST DOES NOT CHECK THAT THE VARIOUS FUNCTIONS ENABLED BY CHARACTERISTIC MODE DATA BITS OPERATE PROPERLY; THE FUNCTIONING OF THESE BITS IS

VERIFIED IN SUBSEQUENT TESTS. ALL COMMANDS EXECUTED IN THIS TEST HAVE THE INTERRUPT ENABLE (IE) BIT CLEARED TO ZERO, SO NO INTERRUPTS SHOULD BE GENERATED. HOWEVER, THE PROGRAM RUNS AT PROCESSOR PRIORITY 0, WITH THE INTERRUPT SERVICE ROUTINE SET UP TO FLAG UNEXPECTED INTERRUPTS. IF AN INTERRUPT OCCURS, A PROBLEM EXISTS IN EITHER THE 11/21+ BUS INTERFACE SECTION OR IN THE ROM OR PIPELINE.

TEST 8: VOLUME CHECK

THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7196 AND APPEARING IN XSTO, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

TEST 9: COMPLETION INTERRUPT

THIS TEST VERIFIES THAT AN INTERRUPT IS GENERATED AT THE COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC PROCESSING OF THE IE BIT.

THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XSTO OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS GENERATED. FINALLY, A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE IE BIT IN XSTO IS 0.

TEST 10: BASIC PACKET PROTOCOL

THIS TEST VERIFIES BASIC OPERATION OF THE MESSAGE BUFFER RELEASE COMMAND, THE FUNCTION OF THE ACK BIT IN THE COMMAND HEADER WORD, AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.

TEST 11: NON-TAPE MOTION COMMANDS

THIS TEST VERIFIES PROPER OPERATION OF THE INITIALIZE COMMAND. TWO SUBTESTS ARE USED. THE FIRST VERIFIES THAT THE COMMAND RUNS TO COMPLETION AND STORES A VALID MESSAGE PACKET. THE SECOND VERIFIES THAT NON ZERO VALUES IN THE COMMAND MODE FIELD CAUSES COMMAND REJECT.

7.0 MAINTENANCE HISTORY

REVISION A MARCH 1982

CVTSAAO => CNTSAAO

JAKI BERG

9-APR-1984

CHANGES WERE MADE TO CVTSAAO TO PRODUCE CNTSAAO FOR THE
FALCON-PLUS PROJECT (SBC-11/21+). CHANGES, MARKED BY
";JB REV A-0", ARE:

- SET THE ODT BREAK VECTOR (LOCATION 140) TO THE
STARTING ADDRESS OF FALCON'S ODT ROM (170000-OCTAL).
- LOWER THE GENERAL INTERRUPT PRIORITY FROM 7 TO 6.
CHANGE DEFAULT CSR ADDRESS FROM 172540 TO 176000.


```

2          .TITLE  TSV2 - PROGRAM HEADER
3          .SBTTL  PROGRAM HEADER
4
10         .MCALL  SVC
11 000000   SVC                      ; INITIALIZE SUPERVISOR MACROS
12         .ENABLE LC
13         .NLIST  BEX,CND
19 000000   .ENABL  ABS,AMA
20         .*2000
21 002000   BGNMOD  TSV2
    002000
22
23
24 ;**
25 ; THE PROGRAM HEADER IS THE INTERFACE BETWEEN
26 ; THE DIAGNOSTIC PROGRAM AND THE SUPERVISOR.
27 ;--
28
29 002000   POINTER BGNSW,BGNSFT,BGNAU,BGNDU,BGNRPT
30 0C2000   HEADER CNTSA,A,0,655,,0
    002000
    002000   103   L$NAME::          ;DIAGNOSTIC NAME
    002001   116   .ASCII  /C/
    002002   124   .ASCII  /N/
    002003   123   .ASCII  /T/
    002004   101   .ASCII  /S/
    002005   000   .ASCII  /A/
    002006   000   .BYTE   0
    002007   000   .BYTE   0
    002010   000   .BYTE   0
    002010   101   L$REV::          ;REVISION LEVEL
    002011   000   .ASCII  /A/
    002011   060   L$DEPO::        ;0
    002012   000   .ASCII  /O/
    002012   000000 L$UNIT::        ;NUMBER OF UNITS
    002014   001217 L$TIML::        ;LONGEST TEST TIME
    002016   045654 L$HPCP::        ;PTR. TO H.W. QUES.
    002020   000   .WORD   L$HARD
    002020   046006 L$SPCP::        ;PTR. TO S.W. QUES.
    002022   000   .WORD   L$SOFT
    002022   002154 L$HPTP::        ;PTR. TO DEF. H.W. PTABLE
    002024   000   .WORD   L$HW
    002024   002164 L$SPTP::        ;PTR. TO S.W. PTABLE
    002026   000   .WORD   L$SW
    002026   046404 L$LADP::        ;DIAG. END ADDRESS
    002030   000   .WORD   L$LAST
    002030   000000 L$STA::        ;RESERVED FOR APT STATS
    002032   000   .WORD   0
    002032   000000 L$CO::        ;
    002034   000   .WORD   0
    002034   000000 L$DTYP::        ;DIAGNOSTIC TYPE
    002036   000   .WORD   0
    002036   000000 L$APT::        ;APT EXPANSION
    002040   000   .WORD   0
    002040   002124 L$DTP::        ;PTR. TO DISPATCH TABLE
    002040   000   .WORD   L$DISPATCH

```

PROGRAM HEADER

002042		L\$PRIO::		;DIAGNOSTIC RUN PRIORITY
002042	000000		.WORD 0	
002044		L\$ENVI::		;FLAGS DESCRIBE HOW IT WAS SETUP
002044	000000		.WORD 0	
002046		L\$EXP1::		;EXPANSION WORD
002046	000000		.WORD 0	
002050		L\$MREV::		;SVC REV AND EDIT #
002050	003		.BYTE C\$REVISION	
002051	003		.BYTE C\$EDIT	
002052		L\$EF::		;DIAG. EVENT FLAGS
002052	000000		.WORD 0	
002054	000000		.WORD 0	
002056		L\$SPC::		
002056	000000		.WORD 0	
002060		L\$DEVP::		; POINTER TO DEVICE TYPE LIST
002060	003376		.WORD L\$DVTYP	
002062		L\$REPP::		;PTR. TO REPORT CODE
002062	022702		.WORD L\$RPT	
002064		L\$EXP4::		
002064	000000		.WORD 0	
002066		L\$EXP5::		
002066	000000		.WORD 0	
002070		L\$AUT::		;PTR. TO ADD UNIT CODE
002070	022370		.WORD L\$AU	
002072		L\$DUT::		;PTR. TO DROP UNIT CODE
002072	022466		.WORD L\$DU	
002074		L\$LUN::		;LUN FOR EXERCISERS TO FILL
002074	000000		.WORD 0	
002076		L\$DESP::		;POINTER TO DIAG. DESCRIPTION
002076	003404		.WORD L\$DESC	
002100		L\$LOAD::		;GENERATE SPECIAL AUTOLOAD EMT
002100	104035		EMT E\$LOAD	
002102		L\$ETP::		;POINTER TO ERRITBL
002102	000000		.WORD 0	
002104		L\$ICP::		;PTR. TO INIT CODE
002104	021546		.WORD L\$INIT	
002106		L\$CCP::		;PTR. TO CLEAN-UP CODE
002106	022654		.WORD L\$CLEAN	
002110		L\$ACP::		;PTR. TO AUTO CODE
002110	022574		.WORD L\$AUTO	
002112		L\$PRT::		;PTR. TO PROTECT TABLE
002112	021536		.WORD L\$PROT	
002114		L\$TEST::		;TEST NUMBER
002114	000000		.WORD 0	
002116		L\$DLY::		;DELAY COUNT
002116	000000		.WORD 0	
002120		L\$HIME::		;PTR. TO HIGH MEM
002120	000000		.WORD 0	

DISPATCH TABLE

```
33                                     .SBTIL DISPATCH TABLE
34
35
36                                     ;**
37                                     ; THE DISPATCH TABLE CONTAINS THE STARTING ADDRESS OF EACH TEST.
38                                     ; IT IS USED BY THE SUPERVISOR TO DISPATCH TO EACH TEST.
39                                     ;-
40 002122                                DISPATCH 11
    002122 000013                        .WORD 11
    002124                                L$DISPATCH::
    002124 023464                        .WORD T1
    002126 023704                        .WORD T2
    002130 024402                        .WORD T3
    002132 025074                        .WORD T4
    002134 026430                        .WORD T5
    002136 027534                        .WORD T6
    002140 031004                        .WORD T7
    002142 034372                        .WORD T8
    002144 035276                        .WORD T9
    002146 040412                        .WORD T10
    002150 043514                        .WORD T11
41
```

DEFAULT HARDWARE P TABLE

```
43 .SBTTL DEFAULT HARDWARE P TABLE
44
45 ;**
46 ; THE DEFAULT HARDWARE P-TABLE CONTAINS DEFAULT VALUES OF
47 ; THE TEST-DEVICE PARAMETERS. THE STRUCTURE OF THIS TABLE
48 ; IS IDENTICAL TO THE STRUCTURE OF THE RUN-TIME P TABLE.
49 ; -
50 002152 BGNHW DFPTBL ;DEFAULT HARD-P TABLE
    002152 000003 .WORD L10000-L$HW/2
    002154 L$HW::
    002154 DFPTBL::
51
52 002154 176000 .WORD 176000 ; 1ST (OF 2) REGISTERS.
53 002156 000224 .WORD 224 ; INTERRUPT VECTOR
54 002160 000200 .WORD PRI04 ; INTERRUPT PRIORITY.
55 002162 ENDHW
    002162 L10000:
```

SOFTWARE P TABLE

```
57          .SBTTL  SOFTWARE P TABLE
58
59          ;**
60          ; THE SOFTWARE P-TABLE CONTAINS THE VALUES OF THE PROGRAM
61          ; PARAMETERS THAT CAN BE CHANGED BY THE OPERATOR.
62          ;--
63          BGNSW  SFPTBL
          002162  000004  .WORD  L10001-L$SW/2
          002164
          002164  L$SW::
          002164  SFPTBL::
64
65          002164  000000  TRANSTST::      .WORD  0      ; ENABLE TEST OF TRANSPORT(S) IF *1
66          002166  000000  NOITS::         .WORD  0      ; INHIBIT ITERATION OPTION.
67                                     ; ... 0 = ITERATE.
68                                     ; ...NZ = INHIBIT ITERATE.
69          002170  000017  LERRMAX::        .WORD  15.     ; LOCAL (PER TEST) ERROR LIMIT
70          002172  000310  GERRMAX::        .WORD  200.    ; GLOBAL (PER UNIT) ERROR LIMIT
71          002174
          002174  ENDSW
          002174  L10001:
72
73          002174  ENDMOD
```

SOFTWARE P-TABLE

```

7          .TITLE  TSV3 - GLOBAL AREAS
8          .SBTTL  GLOBAL EQUATES SECTION
13
19
20 002174   BGNMOD  TSV3
21          TSV3::
22
23          .SBTTL  GLOBAL EQUATES SECTION
24
25          ;**
26          ; THE GLOBAL EQUATES SECTION CONTAINS PROGRAM EQUATES THAT
27          ; ARE USED IN MORE THAN ONE TEST.
28          ;--
29
33 002174   EQUALS          ; GET STANDARD EQUATES.

;
; BIT DIFINITIONS
;
100000     BIT15== 100000
040000     BIT14== 40000
020000     BIT13== 20000
010000     BIT12== 10000
004000     BIT11== 4000
002000     BIT10== 2000
001000     BIT09== 1000
000400     BIT08== 400
000200     BIT07== 200
000100     BIT06== 100
000040     BIT05== 40
000020     BIT04== 20
000010     BIT03== 10
000004     BIT02== 4
000002     BIT01== 2
000001     BIT00== 1

;
001000     BIT9==  BIT09
000400     BIT8==  BIT08
000200     BIT7==  BIT07
000100     BIT6==  BIT06
000040     BIT5==  BIT05
000020     BIT4==  BIT04
000010     BIT3==  BIT03
000004     BIT2==  BIT02
000002     BIT1==  BIT01
000001     BIT0==  BIT00

;
; EVENT FLAG DEFINITIONS
; EF32:EF17 RESERVED FOR SUPERVISOR TO PROGRAM COMMUNICATION
;
;
000040     EF.START==      32.          ; BIT POSITION IN SECOND STATUS WORD
000037     EF.RESTART==    31.          ; (100000) START COMMAND WAS ISSUED
000036     EF.CONTINUE==   30.          ; (040000) RESTART COMMAND WAS ISSUED
000035     EF.NEW==        29.          ; (020000) CONTINUE COMMAND WAS ISSUED
000034     EF.PWR==        28.          ; (010000) A NEW PASS HAS BEEN STARTED
;                                     ; (004000) A POWER FAIL/POWER UP OCCURRED
;

```


GLOBAL EQUATES SECTION

```

;
; PRIORITY LEVEL DEFINITIONS
;
000340 PRI07== 340
000300 PRI06== 300
000240 PRI05== 240
000200 PRI04== 200
000140 PRI03== 140
000100 PRI02== 100
000040 PRI01== 40
000000 PRI00== 0

```

```

;
; OPERATOR FLAG BITS
;
000004 EVL== 4
000010 LOT== 10
000020 ADR== 20
000040 IDU== 40
000100 ISR== 100
000200 UAM== 200
000400 BOE== 400
001000 PNT== 1000
002000 PRI== 2000
004000 IXE== 4000
010000 IBE== 10000
020000 IER== 20000
040000 LOE== 40000
100000 HOE== 100000

```

34
35 002174

```

;KT11
.SBTTL MEMORY MANAGEMENT DEFINITIONS ;DEFINE MEMORY MANAGEMENT REGISTERS
;*KT11 VECTOR ADDRESS
000250 MMVEC= 250
;*KT11 STATUS REGISTER ADDRESSES
177572 SR0= 177572
177574 SR1= 177574
177576 SR2= 177576
172516 SR3= 172516
;IF N8
;*USER "I" PAGE DESCRIPTOR REGISTERS
UIPDR0= 177600
UIPDR1= 177602
UIPDR2= 177604
UIPDR3= 177606
UIPDR4= 177610
UIPDR5= 177612
UIPDR6= 177614
UIPDR7= 177616
;IF N8
;*USER "D" PAGE DESCRIPTOR REGISTERS
UDPDR0= 177620
UDPDR1= 177622
UDPDR2= 177624
UDPDR3= 177626
UDPDR4= 177630
UDPDR5= 177632
UDPDR6= 177634

```

```
UDPDR7= 177636
.ENDC
;*USER "I" PAGE ADDRESS REGISTERS
UIPAR0= 177640
UIPAR1= 177642
UIPAR2= 177644
UIPAR3= 177646
UIPAR4= 177650
UIPAR5= 177652
UIPAR6= 177654
UIPAR7= 177656
.IF NB
;*USER "D" PAGE ADDRESS REGISTERS
UDPAR0= 177660
UDPAR1= 177662
UDPAR2= 177664
UDPAR3= 177666
UDPAR4= 177670
UDPAR5= 177672
UDPAR6= 177674
UDPAR7= 177676
.ENDC
.ENDC
.IF NB
;*SUPERVISOR "I" PAGE DESCRIPTOR REGISTERS
SIPDR0= 172200
SIPDR1= 172202
SIPDR2= 172204
SIPDR3= 172206
SIPDR4= 172210
SIPDR5= 172212
SIPDR6= 172214
SIPDR7= 172216
.IF NB
;*SUPERVISOR "D" PAGE DESCRIPTOR REGISTERS
SDPDR0= 172220
SDPDR1= 172222
SDPDR2= 172224
SDPDR3= 172226
SDPDR4= 172230
SDPDR5= 172232
SDPDR6= 172234
SDPDR7= 172236
.ENDC
;*SUPERVISOR "I" PAGE ADDRESS REGISTERS
SIPAR0= 172240
SIPAR1= 172242
SIPAR2= 172244
SIPAR3= 172246
SIPAR4= 172250
SIPAR5= 172252
SIPAR6= 172254
SIPAR7= 172256
.IF NB
;*SUPERVISOR "D" PAGE ADDRESS REGISTERS
SDPAR0= 172260
SDPAR1= 172262
```


MEMORY MANAGEMENT DEFINITIONS

SEQ 0027

```
SDPAR2= 172264
SDPAR3= 172266
SDPAR4= 172270
SDPAR5= 172272
SDPAR6= 172274
SDPAR7= 172276
.ENDC
.ENDC
; *KERNEL "I" PAGE DESCRIPTOR REGISTERS
172300 KIPDR0= 172300
172302 KIPDR1= 172302
172304 KIPDR2= 172304
172306 KIPDR3= 172306
172310 KIPDR4= 172310
172312 KIPDR5= 172312
172314 KIPDR6= 172314
172316 KIPDR7= 172316
; IF NB
; *KERNEL "D" PAGE DESCRIPTOR REGISTERS
KDPDR0= 172320
KDPDR1= 172322
KDPDR2= 172324
KDPDR3= 172326
KDPDR4= 172330
KDPDR5= 172332
KDPDR6= 172334
KDPDR7= 172336
.ENDC
; *KERNEL "I" PAGE ADDRESS REGISTERS
172340 KIPAR0= 172340
172342 KIPAR1= 172342
172344 KIPAR2= 172344
172346 KIPAR3= 172346
172350 KIPAR4= 172350
172352 KIPAR5= 172352
172354 KIPAR6= 172354
172356 KIPAR7= 172356
; IF NB
; *KERNEL "D" PAGE ADDRESS REGISTERS
KDPAR0= 172360
KDPAR1= 172362
KDPAR2= 172364
KDPAR3= 172366
KDPAR4= 172370
KDPAR5= 172372
KDPAR6= 172374
KDPAR7= 172376
.ENDC
```

TSV05 REGISTER AND PACKET DEFINITIONS

```

40          .SBTTL  TSV05 REGISTER AND PACKET DEFINITIONS
41
42          ;
43          ; SOME GENERAL EQUATES.
44          ;
45
46          000004    ERRVEC==      4          ; POINTER TO ERROR VECTOR FOR BUS TIME OUT.
47          000060    TTIVEC==     60          ; INTERRUPT VECTOR FOR CONSOLE INPUT
48          177560    TTICSR==    177560       ; BUS ADDRESS OF CONSOLE INPUT
49          177562    TTIBFR==    177562       ; CONSOLE INPUT DATA BUFFER
50          177520    BDVPCR==    177520       ; BDV11 PAGE CONTROL REGISTER
51
52          ;*
53          ;BIT DEFINITIONS FOR TSSR REGISTER
54          ;-
55
56          100000    SC=      BIT15          ;SPECIAL CONDITION
57          040000    BIE=     BIT14          ;BUS INTERFACE ERROR
58          020000    SCE=     BIT13          ;SANITY CHECK ERROR
59          010000    RMR=     BIT12          ;MODIFICATION REFUSED
60          004000    NXM=     BIT11          ;NONEXISTANT MEMORY ERROR
61          002000    NBA=     BIT10          ;NEED BUFFER ADDRESS
62          001400    HIADDR= BIT9:BIT8       ;EXTENDED ADDRESS BITS
63          000200    SSR=     BIT7           ;SUB SYSTEM READY
64          000100    OFL=     BIT6           ;OFF LINE BIT
65          000060    FATERR= BIT4:BIT5       ;FATAL TERMINATION ERROR CODES
66          000016    TERCLS= BIT3:BIT2:BIT1  ;TERMINATION CODES
67
68          ;*
69          ;
70          ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 0
71          ;(XST0)
72          ;
73          ;-
74
75          100000    XSOTMK= BIT15          ;TAPE MARK DETECTED
76          040000    XSORLS= BIT14          ;RECORD LENGTH SHORT
77          020000    XSOLET= BIT13          ;LOGICAL END OF TAPE
78          010000    XSORLL= BIT12          ;RECORD LENGTH LONG
79          004000    XSOWLE= BIT11          ;WRITE LOCK ERROR
80          002000    XSONEF= BIT10          ;NON EXECUTABLE FUNCTION
81          001000    XSOILC= BIT9           ;ILLEGAL COMMAND
82          000400    XSOILA= BIT8           ;ILLEGAL ADDRESS
83          000200    XSOMOT= BIT7           ;TAPE IN MOTION
84          000100    XSOONL= BIT6           ;TRANSPORT ON LINE
85          000040    XSOIE=  BIT5           ;INTERRUPT ENABLE
86          000020    XSOVCK= BIT4           ;VOLUME CHECK BIT
87          000010    XSOPED= BIT3           ;PHASE ENCODED DRIVE
88          000004    XSOWLK= BIT2           ;WRITE LOCKED
89          000002    XS0BOT= BIT1           ;BEGINNING OF TAPE
90          000001    XSOEOT= BIT0           ;END OF TAPE
91
92          ;*
93          ;
94          ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 1
95          ;(XST1)
96

```

TSV05 REGISTER AND PACKET DEFINITIONS

```

97
98      100000      ;
99      040000      X1.DLT = BIT15      ;DATA LATE
100     020000      X1.SPARE= BIT14      ;NOT USED
101     017375      X1.COR = BIT13      ;CORRECTABLE DATA ERROR
102     000400      X1.MBZ = BIT12·BIT11·BIT10·BIT9·BIT7·BIT6·BIT5·BIT4·BIT3·BIT2·BIT0 ;ALWAYS 0
103     000002      X1.RBP = BIT8        ;READ BUS PARITY ERROR
104                                     X1.UNC = BIT1        ;UNCORRECTABLE DATA OR HARD ERROR
105
106      ;*
107      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 2
108      ;(XST2)
109      ;-
110     100000      X2.OPM = BIT15        ;OPERATION IN PROGRESS (TAPE MOVING)
111     040000      X2.RCE = BIT14        ;RAM CHECKSUM ERROR
112     035400      X2.SPARE= BIT13·BIT12·BIT11·BIT9·BIT8      ;NOT USED BY TSV05 (ALWAYS=0)
113     002000      X2.WCF = BIT10        ;WRITE CLOCK FAILURE (FIFO NOT EMPTIED BY TRANSPORT)
114     000200      X2.EXTF = BIT7        ;IF WRITE CHAR CMD THEN = EXTENDED FEATURES ENABLED
115     000100      X2.BUFE = BIT6        ;IF WRITE CHAR CMD THEN = BUFFERING ENABLED
116     000077      X2.REV = 000077      ;IF WRITE CHAR CMD THEN = MICROCODE REVISION LEVEL
117     000007      X2.UNIT = BIT2·BIT1·BIT0 ;IF GET STATUS THEN = CURRENTLY SELECTED UNIT NO.
118
119      ;*
120      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 3
121      ;(XST3)
122      ;-
123     177400      X3.MDE = 177400      ;MICRO-DIAGNOSTIC ERROR CODE
124     000200      X3.SPARE= BIT7        ;NOT USED BY TSV05
125     000100      X3.OPI = BIT6        ;OPERATION INCOMPLETE
126     000040      X3.REV = BIT5        ;REVERSE
127     000020      X3.TRF = BIT4        ;TRANSPORT RESPONSE FAILURE
128     000010      X3.DCK = BIT3        ;DENSITY CHECK
129     000006      X3.MBZ =BIT2·BIT1      ;NOT USED ALWAYS 0
130     000001      X3.RIB = BIT0        ;REVERSE INTO BOT
131
132      ;*
133      ;BIT DEFINITIONS FOR EXTENDED STATUS REGISTER 4
134      ;(XST4)
135      ;
136     100000      X4.HSP = BIT15        ;HIGH SPEED
137     040000      X4.RCE = BIT14        ;RETRY COUNT EXCEEDED
138     020000      X4.TSM = BIT13        ;TRANSPORT SPECIAL MODE
139     017400      X4.MBZ = BIT12·BIT11·BIT10·BIT9·BIT8      ;NOT USED ALWAYS 0
140     000377      X4.WRC = 000377      ;WRITE RETRY COUNT FIELD
141
142      ;*
143      ;
144      ;TSSR TERMINATION CODES (BIT 0-2)
145      ;
146      ;
147
148     000006      TSREJ= 3·2            ;COMMAND REJECTED
149     000006      UNREC= 6             ;UNRECOVERABLE ERROR
150
151      ;*
152      ;
153      ;DEVICE REGISTER OFFSETS

```

TSV05 REGISTER AND PACKET DEFINITIONS

```

154      ;
155      ;
156
157      000000      TSBA== 0
158      000000      TSDB== 0      ;TSDB/TSBA REGISTER
159      000001      TSBAH== 1
160      000001      TSDBH== 1      ;TSDB/TSBA REGISTER HIGH BYTE
161      000002      TSSR== 2      ;TSSR REGISTER
162      000003      TSSRH== 3      ;TSSR REGISTER HIGH BYTE
163
164      ;*
165      ; TSDB ADDRESS BIT DEFINITIONS
166      ; -
167      000003      A1716 = BIT1:BIT0      ;ADDRESS BITS 17:16 ARE IN 1:0
168
169      ;*
170      ; COMMAND DEFINITIONS
171      ; -
172      000017      P.GETSTAT = 17      ;GET STATUS
173      000013      P.INIT = 13      ;INITIALIZE
174      000012      P.CONTROL = 12      ;CONTROL COMMANDS
175      000011      P.FORMAT = 11      ;FORMAT
176      000010      P.POSITION = 10      ;POSITION
177      000006      P.WRTSUB = 6      ;SUBSYSTEM WRITE
178      000005      P.WRITE = 5      ;WRITE
179      000004      P.WRTCHAR = 4      ;WRITE CHARACTERISTICS
180      000001      P.READ = 1      ;READ
181
182      ;*
183      ; COMMAND PACKET HEADER WORD BIT DEFINITIONS
184      ; -
185      100000      P.ACK = BIT15      ;BUFFER AVAIL FOR CONTROLLER
186      040000      P.CVC = BIT14      ;CLEAR VOLUME CHECK
187      020000      P.OPP = BIT13      ;REVERSE SEQUENCE OF DATA BITS
188      010000      P.SWB = BIT12      ;SWAP BYTES IN MEMORY
189      007400      P.MODE = BIT11:BIT10:BIT9:BIT8 ;EXTENDED COMMAND MODE FIELD
190      000200      P.IE = BIT7      ;INTERRUPT ENABLE
191      000140      P.FMT = BIT6:BIT5      ;PACKET HEADER TYPE (ALWAYS=0)
192      000037      P.CMD = 37      ;MAJOR COMMAND FIELD
193
194      ;*
195      ; CONTROL COMMAND MODE CODES
196      ; -
197      000000      PC.RELEASE = 0*256.      ;RELEASE BUFFER
198      000400      PC.REWIND = 1*256.      ;REWIND
199      001000      PC.NOOP = 2*256.      ;NO OP
200      002000      PC.IEREW = 4*256.      ;REWIND IMMEDIATE INTERRUPT
201      002400      PC.ERASE = 5*256.      ;SECURITY ERASE
202
203      ;*
204      ; CONTROLLER RAM DEFINITIONS
205      ; -
206      000167      RMCHBEG = 167      ;CHARACTERISTICS IO DATA BEGIN RAM ADDRESS
207      000200      RMCHEND = 200      ;CHARACTERISTICS IO DATA END RAM ADDRESS
208      000201      RMPKTBEG = 201      ;COMMAND PACKET BEGIN RAM ADDRESS
209      000210      RMPKTEND = 210      ;COMMAND PACKET END RAM ADDRESS
210      000215      RMMSGBEG = 215      ;MESSAGE BUFFER BEGIN RAM ADDRESS
211      000234      RMMSGEND = 234      ;MESSAGE BUFFER END RAM ADDRESS

```

TSV05 REGISTER AND PACKET DEFINITIONS

```

211      ;*
212      ;
213      ;REGISTER DEFINITIONS IN THE MESSAGE BUFFER
214      ;
215      ;-
216
217      000006      XST0== 6      ;EXTENDED STATUS REGISTER 0 (WORD 4)
218      000010      XST1== 8      ;EXTENDED STATUS REGISTER 1 (WORD 5)
219      000012      XST2== 10     ;EXTENDED STATUS REGISTER 2 (WORD 6)
220      000014      XST3== 12     ;EXTENDED STATUS REGISTER 3 (WORD 7)
221      000016      XST4== 14     ;EXTENDED STATUS REGISTER 4 (WORD 8)
222
223
224      ;*
225      ;
226      ;OFFSETS TO WORD LOCATIONS IN PACKET DEFINITIONS
227      ;
228      ;-
229
230      000002      PKLOW   = 2      ;LOW ORDER CHARACTERISTIC DATA POINTER
231      000004      PKHI    = 4      ;HIGH ORDER CHARACTERISTIC DATA POINTER
232      000006      PKBCNT  = 6      ;NUMBER OF BYTES IN DATA PACKET
233
234      000010      EXBCNT=10      ;NUMBER OF BYTES IN EXTENDED DATA PACKET
235
236      ;*
237      ;DATA PACKET OFFSETS FOR WRITE SUBSYSTEM COMMAND
238      ;-
239      000000      BSELO   = 0      ;BYTE 0
240      000001      BSEL1   = 1      ;BYTE 1
241      000002      SEL2    = 2      ;WORD 2
242      000004      SELDATA = 4      ;WORD 3
243
244      ;*
245      ;BSELO SELECT CODES FOR WRITE SUBSYSTEM COMMAND
246      ;
247      000000      PW.NOP    = 0      ;NO-OP
248      000001      PW.RDRAM  = 1      ;READ RAM
249      000002      PW.WTRAM  = 2      ;WRITE RAM
250      000003      PW.RFIFO  = 3      ;READ FIFO
251      000004      PW.WFIFO  = 4      ;WRITE FIFO
252      000005      PW.RDSTAT = 5      ;READ STATUS
253      000006      PW.WCTL   = 6      ;WRITE TAPE CONTROL
254      000007      PW.WFMT   = 7      ;WRITE TAPE FORMAT
255      000010      PW.WMISC   = 10     ;WRITE MISCELLANEOUS
256      000011      PW.WNPR   = 11     ;WRITE NPR CONTROL
257      000020      PW.D22    = 20     ;DO MICROTEST 22
258      000021      PW.D11    = 21     ;DO MICROTEST 11
259      000022      PW.D13    = 22     ;DO MICROTEST 13
260      000023      PW.NO1311 = 23     ;DISABLE MICROTEST 11 AND 13
261      000024      PW.RDEXT  = 24     ;READ EXT. TAPE STATUS (NOT SUPPORTED BY ALL TRANSPORTS)
262
263      ;*
264      ;BSEL1 CODES FOR WRITE TAPE CONTROL
265      ;
266      000200      WC.IFAD    = BIT7   ;IFAD - FORMATTER ADDRESS
267      000100      WC.IOTAD   = BIT6   ;ITADO - TRANSPORT ADDRESS BIT 0

```

TSV05 REGISTER AND PACKET DEFINITIONS

```

268      000040      WC.I1TAD      ▫ BIT5      ;ITAD1 - TRANSPORT ADDRESS BIT 1
269      000020      WC.ISRESV     ▫ BIT4      ;IRESV5 - RESERVED #5
270      000010      WC.IREW      ▫ BIT3      ;IREW - REWIND
271      000004      WC.IRWU      ▫ BIT2      ;IRWU - REWIND AND UNLOAD
272      000002      WC.IFEN      ▫ BIT1      ;IFEN - FORMATTER ENABLE
273      000001      WC.IGO       ▫ BIT0      ;GO
274
275      ;*
276      ;BSEL1 CODES FOR WRITE FORMAT
277      ;-
278      000200      WF.IHISP      ▫ BIT7      ;IHISP - HIGH SPEED
279      000100      WF.IWRT      ▫ BIT6      ;IWRT - WRITE
280      000040      WF.IREV      ▫ BIT5      ;IREV - REVERSE
281      000020      WF.IWFM      ▫ BIT4      ;IWFM - WRITE FILE MARK
282      000010      WF.IEDIT      ▫ BIT3      ;IEDIT - EDIT
283      000004      WF.IERASE     ▫ BIT2      ;IERASE - ERASE
284      000002      WF.I3RESV     ▫ BIT1      ;IRESV3 - RESERVED #3
285      000001      WF.I4RESV     ▫ BIT0      ;IRESV4 - RESERVED #4
286
287
288      ;*
289      ;BSEL1 CODES FOR WRITE MISCELLANEOUS SUBCOMMAND
290      ;-
291      000200      MS.EXT        ▫ BIT7      ;INVERT SENSE OF EXTENDED FEATURES SWITCH
292      000020      MS.RSFIFO     ▫ BIT4      ;RESET FIFO AND INPUT PARITY ERRORR
293      000010      MS.RSTAPE     ▫ BIT3      ;RESET TAPE STATUS IN 2 FLIP-FLOPS
294      000006      MS.ATTN      ▫ BIT2:BIT1 ;ATTENTION TRIGGER FIELD
295      000001      MS.RSD       ▫ BIT0      ;RESET TIMER A,B THEN DELAY TIMES IN SEL2
296
297      ;*
298      ; MS.ATTN SUBCODES
299      ;-
300      000000      MSA.NOP      ▫ 0*2      ;NO OP (NOTHING TRIGGERED)
301      000002      MSA.VOL      ▫ 1*2      ;SIMULATE ON-LINE/OFF-LINE TRANSITION
302      000004      MSA.NRAM     ▫ 2*2      ;FORCE NON-FATAL RAM ERROR (FORCES ERRCODE 54)
303      000006      MSA.FRAME     ▫ 3*2      ;FORCE FATAL RAM ERROR (CAUSES SCE TO SET)
304
305      ;*
306      ; WRITE SUBSYSTEM WRITE NPR BSEL1 BIT DEFINITIONS
307      ;-
308      000200      NP.IR         ▫ BIT7      ;INTERRUPT REQUEST (0-1 TRANSITION)
309      000100      NP.OUT        ▫ BIT6      ;TAPE DATA DIRECTION OUT (0= IN)
310      000040      NP.LOOP       ▫ BIT5      ;ENABLE TRANSPORT LOOPBACK
311      000020      NP.WRP        ▫ BIT4      ;WRITE CORRECT PARITY (SET=0 TO WRITE WRONG)
312
313      ;*
314      ; READ STATUS MESSAGE BUFFER BIT DEFINITIONS
315      ;-
316      000200      S2.DIM        ▫ BIT7      ;WORD #9 BYTE 2 DATA IN MISS
317      000100      S2.ILW        ▫ BIT6      ;
318      000040      S2.OUTRDY      ▫ BIT5      ; ILW H
319      000020      S2.INRDY       ▫ BIT4      ; OUT RDY H
320      000010      S2.ATIMR       ▫ BIT3      ; IN RDY H
321      000004      S2.BTIMR       ▫ BIT2      ; TIMER A FLAG H
322      000003      S2.UNDEF       ▫ BIT1:BIT0 ; TIMER B FLAG H
323      100000      S1.PARIN       ▫ BIT15     ;(UNDEFINED)
324      040000      S1.I2RESV     ▫ BIT14     ;WORD #8 BYTE 1 PARIN H
325      020000      S1.I1RESV     ▫ BIT13     ; IRESV2
326      010000      S1.IEOT       ▫ BIT12     ; IRESV1
327      ; IEOT L

```


TSV05 REGISTER AND PACKET DEFINITIONS

325	004000	S1.IIDENT	▪ BIT11	:	IIDENT H
326	002000	S1.ICER	▪ BIT10	:	ICER H
327	001000	S1.IFMK	▪ BIT9	:	IFMK H
328	000400	S1.IHER	▪ BIT8	:	IHER H
329	000200	S0.ISPEED	▪ BIT7	WORD #8 BYTE 0	ISPEED H
330	000100	S0.IRDY	▪ BIT6	:	IRDY L
331	000040	S0.IONL	▪ BIT5	:	IONL L
332	000020	S0.ILDP	▪ BIT4	:	ILDP L
333	000010	S0.IDBY	▪ BIT3	:	IDBY L
334	000004	S0.IRWD	▪ BIT2	:	IRWD L
335	000002	S0.IFBY	▪ BIT1	:	IFBY L
336	000001	S0.IFPT	▪ BIT0	:	IFPT L
337					
338					

SPECIAL MACROS AND OPDEFS.

```

340          .SBTTL SPECIAL MACROS AND OPDEFS.
341
342
343      ;*
344      ;SAVE GENERAL REGS 1 TO 5
345      ;
346
347          .MACRO SAVREG
348          JSR    R5,REGSAV
349          .ENDM
350
351      ;*
352      ; MACRO TO FORCE AN ERROR
353      ;-
354      .MACRO FORCERROR TAG,NOTSSR
355      .NLIST
356      .IIF NDF LISTALL, .NLIST
357      .LIST
358      .IF B NOTSSR
359          MOV     TSSR(R5),R1      ;READ TSSR
360      .ENDC
361          MOV     FORCER,FORCER    ;IS FORCER SET? (LEAVE C BIT ALONE)
362          BNE     TAG              ;BR IF YES
363      .NLIST
364      .IIF NDF LISTALL, .LIST
365      .LIST
366      .ENDM
367
368      ;*
369      ; MACRO TO FORCE AN EXIT TO AVOID SECTION ITERATIONS
370      ; WILL EXIT TO A LABEL IF FORCER IS NEGATIVE
371      ; SO TO FORCE ERRORS AND EXIT ON 1 ERROR SET
372      ; FORCER TO 177777
373      ; TO FORCE ERRORS AND ITERATIONS SET FORCER TO 1.
374      ;-
375      .MACRO FORCEEXIT TAG
376      .NLIST
377      .IIF NDF LISTALL, .NLIST
378      .LIST
379          MOV     FORCER,FORCER    ;IS FORCER NEGATIVE?
380          BMI     TAG              ;BR IF YES
381      .NLIST
382      .IIF NDF LISTALL, .LIST
383      .LIST
384      .ENDM
385      ;*
386      ; MACRO TO INCREMENT ERROR COUNTS
387      ;
388      .MACRO NEXT.ERRNO
389      .NLIST
390      ;;;.IIF NDF LISTALL, .NLIST
391          ERRNO=ERRNO+1
392      ;;;.IIF NDF LISTALL, .LIST
393      .LIST
394      .ENDM
395
396      ;*

```


SPECIAL MACROS AND OPDEFS.

```

397      ;MACRO TO PERFORM XOR
398      ;
399
400      .MACRO XOR      A,B
401      MOV      A,-(SP)
402      BIC      B,(SP)
403      BIC      A,B
404      BIS      (SP)+,B
405      .ENDM
406
407      000000      EN=0      ; INITIALIZE ERROR NUMBER
408      .SBTTL FORCER      FORCE ERROR FLAG
409
410      ;
411      ; THE FOLLOWING LOCATIONS MAY BE PATCHED BY THE USER
412      ; TO OBTAIN THE RESULTS DESCRIBED FOR EACH.
413      ;
414
415      002174      000000      FORCER::      0      ; FORCE TYPE ALL HARD ERRORS (THE ONES CALLED
416      ; - BY THE MACRO "IFERROR"). AN ERROR NEED NOT -
417      ; - EXIST, JUST ASSUME AND TYPE THE MESSAGE.
418
419
420

```

GLOBAL DATA SECTION

```

422          .SBTTL  GLOBAL DATA SECTION
423
424          ;**
425          ;THE GLOBAL DATA SECTION CONTAINS DATA THAT ARE USED
426          ;IN MORE THAN ONE TEST.
427          ;
428
429          ;
430          ;THE FOLLOWING DATA ARE SET FOR EACH UNIT AT INIT TIME.
431          ;SINGLE UNIT DEFAULTS (LISTED) ARE IN THE DEFAULT P-TABLE.
432          ;
433 002176 000000  EPRTSW::      .WORD  0      ;PRINT SWITCH
434 002200 000000  UNITN::      .WORD  0      ;UNIT # UNDER TEST.
435 002202 000000  QVP::       .WORD  0      ;QUICK VERIFY FLAG.
436 002204 000000  CSRADDR::   .WORD  0      ;ADDRESS OF CSR FOR CURRENT DEVICE
437 002206 000224  IVEC::      .WORD  224    ;INTERRUPT VECTOR
438 002210 000200  IPRI::      .WORD  PRI04   ;INTERRUPT PRIORITY.
439 002212 000000  TSTCNT::    .WORD  0      ;NUMBER OF TESTS RUN IN THIS PASS
440 002214 000000  LOOPCNT::   .WORD  0      ;REMAINING ITERATION COUNT FOR TEST
441 002216 000000  DEVCNT::    .WORD  0      ;NUMBER OF DEVICE UNDER TEST
442 002220 000000  FATFLG::    .WORD  0      ;SET IF FATAL ERROR IS DETECTED IN TEST
443 002222 000000  INTRECV::   .WORD  0      ;SET IF TAPE INTERRUPT WAS RECEIVED
444 002224 000000  EXTFEA::    .WORD  0      ;EXTENDED FEATURES SOFTWARE SW 0=OFF;1=ON
445 002226 000000  BENBSW::    .WORD  0      ;BUFFER ENABLE SWITCH SW 0=OFF;1=ON
446 002230 000000  EXPD::      .WORD  0      ;EXPECTED RAM DATA FOR PRAMPKT ROUTINE
447 002232 000000  RECV::      .WORD  0      ;RECEIVED RAM DATA FOR PRAMPKT ROUTINE
448 002234 000000  ERRHI::     .WORD  0      ;HIGH ADDRESS MEMORY ERROR
449 002236 000000  ERRLO::     .WORD  0      ;LOW ADDRESS MEMORY ERROR
450 002240 000000  RAMDATA::   .BLKW  16.    ;DATA READ FROM RAM PACKET OR MESSAGE BUF AREA
451 002300 000000  RAMSIZ::    .WORD  0      ;RAM DATA SIZE FOR PRAMPKT ROUTINE
452 002302 000000  RCVHIADD::  .WORD  0      ;RECEIVED BUFFER HIGH ADDRESS
453 002304 000000  RCVLOADD::  .WORD  0      ;RECEIVED BUFFER LOW ADDRESS
454 002306 000000  COUNT::     .WORD  0      ;TEST COUNT PATTERN
455 002310 000000  DATA::     .WORD  0      ;TEST DATA
456 002312 000000  TSTFLAG::   .WORD  0      ;TEST FLAG WORD
457 002314 000000  TSTPTR::    .WORD  0      ;TSTBLK POINTER
458 002316 000000  PRMNO::     .WORD  0      ;PRINT ROUTINE TEMP
459 002320 000000  EXPMSG::     .BLKB  100.   ;EXPECTED MESSAGE BUFFER DATA
460 002464 000000  RECMSG::     .BLKB  100.   ;RECEIVED MESSAGE BUFFER DATA
461 002630 000000  TMPBFR::    .BLKB  80.    ;TEMPORARY STORAGE FOR PRINT

```

TSTBLK TEST DATA TABLE

```

463                                     .SBTTL TSTBLK - TEST DATA TABLE
464
465                                     ;*
466                                     ;
467                                     ; THIS TABLE CONTAINS TEST DATA USED IN SEVERAL TESTS
468                                     ;
469                                     ; IN SEQUENCE THE DATA IS:
470                                     ;
471                                     ;     ALL ZEROS
472                                     ;     ALL ONES
473                                     ;     WALKING ONES
474                                     ;     WALKING ZEROS
475                                     ;     ALTERNATING ONES AND ZEROS
476                                     ;
477                                     ; -
478
479 002750 TSTBLK::
480 002750 000000 .WORD 0 ; ALL ZEROS
481 002752 177777 .WORD 177777 ; ALL ONES
482 002754 000001 .WORD BIT0 ; DATA FOR WALKING ONES
483 002756 000002 .WORD BIT1
484 002760 000004 .WORD BIT2
485 002762 000010 .WORD BIT3
486 002764 000020 .WORD BIT4
487 002766 000040 .WORD BIT5
488 002770 000100 .WORD BIT6
489 002772 000200 .WORD BIT7
490 002774 000400 .WORD BIT8
491 002776 001000 .WORD BIT9
492 003000 002000 .WORD BIT10
493 003002 004000 .WORD BIT11
494 003004 010000 .WORD BIT12
495 003006 020000 .WORD BIT13
496 003010 040000 .WORD BIT14
497 003012 100000 .WORD BIT15
498 003014 177776 .WORD *CBIT0 ; DATA FOR WALKING ZEROS
499 003016 177775 .WORD *CBIT1
500 003020 177773 .WORD *CBIT2
501 003022 177767 .WORD *CBIT3
502 003024 177737 .WORD *CBIT5
503 003026 177677 .WORD *CBIT6
504 003030 177577 .WORD *CBIT7
505 003032 177377 .WORD *CBIT8
506 003034 176777 .WORD *CBIT9
507 003036 175777 .WORD *CBIT10
508 003040 173777 .WORD *CBIT11
509 003042 167777 .WORD *CBIT12
510 003044 157777 .WORD *CBIT13
511 003046 137777 .WORD *CBIT14
512 003050 077777 .WORD *CBIT15
513 003052 125252 .WORD 125252 ; ALTERNATING ONES, ZEROS
514 003054 052525 .WORD 052525 ; ALTERNATING ONES, ZERO OPPOSITE FROM ABOVE
515 003056
TBLEND==.

```

GLOBAL ENVIRONMENT STORAGE

```

517          .SBTTL GLOBAL ENVIRONMENT STORAGE
518
519          ;
520          ; STORAGE FOR DEVICE REGISTERS
521          ;
522          DUMMY: 0,100000,0,0 ; DUMMY DEVICE REGISTERS...
523                  0,0,0,0,0,0,0,0 ; ...FOR MULTI-UNIT CHECKOUT.
524
525
526          DUFLG:: .WORD 0 ; "DROPPED UNIT" FLAG.
527                  ; INHIBITS CODE IN "CLEAN-UP".
528          NODEV:: .WORD 0 ; FLAG TO SAY NO DEVICE.
529
530          TEMP1:: .WORD 0 ; SOME TEMP LOCATIONS.
531          TEMP2:: .WORD 0
532          XXCOMM:: .WORD 0 ; XXDP, COMM BLOCK POINTER.
533          FREE:: .WORD 0 ; 1ST FREE MEMORY ADDRESS...
534          FRESIZ:: .WORD 0 ; ...AND SIZE (IN WORDS).
535          FREEHI: .WORD 0 ; LAST WORD IN FREE SPACE
536          KTFLG:: .WORD 0 ; KT11, MEM AVAIL FLAG -
537                  ; - .WORD 0 = <24K OR NO KT -
538                  ; - NZ = >24K AND KT.
539          KTENABLE:: .WORD 0 ; SET BY TEST ROUTINES TO FLAG >28K UNDER TEST
540          NXMFLG:: .WORD 0 ; SET IF WE CAN TEST CLEARED OTHERWISE
541          NXML0:: .WORD 0 ; NXM LO ADDRESS BITS
542          NXMHI:: .WORD 0 ; NXM HI ADDRESS BITS FOR DAL'S 16-21
543          T23A:: .WORD 0 ; 11/23A FLAG
544          T23B:: .WORD 0 ; 11/23B FLAG
545          T3BFLG:: .WORD 0 ; TEST 3B FLAG +0
546          PST32W:: .WORD 2000 ; 32W BLOCK ADDRESS FOR 32K START
547          SIFLAG:: .WORD 0 ;
548          BADDAT:: .WORD 0 ; ACTUAL DATA
549          GDDAT:: .WORD 0 ; EXPECTED DATA
550          LOOPFL:: .WORD 0
551          CTAB:: ; CONFIGURATION TABLES.
552          CTABM:: .WORD 0 ; CONFIG WORK.
553                  .WORD 0
554                  .WORD 0
555                  .WORD 0
556          CTABE:: .WORD -1 ; END OF MEM TABLE.
557          CTABE::
558          ; ERROR STATISTICS TABLE (1 WORD PER UNIT), 64 UNITS MAX:
559          ;
560          ; 0 = UNIT NOT TESTED
561          ; 100000 = UNIT ONLINE, NO ERRORS
562          ; 10XXXX = UNIT ONLINE, ENCOUNTERED XXXX ERRORS
563          ; 160000 = UNIT DROPPED, NON-EXISTENT DEVICE REGISTER
564          ; 160001 = UNIT DROPPED, NOT IDLE AT START
565          ; 14XXXX = UNIT DROPPED, ENCOUNTERED XXXX ERRORS
566          ;
567          ERTABL: .BLKW 64.
568          ERTABE: .WORD 0
569
570          SKIPT: .WORD 0 ; 1=SKIP SUBTEST 0=NO SKIP OF SUBTEST

```

GLOBAL TEXT MESSAGES

```

572          .SBTTL  GLOBAL TEXT MESSAGES
573          ;**
574          ; THE GLOBAL TEXT SECTION CONTAINS FORMAT STATEMENTS,
575          ; MESSAGES, AND ASCII INFORMATION THAT ARE USED IN
576          ; MORE THAN ONE TEST.
577          ; -
578
579          ;*
580          ; NAMES OF DEVICES SUPPORTED
581          ; -
582
583          DEVTYP  <TSV05>
584          L$DVTYP: .ASCIZ  #TSV05#
585          .EVEN
586
587          ;*
588          ; TEST DESCRIPTION
589          ; -
590
591          DESCRIPT <***** TSV05 LOGIC DIAGNOSTIC - REPLACE M7196 IF ERROR *****>
592          L$DESC:  .ASCIZ  /***** TSV05 LOGIC DIAGNOSTIC - REPLACE M7196 IF ERROR *****/
593          .EVEN
594
595          ;*
596          ; BIT TO ASCII CONVERSION FOR TSSR REGISTER
597          ; -
598
599          TSSRBIT: .WORD    1$,2$,3$,4$,5$,6$,7$,8$
600                  .WORD    9$,10$,11$,12$,13$,14$,15$,16$
601
602          1$:      .ASCIZ  'SC'
603          2$:      .ASCIZ  'BIE'
604          3$:      .ASCIZ  'SCE'
605          4$:      .ASCIZ  'RMR'
606          5$:      .ASCIZ  'NXM'
607          6$:      .ASCIZ  'NBA'
608          7$:      .ASCIZ  'BIT9'
609          8$:      .ASCIZ  'BIT8'
610          9$:      .ASCIZ  'SSR'
611          10$:     .ASCIZ  'OFL'
612          11$:     .ASCIZ  'BIT5'
613          12$:     .ASCIZ  'BIT4'
614          13$:     .ASCIZ  'BIT3'
615          14$:     .ASCIZ  'BIT2'
616          15$:     .ASCIZ  'BIT1'
617          16$:     .ASCIZ  'BIT0'
618          .EVEN
619
620          SFIERR: .ASCIZ  'TSSR ERROR AFTER SOFT INIT'
621          SFHERR: .ASCIZ  'TSSR ERROR AFTER BUS RESET'
622          NXR:    .ASCIZ  / NON-EXISTANT DEVICE REGISTER/
623          NXR:    .ASCIZ  /#A ADDRESS: #06/
624          TSSX:   .ASCII  /#A TSBA,TSSR EXP'D: #06#A,#06#N/
625          TSSX:   .ASCIZ  /#A TSBA,TSSR REC'D: #06#A,#06#N/

```

GLOBAL TEXT MESSAGES

```

644 004113      045      116      045 FUSI:  .ASCII /#N#A/
645 004117      040      040      125 USI:  .ASCIZ / UNEXPECTED INTERRUPT/
646 004146      040      040      111 NSI:  .ASCIZ / INTERRUPT EXPECTED, NOT RECEIVED/
647 004211      045      116      045 FNOINTR: .ASCII /#N#A/
648 004215      040      040      116 NOINTR: .ASCIZ / NO INTERRUPT WAS GENERATED/
649 004252      040      040      111 IFALT:  .ASCIZ / INTERRUPT FAULT/
650 004274      045      101      040 INTX:  .ASCIZ /#A CPU PC: #06#A TSBA: #06/
651 004331      040      040      042 NOINIT: .ASCIZ / "BUS INIT" DIDN'T INITIALIZE CONTROLLER/
652 004403      040      040      042 NSINIT: .ASCIZ / "SOFT-INIT" DIDN'T INITIALIZE THE DPU/
653 004453      040      040      042 BRINIT: .ASCIZ / "BUS-RESET" DIDN'T INITIALIZE THE DPU/
654
655 004523      000
656 004524      045      116      000 NUL:  .ASCIZ //
657 004527      045      101      040 NULCR: .ASCIZ /#N/
658 004563      045      116      045 EXPGOT: .ASCIZ /#A EXP'D: #06#A, REC'D: #06/
659 004637      045      101      040 EXPGT2: .ASCIZ /#N#A EXP'D: #06#A, #06#N#A REC'D: #0#A, #06/
660 004741      122      101      040 DUAD12: .ASCIZ /#A REG(W) WRITTEN TO: #06#A REG(R) READ, EXP'D: #06#A, REC'D: #06/
661 005007      040      040      115 PKTRAM: .ASCIZ 'RAM Contents Do Not Match Packet Sent'
662 005052      127      122      103 SCME:  .ASCIZ / CONFIG DOESN'T MATCH MFG. MASTER/
663 005107      124      123      111 WRTMSG: .ASCIZ 'WRITE CHARACTERISTICS Failed'
664 005202      124      123      123 WRTERR: .ASCIZ 'TSSR Incorrect After WRITE Command, More Bits Set Than SSR'
665 005274      106      101      123 RDERR:  .ASCIZ 'TSSR Incorrect After READ Command, More Bits Set Than SSR'
666 005366      105      122      124 SCHERR: .ASCIZ 'FATAL ERROR IN SUBTEST - CHECK TAPE,CABLES,TRANSPORT etc.'
667 005454      045      116      122 RETERR: .ASCIZ 'ERROR IN SUBTEST - WRITE DATA RETRY FIVE TIMES FAILED'
668 005550      045      116      045 NOMEM: .ASCIZ '#N#A ***** NO NXM ADDRESS--CANNOT TEST NXM TIMEOUT. *****N'
669 005641      045      116      045 M8186: .ASCIZ '#N#A ***** 11/23A SYSTEM *****N'
670
671
672
673
673

```


GLOBAL ERROR REPORT SECTION

```

675                                     .SBTTL GLOBAL ERROR REPORT SECTION
676
677
678                                     ;**
679                                     ; THE GLOBAL ERROR REPORT SECTION CONTAINS THE PRINTB AND PRINTX
680                                     ; CALLS THAT ARE USED IN MORE THAN ONE TEST.
681                                     ; ASCII TEXT STRINGS ARE FOUND IN THE GLOBAL TEXT SECTION.
682                                     ; -
683 005732                                BGNMSG  NXRERR                                ;NON EXISTANT DEVICE REGISTER.
684 005732                                NXRERR::
685 005732 013746 003110                    PRINTX  #NXRX,NODEV                    ;NODEV = NEXM ADDRESS.
686 005736 012746 003773                    MOV     NODEV,-(SP)
687 005742 012746 000002                    MOV     #NXRX,-(SP)
688 005746 010600                            MOV     #2,-(SP)
689 005750 104415                            TRAP    C$PNTX
690 005752 062706 000006                    ADD     #6,SP
691 005756 004737 005764                    JSR     PC,EXTEND                    ; PRINT EXTENSION IF REQUIRED.
692 005762
693 005762 104423                    L10002:
694                                     TRAP    C$MSG
695
696                                     ;
697                                     ; THIS ROUTINE APPENDS A UNIQUE EXTENSION (IF REQUIRED)
698                                     ; TO ANY OF THE ABOVE ERROR SIGNATURES.
699                                     ;
700 EXTEND: TST      (PC),
701 EXTA:  0
702        BEQ      1$
703        JSR      PC,EXTA
704        PRINTX   #NULCR
705        MOV      #NULCR,-(SP)
706        MOV      #1,-(SP)
707        MOV      SP,RO
708        TRAP     C$PNTX
709        ADD      #4,SP
710        RTS      PC
711
712 1$:
713 005776 012746 004524                    PRINTX  #NULCR
714 006002 012746 000001                    MOV     #NULCR,-(SP)
715 006006 010600                            MOV     #1,-(SP)
716 006010 104415                            TRAP    C$PNTX
717 006012 062706 000004                    ADD     #4,SP
718 006016 000207

```

PRITSSR PRINT TSSR CONTENTS

```

701                                     .SBTTL PRITSSR - PRINT TSSR CONTENTS
702
703
704
705
706
707
708
709
710
711
712
713
714
715
716
717
718
719 006020 PRITSSR:
720 006020 SAVREG
721 006024 010104 MOV R1,R4
722 006026 PRINTB #TSSRFOR,R4
006026 010446 MOV R4,-(SP)
006030 012746 006411 MOV #TSSRFOR,-(SP)
006034 012746 000002 MOV #2,-(SP)
006040 010600 MOV SP,R0
006042 104414 TRAP C:PNTB
006044 062706 000006 ADD #6,SP
723 006050 010400 MOV R4,R0
724 006052 004737 016034 JSR PC,CHKAMB
725 006056 103410 BCS 5:
726 006060 PRINTX #AMBTSSR
006060 012746 006631 MOV #AMBTSSR,-(SP)
006064 012746 000001 MOV #1,-(SP)
006070 010670 MOV SP,R0
006072 104415 TRAP C:PNTX
006074 062706 000004 ADD #4,SP
727 006100 010403 5: MOV R4,R3
728 006102 042703 001476 BIC #HIADDR:FATERR:TERCLS,R3
729 006106 001434 BEQ 20:
730 006110 012702 002630 MOV #TMPBFR,R2
731 006114 012701 003476 MOV #TSSRBIT,R1
732 006120 005703 10: TST R3
733 006122 001413 BEQ 15:
734 006124 000241 CLC
735 006126 006103 ROL R3
736 006130 103006 BCC 13:
737 006132 011100 MOV (R1),R0
738 006134 112022 11: MOV (R0),R2
739 006136 001376 BNE 11:
740 006140 112762 000054 177777 MOV #1,R2
741 006146 005721 13: TST (R1)
742 006150 000763 BR 10:
743 006152 105042 15: CLRB
744 006154 PRINTX #TSSDEF,#TMPBFR
006154 012746 002630 MOV #TMPBFR,(SP)
006160 012746 006602 MOV #TSSDEF,(SP)

```


PRITSSR PR'NT TSSR CONTENTS

```

006164 012746 000002      MOV      #2,-(SP)
006170 010600      MOV      SP,R0
006172 104415      TRAP     C$PNTX
006174 062706 000006      ADD      #6,SP

745
746 006200 010403      20$:  MOV      R4,R3      ;GET THE TSSR CONTENTS
747 006202 042703 177761 BIC      #+CTERCLS,R3 ;CLEAR ALL BUT TERMINATION
748 006206 016303 006672 MOV      TCOCOD(R3),R3 ;GET THE TERMINATION CODE MEANING
749 006212      PRINTX   #TCOASC,R3 ;PRINT THE TERMINATION CODE
      006212 010346      MOV      R3,-(SP)
      006214 012746 006472 MOV      #TCOASC,-(SP)
      006220 012746 000002 MOV      #2,-(SP)
      006224 010600      MOV      SP,R0
      006226 104415      TRAP     C$PNTX
      006230 062706 000006 ADD      #6,SP
750 006234 010403      MOV      R4,R3      ;TSSR CONTENTS AGAIN
751 006236 042703 177717 BIC      #+CFATERR,R3 ;CLEAR ALL BUT FATAL TERMINATION
752 006242 001416      BEQ      25$      ;DON'T PRINT IF ZERO
753 006244 006203      ASR      R3
754 006246 006203      ASR      R3
755 006250 006203      ASR      R3      ;ALINE TERMINATION CODE FOR INDEX
756 006252 016303 007232 MOV      TSFCOD(R3),R3 ;GET THE FATAL TERMINATION CODE
757 006256      PRINTX   #TFCASC,R3 ;PRINT THE FATAL TERMINATION CODE
      006256 010346      MOV      R3,-(SP)
      006260 012746 006533 MOV      #TFCASC,-(SP)
      006264 012746 000002 MOV      #2,-(SP)
      006270 010600      MOV      SP,R0
      006272 104415      TRAP     C$PNTX
      006274 062706 000006 ADD      #6,SP
758 006300 042704 176377      25$: BIC      #+CHIADDR,R4 ;CLEAR ALL BUT EXTENDED ADDRESS
759 006304 001411      BEQ      30$      ;DON'T PRINT IF ZERO
760 006306      PRINTX   #TEXASC,R4 ;PRINT THE EXTENDED ADDRESS BITS
      006306 010446      MOV      R4,-(SP)
      006310 012746 006431 MOV      #TEXASC,-(SP)
      006314 012746 000002 MOV      #2,-(SP)
      006320 010600      MOV      SP,R0
      006322 104415      TRAP     C$PNTX
      006324 062706 000006 ADD      #6,SP
761 006330 013703 002176      30$: MOV      EPRTSW,R3      ;PRINT MESSAGE BUFFER ADDRESS
762 006334      PRINTX   R3      ;PRINT PROPER MESSAGE
      006334 010346      MOV      R3,-(SP)
      006336 012746 000001 MOV      #1,-(SP)
      006342 010600      MOV      SP,R0
      006344 104415      TRAP     C$PNTX
      006346 062706 000004 ADD      #4,SP
763 006352 000207      RTS      PC      ;RETURN TO CALLER
764
766 006354      EPRT2:
767 006354      045      116      045 EPRT1: .ASCIZ '###A *****REPLACE M7196*****'
782 006411      045      116      045 TSSRFOR: .ASCIZ '###A TSSR = #06'
783 006431      045      116      045 TEXASC: .ASCIZ '###A Extended Address Bits = #06'
784 006472      045      116      045 TCOASC: .ASCIZ '###A Termination Class Code = #T'
785 006533      045      116      045 TFCASC: .ASCIZ '###A Fatal Termination Class Code = #T'
786 006602      045      116      045 TSSDEF: .ASCIZ '###A TSSR Bits Set: #T'
787 006631      045      116      045 AMBTSSR: .ASCIZ '###A TSSR Contents Are Ambiguous'
788
789 006672 006712 006735 006763 TCOCOD: .WORD 1$,2$,3$,4$,5$,6$,7$,8$

```

PRITSSR - PRINT TSSR CONTENTS

790	006712	116	157	162	1:	.ASCIZ	'Normal Termination'
791	006735	124	145	162	2:	.ASCIZ	'Termination Condition'
792	006763	124	141	160	3:	.ASCIZ	'Tape Status Alert'
793	007005	106	165	156	4:	.ASCIZ	'Function Reject'
794	007025	122	145	143	5:	.ASCIZ	'Recoverable Error Tape Position One Record Down'
795	007107	122	145	143	6:	.ASCIZ	'Recoverable Error - Tape Was Not Moved'
796	007156	125	156	162	7:	.ASCIZ	'Unrecoverable Error'
797	007202	106	141	164	8:	.ASCIZ	'Fatal Controller Error'
798						.EVEN	
799							
800	007232	007242	007276	007307	TSFCOD:	.WORD	1,2,3,4
801	007242	111	156	164	1:	.ASCIZ	'Internal Diagnostic Failure'
802	007276	122	145	163	2:	.ASCIZ	'Reserved'
803	007307	102	165	163	3:	.ASCIZ	'Bus Interface or Sanity Check Error'
804	007353	122	145	163	4:	.ASCIZ	'Reserved'
805						.EVEN	

PRIPKT - PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET

```

807 .SBTTL PRIPKT PRINT THE ADDRESS/CONTENTS OF COMMAND PACKET
808
809 ;*
810 ;THIS ROUTINE PRINTS THE ADDRESS AND CONTENTS OF A COMMAND PACKET.
811 ;THIS ROUTINE IS NORMALLY ONLY CALLED FROM A PRINT ROUTINE.
812 ;
813 ;INPUT:
814 ;
815 ; R0 NUMBER OF WORDS IN PACKET
816 ; R3 HIGH ORDER COMMAND PACKET ADDRESS
817 ; R4 ADDRESS OF COMMAND PACKET
818 ;
819 ; NOTE: R3 IS ICNORED IF THE KTENABLE FLAG IS CLEAR.
820 ;
821
822 PRIPKT::
823 SAVREG ;SAVE THE REGISTERS
824 MOV R0,R5 ;SAVE NO. OF WORDS IN PACKET
825 TST KTENABLE ;ABOVE 28K UNDER TEST?
826 BNE 10$ ;BR IF YES
827 CLR R3 ;SET HIGH ORDER ADDRESS TO 0
828 10$: MOV R3,R1 ;COPY HIGH ORDER ADDRESS
829 MOV R4,R0 ;GET LOWER ADDRESS
830 ROL R0 ;SHIFT BIT 15 INTO C BIT
831 ROL R1 ;AND INTO HIGH ORDER.
832 PRINTB @PKTADD,R1,R4 ;PRINT PACKET ADDRESS
      MOV R4,-(SP)
      MOV R1,-(SP)
      MOV @PKTADD,-(SP)
      MOV @3,-(SP)
      MOV SP,R0
      TRAP C:PNTB
      ADD @10,SP
833 15$: MOV R3,R0 ;GET HIGH ORDER ADDRESS
834 BEQ 20$ ;BR IF NOT ABOVE 28K.
835 MOV R4,R1 ;GET LOW ORDER ADDRESS
836 JSR PC,SETMAP ;SETUP PAR6 MAPPING FOR 18 BIT ADDRESS
837 MOV R0,R4 ;GET RETURNED PAR6 ADDRESS BIAS
838 20$: CLR R1 ;SAVE WORD NUMBER
839 25$: MOV (R4)+,R2 ;GET PACKET CONTENTS
840 PRINTB @PKTFRM,R1,R2 ;PRINT THE DATA
      MOV R2,-(SP)
      MOV R1,-(SP)
      MOV @PKTFRM,(SP)
      MOV @3,-(SP)
      MOV SP,R0
      TRAP C:PNTB
      ADD @10,SP
841 INC R1 ;NEXT WORD NUMBER
842 CMP R1,R5 ;DONE ALL PACKET WORDS?
843 BLT 25$ ;LOOP TILL ALL DONE
844 RTS PC ;RETURN
845
846 007512 045 116 045 PKTFRM: .ASCIZ 'N#A Packet Word #D1#A = #06'
847 007550 045 116 045 PKTADD: .ASCIZ 'N#A Packet Address = #01#05'
848 .EVEN
849

```

PRIBXOR PRINT EXPD, RECV AND XOR BYTE

```

851                                     .SBTTL PRIBXOR - PRINT EXPD, RECV AND XOR BYTE
852
853                                     ;*
854                                     ;
855                                     ;PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE DATA BYTE
856                                     ;THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
857                                     ;
858                                     ;INPUTS:
859                                     ;
860                                     ;      R1      RECEIVED DATA
861                                     ;      R2      EXPECTED DATA
862                                     ;
863                                     ;OUTPUT:
864                                     ;
865                                     ;      R0      XOR OF EXPECTED/RECEIVED DATA
866                                     ;
867                                     ;-
868
869 007606 PRIBXOR::
870 007606 SAVREG                                ;SAVE THE REGISTERS
871 007612 010203 MOV      R2,R3                ;EXPECTED DATA
872 007614 XOR      R1,R3                ;FORM THE EXCLUSIVE OR
873 007624 012700 177400 MOV      #C<377>,R0    ;BYTE MASK
874 007630 040001 BIC      R0,R1                ;SAVE LOW BYTE RECV
875 007632 040002 BIC      R0,R2                ;SAVE LOW BYTE EXPD
876 007634 040003 BIC      R0,R3                ;SAVE LOW BYTE XOR
877 007636 PRINTB  #XORBFOR,R2,R1,R3 ;PRINT THE MESSAGE
      007636 010346 MOV      R3,-(SP)
      007640 010146 MOV      R1,-(SP)
      007642 010246 MOV      R2,-(SP)
      007644 012746 007670 MOV      #XORBFOR,-(SP)
      007650 012746 000004 MOV      #4,-(SP)
      007654 010600 MOV      SP,R0
      007656 104414 TRAP     C#PNTB
      007660 062706 000012 ADD      #12,SP
878 007664 010300 MOV      R3,R0                ;R0 HAS XOR ON RETURN
879 007666 000207 RTS      PC                ;RETURN TO CALLER
880
881 007670 045      116      045 XORBFOR: .ASCIZ 'N#A EXPD: #03#A RECV: #03#A XOR: #03'
882                                     .EVEN
883

```

PRIXOR PRINT EXPD, RECV AND XOR

```

885 .SBTTL PRIXOR - PRINT EXPD, RECV AND XOR
886
887 ;*
888 ;
889 ;PRINT EXPECTED DATA, RECEIVED DATA, AND XOR OF THE TWO
890 ;THIS ROUTINE IS NORMALLY CALLED ONLY FOR PRINT ROUTINES.
891 ;
892 ;INPUTS:
893 ;
894 ;      R1      RECEIVED DATA
895 ;      R2      EXPECTED DATA
896 ;
897 ;OUTPUT:
898 ;
899 ;      R0      XOR OF EXPECTED/RECEIVED DATA
900 ;
901 ;-
902
903 PRIXOR::
904     SAVREG                ;SAVE THE REGISTERS
905     MOV      R2,R3        ;EXPECTED DATA
906     XOR      R1,R3        ;FORM THE EXCLUSIVE OR
907     PRINTB   @XORFOR,R2,R1,R3 ;PRINT THE MESSAGE
908     MOV      R3,-(SP)
909     MOV      R1,-(SP)
910     MOV      R2,-(SP)
911     MOV      @XORFOR,-(SP)
912     MOV      @4,-(SP)
913     MOV      SP,R0
914     TRAP     C$PNTB
915     ADD      @12,SP
916     MOV      R3,R0        ;R0 HAS XOR ON RETURN
917     RTS      PC          ;RETURN TO CALLER
918
919 045 XORFOR: .ASCIZ 'N#A EXPD: #06#A RECV: #06#A XOR: #06#A'
920 .EVEN

```

PRIEQU PRINT BIT NUMBERS AS ASCII EQUIVALENT

```

914 .SBTTL PRIEQU - PRINT BIT NUMBERS AS ASCII EQUIVALENT
915
916 ;*
917 ;
918 ;ROUTINE TO CONVERT BIT VALUES TO ASCII AND PRINT THE STRING
919 ;THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE
920 ;
921 ;INPUTS:
922 ;
923 ; R0 OCTAL VALUE TO CONVERT
924 ; R1 TABLE OF POINTERS TO ASCII EQUIVALENT
925 ;
926 ;
927
928 PRIEQU: SAVREG ;SAVE THE REGISTERS
929 010054 RTS PC ;RETURN TO CALLER
930 010060 000207
931
932
933
934
935 .SBTTL PRIRAM - PRINT RAM ADDRESS
936
937 ;*
938 ;
939 ;PRINT CONTROLLER RAM ADDRESS.
940 ;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
941 ;
942 ;INPUTS:
943 ;
944 ; R4 RAM ADDRESS
945 ;
946
947 PRIRAM: SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
948 010062 PRINTB #RAMFOR,R4 ;PRINT RAM ADDRESS IN ERROR
949 010066 010446 MOV R4,-(SP)
950 010070 012746 010112 MOV #RAMFOR,-(SP)
951 010074 012746 000002 MOV #2,-(SP)
952 010100 010600 MOV SP,R0
953 010102 104414 TRAP C#PNTB
954 010104 062706 000006 ADD #6,SP
955 010110 000207 RTS PC ;RETURN
956
957
958
959
960
961
962
963
964
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PRIADD PRINT MEMORY ERROR ADDRESS

```

965
966
967 010154
968 010154
969 010160 013700 002234
970 010164 013701 002236
971 010170 010102
972 010172 006101
973 010174 006100
974 010176
    010176 010246
    010200 010046
    010202 012746 010224
    010206 012746 000003
    010212 010600
    010214 104414
    010216 062706 000010
975 010222 000207
976
977 010224 045 116 045 PRIA0: .ASCIZ 'MEMORY ERROR ADDRESS = 0105'
978 .EVEN
979
980
981 .SBTTL PRITADD - PRINT MEMORY TEST ADDRESS
982
983
984
985
986
987
988
989
990
991
992
993 010270
994 010270
995 010274 013702 002234
996 010300 013701 002236
997
998
999
1000 010304
    010304 010146
    010306 012746 010352
    010312 012746 000002
    010316 010600
    010320 104414
    010322 062706 000006
1001 010326
    010326 010246
    010330 012746 010415
    010334 012746 000002
    010340 010600
    010342 104414
    010344 062706 000006
1002 010350 000207

```

```

;
;
PRIADD:
    SAVREG
    MOV     ERRHI,R0
    MOV     ERRLO,R1
    MOV     R1,R2
    ROL     R1
    ROL     R0
    PRINTB  @PRIA0,R0,R2
    MOV     R2,-(SP)
    MOV     R0,-(SP)
    MOV     @PRIA0,-(SP)
    MOV     @3,-(SP)
    MOV     SP,R0
    TRAP    C:PNTB
    ADD     @10,SP
    RTS     PC
;SAVE R1-R5 UNTIL NEXT RETURN
;GET HIGH ADDRESS
;GET LOW ADDRESS
;COPY LOW ADDRESS
;SHIFT BIT 15 TO C BIT
;SHIFT INTO HIGH ORDER
;PRINT MEMORY ADDRESS IN ERROR
;RETURN

045 PRIA0: .ASCIZ 'MEMORY ERROR ADDRESS = 0105'
.EVEN

.SBTTL PRITADD - PRINT MEMORY TEST ADDRESS
;
;
;PRINT MEMORY ADDRESS
;THIS ROUTINE IS NORMALLY CALLED ONLY FROM PRINT ROUTINES.
;
; IMPLICIT INPUTS
;
;     ERRHI - HIGH ORDER ADDRESS
;     ERRLO - LOW ORDER ADDRESS
;
;-
PRITADD:
    SAVREG
    MOV     ERRHI,R2
    MOV     ERRLO,R1
    MOV     R1,R2
    ROL     R1
    ROL     R0
    PRINTB  @PRIT0,R1
    MOV     R1,-(SP)
    MOV     @PRIT0,-(SP)
    MOV     @2,-(SP)
    MOV     SP,R0
    TRAP    C:PNTB
    ADD     @6,SP
    PRINTB  @PRIT1,R2
    MOV     R2,-(SP)
    MOV     @PRIT1,-(SP)
    MOV     @2,-(SP)
    MOV     SP,R0
    TRAP    C:PNTB
    ADD     @6,SP
    RTS     PC
;SAVE R1-R5 UNTIL NEXT RETURN
;GET HIGH ADDRESS
;GET LOW ADDRESS
;COPY LOW ADDRESS
;SHIFT BIT 15 TO C BIT
;SHIFT INTO HIGH ORDER
;PRINT MEMORY ADDRESS LOW IN ERROR
;PRINT MEMORY ADDRESS HIGH IN ERROR
;RETURN

```


SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

.SBTTL SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

```

1011                                     .SBTTL  SPACE  - SPACE RECORDS (FORWARD AND REVERSE) COMMAND
1012
1013                                     ;+
1014                                     ;
1015                                     ;ROUTINE TO ISSUE A SPACE RECORDS
1016                                     ;COMMAND (FORWARD OR REVERSE)
1017                                     ;
1018                                     ;INPUT:
1019                                     ;
1020                                     ;      R3      NUMBER OF RECORDS TO BE SPACED OVER
1021                                     ;              BIT15 CONTROLS DIRECTION
1022                                     ;              BIT15 = 0 IS FORWARD
1023                                     ;              BIT15 = 1 IS REVERSE
1024                                     ;      R5      FIRST DEVICE UNIBUS ADDRESS
1025                                     ;
1026                                     ;      REQUIRES A WRITE CHARACTERISTICS DONE PREVIOUSLY
1027                                     ;
1028                                     ;OUTPUT:
1029                                     ;
1030                                     ;      CARRY    SET - SPACE RECORDS COMMAND OK
1031                                     ;              CLR - SPACE RECORDS FAILED
1032                                     ;
1033                                     ;
1034                                     ;      R0      THE CONTENTS OF R4 IS MOVED TO R0
1035                                     ;
1036                                     ;
1037                                     ;IMPLICIT OUTPUT:
1038                                     ;
1039                                     ;      TAPE HAS BEEN MOVED
1040                                     ;
1041                                     ;SIDE EFFECTS:
1042                                     ;
1043                                     ;
1044                                     ;-
1045
1046 010462                                SPACE::
1047 010462                                SAVREG                                ;SAVE THE GENERAL REGISTERS
1048 010466 012737 000764 010650          MOV      #500.,SDELAY                ;SET UP DELAY
1049 010474 012737 140010 010640          MOV      #140010,80$              ;SET UP COMMAND, SPACE FORWARD
1050 010502 005703                        TST      R3                        ;CHECK FOR DIRECTION
1051 010504 100403                        BMI      5$                        ;BR, IF REVERSE INDICATED
1052 010506 010337 010642                MOV      R3,90$                    ;LOAD UP NUMBER OF RECORDS TO SPACE
1053 010512 000407                        BR       10$                      ;GO DO COMMAND
1054 010514 042703 100000                5$:  BIC      #BIT15,R3              ;CLEAR DIRECTION BIT
1055 010520 010337 010642                MOV      R3,90$                    ;LOAD UP NUMBER OF RECORDS TO SPACE
1056 010524 052737 000400 010640          BIS      #BIT8,80$              ;SET REVERSE BIT IN COMMAND PACKET
1057 010532 012704 010640                10$:  MOV      #80$,R4              ;SET UP R4 WITH PACKET ADDRESS
1058 010536 010465 000000                MOV      R4,TSDB(R5)              ;SEND OUT COMMAND
1059 010542 004737 016240                15$:  JSR      PC,WAITF              ;WAIT FOR SSR
1060 010546 103420                        BCS      20$                      ;BR, IF SSR IS SET AND OK
1061 010550                        DELAY      250                          ;DELAY ABOUT .25 SECONDS
1062 010550 012727 000250                MOV      #250,(PC)+
1063 010554 000000                        .WORD      0
1064 010556 013727 002116                MOV      L$DLY,(PC)+
1065 010562 000000                        .WORD      0
1066 010564 005367 177772                DEC      -6(PC)
1067 010570 001375                        BNE      . 4

```

SPACE - SPACE RECORDS (FORWARD AND REVERSE) COMMAND

	010572	005367	177756		DEC	-22(PC)	
	010576	001367			BNE	.-20	
1062	010600	005337	010650		DEC	SDELAY	;BUMP DELAY COUNTER DOWN
1063	010604	001356			BNE	15\$	;BR, IF MORE DELAY
1064	010606	000411			BR	60\$	;BR IF TROUBLE CARRY = CLEAR
1065	010610	016501	000002	20\$:	MOV	TSSR(R5),R1	;READ TSSR
1066	010614	012702	000200		MOV	SSR,R2	;SET UP EXPECTED
1067	010620	020201		25\$:	CMP	R2,R1	;ARE THEY OK
1068	010622	001401			BEQ	40\$	;BR, IF EQUAL = OK
1069	010624	000402			BR	60\$	;TROUBLE EXIT
1070	010626	000261		40\$:	SEC		;SET CARRY NO TROUBLE
1071	010630	000401			BR	70\$	;EXIT
1072	010632	000241		60\$:	CLC		;CARRY CLEAR = ERROR
1073	010634			70\$:			
1074	010634	010400			MOV	R4,R0	;PASS PACKET ADDRESS
1075	010636	000207			RTS	PC	;RETURN

SPACE SPACE RECORDS (FORWARD AND REVERSE) COMMAND

1077			:		
1078			:		
1079			:		
1080			:	PACKET FOR SPACE COMMAND	
1081			:		
1085			:		
1086			:	COMMAND WORD	
1087	010640	000000	801:	.WORD	
1088			:	NUMBER OF RECORDS TO BE SPACED OVER WORD	
1089	010642	000000	901:	.WORD	
1090	010644	000000		.WORD	
1091	010646	000000		.WORD	
1092	010650	000000	SDELAY:	.WORD 0	:DELAY COUNTER
1093				.EVEN	

WRTHMR WRITE CHARACTERISTICS COMMAND

```

1095          .SBTTL WRTHMR - WRITE CHARACTERISTICS COMMAND
1096
1097          ;
1098          ;
1099          ;ROUTINE TO ISSUE A WRITE CHARACTERISTICS
1100          ;COMMAND SO THAT OTHER COMMANDS WILL BE ACCEPTED
1101          ;
1102          ;INPUT:
1103          ;
1104          ;      R4      ADDRESS OF PACKET FROM TEST
1105          ;      R5      FIRST DEVICE UNIBUS ADDRESS
1106          ;      REQUIRES A CALL TO SOFINIT BE DONE PREVIOUSLY
1107          ;
1108          ;OUTPUT:
1109          ;
1110          ;      R0      TSSR CONTENTS
1111          ;      CARRY   SET - WRITE CHARACTERISTICS COMMAND OK
1112          ;              CLR - WRITE CHARACTERISTICS FAILED
1113          ;
1114          ;IMPLICIT OUTPUT:
1115          ;
1116          ;      MESSAGE BUFFER AND OTHER BUFFERS ALL SET UP
1117          ;      SOFTWARE SWITCHES SET AS FOLLOWS:
1118          ;              EXTFEA = EXTENDED FEATURES PRESENT
1119          ;              BENBSW = BUFFER ENABLE SWITCH ON OR OFF
1120          ;
1121          ;
1122          ;SIDE EFFECTS:
1123          ;
1124          ;
1125          ;-
1126
1127 010652 WRTHMR::
1128 010652          SAVREG
1129 010656          CLR BENBSW          ;SAVE THE GENERAL REGISTERS
1130 010662          CLR EXTFEA          ;CLEAR BUFFER ENABLE SWITCH
1131 010666          005037 002226          ;CLEAR EXTENDED FEATURES SW SWITCH
1132 010672          004737 016326          ;SEND OUT COMMAND
1133 010676          103401          ;WAIT FOR SSR
1134 010700          000435          ;BR, IF SSR IS SET AND OK
1135 010702          016501 000002          ;BR IF TROUBLE CARRY = CLEAR
1136 010706          012702 000200          ;READ TSSR
1137 010712          032701 000100          ;SET UP EXPECTED
1138 010716          001402          ;WAS OFF LINE SET IN TSSR
1139 010720          052702 000100          ;BR, IF NO OFL SET
1140 010724          020201          ;MAKE THEM LOOK ALIKE
1141 010726          001401          ;ARE THEY OK
1142 010730          000421          ;BR, IF EQUAL = OK
1143 010732          062704 000010          ;TROUBLE EXIT
1144 010736          011403          ;POINT TO WRT CHARA DATA PACKET
1145 010740          032763 000200 000012  ;GET ADDRESS OF MESSAGE BUFFER
1146 010746          001402          ;EXTENDED FEATURES BIT SET?
1147 010750          005237 002224          ;BR IF NO
1148 010754          032763 000100 000012  ;SET EXTENDED FEATURES SW SWITCH
1149 010754          001402          ;BUFFER ENABLE SWITCH SET
1150 010762          005237 002226          ;BR, IF SWITCH NOT SET
1151 010764          005237 002226          ;SET SOFTWARE SWITCH FOR ENABLED

```

D5

SEQ 0055

WRITCHR WRITE CHARACTERISTICS COMMAND

1152	010770		501:			
1153	010770	000261		SEC		;SET CARRY NO TROUBLE
1154	010772	000401		BR	701	;EXIT
1155	010774	000241	601:	CLC		;CARRY CLEAR = ERROR
1156	010776	016500 000002	701:	MOV	TSSR(R5),R0	;RETURN TSSR CONTENTS
1157	011002	000207		RTS	PC	;RETURN
1158						
1159						

REWIND - POSITION TAPE (REWIND) COMMAND

```

1161 .SBTTL REWIND POSITION TAPE (REWIND) COMMAND
1162
1163 ;*
1164 ;
1165 ; THIS ROUTINE WILL REWIND THE SELECTED TAPE.
1166 ;
1167 ; CAUTION: THE ROUTINE DOES NOT WAIT FOR BOT
1168 ; TO ARRIVE. ALSO THE CALLER MUST CHECK FOR
1169 ; SSR TO SET IN THE TSSR
1170 ;
1171 ;
1172 ; CALLING SEQUENCE:
1173 ;
1174 ; DO A SOFT INIT
1175 ; DO A WRITE CHARACTERISTICS
1176 ; JSR PC,REWIND
1177 ;
1178 ; INPUT:
1179 ;
1180 ; R5 FIRST DEVICE UNIBUS ADDRESS
1181 ;
1182 ;
1183 ; OUTPUT
1184 ;
1185 ; R0 THE CONTENTS OF R4 IS PASSED TO R0
1186 ;
1187 ;
1188 ;
1189 REWIND::
1190 SAVREG
1191 MOV @RWPACK,R4 ;SAVE R1-R5 UNTIL NEXT RETURN
1192 MOV R4,TSDB(R5) ;GET PACKET ADDRESS
1193 MOV @360.,R3 ;SEND PACKET ADDRESS TO EXECUTE
1194 JSR PC,WAITF ;ENOUGH TIME FOR 2400' REEL TO REWIND
1195 BCS 20$ ;WAIT FOR SSR TO SET
1196 DELAY 250. ;LEAVE WHEN SSR IS SET
1197 MOV @250.,(PC). ;WAIT FOR .25 SECONDS
1198 .WORD 0
1199 MOV L$DLY,(PC).
1200 .WORD 0
1201 DEC -6(PC)
1202 BNE .-4
1203 DEC -22(PC)
1204 BNE .-20
1205 DEC R3 ;BUMP COUNTER DOWN
1206 BNE 10$ ;KEEP GOING
1207 CLC ;CLEAR CARRY TO SET ERROR
1208 MOV R4,R0 ;PASS THE PACKET ADDRESS
1209 RTS PC ;RETURN
1210
1211 RWPACK: .-<..10>E177770
1212 .WORD 102010 ;POSITION COMMAND (REWIND)
1213 .WORD 0 ;NOT USED

```


CKRAM - COMPARE RAM TO I/O PACKET

```

1212          .SBTTL  CKRAM  - COMPARE RAM TO I/O PACKET
1213
1214          ;*
1215          ;
1216          ;ROUTINE TO READ THE FIRST 8 BYTES FROM RAM
1217          ;MEMORY AND COMPARE THIS DATA TO A COMMAND PACKET.
1218          ;
1219          ;INPUT:
1220          ;
1221          ;      R4      ADDRESS OF THE COMMAND PACKET
1222          ;      R5      FIRST DEVICE UNIBUS ADDRESS
1223          ;
1224          ;OUTPUT:
1225          ;
1226          ;      CARRY   SET - RAM MATCHES PACKET
1227          ;              CLR - RAM DOES NOT MATCH PACKET
1228          ;
1229          ;IMPLICIT OUTPUT:
1230          ;
1231          ;      THE TABLE RAMDATA IS FILLED WITH THE
1232          ;      DATA HELD IN R4.
1233          ;      RAMSIZ IS SET TO 8. FOR PRAMPKT ROUTINE
1234          ;
1235          ;SIDE EFFECTS:
1236          ;
1237          ;      THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
1238          ;
1239          ;-
1240
1241          CKRAM::
1242          SAVREG
1243          MOV     @RAMDATA,R1          ;SAVE THE GENERAL REGISTERS
1244          MOV     @RMPKTBEG,R2        ;ADDRESS TO SAVE THE RAM DATA
1245          CLR     R3                  ;BYTE ADDRESS OF FIRST RAM DATA
1246          JSR     PC,CHKTSSR          ;CLEAR THE ERROR FLAG
1247          JSR     PC,CHKTSSR          ;WAIT FOR SSR
1248          MOV     R2,TSDB(R5)         ;SET MAINTENANCE MODE
1249          JSR     PC,CHKTSSR          ;WAIT FOR SSR TO SET
1250          JSR     PC,CHKTSSR          ;SELECT NEXT RAM ADDRESS
1251          MCVB    TSBA(R5),(R1)       ;WAIT FOR SSR TO SET
1252          CMPB    (R1),.(R4)         ;READ THE RAM DATA
1253          BEQ     20$                ;COMPARE TO EXPECTED
1254          INC     R3                  ;BRANCH IF OK
1255          INC     R2                  ;SET ERROR FLAG
1256          CMP     R2,@RMPKTEND       ;ADDRESS OF NEXT RAM LOCATION
1257          BLE     10$                ;REACHED END YET ?
1258          TST     R3                  ;BRANCH TILL ALL READ
1259          BEQ     30$                ;WAS AN ERROR FOUND ?
1260          CLC                     ;BRANCH IF NOT
1261          BR      50$                ;CLEAR CARRY TO SHOW ERROR
1262          SEC                     ;AND EXIT
1263          MOV     @R.,RAMSIZ         ;SHOW GOOD COMPARE
1264          RTS     PC                 ;SETUP RAMSIZ FOR PRAMPKT ROUTINE
1265                                     ;RETURN

```

CKRAM2 COMPARE RAM TO I/O CHARACTERISTICS DATA

```

1267 .SBTTL CKRAM2 COMPARE RAM TO I/O CHARACTERISTICS DATA
1268 ;*
1269 ;
1270 ;ROUTINE TO READ THE FIRST 8 OR 10 BYTES FROM RAM
1271 ;MEMORY AND COMPARE THIS DATA TO A CHARACTERISTICS DATA BLOCK.
1272 ;
1273 ;INPUT:
1274 ;
1275 ; R4 ADDRESS OF THE CHARACTERISTICS DATA
1276 ; R5 FIRST DEVICE UNIBUS ADDRESS
1277 ;
1278 ;OUTPUT:
1279 ;
1280 ; CARRY SET - RAM MATCHES PACKET
1281 ; CLR - RAM DOES NOT MATCH PACKET
1282 ;
1283 ;IMPLICIT OUTPUT:
1284 ;
1285 ; THE TABLE RAMDATA IS FILLED WITH THE
1286 ; DATA HELD IN RAM.
1287 ; RAMSIZ IS SET TO 8. OR 10. FOR PRAMPKT ROUTINE
1288 ;
1289 ;SIDE EFFECTS:
1290 ;
1291 ; THE SUBSYSTEM IS LEFT IN MAINTENANCE MODE
1292 ;
1293 ;-
1294
1295 011214 CKRAM2:: SAVREG ;SAVE THE GENERAL REGISTERS
1296 011214 MOV #RAMDATA,R1 ;ADDRESS TO SAVE THE RAM DATA
1297 011220 012701 002240 MOV #RMCHREG,R2 ;BYTE ADDRESS OF FIRST RAM DATA
1298 011224 012702 000167 CLR R3 ;CLEAR THE ERROR FLAG
1299 011230 005003 JSR PC,CHKTSSR ;WAIT FOR SSR
1300 011232 004737 016326 JSR #0,TSDB(R5) ;SET MAINTENANCE MODE
1301 011236 112765 000000 000000 10$: MOVB PC,CHKTSSR ;WAIT FOR SSR TO SET
1302 011244 004737 016326 JSR R2,TSDB(R5) ;SELECT NEXT RAM ADDRESS
1303 011250 010265 000000 JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
1304 011254 004737 016326 MOVB TSBA(R5),(R1) ;READ THE RAM DATA
1305 011260 116511 000000 CMPB (R1)+,(R4)+ ;COMPARE TO EXPECTED
1306 011264 122124 BEQ 20$ ;BRANCH IF OK
1307 011266 001401 INC R3 ;SET ERROR FLAG
1308 011270 005203 INC R2 ;ADDRESS OF NEXT RAM LOCATION
1309 011272 005202 20$: MOV #8.,RAMSIZ ;ASSUME EXTFEA NOT SET
1310 011274 012737 000010 002300 TST EXTFEA ;IS THE SOFTWARE EXTENDED FEATURES SET
1311 011302 005737 002224 BEQ 25$ ;BR, IF NOT SET
1312 011306 001407 25$: MOV #10.,RAMSIZ ;SET RAMSIZ FOR EXTEND FEATURES
1313 011310 012737 000012 002300 CMP R2,#RMCHEND ;AT END OF EXTENDED BUFFER
1314 011316 020227 000200 BLE 10$ ;BR, IF NOT AT END YET
1315 011322 003750 BR 27$ ;AT END BRANCH
1316 011324 000403 27$: CMP R2,#RMCHEND-2 ;REACHED END YET ?
1317 011326 020227 000176 25$: BLE 10$ ;BRANCH TILL ALL READ
1318 011332 003744 27$: TST R3 ;WAS AN ERROR FOUND ?
1319 011334 005703 BEQ 30$ ;BRANCH IF NOT
1320 011336 001402 CLC ;CLEAR CARRY TO SHOW ERROR
1321 011340 000241 BR 50$ ;AND EXIT
1322 011342 000401 50$: SEC ;SHOW GOOD COMPARE
1323 011344 000261

```


H5

TSV3 GLOBAL AREAS MACRO M1200 23 MAR 84 09:44 PAGE 32-1

CKRAM2 COMPARE RAM TO I/O CHARACTERISTICS DATA

SEQ 0059

1324 011346 000207
1325

504: RTS PC

;RETURN

CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS

1327						.SBTTL CKMSG - COMPARE WRITE CHAR. MESSAGE BUFFERS	
1328						;	
1329						;	
1330						;ROUTINE TO COMPARE A WRITE CHARACTERISTICS EXPD AND RECV	
1331						;BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR	
1332						;ERROR PRINT ROUTINES.	
1333						;	
1334						;INPUT:	
1335						;	
1336						;	
1337						;	
1338						;	
1339						;	
1340						;	
1341						;	
1342						;	
1343						;	
1344						;	
1345						;	
1346						;	
1347						;	
1348						;	
1349						;	
1350						;	
1351						;	
1352	011350					CKMSG::	
1353	011350					SAVREG	
1354	011354	010037	002302			MOV R0,RCVHIADD	;SAVE R1-R5 UNTIL NEXT RETURN
1355	011360	010137	002304			MOV R1,RCVLOAD	;SAVE RECV HIGH ADDRESS
1356	011364	005737	003130			TST KTENABLE	;SAVE RECV LOW ADDRESS
1357	011370	001403				BEQ 10\$	;TESTING ABOVE 28K?
1358	011372	004737	017306			JSR PC,SETMAP	;BR IF NO
1359	011376	010001				MOV R0,R1	;RETURN ADDRESS BIASED TO PAR6 IN R0
1360	011400	005004				10\$: CLR R4	;GET RETURNED ADDRESS BIASED TO PAR6
1361	011402	005003				CLR R3	;WORD IN BUFFER
1362	011404	010205				MOV R2,R5	;CLEAR ERROR SEEN FLAG
1363	011406	011264	002320			15\$: MOV (R2),EXPMSG(R4)	;GET EXPD BUFFER ADDRESS
1364	011412	011164	002464			MOV (R1),RECMSG(R4)	;SAVE EXPD FOR ERROR REPORT
1365	011416	022221				CMP (R2), (R1)	;SAVE RECV FOR ERROR REPORT
1366	011420	001401				BEQ 25\$	;EXPD EQUAL RECV?
1367	011422	005203				INC R3	;BR IF YES
1368	011424	062704	000002			25\$: ADD #2,R4	;SET ERROR SEEN FLAG
1369	011430	020427	000014			CMP R4,#14	;POINT TO NEXT WORD ADDRESS
1370	011434	003764				BLE 15\$	;DONE FIRST 7 WORDS?
1371	011436	032765	000200	000012		BIT #X2.EXTF,XST2(R5)	;BR IF NO
1372	011444	001403				BEQ 50\$	;IS EXTENDED FEATURES SET IN EXPD?
1373	011446	020427	000016			CMP R4,#16	;BR IF NO
1374	011452	003755				BLE 15\$	;DONE EXTENDED FEATURES WORD?
1375	011454	005703				50\$: TST R3	;BR IF NO
1376	011456	001402				BEQ 55\$	;ANY ERRORS SEEN?
1377	011460	000241				CLC	;BR IF NO
1378	011462	000401				BR 60\$	;SET FAILURE
1379	011464	000261				55\$: SEC	;SET SUCCESS
1380	011466	000207				60\$: RTS PC	RETURN
1381							

CKMSG2 COMPARE EXPD RECV MESSAGE BUFFERS

```

1383 .SBTTL CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS
1384
1385 ;
1386 ;ROUTINE TO COMPARE AN EXPECTED AND RECEIVED MESSAGE
1387 ;BUFFER. THE EXPECTED AND RECEIVED BUFFERS ARE STORED FOR
1388 ;ERROR PRINT ROUTINES.
1389
1390 ;INPUT:
1391
1392 ; R0 RECV MESSAGE BUFFER HIGH ORDER ADDRESS
1393 ; R1 RECV MESSAGE BUFFER LOW ORDER ADDRESS
1394 ; R2 EXPD MESSAGE BUFFER ADDRESS
1395 ; R3 NUMBER OF BYTES TO COMPARE
1396
1397 ;OUTPUT:
1398
1399 ; CARRY SET - MESSAGE BUFFERS MATCH
1400 ; CLR - MESSAGE BUFFERS DON'T MATCH
1401
1402 ;IMPLICIT OUTPUT:
1403
1404 ; EXPMSG BUFFER IS SET TO EXPD DATA
1405 ; RECVMSG BUFFER IS SET TO RECV DATA
1406 ; RCVHIADD SET TO HIGH ORDER ADDRESS OF RECV
1407 ; RCVLOAD SET TO LOW ORDER ADDRESS OF RECV
1408
1409 ;
1410 011470 CKMSG2::
1411 011470 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1412 011474 020327 000144 CMP R3,#RECVMSG-EXPMSG,680 ;IS COUNT ABOVE MAX ALLOWED?
1413 011500 003412 BLE 5$ ;BR IF NO
1414 011502 012703 000144 MOV #RECVMSG-EXPMSG,R3,680
1415 011506 PRINTF #DEBUGMSG ;880
1416 011506 012746 011622 MOV #DEBUGMSG,-(SP)
1417 011512 012746 000001 MOV #1,-(SP)
1418 011516 010600 MOV SP,R0
1419 011520 104417 TRAP C$PRINTF
1420 011522 062706 000004 ADD #4,SP
1421 011526 010037 002302 5$: MOV R0,RCVHIADD ;SAVE RECV HIGH ADDRESS
1422 011532 010137 002304 MOV R1,RCVLOAD ;SAVE RECV LOW ADDRESS
1423 011536 005737 003130 TST KTENABLE ;TESTING ABOVE 28K?
1424 011542 001403 BEQ 10$ ;BR IF NO
1425 011544 004737 017306 JSR PC,SETMAP ;RETURN ADDRESS BIASED TO PAR6 IN R0
1426 011550 010001 MOV R0,R1 ;GET RETURNED ADDRESS BIASED TO PAR6
1427 011552 005004 10$: CLR R4 ;WORD IN BUFFER
1428 011554 005005 CLR R5 ;CLEAR ERROR SEEN FLAG
1429 011556 111264 002320 15$: MOVB (R2),EXPMSG(R4) ;SAVE EXPD FOR ERROR REPORT
1430 011562 111164 002464 MOVB (R1),RECVMSG(R4) ;SAVE RECV FOR ERROR REPORT
1431 011566 122221 CMPB (R2)+,(R1)+ ;EXPD EQUAL RECV?
1432 011570 001401 BEQ 25$ ;BR IF YES
1433 011572 005205 INC R5 ;SET ERROR SEEN FLAG
1434 011574 062704 000001 25$: ADD #1,R4 ;POINT TO NEXT BYTE
1435 011600 020403 CMP R4,R3 ;DONE ALL BYTES?
1436 011602 002001 BGE 50$ ;BR IF YES
1437 011604 000764 BR 15$ ;DO NEXT BYTE
1438 011606 005705 50$: TST R5 ;ANY ERRORS SEEN?
1439 011610 001402 BEQ 55$ ;BR IF NO

```

K5

TSV3 GLOBAL AREAS MACRO M1200 23 MAR 84 09:44 PAGE 34-1

SEQ 0062

CKMSG2 COMPARE EXPD RECV MESSAGE BUFFERS

```
1435 011612 000241          CLC          ;SET FAILURE
1436 011614 000401          BR          60$          ;
1437 011616 000261          55$: SEC          ;SET SUCCESS
1438 011620 000207          60$: RTS          PC          ;RETURN
1439
1440 011622      120      122      117 DEBUGMSG: .ASCIZ 'PROGRAM INTERNAL ERROR -CKMSG2 MESSAGE BUFFER EXCEEDED ' ;@@D
1441 011712      045      116      045 FERCM: .ASCII /NMA ***/
1442 011723      040      040      124 ERCM: .ASCIZ / TSSR ERROR CODE REC'D * /
1443 011756      056      056      056 SIMSG: .ASCIZ /... AFTER DOING SOFT INIT/
1444 012011      124      105      123 TINERR: .ASCIZ /TEST: .../
1445 .EVEN
```

CKMSG2 COMPARE EXPD RECV MESSAGE BUFFERS

```

1447
1448
1449
1450
1451
1452
1453
1454
1455
1456
1457
1458
1459
1460
1461
1462
1463 012024      BGNMSG  SFMSG
      012024
1464 012024 004737 006020      SFMSG: JSR  PC,PRITSSR ;PRINT CONTENTS OF TSSR REGISTER
1465 012030 004737 017172      JSR  PC,CKDROP  ;DROP UNIT, IF ALLOWED
1466 012034      ENDMMSG
      012034
      012034 104423
1467
1468
1469
1470
1471
1472
1473
1474
1475
1476
1477
1478
1479 012036      BGNMSG  PKTSSR
      012036
1480 012036 004737 006020      PKTSSR: JSR  PC,PRITSSR ;PRINT THE CONTENTS OF TSSR REGISTER
1481 012042 012700 000004      MOV  #4,R0 ;NO. OF WORDS IN PACKET
1482 012046 004737 007364      JSR  PC,PRIPKT ;PRINT THE CONTENTS OF COMMAND PACKET
1483 012052      ENDMMSG
      012052
      012052 104423
1484
1485
1486
1487
1488
1489
1490
1491
1492
1493
1494
1495
1496 012054      BGNMSG  PKTGETS
      012054
      PKTGETS:

```

CKMSG2 - COMPARE EXPD RECV MESSAGE BUFFERS

```

1497 012054 004737 006020      JSR    PC,PRITSSR      ;PRINT THE CONTENTS OF TSSR REGISTER
1498 012060 012700 000002      MOV     #2,R0          ;NO. OF WORDS IN GET STATUS PACKET
1499 012064 004737 007364      JSR     PC,PRIPKT      ;PRINT THE CONTENTS OF COMMAND PACKET
1500 012070      ENDMSG
      012070
      012070 104423      L10005:   TRAP     C$MSG
1501
1502
1503
1504      ;*
1505      ;PRINT TSSR ERRORS FOR INITIALIZATION TESTS
1506      ;
1507      ;INPUTS:
1508      ;
1509      ;      R1      TSSR CONTENTS
1510      ;      R4      ADDRESS OF COMMAND PACKET
1511      ; -
1512 012072      BGNMSG   SFFMSG
      012072      SFFMSG:  JSR     PC,PRITSSR      ;PRINT CONTENTS OF TSSR REGISTER
1513 012072 004737 006020      ENDMSG
1514 012076      L10006:   TRAP     C$MSG
      012076 104423
1515
1516
1517      .SBTTL  PKTMES - PRINT TSSR AND MESSAGE BUFFER
1518      ;*
1519      ;
1520      ;PRINT ROUTINE TO PRINT THE CONTENTS OF TSSR AND MESSAGE
1521      ;BUFFER FOR ERROR REPORTS
1522      ;
1523      ;INPUTS:
1524      ;
1525      ;      R1      CONTENTS OF TSSR
1526      ;      R2      LOW ORDER MESSAGE BUFFER
1527      ;      R3      HIGH ORDER MESSAGE BUFFER ADDRESS
1528      ;      NOTE: R3 IS IGNORED IF KTENABLE FLAG IS CLEAR
1529      ; -
1530 012100      BGNMSG   PKTMES
      012100      PKTMES:  JSR     PC,PRITSSR      ;PRINT CONTENTS OF TSSR
1531 012100 004737 006020      MOV     R2,R0          ;LOW ORDER ADDRESS
1532 012104 010200      MOV     R3,R1          ;HIGH ORDER ADDRESS
1533 012106 010301      JSR     PC,PRMESS      ;PRINT THE MESSAGE BUFFER
1534 012110 004737 014232      ENDMSG
1535 012114      L10007:   TRAP     C$MSG
      012114 104423
1536

```


ADDSSR PRINT TEST ADDRESS AND TSSR

```

1538 .SBTTL ADDSSR - PRINT TEST ADDRESS AND TSSR
1539 ;*
1540 ;PRINT ROUTINE TO PRINT THE CONTENTS OF
1541 ;TSSR AND A MEMORY TEST ADDRESS
1542 ;
1543 ;INPUTS:
1544 ;
1545 ; R5 FIRST DEVICE UNIBUS ADDRESS
1546 ; ERRHI HIGH ORDER MEMORY TEST ADDRESS
1547 ; ERRLO LOW ORDER MEMORY TEST ADDRESS
1548 ;
1549 ;
1550 012116 BGNMSG ADDSSR
1551 012116 ADDSSR::
1552 012116 004737 010270 JSR PC,PRITADD ;PRINT MEMORY TEST ADDRESS
1553 012122 016501 000002 MOV TSSR(R5),R1 ;GET CURRENT TSSR
1554 012126 004737 006020 JSR PC,PRITSSR ;PRINT THE CONTENTS OF TSSR REGISTER
1555 012132 ENDMMSG
1556 012132 L10010:
1557 012132 104423 TRAP C$MSG
1558 ;
1559 .SBTTL MSGEXP - PRINT WRITE CHAR. EXPD-RECV MESSAGE BUFFERS
1560 ;*
1561 ;PRINT ROUTINE TO PRINT WRITE CHARACTERISTIC MESSAGE BUFFER
1562 ;
1563 ;IMPLICIT INPUTS:
1564 ;
1565 ; EXPMSG - EXPECTED MESSAGE BUFFER
1566 ; RECMMSG - RECEIVED MESSAGE BUFFER
1567 ; RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1568 ; RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1569 ;-
1570 012134 BGNMSG MSGEXP
1571 012134 MSGEXP::
1572 012134 012700 000007 MOV #7,R0 ;ASSUME NO EXT FEATURES
1573 012140 005737 002224 TST EXTFEA ;EXT FEATURES SET?
1574 012144 001402 BEQ 5$ ;BR IF NO
1575 012146 012700 000010 MOV #8.,R0 ;EXT FEATURE BUFFER IS 8 WORDS
1576 012152 004737 014542 JSR PC,PRMSGEXP ;PRINT EXPD/RECV MESSAGE BUFFERS
1577 012156 ENDMMSG
1578 012156 L10011:
1579 012156 104423 TRAP C$MSG

```

FIFEXP - PRINT FIFO EXP/RCV DATA

```

1579                                     .SBTTL FIFEXP - PRINT FIFO EXP/RCV DATA
1580                                     ;
1581                                     ;
1582                                     ;PRINT ROUTINE TO PRINT FIFO EXP/RCV DATA
1583                                     ;
1584                                     ;      R1      - BYTE COUNT
1585                                     ;
1586                                     ;IMPLICIT INPUTS:
1587                                     ;
1588                                     ;      EXPMSG   - EXPECTED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY
1589                                     ;      RECMMSG   - RECEIVED MESSAGE BUFFER (CONTAINS FIFO DATA ONLY)
1590                                     ;
1591                                     BGNMSG FIFEXP
1592 FIFEXP:
1593                                     PRINTX #FIF1MSG,R1      ;PRINT BYTES TRANSFERRED
1594                                     MOV      R1,-(SP)
1595                                     MOV      #FIF1MSG,-(SP)
1596                                     MOV      #2,-(SP)
1597                                     MOV      SP,R0
1598                                     TRAP     C1PNTX
1599                                     ADD      #6,SP
1600                                     PRINTX #FIF2MSG      ;PRINT HEADER MSG
1601                                     MOV      #FIF2MSG,-(SP)
1602                                     MOV      #1,-(SP)
1603                                     MOV      SP,R0
1604                                     TRAP     C1PNTX
1605                                     ADD      #4,SP
1606                                     MOV      R1,R0      ;GET BYTE COUNT
1607                                     JSR      PC,PRBYTEXP  ;PRINT FIFO BYTES IN ERROR
1608                                     ENDMMSG
1609
1610 L10012:
1611                                     TRAP     C1MSG
1612                                     .ASCIZ  '###A NUMBER OF BYTES TRANSFERRED = #02'
1613                                     .ASCIZ  '###A FIFO DATA BYTES IN ERROR:'
1614                                     .EVEN

```


MSGSTAT PRINT STATUS HEADER AND MESSAGE BUFFERS

```

1602          .SBTTL MSGSTAT - PRINT STATUS HEADER AND MESSAGE BUFFERS
1603          ;
1604          ;
1605          ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
1606          ;
1607          ;
1608          ;IMPLICIT INPUTS:
1609          ;
1610          ;     EXPMSG - EXPECTED MESSAGE BUFFER
1611          ;     RECMMSG - RECEIVED MESSAGE BUFFER
1612          ;     RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1613          ;     RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1614          ;
1615          BGNMSG MSGSTAT
1616          MSGSTAT:
1617          100:  MOV     #STATCOD,R1      ;ASCII ADDRESS TABLE
1618          BEQ     200                  ;DONE ALL MSG LINES?
1619          PRINTX  R0                    ;BR IF YES
1620          MOV     R0,-(SP)              ;PRINT STATUS BIT NAMES
1621          MOV     #1,-(SP)
1622          MOV     SP,R0
1623          TRAP    C#PRINTX
1624          ADD     #4,SP
1625          BR      100                  ;DO ANOTHER MSG LINE
1626          200:  MOV     #10,R0         ;NUMBER OF WORDS IN A READ STATUS BUFFER
1627          JSR     PC,PRMSGEXP          ;PRINT EXPD/RCV MESSAGE BUFFERS
1628          ENDMSG
1629          L10013:
1630          TRAP    C#MSG
1631          STATCOD: .WORD 10,20,30,40,50,60,0
1632          10: .ASCIZ 'ENSA Tape Bus Signals in Word #8:'
1633          20: .ASCIZ 'ENSA PARERR<15> IEOT <12> IFMK <9> IRDY<6> IRWD<2>'
1634          30: .ASCIZ 'ENSA IRESV2<14> IIDENT<11> IHER <8> IONL<5> IFBY<1>'
1635          40: .ASCIZ 'ENSA IRESV1<13> ICER <10> ISPEED<7> ILDP<4> IFPT<0>'
1636          50: .ASCIZ 'ENSA Tape Bus Signals in Word #9:'
1637          60: .ASCIZ 'ENSA DATMIS<7> ILW<6> OUTRDY<5> INRDY<4>'
1638          .EVEN
1639          ;
1640          .SBTTL MSGLOOP - PRINT LOOPBACK HEADER AND MESSAGE BUFFERS
1641          ;
1642          ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
1643          ;
1644          ;IMPLICIT INPUTS:
1645          ;
1646          ;     EXPMSG - EXPECTED MESSAGE BUFFER
1647          ;     RECMMSG - RECEIVED MESSAGE BUFFER
1648          ;     RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1649          ;     RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1650          ;
1651          BGNMSG MSGLOOP
1652          MSGLOOP:
1653          100:  MOV     #LOOPCOD,R1      ;ASCII ADDRESS TABLE

```

MSGLOOP PRINT LOOPBACK HEADER AND MESSAGE BUFFERS

```

1650 013060 012100          10:  MOV    (R1)+,R0          ;DONE ALL MSG LINES?
1651 013062 001410          BEQ     20:                  ;BR IF YES
1652 013064          PRINTX  R0                          ;PRINT STATUS BIT NAMES
      013064 010046          MOV     RO,-(SP)
      013066 012746 000001    MOV     #1,-(SP)
      013072 010600          MOV     SP,R0
      013074 104415          TRAP    C#PNTX
      013076 062706 000004    ADD     #4,SP
1653 013102 000766          BR      10:                  ;DO ANOTHER MSG LINE
1654 013104 012700 000012    20:  MOV     #10,R0          ;NUMBER OF WORDS IN A READ STATUS BUFFER
1655 013110 004737 014542    JSR      PC,PRMSGEXP        ;PRINT EXPD/RECV MESSAGE BUFFERS
1656 013114          ENDMMSG
      013114          L10014:
      013114 104423          TRAP    C#MSG
1657
1658 013116 013136 013211 013310 LOOPCOD: .WORD 1#,2#,3#,4#,5#,6#,7#,0
1659 013136 045 116 045 1#: .ASCIZ '###A Tape Bus Loopback Signals in Word #8:'
1660 013211 045 116 045 2#: .ASCIZ '###A PARERR<15> IRESV2<14> IRESV1<13>'
1661 013310 045 116 045 3#: .ASCIZ '###A IHISP=>IEOT<12> IWRT=>IIDENT<11> IREV =>ICER <10>'
1662 013407 045 116 045 4#: .ASCIZ '###A IWM =>IFMK<09> IEDIT=>IHER <08> IFAD =>ISPEED<07>'
1663 013506 045 116 045 5#: .ASCIZ '###A ITADO=>IRDY<06> ITAD1=>IONL <05> IERASF=>ILDOP <04>'
1664 013605 045 116 045 6#: .ASCIZ '###A IREW =>IDBY<03> IRWU =>IRWD <02> IFEN =>IFBY <01>'
1665 013704 045 116 045 7#: .ASCIZ '###A IGO =>IFPT<00>'
1666          .EVEN
1667

```

MSGSUB PRINT WRITE SUBSYSTEM MESSAGE BUFFER

```

1669      .SBTTL MSGSUB - PRINT WRITE SUBSYSTEM MESSAGE BUFFER
1670      ;*
1671      ;
1672      ;PRINT ROUTINE TO PRINT MESSAGE BUFFER EXPD/RCV
1673      ;
1674      ;
1675      ;IMPLICIT INPUTS:
1676      ;
1677      ;      EXPMSG - EXPECTED MESSAGE BUFFER
1678      ;      RECMSG - RECEIVED MESSAGE BUFFER
1679      ;      RCVHIADD - RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1680      ;      RCVLOADD - RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1681      ; -
1682      BGNMSG MSGSUB
1683      MSGSUB:  MOV     #10.,R0      ;SIZE OF WRITE SUBSYSTEM BUFFER
1684      013732 012700 000012      JSR     PC,PRMSGEXP      ;PRINT EXPD/RCV MESSAGE BUFFERS
1685      013736 004737 014542      ENDMSG
1686      L10015:
1687      013742 104423      TRAP     C#MSG
1688
1689
1690
1691      .SBTTL MEMADD - PRINT MEMORY ADDRESS DATA ERROR
1692      ;*
1693      ;
1694      ;PRINT ROUTINE TO PRINT MEMORY ADDRESS DATA COMPARE ERROR
1695      ;
1696      ;IMPLICIT INPUTS:
1697      ;
1698      ;      ERRHI - MEMORY ERROR HIGH ORDER ADDRESS
1699      ;      ERRLO - MEMORY ERROR LOW ORDER ADDRESS
1700      ;      EXP - EXPECTED DATA
1701      ;      RECV - RECEIVED DATA
1702      ; -
1703      BGNMSG MEMADD
1704      MEMADD:  JSR     PC,PRIADD      ;PRINT MEMORY ADDRESS IN ERROR
1705      013744 004737 010154      MOV     EXPD,R1      ;GET EXPD DATA
1706      013750 013701 002230      MOV     RECV,R2      ;GET RECEIVED DATA
1707      013754 013702 002232      JSR     PC,PRIOR      ;PRINT EXPD/RCV
1708      013760 004737 007736      ENDMSG
1709      L10016:
1710      013764 104423      TRAP     C#MSG

```

PRAMPKT PRINT RAM AND PACKET DATA

```

1711 .SBTTL PRAMPKT - PRINT RAM AND PACKET DATA
1712 ;*
1713 ;
1714 ;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
1715 ;WHEN THE RAM DATA DOES NOT MATCH.
1716 ;
1717 ;INPUTS:
1718 ;
1719 ;      R4      POINTER TO COMMAND PACKET
1720 ;
1721 ;IMPLICIT INPUTS:
1722 ;
1723 ;      RAMDATA      DATA AS READ FROM THE RAM
1724 ;      RAMSIZ       NUMBER OF BYTES IN PACKET
1725 ;                  IF RAMSIZ=0 THEN DEFAULT TO 8.
1726 ;
1727 ;IMPLICIT OUTPUTS:
1728 ;
1729 ;      RAMSIZ      SET TO 0
1730 ;
1731 ;-
1732 PRAMPKT:
1733     SAVREG
1734     MOV     #RAMDATA,R1
1735     CLR     R2
1736     5$:    CMPB    (R1)+,(R4)+
1737     BNE     7$
1738     FORCERROR    7$,NOTSSR
1739     BR      10$
1740     7$:    MOVB    -1(R1),R5
1741     MOVB    -1(R4),R3
1742     XOR     R5,R3
1743     BIC     #177400,R3
1744     MOVB    -1(R1),RECV
1745     MOVB    -1(R4),EXPD
1746     PRINTB  #RAMASC,R2,RECV,EXPD,R3
1747     MOV     R3,-(SP)
1748     MOV     EXPD,-(SP)
1749     MOV     RECV,-(SP)
1750     MOV     R2,-(SP)
1751     MOV     #RAMASC,-(SP)
1752     MOV     #5,-(SP)
1753     MOV     SP,R0
1754     TRAP    C#PNTB
1755     ADD     #14,SP
1756     10$:   INC     R2
1757     TST     RAMSIZ
1758     BEQ     15$
1759     CMP     R2,RAMSIZ
1760     BLE     5$
1761     BR      25$
1762     15$:   CMP     R2,#8.
1763     BLT     5$
1764     20$:   BLT     5$
1765     25$:   CLR     RAMSIZ
1766     RTS     PC
1767
1768 ;SAVE R1-R5 UNTIL NEXT RETURN
1769 ;DATA FROM THE RAM
1770 ;INIT BYTE NUMBER
1771 ;COMPARE EXPECTED, RECEIVED
1772 ;BR IF NO MATCH
1773
1774 ;SSD
1775 ;GET RECV RAM DATA
1776 ;GET EXPD PACKET DATA
1777 ;XOR EXPD/RECV
1778 ;LOW BYTE ONLY
1779 ;GET RECEIVED RAM DATA
1780 ;GET EXPECTED RAM DATA
1781
1782 ;UPDATE BYTE COUNT
1783 ;DEFAULT TO 8.?
1784 ;BR IF YES
1785 ;DONE ALL BYTES?
1786 ;BR IF NO
1787
1788 ;DONE DEFAULT NUMBER OF BYTES?
1789 ;BR IF NO
1790 ;SET DEFAULT RAMSIZ
1791 ;RETURN
1792
1793 045 RAMASC: .ASCIZ 'N#A BYTE: #D2#A RAM: #03#A Packet: #03#A XOR:#03'

```

TSV3 GLOBAL AREAS MACRO M1200 23 MAR-84 09:44 PAGE 40-1

G6

PRAMPKT PRINT RAM AND PACKET DATA

SEQ 0071

1759
1760
1761

.EVEN

PRMESS PRINT CONTENTS OF MESSAGE BUFFER

```

1763                                     .SBTTL PRMESS - PRINT CONTENTS OF MESSAGE BUFFER
1764                                     ;*
1765                                     ;
1766                                     ;THIS ROUTINE PRINTS THE CONTENTS OF
1767                                     ;THE 7 OR 8 WORD MESSAGE BUFFER RETURNED BY THE
1768                                     ;TSV-05.
1769                                     ;
1770                                     ;INPUT:
1771                                     ;
1772                                     ;      R0      LOW ORDER ADDRESS OF MESSAGE BUFFER
1773                                     ;      R1      HIGH ORDER ADDRESS OF MESSAGE BUFFER
1774                                     ;      NOTE: R1 IS IGNORED IF KENABLE FLAG IS CLEAR
1775                                     ;
1776                                     ;THIS ROUTINE IS NORMALLY CALLED FROM A PRINT ROUTINE
1777                                     ;
1778                                     ;-
1779
1780 014232 PRMESS: SAVREG                                ;SAVE THE REGISTERS
1781 014232      MOV      R0,R5                                ;SAVE LOW ORDER ADDRESS
1782 014236      TST      KENABLE                                ;ADDRESS ABOVE 28K?
1783 014240      BNE      10$                                ;BR IF YES
1784 014244      CLR      R1                                ;SET HIGH ORDER ADDRESS TO 0
1785 014246      005001
1786 014250      010103      10$: MOV      R1,R3                ;SAVE HIGH ORDER ADDRESS
1787 014252      006100      ROL      R0                        ;SHIFT BIT15 TO C BIT
1788 014254      006101      ROL      R1                        ;SHIFT TO HIGH ORDER FOR PRINTOUT
1789 014256      PRINTX   #PROASC,R1,R5                        ;PRINT MESSAGE BUFFER ADDRESS
1790 014256      010546      MOV      R5,-(SP)
1791 014260      010146      MOV      R1,-(SP)
1792 014262      012746      014410      MOV      #PROASC,-(SP)
1793 014266      012746      000003      MOV      #3,-(SP)
1794 014272      010600      MOV      SP,R0
1795 014274      104415      TRAP     C:PNTX
1796 014276      062706      000010      ADD      #10,SP
1797 014302      PRINTX   #PR1ASC                                ;PRINT HEADER FOR CONTENTS
1798 014302      012746      014455      MOV      #PR1ASC,-(SP)
1799 014306      012746      000001      MOV      #1,-(SP)
1800 014312      010600      MOV      SP,R0
1801 014314      104415      TRAP     C:PNTX
1802 014316      062706      000004      ADD      #4,SP
1803 014322      005004      CLR      R4                                ;NUMBER OF THE NEXT WORD
1804 014324      010501      MOV      R5,R1                                ;COPY LOW ORDER ADDRESS
1805 014326      010300      MOV      R3,R0                                ;COPY HIGH ORDER ADDRESS
1806 014330      001403      BEQ      20$                                ;BR IF NOT ABOVE 28K
1807 014332      004737      017306      JSR      PC,SETMAP                ;SETUP PAR ADDRESS IN R0
1808 014336      010005      MOV      R0,R5                                ;GET PAR FORMAT ADDRESS ABOVE 28K
1809 014340      PRINTX   #PRASC,R4,(R5)+                        ;PRINT THE CONTENTS OF MEMORY BUFFER
1810 014340      012546      MOV      (R5),-(SP)
1811 014342      010446      MOV      R4,-(SP)
1812 014344      012746      014513      MOV      #PRASC,(SP)
1813 014350      012746      000003      MOV      #3,-(SP)
1814 014354      010600      MOV      SP,R0
1815 014356      104415      TRAP     C:PNTX
1816 014360      062706      000010      ADD      #10,SP
1817 014364      005204      INC      R4                                ;NUMBER OF THE NEXT
1818 014366      020427      000007      CMP      R4,#7                ;DONE ALL YET ?
1819 014372      003005      BGT      50$                                ;BRANCH IF ALL DONE

```


PRMESS - PRINT CONTENTS OF MESSAGE BUFFER

```

1801 014374 002761          BLT      20$          ;PRINT FIRST 7 WORDS
1802 014376 032763 000200 000012      BIT      0X2.EXTF,XST2(R3);EXTENDED FEATUTES ON ?
1803 014404 001355          BNE      20$          ;PRINT EXTENDED STATUS WORD
1804 014406 000207          50$:      RTS      PC          ;RETURN
1805
1806 014410      045      116      045 PROASC: .ASCIZ 'N$A Message Buffer Address - 0105'
1807 014455      045      116      045 PRIASC: .ASCIZ 'N$A Message Buffer Contents:'
1808 014513      045      116      045 PRASC:  .ASCIZ 'N$A      Word01$A: 0'
1809                      .EVEN

```

PRMSGEXP PRINT EXPD/RCV MESSAGE BUFFERS

SEQ 0074

```

1811 .SBTTL PRMSGEXP - PRINT EXPD/RCV MESSAGE BUFFERS
1812 ;
1813 ;
1814 ;ROUTINE TO PRINT EXPECTED AND RECEIVED MESSAGE BUFFERS
1815 ;
1816 ; R0 NUMBER OF WORDS IN BUFFER
1817 ;
1818 ;IMPLICIT INPUTS:
1819 ;
1820 ; EXPMSG - EXPECTED MESSAGE BUFFER
1821 ; RECMMSG - RECEIVED MESSAGE BUFFER
1822 ; RCVHIADD- RECEIVED MESSAGE BUFFER HIGH ORDER ADDRESS
1823 ; RCVLOADD- RECEIVED MESSAGE BUFFER LOW ORDER ADDRESS
1824 ;
1825 PRMSGEXP::
1826 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1827 MOV R0,R5 ;SAVE NUMBER OF WORDS
1828 MOV RCVLOADD,R0 ;GET RECV LOW ADDRESS
1829 MOV R0,R4 ;COPY LOW ADDRESS
1830 MOV RCVHIADD,R1 ;GET RECV HIGH ADDRESS
1831 ROL R0 ;SHIFT BIT15 TO C BIT
1832 ROL R1 ;SHIFT TO HIGH ORDER FOR PRINTOUT
1833 PRINTX @PRMSG0,R1,R4 ;PRINT MESSAGE BUFFER ADDRESS
1834 MOV R4,-(SP)
1835 MOV R1,-(SP)
1836 MOV @PRMSG0,-(SP)
1837 MOV @3,-(SP)
1838 MOV SP,R0
1839 TRAP C:PNTX
1840 ADD @10,SP
1841 PRINTX @PRMSG1 ;PRINT HEADER FOR CONTENTS
1842 MOV @PRMSG1,-(SP)
1843 MOV @1,-(SP)
1844 MOV SP,R0
1845 TRAP C:PNTX
1846 ADD @4,SP
1847 CLR R4 ;NUMBER OF THE CURRENT WORD
1848 MOV @EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
1849 MOV @RECMMSG,R2 ;GET RECV BUFFER ADDRESS
20$: MOV (R1),R0 ;GET EXPD
1850 MOV (R2),R3 ;GET RECV
1851 XOR R0,R3 ;XOR EXPD/RCV
1852 PRINTX @PRMSG2,R4,(R1),,(R2),,R3
1853 MOV R3,-(SP)
1854 MOV (R2),,-(SP)
1855 MOV (R1),,-(SP)
1856 MOV R4,-(SP)
1857 MOV @PRMSG2,-(SP)
1858 MOV @5,-(SP)
1859 MOV SP,R0
1860 TRAP C:PNTX
1861 ADD @14,SP
1862 INC R4 ;NUMBER OF THE NEXT
1863 CMP R4,R5 ;DONE ALL YET?
1864 BGE 50$ ;BR IF YES
1865 BR 20$ ;DO ANOTHER
1866 50$: RTS PC ;RETURN

```


K6

TSV3 GLOBAL AREAS MACRO M1200 23-MAR 84 09:44 PAGE 42-1

SEQ 0075

PRMSGEXP PRINT EXPD/RECV MESSAGE BUFFERS

1847
1848 014722 045 116 045 PRMSG0: .ASCIZ 'NNA Message Buffer Address = 0105'
1849 014767 045 116 045 PRMSG1: .ASCIZ 'NNA Message Buffer Contents:'
1850 015025 045 116 045 PRMSG2: .ASCIZ 'NNA WORD 0D2A EXPD: 06A RECV: 06A XOR: 06'
1851 .EVEN
1852

PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER

```

1854 .SBTTL PRBYTEXP - PRINT ERROR BYTES IN EXP/REC MESSAGE BUFFER
1855 ;*
1856 ;
1857 ;ROUTINE TO PRINT ERROR BYTES IN MESSAGE BUFFERS
1858 ; ONLY THE FIRST 8 ERRORS ENCOUNTERED ARE PRINTED DUE TO SCREEN SPACE
1859 ;
1860 ; RO - NUMBER OF BYTES IN BUFFER
1861 ;
1862 ;IMPLICIT INPUTS:
1863 ;
1864 ; EXPMSG EXPECTED MESSAGE BUFFER
1865 ; RECMG - RECEIVED MESSAGE BUFFER
1866 ;
1867 PRBYTEXP::
1868 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
1869 MOV R0,R5 ;SAVE NUMBER OF BYTES
1870 CLR PRMNO ;INIT ERROR COUNT
1871 CLR R4 ;NUMBER OF THE CURRENT BYTE
1872 MOV @EXPMSG,R1 ;GET EXPD BUFFER ADDRESS
1873 MOV @RECMG,R2 ;GET RECV BUFFER ADDRESS
1874 20$: MOVB (R1),R0 ;GET EXPD BYTE
1875 BIC @C<377>,R0 ;CLEAR UPPER BYTE
1876 MOVB R0,PRBEXP ;SAVE FOR ERROR REPORT
1877 MOVB (R2),R3 ;GET RECV BYTE
1878 BIC @C<377>,R3 ;CLEAR UPPER BYTE
1879 MOVB R3,PRBREC ;FOR ERROR REPORT
1880 XOR R0,R3 ;XOR EXPD/RECV
1881 CMPB (R1)+,(R2)+ ;EXPD = RECV?
1882 BEQ 30$ ;BR IF YES
1883 INC PRMNO ;UPDATE ERROR COUNT
1884 000010 CMP PRMNO,#8. ;PRINTED 8?
1885 BHI 30$ ;BR IF YES
1886 27$: PRINTX @PRBMSG,R4,PRBEXP,PRBREC,R3
1887 MOV R3,-(SP)
1888 MOV PRBREC,-(SP)
1889 MOV PRBEXP,-(SP)
1890 MOV R4,-(SP)
1891 MOV @PRBMSG,-(SP)
1892 MOV #5,-(SP)
1893 MOV SP,R0
1894 TRAP C:PNTX
1895 ADD #14,SP
1896 50$: FORCEXIT 50$ ;000
1897 BR 35$ ;000
1898 30$: FORCERROR 27$,NOTSSR ;000
1899 35$:
1900 INC R4 ;NUMBER OF THE NEXT
1901 CMP R4,R5 ;DONE ALL YET?
1902 BGE 50$ ;BR IF YES
1903 BR 20$ ;DO ANOTHER
1904 50$: PRINTX @PRBTOT,PRMNO ;PRINT TOTAL ERROR COUNT
1905 MOV PRMNO,-(SP)
1906 MOV @PRBTOT,-(SP)
1907 MOV #2,-(SP)
1908 MOV SP,R0
1909 TRAP C:PNTX

```

PRBYTEXP PRINT FRROR BYTES IN EXP/REC MESSAGE BUFFER

```
1897 015320 062706 000006 ADD #6,SP
1898 015324 000207 RTS PC ;RETURN
1899 015326 045 116 045 PRBMSG: .ASCIZ 'N#A BYTE #D2#A EXPD: #03#A RECV: #03#A XOR: #03'
1900 015413 045 116 045 PRBTOT: .ASCIZ 'N#A NUMBER OF BYTES IN ERROR = #D2'
1901 .EVEN
1902 015460 000000 PRBEXP: .WORD 0 ;EXPD
1903 015462 000000 PRBREC: .WORD 0 ;RECV
1904
```

EXPREC PRINT EXPD/RECV WORD DATA

```
1906 .SBTTL EXPREC - PRINT EXPD/RECV WORD DATA
1907 ;
1908 ;
1909 ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
1910 ;
1911 ;INPUTS:
1912 ;
1913 ; R1 RECEIVED DATA
1914 ; R2 EXPECTED DATA
1915 ;
1916 ;
1917 ;
1918 015464 BGNMSG EXPREC
015464 EXPREC:: JSR PC,PRIXOR ;PRINT THE DATA
1919 015464 004737 007736 ENDMSG
1920 015470 L10017: TRAP C#MSG
015470 104423
1921
1922
```

EXPBREC PRINT EXPD/RECV BYTE DATA

1924
1925
1926
1927
1928
1929
1930
1931
1932
1933
1934
1935
1936
1937 015472
015472
1938 015472 004737 007606
1939 015476
015476
015476 104423

1940
1941
1942
1943
1944
1945
1946
1947
1948
1949
1950
1951
1952
1953
1954
1955
1956
1957
1958
1959
1960
1961
1962
1963
1964 015500
015500
1965 015500 004737 013766
1966 015504
015504
015504 104423

1967
1968
1969
1970
1971
1972
1973
1974

```
.SBTTL EXPBREC - PRINT EXPD/RECV BYTE DATA
;*
;PRINT ROUTINE TO DISPLAY BYTE EXPD/RECV DATA
;
;INPUTS:
;      R1      RECEIVED DATA BYTE
;      R2      EXPECTED DATA BYTE
;
EXPBREC: BGNMSG EXPBREC
;JSR PC,PRIBXOR ;PRINT THE DATA
ENDMSG
L10020: TRAP CMSG
```

```
.SBTTL RAMERR - PRINT RAM AND PACKET DATA
;*
;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
;
;INPUTS:
;      R4      POINTER TO COMMAND PACKET
;
;IMPLICIT INPUTS:
;      RAMDATA  DATA AS READ FROM THE RAM
;      RAMSIZ   NUMBER OF BYTES IN PACKET
;              IF RAMSIZ=0 THEN DEFAULT TO 8.
;
;IMPLICIT OUTPUTS:
;      RAMSIZ   SET TO 0
;-
```

```
BGNMSG RAMERR
RAMERR: JSR PC,PRAMPKT ;PRINT RAM/PACKET DATA
ENDMSG
L10021: TRAP CMSG
```

```
.SBTTL RAHTADD PRINT TEST ADDRESS, RAM AND PACKET DATA
;*
;PRINT ROUTINE TO DISPLAY RAM/PACKET DATA
;
;INPUTS:
```

RAMTADD PRINT TEST ADDRESS, RAM AND PACKET DATA

```

1975
1976      R4      POINTER TO COMMAND PACKET
1977
1978      ;IMPLICIT INPUTS:
1979
1980      ;      RAMDATA      DATA AS READ FROM THE RAM
1981      ;      RAMSIZ      NUMBER OF BYTES IN PACKET
1982      ;                  IF RAMSIZ=0 THEN DEFAULT TO 8.
1983      ;      ERRHI      HIGH ORDER TEST ADDRESS
1984      ;      ERRLO      LOW ORDER TEST ADDRESS
1985
1986      ;IMPLICIT OUTPUTS:
1987
1988      ;      RAMSIZ      SET TO 0
1989      ;
1990
1991      BGNMSG  RAMTADD
1992      RAMTADD:
1993      JSR     PC,PRITADD      ;PRINT TEST ADDRESS
1994      JSR     PC,PRAMPKT     ;PRINT RAM/PACKET DATA
1995      ENDMSG
1996
1997      L10022:
1998      TRAP    C#MSG
1999
2000      .SBTTL  RAMEXP - PRINT RAM EXPD/RECV DATA
2001
2002      ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
2003      ;
2004      ;IMPUTS:
2005      ;
2006      ;      R1      RECEIVED DATA
2007      ;      R2      EXPECTED DATA
2008      ;      R4      CONTROLLER RAM ADDRESS
2009      ;
2010      BGNMSG  RAMEXP
2011      RAMEXP:
2012      BIC     #C<377>,R1      ;SAVE EXPD RAM DATA BYTE
2013      BIC     #C<377>,R2      ;SAVE EXPD RAM DATA BYTE
2014      JSR     PC,PRIRAM      ;PRINT THE RAM ADDRESS
2015      JSR     PC,PRIXOR      ;PRINT THE DATA
2016      ENDMSG
2017      L10023:
2018      TRAP    C#MSG
2019
2020      .SBTTL  TIMEXP - PRINT TIMER A,B AND EXP/REC
2021
2022      ;PRINT ROUTINE TO DISPLAY EXPD/RECV DATA
2023      ;AND TIMER A,B HEADER MESSAGE
2024      ;
2025      ;IMPUTS:
2026      ;
2027      ;      R1      RECEIVED DATA
2028      ;      R2      EXPECTED DATA

```


TIMEXP PRINT TIMER A,B AND EXP/REC

```

2026      ;
2027
2028 015542      BGNMSG  TIMEXP
015542      TIMEXP::
2029 015542      PRINTX  #TIMSGO      ,PRINT HEADER
015542 012746 015570      MOV      #TIMSGO,-(SP)
015546 012746 000001      MOV      #1,-(SP)
015552 010600      MOV      SP,R0
015554 104415      TRAP      C#PNTX
015556 062706 000004      ADD      #4,SP
2030 015562 004737 007736      JSR      PC,PRIXOR      ,PRINT THE DATA
2031 015566      ENDMMSG
015566      L10024:
015566 104423      TRAP      C#MSG
2032
2033
2034 015570      045      116      045 TIMSGO: .ASCIZ 'TIMER A STATUS IS IN BIT 3'
2035      .EVEN          'TIMER B STATUS IS IN BIT 2'

```

BADSSR PRINT TSSR ERRORS ON DATA TRANSFERS

SEQ 0082

```

2037
2038
2039
2040
2041
2042
2043
2044
2045
2046
2047
2048
2049
2050 015670
      015670
2051 015670 010246
2052 015672 042702 177400
2053 015676
      015676 010246
      015700 012746 015730
      015704 012746 000002
      015710 010600
      015712 104414
      015714 062706 000006
2054 015720 012602
2055 015722 004737 006020
2056 015726
      015726
      015726 104423
2057 015730 045 116
2058

      .SBTTL BADSSR - PRINT TSSR ERRORS ON DATA TRANSFERS
      ;
      ;PRINT ROUTINE FOR TSSR ERRORS ON DATA TRANSFERS
      ;
      ;INPUTS:
      ;
      ;      R1      CONTENTS OF TSSR
      ;      R2      DATA WRITTEN (8 BITS)
      ;
      ;-
      BGNMSG BADSSR
BADSSR:
      MOV      R2,-(SP)
      BIC      #177400,R2
      PRINTB   #XFERASC,R2
      MOV      R2,-(SP)
      MOV      #XFERASC,-(SP)
      MOV      #2,(SP)
      MOV      SP,R0
      TRAP     C#PNTB
      ADD      #6,SP
      MOV      (SP),R2
      JSR      PC,PRITSSR
      ENDMSG
      ;SAVE DATA TRANSFERRED
      ;GET JUST ONE BYTE
      ;RESTORE R2
      ;DECODE TSSR CONTENTS
      L10025:
      TRAP     C#MSG
      .ASCIZ   'N/A Data Transferred = 03'

```


GLOBAL SUBROUTINES SECTION

2060
2061
2062
2063
2064
2065
2066

.SBTTL GLOBAL SUBROUTINES SECTION

;;
; THE GLOBAL SUBROUTINES SECTION CONTAINS THE SUBROUTINES
; THAT ARE USED IN MORE THAN ONE TEST.
;

SOFINIT SOFT INITIALIZE OF CONTROLLER

```

2068                                     .SBTTL  SOFINIT - SOFT INITIALIZE OF CONTROLLER
2069
2070                                     ;*
2071                                     ;
2072                                     ;ROUTINE TO DO A SOFT INITIALIZE OF THE CONTROLLER
2073                                     ;BY WRITING INTO THE TSSR REGISTER. AFTER THE INIT,
2074                                     ;THE TSSR REGISTER IS TESTED FOR ERRORS. ANY ERRORS
2075                                     ;DETECTED SHOULD BE TREATED AS DEVICE FATAL ERRORS.
2076                                     ;
2077                                     ;INPUTS:
2078                                     ;
2079                                     ;      R5      ADDRESS OF FIRST REGISTER      ,
2080                                     ;
2081                                     ;OUTPUTS:
2082                                     ;
2083                                     ;      R0      CONTENTS OF TSSR, IF ERROR
2084                                     ;      CARRY    SET IF INIT WAS OKAY
2085                                     ;              CLEAR IF FATAL ERROR
2086                                     ;
2087                                     ;CALLING SEQUENCE:
2088                                     ;
2089                                     ;      MOV      #ADDRESS,R5
2090                                     ;      JSR      PC,SOFINIT
2091                                     ;      BCS      CONTINUE
2092                                     ;      ERDF                     ;REPORT FATAL ERROR
2093                                     ;
2094                                     ;-
2095
2096 015764 SOFINIT::
2097 015764         SAVREG                                ; SAVE THE REGISTERS
2098 015770 012765 000000 000002         MOV      #0,TSSR(R5)      ; DO THE INIT.
2099 015776 004737 016240                JSR      PC,WAITF        ; WAIT FOR SSR
2100 016002 016500 000002         MOV      TSSR(R5),R0          ;GET THE TSSR REGISTER
2101 016006 010004                MOV      R0,R4                ;TSSR CONTENTS
2102 016010 042704 176277         BIC      #C<HIADDR!OFL>,R4
2103 016014 052704 002200         BIS      #SSR!NBA,R4          ;R4 HAS EXPECTED CONTENTS
2104 016020 020400                CMP      R4,R0                ;ONLY EXPECTED BITS SET ?
2105 016022 001402                BEQ      5$                    ;BRANCH IF OKAY
2106 016024 000241                CLC                               ;CLEAR THE CARRY FOR ERROR
2107 016026 000401                BR       10$                   ;GO TO EXIT
2108 016030 000261 5$: SEC          ;SET THE CARRY BIT
2109 016032 000207 10$: RTS      PC          ;RETURN TO CALLER

```

CHKAMB CHECK TSSR FOR AMBIGUITY

```

2111 .SBTTL CHKAMB - CHECK TSSR FOR AMBIGUITY
2112
2113 ;*
2114 ;
2115 ; THIS ROUTINE TESTS THE CONTENTS OF THE TSSR REGISTER
2116 ; FOR AMBIGUITY
2117 ;
2118 ; INPUT:
2119 ;
2120 ; R0 CONTENTS OF TSSR
2121 ;
2122 ; OUTPUT:
2123 ;
2124 ; R0 CONTENTS OF TSSR
2125 ;
2126 ; CARRY SET - NO AMBIGUITY
2127 ; CLR - AMBIGUOUS CONTENTS
2128 ;
2129 ; -
2130
2131 CHKAMB: SAVREG ;SAVE THE GENERAL REGISTERS
2132 MOV R0,R4 ;CONTENTS OF TSSR
2133 BIT #SC,R0 ;IS BIT 15 SET ?
2134 BNE 5$ ;BRANCH IF YES
2135 BIT #C<NBA!OFL!SSR!HIADDR>,R0 ;ANY OTHER BITS SET ?
2136 BNE 40$ ;MUST BE AN ERROR
2137 BR 45$ ;RETURN WITH SUCCESS
2138 5$: BIT #SSR,R0 ;IS READY BIT SET ?
2139 BNE 10$ ;BRANCH IF READY BIT IS SET.
2140 BIT #BITS,R0 ;IS FATAL ERROR BIT SET ?
2141 BEQ 40$ ;ERROR IF NOT
2142 BIC #CTERCLS,R4 ;CLEAR ALL BUT TERMINATION CODE
2143 CMP R4,#16 ;ALL THREE BITS MUST BE SET
2144 BNE 40$ ;ERROR IF NOT SET
2145 BR 45$ ;OK IF ALL ARE SET
2146 10$: BIT #BITS,R0 ;IS FATAL ERROR BIT SET ?
2147 BEQ 45$ ;ERROR IF BIT IS SET WITH SSR
2148 BIT #BIT2!BIT1,R0 ;IS THIS A FUNCTION REJECT
2149 BNE 45$ ;BR, IF TSSR IS OK
2150 40$: CLC ;AMBIGUOUS CONTENTS
2151 BR 50$
2152 45$: SEC ;SHOW SUCCESS - NO AMBIGUITY
2153 50$: RTS PC ;RETURN TO CALLER
2154
2155

```

ENAIN,DSBINT - ENABLE/DISABLE INTERRUPTS

```

2157          .SBITL ENAIN,DSBINT - ENABLE/DISABLE INTERRUPTS
2158
2159          ;
2160          ; DEFAULT DISPLAY INTERRUPT HANDLERS.
2161          ; IF DISPLAY TIME-OUT, REPORT DEV FATAL, AND ABORT PASS.
2162          ; OTHERWISE, SAVE DPU REGISTERS AND DISMISS.
2163          ;
2164          ; BIT DEFINITIONS FOR "INTMASK" AND "INTFLAG" BYTES:
2165          ;
2166          IOKCKIN=BIT7      ; DON'T CHECK FOR BAD INTERRUPTS      TEST WILL.
2167          IOKSTP=BIT0      ; EXPECT "STOP" INTERRUPT.
2168          ;
2169          ; INTERRUPT MASK -- SAYS EXPECTING INTERRUPTS
2170          INTMASK: .BYTE 0
2171          ; INTERRUPT FLAG -- SAYS WE GOT ONE (IF POSITIVE)
2172          INTFLAG: .BYTE 0
2173          ;
2174          ; SAVED INTERRUPT VECTOR:
2175          INTVEC: .WORD 0
2176          ; SAVE CPU PC
2177          INTCPC: .WORD 0
2178          ;
2179          ; SUBROUTINE TO ENABLE INTERRUPTS:
2180          ENAIN: MOV     R0, -(SP)      ; SAVE R0
2181                  MOV     IVEC, R0      ; GET POINTER TO VECTORS
2182                  MOV     @INTR, (R0)+  ; SET UP INTERRUPT VECTOR
2183                  MOV     @PRI06, (R0)+
2184                  MOV     (SP), R0      ; RESTORE R0
2185                  MOV     (SP), -(SP)
2186                  MOV     @0, 2(SP)    ; SET CPU TO LEVEL 0
2187                  RTI
2188          ;
2189          ; SUBROUTINE TO DISABLE INTERRUPTS (RAISE PRIORITY TO LEVEL 6)
2190          DSBINT: MOV     (SP), -(SP)
2191                  MOV     @PRI06, 2(SP)
2192                  RTI
2193

```

J7

INTR - INTERRUPT HANDLERS

SEQ 0087

```

2195
2196
2197 016206
      016206
2198 016206 012737 000001 002222
2199 016214 105037 016135
2200 016220 132737 000001 016134
2201 016226 001003
2202 016230 152737 000001 016135
2203
2204
2205 016236
2206 016236
      016236
      016236 000002
2207
2208

      .SBTTL INTR - INTERRUPT HANDLERS
      BGNSRV INTR
INTR::
      MOV #1,INTRECV ;SE FLAG TO SHOW INTERRUPT RECEIVED
      CLRB INTFLAG ;CLEAR FLAG TO SAY WE GOT INTERRUPT
      BITB #IOKSTP,INTMASK ;EXPECTING STOP INTERRUPT?
      BNE 1$ ;BR IF YES
      BISB #IOKSTP,INTFLAG ;NO. SET THE ERROR FLAG.
      ;SAVE REGISTERS, MSG BUFFER, ETC.
      1$:
      ENDSRV
L10026:
      RTI

```

WAITF WAIT FOR SUBSYSTEM READY

```

2210          .SBTTL WAITF - WAIT FOR SUBSYSTEM READY
2211          ;
2212          ; SUBROUTINE TO WAIT FOR THE SUBSYSTEM READY FLAG
2213          ;
2214          ; INPUTS:
2215          ;
2216          ; R5      ADDRESS OF FIRST DEVICE REGISTER
2217          ;
2218          ; OUTPUTS:
2219          ;
2220          ; R0      CONTENTS OF LAST TSSR READ
2221          ; CARRY   SET - READY BIT SET
2222          ;         CLR - TIMEOUT WAITING FOR READY
2223          ;
2224 016240 000401 WAITF:: BR      1$      ;NOP WHEN SUPER FIXED
2225 016242          BREAK          ; DO A SUPVSR BREAK FIRST.
2226 016242 104422          TRAP     C$BRK
2227 016244 012746 003000 1$:      MOV     $3000, -(SP) ;300 MSEC TIMER
2228 016250 016500 000002 2$:      MOV     TSSR(R5),R0 ;READ THE TSSR REGISTER
2229 016254 105700          TSTB     R0      ;TEST FOR READY BIT SET
2230 016256 100420          BMI      3$      ; EXIT ON STOP FLAG.
2231 016260          DELAY     1      ; WAIT 100 USEC
2232 016260 012727 000001          MOV     $1,(PC)+
2233 016264 000000          .WORD     0
2234 016266 013727 002116          MOV     L$DLY,(PC)+
2235 016272 000000          .WORD     0
2236 016274 005367 177772          DEC     -6(PC)
2237 016300 001375          BNE      . 4
2238 016302 005367 177756          DEC     -22(PC)
2239 016306 001367          BNE      . 20
2240 016310 005316          DEC     (SP)      ;REDUCE DELAY COUNT
2241 016312 001356          BNE      2$      ;RETRY UNTIL TIMER EXPIRES
2242 016314 000241          CLC          ; C = 0, CONTROLLER STILL RUNNING...
2243 016316 000401          BR      4$      ;...OR HUNG UP AFTER 300 MSEC.
2244 016320 000261          3$:      SEC          ; C = 1, CONTROLLER IS STOPPED.
2245 016322 005326          4$:      DEC     (SP)+ ;RESTORE STACK WITHOUT CHANGING CARRY BIT
2246 016324 000207          RTS      PC

```


CHKTSSR - CHECK TSSR FOR READY

```

2240 .SBTTL CHKTSSR CHECK TSSR FOR READY
2241
2242 ;*
2243 ;
2244 ;THIS ROUTINE WAITS FOR READY IN THE TSSR
2245 ;AND TESTS FOR AMBIGUOUS BIT SETTINGS IN TSSR.
2246 ;
2247 ;INPUT:
2248 ;
2249 ; R5 ADDRESS OF CSR REGISTERS
2250 ;
2251 ;OUTPUT:
2252 ;
2253 ; R0 CONTENTS OF TSSR
2254 ; CARRY SET - OKAY
2255 ; CLR - NOT READY AMBIGUOUS, OR SC SET
2256 ;
2257 ;-
2258
2259 016326 CHKTSSR:
2260 016326 004737 016240 JSR PC, WAITF ;WAIT FOR READY
2261 016332 103014 BCC 20$ ;BRANCH IF TIME OUT
2262 016334 004737 016034 JSR PC, CHKAMB ;TSSR AMBIGUOUS?
2263 016340 103006 BCC 10$ ;BR IF YES
2264 016342 032700 100000 BIT #SC, R0 ;SPECIAL CONDITION SET?
2265 016346 001405 BEQ 15$ ;BR IF NO
2266 016350 032700 074000 BIT #<SCE!BIE!RMR!NXM>, R0 ;ANY ERROR BITS SET?
2267 016354 001402 BEQ 15$ ;BR IF NO
2268 016356 000241 10$: CLC ;SET FAILURE
2269 016360 000401 BR 20$ ;
2270 016362 000261 15$: SEC ;SET SUCCESS
2271 016364 000207 20$: RTS PC ;RETURN TO CALLER

```

XNXM - CHECK FOR NONEXISTENT MEMORY

```

2273 .SBTTL XNXM - CHECK FOR NONEXISTENT MEMORY
2274 ;
2275 ; ROUTINE TO TEST FOR A NEXM IN THE RANGE (R1) THRU (R2).
2276 ; ON RETURN, IF "C" = 1, (R1) = NEXM ADDRESS.
2277 ; "C" = 0, ALL ADDRESSES OK.
2278 ;
2279 ;CALL: MOV ADR1,R1
2280 ;      MOV ADR2,R2
2281 ;      JSR PC,NXM
2282 ;      RETURN ;TEST "C" AND PROCEED.
2283 ;
2284 016366 012737 016420 000004 XNXM: MOV #2$,#04 ; SET BUSERR VECTOR.
2285 016374 012737 000200 000006 MOV #PRI04,#06
2286 016402 005003 CLR R3 ; FLAG.
2287 016404 005711 1$: TST (R1) ; TEST THE ADDRESS(ES).
2288 ; IF ANY TRAP, CONTINUE AT 2$.
2289 016406 020102 CMP R1,R2 ; OTHERWISE, CONTINUE HERE.
2290 016410 001407 BEQ 3$ ; BR IF FINISHED (NO NEXM'S).
2291 016412 062701 000002 ADD #2,R1 ; SET NEXT ADDRESS...
2292 016416 000772 BR 1$ ; ...AND CONTINUE.
2293 ;
2294 016420 005103 2$: COM R3 ; GOT ONE, SET FLAG...
2295 016422 012716 016430 MOV #3$,(SP)
2296 016426 000002 RTI ; ...AND DISMISS INTERRUPT...
2297 016430 3$: CLRVEC #4 ; ...AND GIVE BACK THE VECTOR.
2298 016430 012700 000004 MOV #4,R0
2299 016434 104436 TRAP C$CVEC
2300 016436 005703 TST R3 ; DID WE CATCH ONE ??
2301 016440 001401 BEQ .+4 ; NO, "C" = 0, SKIP NEXT.
2302 016442 000261 SEC ; YES, "C" = 1, (R1) = NEXM ADDR.
2303 016444 000207 RTS PC
2304 ;
2305 ;
2306 .SBTTL TSTLOOP CHECK ITERATION COUNT
2307 ;
2308 ; SUBROUTINE TO EXECUTE TEST ITERATIONS.
2309 ; EXIT WITH "C" SET IF LOOPS ALLOWED AND LOOP COUNT NON-ZERO.
2310 ; LOOP COUNTER IS SET BY "BEGIN.TEST" MACRO.
2311 ;
2312 ; CALL: LOOPTO ARG
2313 ;
2314 016446 TSTLOOP: TST NOITS ; ITERATIONS INHIBITED?
2315 016446 005737 002166 BNE 1$ ; YES.
2316 016452 001006 TST QVP ; NO.
2317 016454 005737 002202 BMI 1$ ; LOOPS DISALLOWED IN QUICK PASS.
2318 016460 100403 DEC LOOPCNT ; BUMP LOOP COUNTER.
2319 016462 005337 002214 BNE 2$
2320 016466 001002 1$: CLC ; LOOP DISALLOWED, OR DONE.
2321 016470 000241 BR 3$
2322 016472 000401 2$: SEC ; LOOP ENABLED.
2323 016474 000261 3$: RTS PC
2324 016476 000207

```


TSTLOOP - CHECK ITERATION COUNT

```

2326
2327
2328
2329      .SBTTL  TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS
2330      ;
2331      ; PRINT THE NUMBER AND NAME OF EACH TEST AS WE GO ALONG.
2332      ; INCREMENT "TESTK" TO INDICATE THE NUMBER OF TESTS
2333      ; IN THE CURRENT RUN SEQUENCE.
2334      ; CLEAR THE ERROR COUNTER AND SIGNATURE EXTENSION FLAGS.
2335      ;
2336      ; INPUT:
2337      ;
2338      ;      R0      POINTER TO TEST ID ASCIZ STRING
2339      ;
2340      ; OUTPUT:
2341      ;
2342      ;      R5      ADDRESS OF FIRST DEVICE REGISTER
2343      ;
2344      ; IMPLICIT OUTPUTS:
2345      ;
2346      ;      TSTCNT  UPDATED TO COUNT TESTS PERFORMED SINCE START OR RESTART
2347      ;
2348      ; SIDE EFFECTS:
2349      ;
2350      ;      INTERRUPT LEVEL IS RASIED TO LEVEL OF
2351      ;      THE DEVICE UNDER TEST
2352      ;
2353      ; -
2354
2354 016500      TSTSETUP::
2355 016500      010046      MOV      R0, -(SP)      ; SAVE THE TEST ID MESSAGE
2356 016502      005037      003150      CLR      SIFLAG      ; CLEAR "SOFT INIT" FLAG
2357 016506      005037      016746      CLR      ERRK      ; CLEAR LOCAL ERROR COUNTER.
2358 016512      005037      005766      CLR      EXTA      ; CLEAR ERROR EXTENSION FLAG.
2359 016516      105037      016134      CLR      INTMASK      ; CLEAR INTERRUPT MASK (CHECK ERROR)
2360 016522      013700      002200      MOV      UNITN, R0      ; GET THE UNIT NUMBER,
2361 016526      006300      ASL      R0      ; ... AND MAKE IT A WORD OFFSET.
2362 016530      005737      003110      TST      NODEV      ; DID STARTUP FIND THE DEVICE?
2363 016534      001430      BEQ      4$      ; BR IF YES
2364 016536      100010      BPL      3$      ; BR IF NOT IDLE
2365 016540      052760      160000      003172      BIS      0160000, ERTABL(R0) ; FLAG ERROR IN THE ERROR TABLE
2366 016546      104455      ERRDF      1, NXR, NXRERR      ; NO DEVICE HERE -- PRINT IT
2367 016556      000407      .WORD      1
2368 016560      052760      160001      003172      3$:      .WORD      NXR
2369 016566      104455      .WORD      NXRERR
2370 016576      012737      177777      003106      2$:      .WORD      2
2371 016604      013700      002200      .WORD      NOINIT
2372 016612      104451      .WORD      0
2373
2374      MOV      0-1, DUFLG      ; DROP THE UNIT
2375      DODU
2376      MOV      UNITN, R0
2377      TRAP      C$DODU
2378      DOCLN
2379      ; ABORT THE PASS

```

TSTSETUP - PRINT TEST NAME AND INIT ERROR COUNTS

SEU 0092

2373	016612	104444			TRAP	CIDCLN		
2374	016614	000423			BR	50		
2375	016616			40:	RFLAGS	R0		; GET THE OPERATOR FLAGS.
	016616	104421			TRAP	CIRFLA		
2376	016620	032700	001000		BIT	@PNT,R0		; PRINT THE TEST NUMBERS?
2377	016624	001412			BEQ	10		; BR IF NO
2378	016626	011600			MOV	(SP),R0		;GET THE ID MESSAGE
2379	016630				PRINTF	@TNAM,R0		;DISPLAY THE TEST ID
	016630	010046			MOV	R0,-(SP)		
	016632	012746	016674		MOV	@TNAM,-(SP)		
	016636	012746	000002		MOV	@2,-(SP)		
	016642	010600			MOV	SP,R0		
	016644	104417			TRAP	CIPNTF		
	016646	062706	000006		ADD	@6,SP		
2380	016652	005237	002212	10:	INC	TSTCNT		; BUMP TEST COUNTER.
2381	016656				SETPRI	IPRI		;PRIORITY THAT OF DEVICE
	016656	013700	002210		MOV	IPRI,R0		
	016662	104441			TRAP	CISPRI		
2382	016664	005726		50:	TST	(SP),		;FIX UP THE STACK
2383	016666	013705	002204		MOV	CSRADDR,R5		; ADDRESS OF TSV REGISTERS ON UNIBUS
2384	016672	000207			RTS	PC		
2385	016674	045	123	045	TNAM:	.ASCIZ	'#S#T#A Test'	
2386						.EVEN		

TSTEND PRINT ERRORS RECEIVED

```

2386                                     .SBTTL TSTEND - PRINT ERRORS RECEIVED
2389
2390 ; AT END OF EACH TEST, PRINT THE NUMBER OF ERRORS RECEIVED
2391 ; IF NORMAL ERROR REPORTING IS DISABLED (FLA:IER).
2392
2393 TSTEND: RFLAGS RO
2394         TRAP C0RFLA
2395         BIT RO, #IER
2396         BEQ 10 ; BR IF "IER" NOT SET.
2397         PRINTF #ESUM,ERRK ; PRINT ERROR COUNT.
2398         MOV ERRK, -(SP)
2399         MOV #ESUM, -(SP)
2400         MOV #2, -(SP)
2401         MOV SP, RO
2402         TRAP C0PNTF
2403         ADD #6, SP
2404         RTS PC
2405
2406 ERRK: 0 ; LOCAL ERROR COUNT.
2407 ESUM: .ASCIZ /%A %D%#A ERRORS/
2408 EMAXDU: .ASCIZ /ERROR LIMIT REACHED -- DROPPING UNIT/
2409         .EVEN
2410
2411                                     .SBTTL INCERK - INCREMENT LOCAL ERROR COUNT
2412
2413 ;*
2414 ; ROUTINES TO INCREMENT LOCAL ERROR COUNT AND CHECK FOR LIMIT:
2415
2416 INCERK: INC ERRK ; INCREMENT LOCAL ERROR COUNT
2417         MOV RO, -(SP) ; SAVE RO
2418         MOV UNITN, RO ; GET UNIT NUMBER
2419         ASL RO ; ... AND MAKE IT A WORD OFFSET
2420         ADD #ERTABL, RO ; RO GETS ADDRESS OF ERROR TABLE ENTRY.
2421         INC (RO) ; INCREMENT THE DEVICE ERROR COUNT
2422         BIT #7777, (RO) ; DID WE OVERFLOW THE FIELD?
2423         BNE 10 ; BR IF NO.
2424         DEC (RO) ; YES -- BACK IT UP TO 7777.
2425         MOV (SP)+, RO ; RESTORE RO
2426         RTS PC ; RETURN TO CALLER.
2427
2428 CKEMAX: MOV RO, -(SP) ; SAVE RO
2429         MOV UNITN, RO ; GET UNIT NUMBER
2430         ASL RO ; ... AND MAKE IT A WORD OFFSET
2431         MOV ERTABL(RO), RO ; GET ERROR TABLE ENTRY
2432         BIC #170000, RO ; EXTRACT ERROR COUNT FIELD
2433         CMP RO, GERRMAX ; IS GLOBAL LIMIT EXCEEDED FOR THIS UNIT?
2434         BHS 10 ; BR IF YES
2435         CMP ERRK, LERRMAX ; IS LOCAL LIMIT EXCEEDED FOR THIS TEST?
2436         BLO 20 ; BR IF NO
2437         RFLAGS RO ; GET OPERATOR FLAGS
2438         TRAP C0RFLA
2439         BIT #IDU, RO ; IS DROPPING INHIBITED?
2440         BNE 20 ; BR IF YES.
2441         MOV #-1, DUFLG ; NO -- DROP THE UNIT
2442         ERRDF 4, EMAXDU
2443         TRAP C0ERDF
2444         .WORD 4
2445         .WORD EMAXDU

```

INCERK - INCREMENT LOCAL ERROR COUNT

SEQ 0094

	017154	000000		.WORD	0	
2434	017156			DODU	UNITN	
	017156	013700	002200	MOV	UNITN,R0	
	017162	104451		TRAP	C#DODU	
2435	017164			DOCLN		
	017164	104444		TRAP	C#DCLN	
2436	017166	012600	24:	MOV	(SP)+,R0	; RESTORE R0
2437	017170	000207		RTS	PC	; RETURN TO CALLER
2438						

CKDROP CHECK IF UNIT SHOULD BE DROPPED

```

2440 .SBTTL CKDROP CHECK IF UNIT SHOULD BE DROPPED
2441 ;
2442 ; CHECK IF UNIT SHOULD BE DROPPED
2443 ;
2444 017172 010046 CKDROP: MOV RO, -(SP)
2445 017174 FORCERROR 1$,NOTSSR
2446 017204 RFLAGS RO
2447 017206 104421 TRAP C#RFLA
2448 017212 032700 000040 BIT #IDU,RO
2449 017214 001010 BNE 1$
2450 017216 012737 177777 003106 MOV (SP),RO
2451 017224 013700 002200 MOV #1,DUFLG
2452 017232 104451 DODU UNITN
2453 017234 012600 MOV UNITN,RO
2454 017236 000207 TRAP C#DODU
2455 ;
2456 ;
2457 ;
2458 ;
2459 ;
2460 ;
2461 ;
2462 ;
2463 017240 DOCLN ;ABORT THE PASS
2464 017240 004737 015764 TRAP C#DCLN
2465 017244 000207 MOV (SP)+,RO
2466 RTS PC
2467
2468

```

```

.SBTTL CONFIG DETERMINE CONFIGURATION OF SYSTEM
;
; SUBROUTINE - DETERMINE CONFIGURATION OF TSV05 SYSTEM.
;
CONFIG:
JSR PC,SOFINIT
RTS PC

```

KTON,KTOFF - ENABLE/DISABLE MEMORY MANAGEMENT

```

2470 .SBTTL KTON,KTOFF ENABLE/DISABLE MEMORY MANAGEMENT
2471 ;
2472 ; SUBROUTINE - ENABLE MEM MGT.
2473 ;
2474 017246 005737 003126 KTON: TST KFLG ; GOT KT?
2475 017252 001403 BEQ 1$ ; NO.
2476 017254 012737 000001 177572 MOV 01,SRO ; YES. ENABLE KT11.
2477 017262 000207 1$: RTS PC
2478
2479
2480
2481 ;
2482 ; SUBROUTINE - DISABLE MEM MGT.
2483 ;
2484 017264 005737 003126 KTOFF: TST KFLG ; GOT KT11?
2485 017270 001405 BEQ 1$ ; NO.
2486 017272 000240 NOP
2487 017274 000240 NOP
2488 017276 012737 000000 177572 MOV 00,SRO ; DISABLE KT.
2489 017304 000207 1$: RTS PC
2490
2491

```


SETMAP SETUP PAR6 MAPPING

```

2493 .SBTTL SETMAP - SETUP PAR6 MAPPING
2494
2495 ;*
2496 ;
2497 ; THIS ROUTINE SETS UP KERNEL PAR6 TP HANDLE
2498 ; AN 18 BIT ADDRESS. THE OFFSET INTO THE PAGE
2499 ; IS RETURNED BIASED TO PAR6.
2500 ;
2501 ; INPUTS:
2502 ;
2503 ; R0 HIGH ORDER ADDRESS BITS
2504 ; R1 LOW ORDER ADDRESS BITS
2505 ;
2506 ; OUTPUTS:
2507 ;
2508 ; R0 OFFSET INTO BLOCK WITH PAR6 BIAS (I.E. THE ADDRESS)
2509 ; CARRY SET IF SUCCESS
2510 ; CLR IF ERROR
2511 ;
2512 SETMAP:
2513 SAVREG
2514 TST KTFLG ;SAVE R1-R4 UNTIL NEXT RETURN
2515 BEQ 10$ ;SYSTEM HAVE ABOVE 28K?
2516 MOV R1,R2 ;BR IF NO
2517 .REPT 6 ;SAVE LOW ORDER BITS
2518 ASR R0
2519 ROR R1 ;CONVERT WORD ADDRESS TO 32W BLOCKS
2520 .ENDR ;MAKE IT DOUBLE PRECISION
2521 BIC #177,R1 ;ALINE FOR LOWER 4K BOUNDARY
2522 CMP R1,KTFLG ;HIGHER THAN EXISTING MEMORY?
2523 BHS 10$ ;BR IF YES
2524 MOV R1,#KIPAR6 ;SETUP MAPPING REGISTER PAR6
2525 BIC #160000,R2 ;SETUP DISPLACEMENT IN PAGE
2526 ADD #140000,R2 ;ADD IN PAR6 BIAS
2527 MOV R2,R0 ;RETURN IN R0
2528 SEC ;SET SUCCESS
2529 BR 15$
2530 10$: CLC ;SET FAILURE
2531 15$: RTS PC ;RETURN
2532

```

FILLMEM FILL MEMORY WITH BACKGROUND PATTERN

```

2534 .SBTTL FILLMEM - FILL MEMORY WITH BACKGROUND PATTERN
2535 ;
2536 ; FILL MEMORY WITH A BACKGROUND PATTERN
2537 ;
2538 ; INPUTS:
2539 ;
2540 ; RO = BACKGROUND PATTERN
2541 ; FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
2542 ; KTFLG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
2543 ;
2544 ; OUTPUTS:
2545 ; NONE
2546 ;
2547 017412 FILLMEM:
2548 017412 SAVREG ;SAVE R1-R5 UNTIL NEXT RETURN
2549 017416 004737 017264 JSR PC,KTOFF ;DISABLE KT.
2550 017422 010003 MOV R0,R3 ;COPY TEST PATTERN
2551 017424 013701 003120 MOV FREE,R1 ;GET FIRST FREE LOCATION
2552 017430 013702 003122 MOV FRESIZ,R2 ;SIZE OF FREE SPACE BELOW 28K.
2553 017434 010321 10$: MOV R3,(R1)+ ;STORE A BACKGROUND WORD
2554 017436 005302 DEC R2 ;DONE ALL MEMORY IN FREE SPACE?
2555 017440 003375 BGT 10$ ;BR IF NO
2556 017442 005737 003126 TST KTFLG ; GOT KT?
2557 017446 001477 BEQ 55$ ; NO. GET OUT.
2558 017450 004737 017246 JSR PC,KTON ; YES. ENABLE KT.
2559 017454 005000 CLR R0 ;HIGH ORDER ADDRESS START
2560 017456 013701 003146 MOV PST32W,R1 ;GET >28K START ADDRESS (IN 32W BLOCKS)
2561 000006 .REPT 6
2562 CLC ;CLEAR C BIT
2563 ROL R1 ;CONVERT BLOCKS TO WORDS
2564 ROL R0 ;MAKE IT DOUBLE PRECISION
2565 .ENDR
2566 017526 004737 017306 JSR PC,SETMAP ;SETUP PAR6 MAPPING REGISTER
2567 017532 010320 30$: MOV R3,(R0)+ ;STORE TEST PATTERN IN >28K ADDRESS
2568 017534 020027 160000 CMP R0,#160000 ;END OF PAR6 MAPPING AREA?
2569 017540 103774 BLO 30$ ;BR IF NO
2570 017542 162700 020000 SUB #20000,R0 ;BACKUP INTO PAR6 MAPPING BEGIN
2571 017546 062737 000200 172354 ADD #200,#KIPAR6 ;POINT TO NEXT 4K BLOCK >28K.
2572 017554 023737 172354 003126 CMP #KIPAR6,KTFLG ;END OF MEMORY?
2573 017562 001427 BEQ 50$ ;BR IF YES
2574 017564 005737 003140 TST T23A ;11/23A?
2575 017570 001407 BEQ 35$ ;NO KEEP GOING
2576 017572 013704 177572 MOV SRO,R4 ;GET SRO CONTENTS
2577 017576 042704 177761 BIC #17761,R4 ;CLEAR ALL BUT PAGE NUMBER
2578 017602 022704 000016 CMP #16,R4 ;SEE IF PAGE 7
2579 017606 001415 BEQ 50$ ;EXIT IF THERE
2580 017610 005737 003142 35$: TST T23B ;11/23B?
2581 017614 001410 BEQ 45$ ;NO KEEP GOING
2582 017616 023727 172354 007600 CMP #KIPAR6,#7600 ;REACHED 18 BITS?
2583 017624 103001 BHIS 40$ ;YES
2584 017626 000403 BR 45$ ;NO KEEP GOING
2585 017630 012737 000020 172516 40$: MOV #20,SRO ;SET 22 BIT RELOCATION
2586 017636 000137 017532 45$: JMP 30$ ;KEEP GOING ON ETC.
2587 017642 004737 017264 50$: JSR PC,KTOFF ;DISABLE KT.
2588 017646 000207 55$: RTS PC
2589

```


CMPMEM COMPARE MEMORY TO BACKGROUND PATTERN

```

2591 .SBTTL CMPMEM - COMPARE MEMORY TO BACKGROUND PATTERN
2592 ;
2593 ; COMPARE MEMORY WITH A BACKGROUND PATTERN
2594 ;
2595 ; INPUTS:
2596 ;
2597 ;     RO = BACKGROUND PATTERN
2598 ;     FREE = FIRST LOCATION AVAILABLE TO DIAGNOSTIC
2599 ;     KTFLG = SET TO HIGHEST MEMORY LOCATION IF > 28K.
2600 ;
2601 ; OUTPUTS:
2602 ;
2603 ;     CARRY - SET IF NO ERROR
2604 ;     CARRY - CLR IF ERROR
2605 ;
2606 ; IMPLICIT OUTPUTS:
2607 ;
2608 ;     ERRHI - ERROR HIGH ADDRESS
2609 ;     ERRLO - ERROR LOW ADDRESS
2610 ;     EXPD  - EXPECTED DATA
2611 ;     RECV  - RECEIVED DATA
2612 ;
2613 ; CMPMEM:
2614 ;     SAVREG
2615 ;     MOV RO,R3 ;SAVE R1-R5 UNTIL NEXT RETURN
2616 ;     JSR PC,KTOFF ;COPY TEST PATTERN
2617 ;     MOV FREE,R1 ;DISABLE KT.
2618 ;     MOV FRESIZ,R2 ;GET FIRST FREE LOCATION
2619 ;     CMP R3,(R1) ;SIZE OF FREE SPACE BELOW 28K.
2620 ;     BEQ 15$ ;FREE SPACE LOCATION EQUAL TO EXPD?
2621 ;     MOV R1,ERRLO ;BR IF YES
2622 ;     CLR ERRHI ;SAVE ADDRESS IN ERROR
2623 ;     MOV R3,EXPD ;NO HIGH ADDRESS
2624 ;     MOV (R1),RECV ;SAVE EXPD FOR ERROR REPORT
2625 ;     BR 50$ ;SAVE RECV FOR ERROR REPORT
2626 ;     TST (R1),R2 ;
2627 ;     DEC R2 ;POINT TO NEXT ADDRESS
2628 ;     BGT 10$ ;DONE ALL MEMORY IN FREE SPACE?
2629 ;     TST KTFLG ;BR IF NO
2630 ;     BEQ 55$ ; GOT KT?
2631 ;     JSR PC,KTON ; NO. GET OUT.
2632 ;     CLR RO ; YES. ENABLE KT.
2633 ;     MOV PST32W,R1 ;HIGH ORDER ADDRESS START
2634 ;     .REPT 6 ;GET >28K START ADDRESS (IN 32W BLOCKS)
2635 ;     ROL R1
2636 ;     ROL RO ;CONVERT BLOCKS TO WORDS
2637 ;     .ENDR ;MAKE IT DOUBLE PRECISION
2638 ;     BIC #177,R1 ;ALINE 4K BOUNDARY
2639 ;     MOV RO,-(SP) ;SAVE HIGH ORDER
2640 ;     MOV R1,(SP) ;SAVE LOW ORDER
2641 ;     JSR PC,SETMAP ;SETUP PAR6 MAPPING REGISTER
2642 ;     MOV RO,R4 ;COPY ADDRESS BIASED TO PAR6
2643 ;     MOV (SP)+,R1 ;RESTORE LOW ORDER IN NON PAR6 FORMAT
2644 ;     MOV (SP)+,RO ;RESTORE HIGH ORDER IN NON PAR6 FORMAT
2645 ;     CMP R3,(R4) ;ABOVE 28K LOCATION EQUAL EXPD?
2646 ;     BEQ 32$ ;BR IF YES
2647 ;     MOV RO,ERRHI ;SAVE HIGH ORDER IN ERROR

```

CMPMEM - COMPARE MEMORY TO BACKGROUND PATTERN

2648	020030	010137	002236		MOV	R1,ERRLO	;SAVE LOW ORDER IN ERROR
2649	020034	010337	002230		MOV	R3,EXPD	;SAVE EXPD FOR ERROR REPORT
2650	020040	011437	002232		MOV	(R4),RECV	;SAVE RECV FOR ERROR REPORT
2651	020044	000421			BR	50\$	;
2652	020046	062701	000002	32\$:	ADD	#2,R1	;UPDATE NON PAR6 ADDRESS
2653	020052	005500			ADC	R0	;MAKE IT DOUBLE PRECISION ADD
2654	020054	062704	000002		ADD	#2,R4	;UPDATE PAR FORMAT ADDRESS
2655	020060	020427	160000		CMP	R4,#160000	;END OF PAR6 MAPPING AREA?
2656	020064	103755			BLO	30\$	;BR IF NO
2657	020066	162704	020000		SUB	#20000,R4	;BACKUP INTO PAR6 MAPPING BEGIN
2658	020072	062737	000200	172354	ADD	#200,#KIPAR6	;POINT TO NEXT 4K BLOCK >28K.
2659	020100	023737	172354	003126	CMP	#KIPAR6,KTFLG	;END OF MEMORY?
2660	020106	101744			BLOS	30\$	;BR IF NO
2661	020110	004737	017264	50\$:	JSR	PC,KTOFF	;TURN OFF MEMORY MAPPING
2662	020114	000241			CLC		;SET FAILURE
2663	020116	000403			BR	60\$	;
2664	020120	004737	017264	55\$:	JSR	PC,KTOFF	;TURN OFF MEMORY MAPPING
2665	020124	000261			SEC		;SET SUCCESS
2666	020126	000207		60\$:	RTS	PC	
2667							

REGSAV SAVE R1 R5 ON STACK

```

2669          .SBTTL  REGSAV  -  SAVE R1 R5 ON STACK
2670          ; *
2671          ;
2672          ; ROUTINE TO
2673          ; SAVE R1 THROUGH R5 ON THE STACK
2674          ;
2675          ; CALLING SEQUENCE:
2676          ;
2677          ;     JSR      R5,REGSAV
2678          ;
2679          ; THIS IS A COOROUTINE WHICH TRANSFER CONTROL BACK TO
2680          ; THE CALLING ROUTINE. AT THE END OF THE CALLING ROUTINE,
2681          ; THE RTS PC RETURNS CONTROL TO THIS ROUTINE TO RESTORE
2682          ; REGISTERS.
2683          ;
2684          ; THIS ROUTINE SHOULD ONLY BE CALLED FROM ROUTINES WHICH ARE
2685          ; CALLED VIA A JSR PC INSTRUCTION
2686          ;
2687          ;
2688          ;
2689 020130      REGSAV:
2690 020130      MOV      R4,-(SP)
2691 020132      MOV      R3,-(SP)
2692 020134      MOV      R2,-(SP)
2693 020136      MOV      R1,-(SP)
2694 020140      MOV      R5,-(SP)
2695 020142      MOV      10.(SP),R5
2696 020146      JSR      PC,@(SP)+
2697 020150      MOV      (SP)+,R1
2698 020152      MOV      (SP)+,R2
2699 020154      MOV      (SP)+,R3
2700 020156      MOV      (SP)+,R4
2701 020160      MOV      (SP)+,R5
2702 020162      RTS      PC
2703

```

000012

GETPAT GET 8 BIT PATTERN FROM OPERATOR

```

2705 .SBTTL GETPAT - GET 8 BIT PATTERN FROM OPERATOR
2706 ;*
2707 ;
2708 ;ROUTINE TO REQUEST AN 8 BIT DATA PATTERN FROM THE OPERATOR
2709 ;
2710 ;INPUTS:
2711 ;
2712 ; NONE.
2713 ;
2714 ;OUTPUTS:
2715 ;
2716 ; RO OCTAL NUMBER FROM THE OPERATOR
2717 ;
2718 ;CALLING SEQUENCE:
2719 ;
2720 ; JSR PC,GETPAT
2721 ;
2722 ;-
2723
2724 020164 GETPAT::
2725 020164 SAVREG ;SAVE THE GENERAL REGISTERS
2726 020170 1$ GMANID DATASC,PATDAT,0,377,0,377,NO
2727 020170 104443 TRAP C$GMAN
2728 020172 000406 BR 10000$
2729 020174 020220 .WORD PATDAT
2730 020176 000022 .WORD T$CODE
2731 020200 020222 .WORD DATASC
2732 020202 000377 .WORD 377
2733 020204 000000 .WORD T$LOLIM
2734 020206 000377 .WORD T$HILIM
2735 020210 10000$:
2736 020210 103367 BNCOMPLETE 1$ ;RETRY IF ERROR
2737 020212 013700 BCC 1$
2738 020216 000207 MOV PATDAT,RO ;DATA PATTERN FROM OPERATOR
2739 RTS PC ;RETURN TO CALLER
2740
2741 ;*
2742 ;LOCAL DATA AREA
2743 ;-
2744
2745 020220 000000 PATDAT: .WORD 0 ;TEMPORARY STORAGE FOR DATA
2746 020222 105 116 124 DATASC: .ASCIZ 'ENTER DATA PATTERN'
2747 .EVEN

```

GETSEL ISSUE MENU AND GET OPERATOR RESPONSE

```

2739 .SBTTL GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE
2740 ;*
2741 ;
2742 ;ROUTINE TO ISSUE A MENU AND GET
2743 ;THE OPERATOR'S RESPONSE.
2744 ;
2745 ;INPUTS:
2746 ;
2747 ;      R0      ADDRESS OF ASCIZ STRING OF MENU
2748 ;      R1      MAXIMUM ALLOWABLE OPERATOR RESPONSE
2749 ;
2750 ;OUTPUTS:
2751 ;
2752 ;      R0      NUMBER OF THE OPERATOR'S SELECTION
2753 ;
2754 ;-
2755
2756 020246 GETSEL::
2757 020246 SAVREG ;SAVE GENERAL REGISTERS
2758 020252 010002 MOV R0,R2 ;SAVE THE MENU ADDRESS
2759 020254 010203 1$: MOV R2,R3 ;START OF MENU STRING
2760 020256 005713 2$: TST (R3) ;END OF ASCII ?
2761 020260 001412 BEQ 3$ ;BRANCH IF ALL LINES DISPLAYED
2762 020262 PRINTF #SELASC,(R3)+ ;DISPLAY THE MENU
2763 020262 012346 MOV (R3)+,-(SP)
2764 020264 012746 020432 MOV #SELASC,-(SP)
2765 020270 012746 000002 MOV #2,-(SP)
2766 020274 010600 MOV SP,R0
2767 020276 104417 TRAP C$PNTF
2768 020300 062706 000006 ADD #6,SP
2769 020304 000764 BR 2$
2770 020306 3$: GMANID MENASC,MENRES,D,-1,0,1,NO
2771 020306 104443 TRAP C$GMAN
2772 020310 000406 BR 10001$
2773 020312 020466 .WORD MENRES
2774 020314 000042 .WORD T$CODE
2775 020316 020437 .WORD MENASC
2776 020320 177777 .WORD -1
2777 020322 000000 .WORD T$LOLIM
2778 020324 177777 .WORD T$HILIM
2779 020326 10001$: BNCOMPLETE 1$ ;RETRY IF ERROR
2780 020326 103352 BCC 1$
2781 020330 013700 020466 MOV MENRES,R0 ;GET THE OPERATOR'S REPLY
2782 020334 020001 CMP R0,R1 ;COMPARE TO MAXIMUM ALLOWED
2783 020336 101411 BLOS 5$ ;BRANCH IF OK
2784 020340 PRINTF #MENERR ;DISPLAY ERROR MESSAGE
2785 020340 012746 020364 MOV #MENERR,-(SP)
2786 020344 012746 000001 MOV #1,-(SP)
2787 020350 010600 MOV SP,R0
2788 020352 104417 TRAP C$PNTF
2789 020354 062706 000004 ADD #4,SP
2790 020360 000735 BR 1$ ;RETRY
2791 020362 000207 5$: RTS PC ;RETURN TO CALLER
2792 020364 045 116 045 MENERR: .ASCIZ 'MNA *** Menu Selection Too Large ***'
2793 020432 045 116 045 SELASC: .ASCIZ 'MNT'
2794 020437 105 156 164 MENASC: .ASCIZ 'Enter Menu Selection: '

```

N8

TSV3 - GLOBAL AREAS MACRO M1200 23 MAR-84 09:44 PAGE 64-1

GETSEL - ISSUE MENU AND GET OPERATOR RESPONSE

SEQ 0104

2775
2776 020466 000000 MENRES: .EVEN .WORD 0

CHKMAN CHECK MANUAL INTERVENTION LEGALITY

```

2778          .SBTTL  CHKMAN  - CHECK MANUAL INTERVENTION LEGALITY
2779
2780          ;
2781          ;ROUTINE TO TEST FOR MANUAL INTERVENTION LEGALITY.
2782          ;
2783          ;INPUT:
2784          ;
2785          ;      NONE.
2786          ;
2787          ;OUTPUT:
2788          ;
2789          ;      CARRY    0      MANUAL INTERVENTION NOT ALLOWED
2790          ;              1      MANUAL INTERVENTION IS OK
2791          ;
2792          ;SIDE EFFECTS:
2793          ;
2794          ;      A MESSAGE IS DISPLAYED WARNING THAT TEST IS
2795          ;      NOT EXECUTED IF MANUAL INTERVENTION IS NOT
2796          ;      ALLOWED.
2797          ;
2798          ;-
2799
2800 020470      CHKMAN::
2801 020470          SAVREG                      ;SAVE THE REGISTERS
2802 020474      MANUAL                          ;SEE IF MANUAL INTERVENTION OK
2803 020474      104450      TRAP    C:MANI
2804 020476      103411      BCOMPLETE 1:      ;BRANCH IF ALLOWED
2805 020500      012746      020524      PRINTF  #NOMAN      ;PRINT THE WARNING MESSAGE
2806 020504      012746      000001      MOV     #NOMAN, -(SP)
2807 020510      010600      MOV     #1, -(SP)
2808 020512      104417      MOV     SP, R0
2809 020514      062706      000004      TRAP    C:PNTF
2810 020520      000241      ADD     #4, SP
2811 020522      000207      CLC                      ;CLEAR CARRY FOR ERROR
2812          1:      RTS     PC                      ;RETURN
2813
2814 020524      045      116      045  NOMAN:  .ASCIZ  '###A *** Manual Intervention not Allowed - Test Aborted ***'
2815          .even

```

ENVIRN - SETUP FREE DIAGNOSTIC SPACE

```

2811 .SBTTL ENVIRN - SETUP FREE DIAGNOSTIC SPACE
2812
2813 ; SUBROUTINE TO SET-UP VARIOUS ENVIRONMENTAL PARAMETERS.
2814 ;
2815 ENVIRN: MEMORY R0
2816 020620 104431 TRAP C:MEM
2817 020622 010037 003120 MOV R0,FREE ; GET 1ST FREE ADDRESS...
2818 020626 062737 000002 003120 ADD #2,FREE
2819 020634 011037 003122 MOV (R0),FRESIZ ;...AND WORD COUNT.
2820 020640 162737 000004 003122 SUB #4,FRESIZ
2821 020646 013702 002012 MOV L:UNIT,R2 ; GET NUMBER OF UNITS
2822 020652 162737 000007 003122 10: SUB #7,FRESIZ ; TAKE AWAY 7 WORDS PER UNIT
2823 020660 005302 DEC R2
2824 020662 001373 BNE 10:
2825 020664 013700 003120 MOV FREE,R0 ;GET FIRST FREE ADDRESS
2826 020670 063700 003122 ADD FRESIZ,R0 ;POINT TO LAST FREE ADDRESS
2827 020674 162700 000002 SUB #2,R0 ;BACKUP 1 WORD
2828 020700 010037 003124 MOV R0,FREEM ;STORE LAST FREE ADDRESS
2829 020704 000240 NOP ;*****
2830 020706 012701 177520 MOV #BDVPCR,R1 ;GET BDV11 PCR ADDRESS
2831 020712 010102 MOV R1,R2 ;COPY TO R2
2832 020714 062702 000002 ADD #2,R2 ;SET THE RANGE
2833 020720 004737 016366 JSR PC,XNKM ;SEE IF WE HAVE ONE
2834 020724 103001 BCC 15: ;OK TO SET FLAGS
2835 020726 000445 BR 40: ;RETURN WITH FLAGS CLEAR
2836 020730 013701 177520 15: MOV BDVPCR,R1 ;SAVE PCR CONTENTS
2837 020734 062701 000001 ADD #1,R1 ;ADD ONE TO IT
2838 020740 012702 177520 MOV #BDVPCR,R2 ;GET BDV11 PCR ADDRESS
2839 020744 005212 INC (R2) ;TRY TO WRITE TO IT
2840 020746 013703 177520 MOV BDVPCR,R3 ;GET RESULTS
2841 020752 020103 CMP R1,R3 ;DID IT CHANGE?
2842 020754 001017 BNE 20: ;NO, MUST BE 11/238
2843 020756 005237 003140 INC T23A ;SET THE FLAG
2844 020762 042737 170000 002120 BIC #170000,L:HIME ;SUPERVISOR COULD BE WRONG
2845 020770 000240 NOP ;BR 40: FOR RELEASE
2846 020772 PRINTF #M8186 ;TELL THE SYSTEM TYPE
2847 020774 012746 005550 MOV #M8186,-(SP)
2848 020776 012746 000001 MOV #1,-(SP)
2849 021002 010600 MOV SP,R0
2850 021004 104417 TRAP C:PNTF
2851 021006 062706 000004 ADD #4,SP
2852 021012 000413 BR 40: ;RETURN
2853 021014 005237 003142 20: INC T238 ;SET THE FLAG
2854 021020 000240 NOP ;BR 40: FOR RELEASE
2855 021022 PRINTF #M8189 ;TELL THE SYSTEM TYPE
2856 021024 012746 005641 MOV #M8189,-(SP)
2857 021026 012746 000001 MOV #1,-(SP)
2858 021032 010600 MOV SP,R0
2859 021034 104417 TRAP C:PNTF
2860 021036 062706 000004 ADD #4,SP
2861 021042 000207 40: RTS PC ;RETURN

```


KTINIT SETUP KT11 MEMORY MANAGEMENT REGISTERS

```

2853                                     .SBTTL  KTINIT  - SETUP KT11 MEMORY MANAGEMENT REGISTERS
2854                                     ;*
2855                                     ;
2856                                     ;ROUTINE TO INIT KT-11
2857                                     ;
2858                                     ;-
2859
2860 021044                                     KTINIT:
2861 021044 005037 003126                     CLR      KTFLG          ; INIT >28K MEMORY FLAG
2862 021050 005037 003130                     CLR      KTENABLE       ; INIT TEST >28K FLAG
2863 021054 023727 002120 001577             CMP      L#HIME,#1577    ; GOT ENOUGH MEMORY (>28K)?
2864 021062 101444                          BLOS     9#              ; NO.
2865 021064 013700 000004                     MOV      @ERRVEC,R0      ; SAVE OLD ERR VEC PTR.
2866 021070 012737 021162 000004             MOV      #2#,@ERRVEC    ; SET ERR VEC PTR.
2867 021076 005737 177572                     TST      @SRO          ; GOT KT11?
2868 021102 000240                          NOP                      ; (TRAP IF NO).
2869 021104 013737 002120 003126             MOV      L#HIME,KTFLG    ; YES. SET KT FLAG.
2870 021112 042737 000177 003126             BIC      #177,KTFLG
2871 021120 010037 000004                     MOV      R0,@ERRVEC    ; RESTORE OLD ERR VEC PTR.
2872 021124 005000                          CLR      R0            ; R0 = AR DATA.
2873 021126 012701 172340                     MOV      @KIPAR0,R1       ; R1 = KI REGS PTR.
2874 021132 012761 077406 177740 1#         MOV      #77406,-40(R1)  ; SET DESCRIPTOR REG.
2875 021140 010021                          MOV      R0,(R1)        ; SET KIPAR REG.
2876 021142 062700 000200                     ADD      #200,R0          ; BUMP AR DATA BY "4K".
2877 021146 020027 002000                     CMP      R0,#2000        ; AT "I/O"?
2878 021152 001367                          BNE      1#              ; NO.
2879 021154 012741 177600                     MOV      #177600,-(R1)    ; YES. SET KIPAR7 FOR I/O.
2880 021160 000405                          BR       9#
2881
2882 021162 012716 021170                     2#         MOV      #6#,(SP)    ; SET UP RETURN
2883 021166 000002                          RTI                      ; RTI TO NEXT LOCATION
2884
2885 021170 010037 000004                     6#         MOV      R0,@ERRVEC    ; RESTORE OLD ERR VEC PTR.
2886
2887 021174 000207                     9#         RTS      PC
2888

```

KTINIT - SETUP KT11 MEMORY MANAGEMENT REGISTERS

```

2890      ;
2891      ; SUBROUTINE TO SET EXTENDED FEATURES SWITCH
2892      ;
2893      ; Requires that SOFINIT and WRTPHR have been done previous to call.
2894      ;
2895      ;
2896      ; INPUTS:
2897      ; R5      CURRENT UNIT NUMBER
2898      ; OUTPUTS:
2899      ; The Extended Features Switch is set.
2900      ;
2901      ;
2902      ;
2903      INVERT::
2904
2905      021176 005737 002224      TST      EXTFEA      ; IS SWITCH SET?
2906      021202 001020      BNE      1$      ; YES,EXIT STAGE RIGHT!(or the next one outa town!)
2907      021204 012737 100206 021250      MOV      #100206,CMDPKT      ; WRT SUB-SYS MEM CMD
2908      021212 012737 021260 021252      MOV      @WSMBK,CMDPKT+2      ; MSG BUF ADDR
2909      021220 012737 000006 021256      MOV      #6,CMDPKT+6      ; BYTE COUNT
2910      021226 012737 100010 021260      MOV      #100010,WSMBK      ; INVERT THE SWITCH
2911      021234 012704 021250      MOV      @CMDPKT,R4      ; SET CMDPKT INTO R4
2912      021240 004737 010652      JSR      PC,WRTPHR      ; DO IT
2913      021244 000207      1$:      RTS      PC      ; RETURN
2914
2915
2916      ; COMMAND PACKET.
2917
2918      021250      .      "      <.,+3>E177774      ;MUST BE ON MOD 4 BOUNDRY.
2919
2920      021250 000000      CMDPKT:: 0      ;1ST WORD IS TS05 COMMAND.
2921      021252 000000      0      ;2ND WORD IS THE BUFFER LOW ADDRESS.
2922      021254 000000      0      ;3RD WORD IS THE BUFFER HIGH ADDRESS.
2923      021256 000000      0      ;4TH WORD IS THE BYTE/RECORD/FILE COUNT.
2924
2925
2926      ; WRITE SUB-SYSTEM MEMORY CHARACTERISTIC BLOCK.
2927
2928      021260 000000      WSMBK:: 0      ;1ST WORD:: SEL 0
2929      021262 000000      0      ;2ND WORD:: SEL 2
2930      021264 000000      0      ;3RD WORD:: SEL 4
2931      .EVEN
2932

```

KTINIT SETUP KT11 MEMORY MANAGEMENT REGISTERS

```

2934      ;
2935      ; SUBROUTINE TO CHECK WETHER OR NOT WE'LL TEST NXM
2936      ;
2937      ;
2938      ; INPUTS:
2939      ; OUTPUTS:
2940      ; The NXMFLG is set if we can test.
2941      ; The NXMLO and NXMHI addresses are setup.
2942      ;
2943      ;
2944      MEMCK::
2945
2946      SAVREG                                ;SAVE THE REGISTERS
2947      CLR      NXMFLG                      ;CLEAR THE FLAG
2948      CLR      NXMLO                      ;CLEAR THE TEST ADDRESS LO
2949      CLR      NXMHI                      ;CLEAR THE TEST ADDRESS HI
2950      TST      T23B                      ;IS IT A 11/23B?
2951      BEQ      1$                          ;NO
2952      CMP      L$HIME,#7777              ; GREATER THAN 128K
2953      BLO      2$                          ; NO
2954      JSR      PC,NXMTST                  ;SETUP THE ADDRESS
2955      BR       13$                        ;SET THE FLAG AND EXIT
2956      TST      T23A                      ;IS IT A 11/23A?
2957      BEQ      4$                          ;NO
2958      CMP      L$HIME,#5777              ;GREATER THAN 96K
2959      BHI      14$                        ;YES,23A/23B WITH 128K MEMORY
2960      CMP      L$HIME,#3777              ;GREATER THAN 64K BUT LESS THAN 92K?
2961      BLO      4$                          ;NO, CHECK 24k
2962      JSR      PC,NXMTST                  ;SETUP THE ADDRESS
2963      BR       13$                        ;SET THE FLAG AND EXIT
2964      CMP      L$HIME,#1577              ;GREATER THAN 24K BUT LESS THAN 64K?
2965      BLO      14$                        ;NO, TELL THEM AND EXIT WITH FLAG CLEAR
2966      JSR      PC,NXMTST                  ;SETUP THE ADDRESS
2967      ADD      #77,NXMHI                  ;FOOL THE 11/02 & 11/03
2968      INC      NXMFLG                      ;SET THE FLAG
2969      BR       15$                        ;EXIT
2970      BR       15$                        ;NOP FOR PRINTOUT
2971      PRINTF   #NOMEM                     ;TELL THEM & EXIT ***NO PRINT*****
2972      MOV      #NOMEM,--(SP)
2973      MOV      #1,--(SP)
2974      MOV      SP,R0
2975      TRAP     C$PNTF
2976      ADD      #4,SP
2977      RTS      PC                          ;RETURN
2978
2979      ;
2980      ; SUBROUTINE TO SETUP THE NXM ADDRESS FOR TESTING
2981      ;
2982      ; OUTPUTS: NXMLO,NXMHI                ;SETUP WITH NXM ADDRESS
2983      ;
2984      ;
2985      NXMTST: MOV      L$HIME,R1            ;GET TOP OF MEMORY
2986      ADD      #200,R1                      ;MAKE IT I/O BLOCK OR OTHER NXM
2987      BIC      #177,R1
2988      MOV      R1,R2                        ;RESAVE RESULTS

```

KTINIT SETUP KT11 MEMORY MANAGEMENT REGISTERS

SEQ 0110

2986		000006		.REPT	6	
2987				ASL	R1	
2988				.ENDR		;PUT IN PLACE FOR XFER
2989	021474	010137	003134	MOV	R1,NXMLO	;SAVE TEST ADDRESS LOW
2990		000012		.REPT	10.	
2991				ASR	R2	
2992				.ENDR		;PUT IN PLACE FOR XFER
2993	021524	042702	177700	BIC	#177700,R2	;DON'T WANT ILA!
2994	021530	010237	003136	MOV	R2,NXMHI	;SAVE TEST ADDRESS HIGH
2995	021534	000207		RTS	PC	;RETURN
2996						
2997						
2998						
2999						
3000	021536					

ENDMOD

KTINIT SETUP KT11 MEMORY MANAGEMENT REGISTERS

```
7
8
9 021536 .TITLE TSV4 - MISCELLANEOUS SECTIONS
021536 BGNMOD TSV4
TSV4::
10
16
```

PROTECTION TABLE

```
18
19 021536 .SBTTL PROTECTION TABLE
    021536 BGNPROT
20 021536 177777 177777 177777 L$PROT::
21 021546 .WORD 1. 1. 1. 1
22      ENDPROT
```

;NO DEVICE PROTECTION REQUIRED.

INITIALIZE SECTION

```

24                                     .SBTTL  INITIALIZE SECTION
25
26                                     ;**
27                                     ;THE INITIALIZE SECTION CONTAINS THE CODING THAT IS PERFORMED
28                                     ;AT THE BEGINNING OF EACH PASS.
29                                     ;
30                                     ;IF "START" OR "RESTART", SET QUICK-PASS FLAG AND BUS INIT.
31                                     ;IF "CONTINUE", NOTHING IS REQUIRED.
32                                     ;
33                                     ;--
34                                     ;*
35                                     ;INSERT TEMPORARY JUMP TO ODT
36                                     ;-
37 021546                               BGNINIT
38 021546                               L$INIT::
39 021546                               SETVEC  #140,#170000,#340           ;ODT ROM ADDRESS           ;JB REV A 0
    021546 012746 000340             MOV      #340,-(SP)
    021552 012746 170000             MOV      #170000,-(SP)
    021556 012746 000140             MOV      #140,-(SP)
    021562 012746 000003             MOV      #3,-(SP)
    021566 104437                   TRAP      C$SVEC
    021570 062706 000010             ADD      #10,SP
40
41 021574 005037 002224             40$: CLR      EXTFEA
42 021600 005037 003132             CLR      NXMFLG
43 021604 012737 006354 002176     MOV      #EPRT1,EPRTSW           ;SET UP PRIMARY MESSAGE FOR REPLACEMENT
44 021612 005037 003150             CLR      SIFLAG               ;CLEAR "SOFT INIT" FLAG
45 021616 005037 003130             CLR      KTENABLE             ;CLEAR TEST ABOVE 28K FLAG
46 021622 005037 002300             CLR      RAMSIZ               ;CLEAR RAM SIZE FOR RAMERR ROUTINE
47 021626                               READEF  #EF.CONTINUE
    021626 012700 006036             MOV      #EF.CONTINUE,R0
    021632 104447                   TRAP      C$REFG
48 021634                               BNCOMPLETE 1$
    021634 103023                   BCC      1$
49 021636 023737 002200 002012     CMP      UNITN,L$UNIT           ;UNIT IN RANGE?
50 021644 103070                   BHIS     4$                   ;BR IF NO.
51 021646 005737 003106             TST      DUFLG               ;DROPPED UNIT?
52 021652 100472                   BMI      NXTU                 ;BR IF YES
53 021654 013701 002200             MOV      UNITN,R1
54 021660 006301                   ASL      R1
55 021662 005761 003172             TST      ERTABL(R1)
56 021666 001516                   BEQ      SETU
57 021670 032761 040000 003172     BIT      #BIT14,ERTABL(R1)       ;DROPPED?
58 021676 001060                   BNE      NXTU
59 021700                               EXIT                       ;DO NOTHING IF "CONTINUE".
    021700 104432                   TRAP      C$EXIT
    021702 000416                   .WORD    L10030-.
60 021704                               1$: READEF  #EF.NEW
    021704 012700 000035             MOV      #EF.NEW,R0
    021710 104447                   TRAP      C$REFG
61 021712                               BNCOMPLETE NXTU           ;TAKE NEXT UNIT IF NOT NEW PASS.
    021712 103052                   BCC      NXTU
62 021714                               READEF  #EF.START
    021714 012700 000040             MOV      #EF.START,R0
    021720 104447                   TRAP      C$REFG
63 021722                               BCOMPLETE 2$

```


INITIALIZE SECTION

```

112 022124      013700 002200      SETU:  GPWARD  UNITN,R0      ;GET UNIT N P-TABLE POINTER.
      022124      104442      MOV      UNITN,R0
      022130      TRAP      C$GPWRD
113 022132      103342      BNCOMPLETE NXTU      ;BR IF UNIT NOT AVAILABLE.
      022132      BCC      NXTU
114 022134      005037 003106      CLR      DUFLG      ;CLEAR "DROPPED" FLAG.
115 022140      005237 002216      INC      DEVCNT
116 022144      012001      MOV      (R0)+,R1      ;GET 1ST REGISTER ADDRESS.
117 022146      010137 002204      MOV      R1,CSRADDR      ;ADDRESS OF REGISTERS OF UNIT UNDER TEST
118
119 022152      012001      MOV      (R0)+,R1      ;GET VECTOR ADDRESS.
120      ;MOV      (R0),R2      ;GET INTERRUPT PRIORITY
121      ;MOV      R2,IPRI      ;SET INTERRUPT PRIORITY.
122 022154      010137 002206      MOV      R1,IVEC      ;SET INTERRUPT VECTOR POINTER...
123 022160      012721 016206      MOV      @INTR,(R1)+      ;...VECTOR...
124 022164      013721 002210      MOV      IPRI,(R1)+      ;...AND PRIORITY.
125
126 022170      1$:
127      ;      TST      QVP      ;1ST PASS ??
128      ;      BEQ      5$      ;NO, SKIP THE PASS 1 STUFF.
129
130      ;
131      ;1ST PASS, CHECK THAT DEVICE ADDRESSES ARE VALID, AND
132      ;THAT THE DISPLAY STATUS IS PROPERLY INITIALIZED.
133      ;
134 022170      013701 002200      MOV      UNITN,R1
135 022174      006301      ASL      R1
136 022176      052761 100000 003172      BIS      @BIT15,ERTABL(R1)      ;SAY DEVICE RUNNING
137 022204      005037 005766      CLR      EXTA      ;CLEAR ERROR EXTENSION FLAG.
138 022210      023727 002012 000001      CMP      L$UNIT,#1      ;ARE WE TESTING MULTIPLE UNITS?
139 022216      101416      BLOS      10$      ;BR IF NO.
140 022220      RFLAGS      RO      ;YES -- GET OPERATOR FLAGS.
      022220      104421      TRAP      C$RFLA
141 022222      032700 001000      BIT      @PNT,R0      ;SHOULD WE PRINT UNIT #?
142 022226      001412      BEQ      10$      ;BR IF NOT.
143 022230      PRINTF      @PUNIT,UNITN      ;PRINT THE UNIT #
      022230      013746 002200      MOV      UNITN,-(SP)
      022234      012746 022322      MOV      @PUNIT,-(SP)
      022240      012746 000002      MOV      @2,-(SP)
      022244      010600      MOV      SP,R0
      022246      104417      TRAP      C$PNTF
      022250      062706 000006      ADD      @6,SP
144 022254      10$:
145 022254      005037 003110      CLR      NODEV
146 022260      013701 002204      MOV      CSRADDR,R1      ;ADDRESS OF FIRST REGISTER
147 022264      010102      MOV      R1,R2      ;START OF REGISTERS
148 022266      062702 000002      ADD      @TSSR,R2      ;ADDRESS OF TSSR REGISTER
149 022272      004737 016366      JSR      PC,XNXM      ;TEST BOTH CONTROLLER REGISTERS...
150 022276      103005      BCC      2$      ;...AND BR IF ALL OK.
151 022300      010137 003110      MOV      R1,NODEV      ;FLAG DEVICE AS NON-EXISTENT
152 022304      012737 177777 003106      MOV      @-1,DUFLG      ;DROP THIS UNIT.
153 022312
154
155      ;
156      ;FINALLY, SET CPU PRIORITY AND WE'RE DONE.
157 022312      5$:      SETPRI      @PRI00      ;ENABLE INTERRUPTS.
      022312      012700 000000      MOV      @PRI00,R0

```

INITIALIZE SECTION

```
      022316 104441      TRAP C$SPRI
158 022320      ENDINIT
      022320      L10030:
      022320 104411      TRAP C$INIT
159
160 022322      045      116      045 PUNIT: .ASCIZ /N$N$A***** TESTING UNIT $D2$A *****/
161      .EVEN
```

ADD AND DROP UNITS SECTIONS

```

163                                     .SBTTL ADD AND DROP UNITS SECTIONS
164
165
166                                     ;**
167                                     ; THE ADD-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
168                                     ; TO BE (A) ADDED TO THE TEST LIST FOR THE FIRST TIME,
169                                     ; OR (B) RE-INSERTED IF IT HAD BEEN PREVIOUSLY DROPPED.
170                                     ;--
170 022370                                BGNAU
171 022370                                L$AU::
172 022370 010001                        MOV     R0,R1                ; GET UNIT TO BE ADDED (R0)
173 022372 006301                        ASL     R1                  ; MAKE IT A WORD INDEX
174 022374 052761 100000 003172        BIS     #100000,ERTABL(R1)    ; SET THE "ACTIVE" BIT
175 022402 042761 040000 003172        BIC     #40000,ERTABL(R1)    ; CLEAR THE "DROPPED" BIT
176 022410                                PRINTF  #1$,R0
177 022410 010046                        MOV     R0,-(SP)
178 022412 012746 022436                MOV     #1$,-(SP)
179 022416 012746 000002                MOV     #2,-(SP)
180 022422 010600                        MOV     SP,R0
181 022424 104417                        TRAP    C$PNTF
182 022426 062706 000006                ADD     #6,SP
183 022432                                EXIT    AU
184 022432 000167                        .WORD   J$JMP
185 022434 000026                        .WORD   L10031-2-
186 022436 045 116 045 1$:              .ASCIIZ /%N%A UNIT %D%A ADDED/
187                                     .EVEN
188
189 022464                                ENDAU                        ; UNUSED.
190 022464                                L10031:
191 022464 104452                        TRAP    C$AU
192
193                                     ;**
194                                     ; THE DROP-UNIT SECTION CONTAINS THE CODING THAT CAUSES A DEVICE
195                                     ; TO BE REMOVED FROM THE TEST LIST.
196
197                                     ;
198                                     ; SUPVSR DOES THE "DROPPING". THIS IS JUST TO TELL THE MAN.
199                                     ; "DROPPED" UNITS ARE RE-SELECTED ON OPERATOR "STA" OR "ADD"
200                                     ; COMMAND, OTHERWISE REMAIN INACTIVE. THE "DISPLAY" COMMAND
201                                     ; WILL PRINT ALL DROPPED UNITS, AND THE P-TABLES OF THOSE
202                                     ; WHICH ARE STILL ACTIVE.
203                                     ; UPON ENTRY, R0 CONTAINS THE UNIT TO BE DROPPED.
204
205 022466                                BGNDU
206 022466                                L$DU::
207 022466 012737 177777 003106        MOV     #-1,DUFLG
208 022474 010001                        MOV     R0,R1
209 022476 006301                        ASL     R1
210 022500 052761 140000 003172        BIS     #140000,ERTABL(R1)    ; SAY DROPPED
211 022506 000240 000240 000240        240,240,240                ; ??????????
212 022514                                PRINTF  #1$,R0
213 022514 010046                        MOV     R0,-(SP)
214 022516 012746 022542                MOV     #1$, (SP)
215 022522 012746 000002                MOV     #2,-(SP)
216 022526 010600                        MOV     SP,R0
217 022530 104417                        TRAP    C$PNTF
218 022532 062706 000006                ADD     #6,SP
219 022536                                EXIT    DU
220 022536 000167                        .WORD   J$JMP
221 022540 000030                        .WORD   L10032 2-

```

ADD AND DROP UNITS SECTIONS

```

200 022542      045      116      045 10:      .ASCIZ  /%N% UNIT %D% DROPPED/
201                                     .EVEN
202 022572                                     ENDDU
    022572                                     L10032:
    022572 104453                                     TRAP      C%DU
203                                     ;**
204                                     ; AUTO-DROP CODE SECTION.
205                                     ;--
206 022574                                     BGNAUTO
    022574                                     L%AUTO::
207 022574 013705 002204                                     MOV      CSRADDR,R5
208 022600 012703 000550                                     MOV      #360.,R3
209 022604 004737 016240 100:      JSR      PC,WAITF      ;POINT TO DEVICE REGISTER
210 022610 103420                                     BCS      200      ;ENOUGH TIME FOR 2400' REEL TO REWIND
211 022612                                     DELAY     250.      ;WAIT FOR SSR TO SET
    022612 012727 000372                                     MOV      #250.,(PC). ;LEAVE WHEN SSR IS SET
    022616 000000                                     .WORD     0          ;WAIT FOR .25 SECONDS
    022620 013727 002116                                     MOV      L%DLY,(PC).
    022624 000000                                     .WORD     0
    022626 005367 177772                                     DEC      -6(PC)
    022632 001375                                     BNE      .-4
    022634 005367 177756                                     DEC      -22(PC)
    022640 001367                                     BNE      .-20
212 022642 005303                                     DEC      R3          ;BUMP COUNTER DOWN
213 022644 001357                                     BNE      100         ;KEEP GOING
214 022646 004737 017172                                     JSR      PC,CKDROP   ;TRY AND DROP UNIT
215 022652                                     200:
216 022652                                     ENDAUTO
    022652                                     L10033:
    022652 104461                                     TRAP      C%AUTO

```

; UNUSED.

				.SBTTL	CLEAN-UP AND REPORT CODING SECTIONS	
218				***		
219				; THE CLEANUP CODING SECTION CONTAINS THE CODING THAT IS		
220				; EXECUTED AT THE END OF EACH PASS (OR SUB-PASS).		
221				; USE TO RETURN DEVICE UNDER TEST TO A NEUTRAL STATE.		
222				--		
223	022654			BGNCLN		
224	022654			L\$CLEAN::		
225	022654	013705	002204	MOV	CSRADDR,R5	;POINT TO DEVICE REGISTER
226	022660	005737	003106	TST	DUFLG	; "DROPPED" FLAG IS SET ON...
227	022664	100405		BMI	1\$	;...AND GROSS CONTROLLER FAULT...
228						;...DON'T TRY TO XCT CLEANUP CODE.
229						
230						
231	022666	012765	000000	MOV	#0,TSSR(R5)	;DO SOFT INIT
232	022674	004737	016240	JSR	PC,WAITF	
233	022700			1\$:		
234	022700			2\$:	ENDCLN	
235	022700			L10034:		
236	022700	104412		TRAP	C\$CLEAN	
237				***		
238				; THE REPORT CODING SECTION CONTAINS THE		
239				; "PRINTS" CALLS THAT GENERATE STATISTICAL REPORTS.		
240				--		
241	022702			BGNRPT		
242	022702			L\$RPT::		
243	022702	012746	023144	PRINTS	#DEVSUM	
244	022706	012746	000001	MOV	#DEVSUM,(SP)	
245	022712	010600		MOV	#1,-(SP)	
246	022714	104416		MOV	SP,R0	
247	022716	062706	000004	TRAP	C\$PNTS	
248	022722	010246		ADD	#4,SP	
249	022724	010346		MOV	R2,-(SP)	
250	022726	010446		MOV	R3,-(SP)	
251	022730	012704	003172	MOV	R4,-(SP)	
252	022734	005003		MOV	#ERTABL,R4	; GET START OF ERROR TABLE.
253	022736	011402		CLR	R3	; CLEAR UNIT NUMBER
254	022740	001467		1\$: MOV	(R4),R2	; GET ERROR TABLE ENTRY & TEST IT.
255	022742	100066		BEQ	4\$	; ZERO IF UNIT NOT RUN
256	022744	032702	040000	BPL	4\$	
257	022750	001015		BIT	#BIT14,R2	; WAS UNIT DROPPED?
258	022752	042702	170000	BNE	2\$	; BR IF YES
259	022756			BIC	#C7777,R2	; GET ERROR COUNT FIELD
260	022756	010246		PRINTS	#DEVONL,R3,R2	; PRINT
261	022760	010346		MOV	R2,-(SP)	
262	022762	012746	023201	MOV	R3,-(SP)	
263	022766	012746	000003	MOV	#DEVONL,-(SP)	
264	022772	010600		MOV	#3,-(SP)	
265	022774	104416		MOV	SP,R0	
266	022776	062706	000010	TRAP	C\$PNTS	
267	023002	000446		ADD	#10,SP	
268	023004	020227	160000	BR	4\$	
269	023010	001012		2\$: CMP	R2,#160000	; WAS UNIT NON-EXISTENT?
270	023012			BNE	3\$	; BR IF NO
271	023012	010346		PRINTS	#DEVNXR,R3	
272	023014	012746	023251	MOV	R3,-(SP)	
273				MOV	#DEVNXR,-(SP)	

CLEAN UP AND REPORT CODING SECTIONS

```

023020 012746 000002      MOV    #2,-(SP)
023024 010600      MOV    SP,R0
023026 104416      TRAP    C:PNTS
023030 062706 000006      ADD    #6,SP
257 023034 000431      BR      4#
258 023036 020227 160001      3# : CMP    R2,#160001      ; WAS UNIT NOT READY AT STARTUP?
259 023042 001012      BNE     30#      ; BR IF NO.
260 023044      PRINTS  #DEVNRD,R3
      023044 010346      MOV    R3,-(SP)
      023046 012746 023333      MOV    #DEVNRD,-(SP)
      023052 012746 000002      MOV    #2,-(SP)
      023056 010600      MOV    SP,R0
      023060 104416      TRAP    C:PNTS
      023062 062706 000006      ADD    #6,SP
261 023066 000414      BR      4#
262 023070 042702 170000      30# : BIC    #C7777,R2
263 023074      PRINTS  #DEVDRD,R3,R2
      023074 010246      MOV    R2,-(SP)
      023076 010346      MOV    R3,-(SP)
      023100 012746 023414      MOV    #DEVDRD,-(SP)
      023104 012746 000003      MOV    #3,-(SP)
      023110 010600      MOV    SP,R0
      023112 104416      TRAP    C:PNTS
      023114 062706 000010      ADD    #10,SP
264 023120 062704 000002      4# : ADD    #2,R4
265 023124 005203      INC     R3
266 023126 020427 003372      CMP    R4,#ERTABE
267 023132 103701      BLO     1#
268 023134 012604      MOV    (SP)+,R4
269 023136 012603      MOV    (SP)+,R3
270 023140 012602      MOV    (SP)+,R2
271 023142      ENDRPT      ; UNUSED.
      023142      L10035:
      023142 104425      TRAP    C:RPT
272
273
274 023144      045      116      045 DEVSUM: .ASCIZ /#N#ADEVICE STATUS SUMMARY:#N/
275 023201      045      101      040 DEVONL: .ASCIZ /#A UNIT #D3#A ONLINE, ERRORS = #D#N/
276 023251      045      101      040 DEVNXR: .ASCIZ /#A UNIT #D3#A DROPPED, NON-EXISTENT REGISTER#N/
277 023333      045      101      040 DEVNRD: .ASCIZ /#A UNIT #D3#A DROPPED, NOT READY AT STARTUP#N/
278 023414      045      101      040 DEVDRD: .ASCIZ /#A UNIT #D3#A DROPPED, ERRORS = #D#N/
279      .EVEN
280
281 023464      ENDMOD
282
283

```


TSV5A HARDWARE TESTS MACRO M1200 23 MAR 84 09:44 PAGE 77

SEQ 0121

CLEAN UP AND REPORT CODING SECTIONS

1
2
9

.TITLE TSV5A - HARDWARE TESTS

10 023464
 023464

TSV5:: BGNMOD TSV5

16
24

TEST 1: BUS RESET TEST

.SBTTL TEST 1: BUS RESET TEST

THIS TEST VERIFIES THAT THE M7196 MODULE'S DEVICE REGISTERS ARE ACCESSIBLE ON THE BUS (SUBTEST 1) AND THEN CHECKS THAT THE BUILT-IN INITIALIZATION SELF-TEST MICRODIAGNOSTIC DID NOT FIND ANY BASIC PROBLEMS IN THE MODULE. AREAS OF LOGIC TESTED BY THE SELF-TEST SEQUENCE ARE AS FOLLOWS: ROM AND PIPELINE REGISTER, SEQUENCER, INTERNAL BUSES, 2901 MICROPROCESSOR, AND, RAM. THIS TEST INITIALIZES THE CONTROLLER BY ISSUING THE BUS INIT SIGNAL VIA A RESET INSTRUCTION, OR BY WRITING INTO THE TSSR REGISTER, WAITS A PERIOD OF TIME (TO ALLOW THE CONTROLLER'S INITIALIZATION MICRODIAGNOSTIC SEQUENCE TO BE COMPLETED), AND THEN CHECKS THE CONTENTS OF THE TSSR REGISTER. SUCCESSFUL INITIALIZATION IS INDICATED BY SUBSYSTEM READY (SSR) AND NEED BUFFER ADDRESS (NBA) BITS BEING SET (1) AND ALL OTHER BITS (EXCEPT A17 AND A16 AND OFL, WHICH ARE IGNORED FOR THIS TEST) BEING CLEAR (0). IF THE CONTENTS OF TSSR ARE NOT AS EXPECTED, AN ERROR REPORT IS ISSUED LISTING THE EXPECTED DATA, ACTUAL DATA, AND THE DISCREPANCIES. THE ERROR REPORT ANALYZES THE TSSR CONTENTS AND DISCERNs AND REPORTS ONE OF THREE POSSIBILITIES:

1. TSSR CONTENTS ARE AMBIGUOUS (ANY OF BITS 11-14 ARE SET, OR STATES OF SSR AND SC BITS DO NOT CORRESPOND TO THE APPARENT ERROR CODE IN BITS 0-5): INDICATES THAT THE TSSR CONTENT CANNOT BE TRUSTED. INDICATES A CATASTROPHIC CONTROLLER MALFUNCTION. THIS IS A FATAL ERROR (EXECUTION IS ABORTED). FIELD ACTION WOULD BE TO REPLACE THE M7196. IF THE M7196 ITSELF IS BEING DEBUGGED, THE PROGRAM SHOULD BE RESTARTED WITH LOOP ON ERROR ENABLED IN ORDER TO PROBE FOR THE PROBLEM.
2. SSR = 0, SC = 0 AND THE ERROR CODE IN BITS 0-5 IS IN THE RANGE 17-13: THIS IS A FATAL ERROR. THE ERROR CODE IS DECODED AND THE APPROPRIATE DESCRIPTION GIVEN. INDICATES THAT A SERIOUS PROBLEM EXISTS.

65	023464			BGNTST	
	023464				T1::
70	023464	012700	023662	MOV	#TST1ID,R0 ;ASCII MESSAGE TO IDENTIFY TEST
71	023470	004737	016500	JSR	PC,TSTSETUP ;DO INITIAL TEST SETUP
72	023474	012737	000024	MOV	#20.,LOOPCNT ;PERFORM 20 ITERATIONS
73	023502				
74	023502	005003		CLR	R3 ;USE R3 AS FATAL ERROR FLAG
75					
76	023504			BGNSUB	////////// BEGIN SUBTEST //////////
	023504				T1.1:
	023504	104402			TRAP C\$BSUB
77					
78	023506			BRESET	;ISSUE A BUS RESET
	023506	104433			TRAP C\$RESET
79	023510	004737	016240	JSR	PC,WAITF ;WAIT FOR READY
80	023514	016501	000002	MOV	TSSR(R5),R1 ;GET THE CONTENTS OF TSSR
81	023520	010102		MOV	R1,R2 ;CONTENTS OF TSSR
82	023522	042702	176277	BIC	#C<HIADDR!OFL>,R2 ;THESE BITS MAY BE SET

TEST 1: BUS RESET TEST

```

83 023526 052702 002200      BIS      #SSR!NBA,R2      ;READY AND NEW DATA SHOULD BE SET
84 023532 020102      CMP      R1,R2      ;COMPARE EXPECTED TO RECEIVED
85 023534 001405      BEQ      10$      ;BRANCH IF COMPARE
89 023536      ERRDF      ERRNO,SFHERR,SFFMSG      ;REPORT A FATAL ERROR
      023536 104455      TRAP      C$ERDF
      023540 000145      .WORD      101
      023542 003701      .WORD      SFHERR
      023544 012072      .WORD      SFFMSG
90 023546 005203      INC      R3      ;SET THE FATAL ERROR FLAG
91 023550      10$:      ENDSUB      ;///////////////// END SUBTEST ///////////////////
92 023550      L10037:      TRAP      C$ESUB
      023550 104403
93

```

TEST 1: BUS RESET TEST

```

95 023552 005703          TST      R3          ;DID WE HAVE FATAL ERROR ?
96 023554 001402          BEQ      20$         ;BRANCH IF NOT
97 023556 004737 017172    JSR      PC,CKDROP ;GO DROP THIS UNIT, IF ALLOWED
98 023562 005003          CLR      R3          ;RESET FATAL ERROR FLAG
99
100
101 023564          BGNSUB          ;////////// BEGIN SUBTEST ////////////
    023564          T1.2:          TRAP      C$BSUB
    023564 104402
102
103 023566 005065 000002    CLR      TSSR(R5)    ;WRITE TO ISSUE A SOFT RESET
104 023572 004737 016240    JSR      PC,WAITF    ;WAIT FOR READY TO SET
105 023576 016501 000002    MOV      TSSR(R5),R1 ;GET REGISTER TSSR DATA
106 023602 010102          MOV      R1,R2        ;CONTENTS OF TSSR
107 023604 042702 176277    BIC      #1C<HIADDR!OFL>,R2 ;THESE BITS MAY BE SET
108 023610 052702 002200    BIS      #SSR!NBA,R2 ;READY AND NEW DATA SHOULD BE SET
109 023614 020102          CMP      R1,R2        ;COMPARE EXPECTED TO RECEIVED
110 023616 001405          BEQ      10$         ;BRANCH IF COMPARE
114 023620          ERDF          ;REPORT A FATAL ERROR
    023620 104455          TRAP      C$ERDF
    023622 000146          .WORD    102
    023624 003646          .WORD    SFIERR
    023626 012072          .WORD    SFFMSG
115 023630 005203          INC      R3          ;SET THE ERROR FLAG
116 023632          10$:
117 023632          ENDSUB          ;////////// END SUBTEST ////////////
    023632 104403          L10040:          TRAP      C$ESUB
118
119
120 023634 005703          TST      R3          ;FATAL ERROR DETECTED ?
121 023636 001402          BEQ      20$         ;BRANCH IF NOT
122 023640 004737 017172    JSR      PC,CKDROP ;SEE IF TIME TO DROP UNIT
123 023644 004737 016446    JSR      PC,TSTLOOP ;SHOULD WE DO ITERATIONS ?
124 023650 103002          BCC      40$         ;BRANCH IF NOT
125 023652 000137 023502    JMP      T1LOOP    ;LOOP UNTIL COUNT EXPIRED
126 023656          40$:          EXIT      TST  ;ALL DONE THIS TEST
    023656 104432          TRAP      C$EXIT
    023660 000022          .WORD    L10036-.
127
128
129          ;*
130          ;LOCAL TEXT MESSAGES FOR TEST
131          ;-
132 023662          111          156          151 TST1ID: .ASCIZ 'Initialization'
133          .EVEN
134 023702          .ENDTST
    023702 104401          L10036:          TRAP      C$ETST
135
136

```

TEST 2: WRAP DATA - HIGH BYTE

[illegible]

TEST 2: WRAP DATA - HIGH BYTE

PC	Address	Offset	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op417	
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SEQ 0127

TEST 2: WRAP DATA - HIGH BYTE

[illegible]

TEST 3: WRAP DATA - LOW BYTE

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264 .SBTTL TEST 3: WRAP DATA - LOW BYTE
265
266 ; THIS TEST FURTHER VERIFIES OPERATION OF MANY OF THE SAME
267 ; ELEMENTS TESTED IN TEST 2, AND ADDITIONALLY VERIFIES:
268 ;
269 ;
270 ; 1. LOW BYTE OF THE TSDB INPUT FILE REGISTER,
271 ;
272 ; 2. LOW BYTE OF INTERNAL DAL BUS DRIVERS ON THE DC005
273 ; TRANSCEIVER CIRCUITS,
274 ;
275 ; 3. BASIC FUNCTIONING OF PARTS OF THE RAM.
276 ;
277 ;
278 ;
279 ; THE PROGRAM WRITES A TEST DATA BYTE INTO THE LOW BYTE OF TSDB,
280 ; WAITS FOR THE SSR BIT IN TSSR TO SET, THEN CHECKS THE CONTENTS
281 ; OF BOTH TSBA AND TSSR. THE MODULE IS FUNCTIONING CORRECTLY IF
282 ; DATA WRITTEN APPEARS IN BOTH BYTES OF TSBA AND THE FINAL CONTENT
283 ; OF TSSR IS CORRECT (SAME AS AFTER INITIALIZATION EXCEPT FOR BITS
284 ; 8 AND 9, WHICH SHOULD CONTAIN BITS 8 AND 9 OF THE DATA PATTERN
285 ; WRITTEN. AN ERROR IS REPORTED AND A DESCRIPTIVE ANALYSIS GIVEN
286 ; IF A DISCREPANCY IN TSBA OR TSSR IS DETECTED. THE ANALYSIS
287 ; LISTS LIKELY FAULTY CANDIDATES FROM THE LOGIC ELEMENTS LISTED
288 ; ABOVE. THE TEST IS REPEATED FOR ALL COMBINATIONS OF TEST DATA
289 ; BYTES (0-377 OCTAL).
290 ;
291 024402 BGNTST
    024402
296 024402 012700 025045 MOV #TST3ID,R0 ;ASCII MESSAGE TO IDENTIFY TEST
297 024406 004737 016500 JSR PC,TSTSETUP ;DO INITIAL TEST SETUP
298 024412 012737 000024 002214 MOV #20.,LOOPCNT ;PERFORM 20 ITERATIONS
299 024420 005004 T3LOOP: CLR R4 ;STARTING DATA PATTERN
300 024422 012703 000001 MOV #1,R3 ;FORCE SOFT INIT ON FIRST PASS
301 024426 005703 5$: TST R3 ;SET IF INIT IS REQUIRED
302 024430 001412 BEQ 10$ ;BRANCH IF NO INIT NEEDED
303 024432 005003 CLR R3 ;ASSUME NO INIT NEEDED ON NEXT LOOP
304 024434 004737 015764 JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER
305 024440 103406 BCS 10$ ;BR IF SOFT INIT = OK
309 024442 010001 MOV R0,R1 ;SAVE CONTENTS OF TSSR
310 024444 ERROF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
    024444 104455 TRAP C$ERDF
    024446 000455 .WORD 301
    024450 003646 .WORD SFIERR
    024452 012024 .WORD SFIMSG
311 024454 005203 INC R3 ;FORCE INIT ON NEXT PASS
312 024456 005037 002220 10$: CLR FATFLG ;CLEAR FATAL ERROR FLAG
313
314 024462 BGNSEG ;>>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>
    024462 104404 TRAP C$BSEG
315
316 024464 110465 000000 MOVB R4,TSDB(R5) ;SET MAINT MODE + WRITE DATA
317 024470 004737 016240 JSR PC,WAITF ;WAIT FOR SSR TO SET
318 024474 103411 BCS 15$ ;BR IF CARRY SET (GOOD RETURN)
319 024476 010001 MOV R0,R1 ;SAVE CONTENTS OF TSSR
320 024500 010402 MOV R4,R2 ;DATA THAT WAS WRITTEN
324 024502 ERROF ERRNO,T3SSR,EXPREC ;DEVICE FATAL SSR FAILED TO SET

```


TEST 3: WRAP DATA - LOW BYTE

024502	104455				TRAP	C\$ERDF
024504	000456				.WORD	302
024506	024774				.WORD	T3SSR
024510	015464				.WORD	EXPREC
325 024512	005203		INC	R3		
326 024514	005237	002220	INC	FATFLG		
327 024520		15\$:	CKLOOP			
024520	104406				TRAP	C\$CLP1
328 024522	005737	002220	TST	FATFLG		
329 024526	001402		BEQ	20\$		
330 024530	004737	017172	JSR	PC,CKDROP		
331 024534	010402		MOV	R4,R2		
332 024536	042702	177774	BIC	#C<BIT0:BIT1>,R2		
333 024542	000302		SWAB	R2		
334 024544	052702	002200	BIS	#SSR!NBA,R2		
335 024550	016501	000002	MOV	TSSR(R5),R1		
336 024554	032701	000100	BIT	#OFL,R1		
337 024560	001402		BEQ	25\$		
338 024562	052702	000100	BIS	#OFL,R2		
339 024566	020201		CMP	R2,R1		
340 024570	001405		BEQ	30\$		
344 024572			ERRHRD	ERRNO,T3TSSR,EXPREC		
024572	104456				TRAP	C\$ERHRD
024574	000457				.WORD	303
024576	024730				.WORD	T3TSSR
024600	015464				.WORD	EXPREC
345 024602	005203		INC	R3		
346 024604		30\$:	CKLOOP			
024604	104406				TRAP	C\$CLP1
347 024606	016501	000000	MOV	TSBA(R5),R1		
348 024612	005002		CLR	R2		
349 024614	150402		BISB	R4,R2		
350 024616	000302		SWAB	R2		
351 024620	150402		BISB	R4,R2		
352 024622	020102		CMP	R1,R2		
353 024624	001405		BEQ	35\$		
357 024626			ERRHRD	ERRNO,T3TSBA,EXPREC		
024626	104456				TRAP	C\$ERHRD
024630	000460				.WORD	304
024632	024664				.WORD	T3TSBA
024634	015464				.WORD	EXPREC
358 024636	005203		INC	R3		
359						
360 024640		35\$:	ENDSEG			
024640						
024640	104405					
361					TRAP	C\$ESEG
362 024642	105204		INCB	R4		
363 024644	001270		BNE	5\$		
364 024646	004737	016446	JSR	PC,TSTLOOP		
365 024652	103002		BCC	40\$		
366 024654	000137	024420	JMP	T3LOOP		
367 024660		40\$:	EXIT	TST		
024660	104432				TRAP	C\$EXIT
024662	000210				.WORD	L10042-
368						
369						

TEST 3: WRAP DATA - LOW BYTE

```
370          ;LOCAL TEXT MESSAGES FOR TEST
371          ;
372
373 024664      124      123      102 T3TSBA: .ASCIIZ 'TSBA Incorrect After TSDB Low Write'
374 024730      124      123      123 T3TSSR: .ASCIIZ 'TSSR Incorrect After TSDB Low Write'
375 024774      116      157      040 T3SSR: .ASCIIZ 'No Sub-System Ready After TSDB Low Write'
376 025045      127      162      141 TST3ID: .ASCIIZ 'Wrap Data - Low Byte'
377          .EVEN
378 025072          ENDTST
      025072
      025072 104401
379
380
```

L10042: TRAP C\$ETST

TEST 4: RAM TEST

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429 025074
      025074
430
431 025074
      025074
      025074 104402
432
437 025076 012700 026404
438 025102 004737 016500
439 025106 012737 000005 002214

```

.SBTTL TEST 4: RAM TEST

THIS TEST VERIFIES THAT ALL LOCATIONS OF THE RAM ON THE M7196 CAN PROPERLY STORE AND READ BACK ALL DATA PATTERNS, AND THAT EACH RAM LOCATION IS UNIQUELY ADDRESSED (I.E., THAT ONE AND ONLY ONE LOCATION IS ACCESSED BY ANY PARTICULAR ADDRESS). THESE TESTS ARE PERFORMED BY THREE SUBTESTS, DESCRIBED BELOW. A BYPRODUCT OF THESE TESTS IS A VERIFICATION OF TWO REGISTERS IN THE 2901 AND THE CAPABILITY OF THE 2901 TO CORRECTLY PERFORM AN ADD.

TEST 4 , SUBTEST 1: -

THIS SUBTEST VERIFIES EACH RAM LOCATION BY FIRST PLACING THE M7196 INTO MAINTENANCE MODE BY WRITING INTO THE LOW BYTE OF TSDB AND THEN PERFORMING THE FOLLOWING SEQUENCE FOR EACH ADDRESS 0-7777 (OCTAL):

1. THE ADDRESS TO BE TESTED IS LOADED INTO THE TSDB (VIA A WORD WRITE).
2. THE ADDRESSED RAM LOCATION IS WRITTEN, THEN READ INTO THE LOW BYTE OF TSBA, BY WRITING A DATA BYTE INTO THE LOW BYTE OF TSDB.
3. THE LOW BYTE OF TSBA IS CHECKED TO SEE IF IT CONTAINS THE DATA PATTERN ORIGINALLY WRITTEN; A DISCREPANCY IS REPORTED AS AN ERROR.
4. THE ADDRESS OF THE LOCATION BEING TESTED IS AGAIN WRITTEN INTO TSDB (WORD WRITE), TO CAUSE THE LOCATION UNDER TEST TO AGAIN BE READ INTO THE LOW BYTE OF TSBA. THE LOW BYTE OF TSBA IS AGAIN CHECKED AND DISCREPANCIES REPORTED.
5. THE HIGH BYTE OF TSBA IS CHECKED; IT SHOULD CONTAIN THE SUM OF THE HIGH AND LOW BYTES LAST WRITTEN INTO TSDB AS A WORD. A DISCREPANCY IS REPORTED AS A 2901 PROBLEM.
6. THE CONTENT OF TSSR IS CHECKED; SETTING OF THE SC BIT IS IGNORED. OTHER DISCREPANCIES IN TSSR ARE REPORTED.

BGNTST

T4::

BGNSUB

;////////// BEGIN SUBTEST //////////

T4.1:

TRAP C8SUB

```

MOV    #TST4ID,R0
JSR    PC,TSTSETUP
MOV    #5,LOOPCNT

```

```

;ASCII MESSAGE TO IDENTIFY TEST
;DO INITIAL TEST SETUP
;PERFORM 5 ITERATIONS

```

TEST 4: RAM TEST

Address	Offset	Hex	Label	Instruction	Comment	Trap	Offset
440	025114		T4LOOP:				
441	025114	004737	015764	JSR	PC,SOFINIT		
442	025120	103405		BCS	20:		
446	025122	010001		MOV	R0,R1		
447	025124			ERRDF	ERRNO,SFIERR,SFIMSG		
	025124	104455				TRAP	C\$ERDF
	025126	000621				.WORD	401
	025130	003646				.WORD	SFIERR
	025132	012024				.WORD	SFIMSG
448	025134	005004		CLR	R4		
449	025136	004737	016326	JSR	PC,CHKTSSR		
450	025142	105065	000000	CLRB	TSDB(R5)		
451	025146						
452	025146			25:			
	025146	104404		RGNSEG			
453						TRAP	C\$BSEG
454	025150	110402		MOVB	R4,R2		
455	025152	004737	016326	JSR	PC,CHKTSSR		
456	025156	010465	000000	MOV	R4,TSDB(R5)		
457	025162	004737	016326	JSR	PC,CHKTSSR		
458	025166	110265	000000	MOVB	R2,TSDB(R5)		
459	025172	004737	016326	JSR	PC,CHKTSSR		
460	025176	016501	000000	MOV	TSBA(R5),R1		
461	025202	120102		CMPB	R1,R2		
462	025204	001404		BEQ	30:		
466	025206			ERRHRD	ERRNO,TSBAM2,EXPREC		
	025206	104456				TRAP	C\$ERHRD
	025210	000622				.WORD	402
	025212	026242				.WORD	TSBAM2
	025214	015464				.WORD	EXPREC
467	025216			30:			
468	025216			ENDSEG			
	025216						
	025216	104405					
469						TRAP	C\$ESEG
470	025220	005204		INC	R4		
471	025222	020427	010000	CMP	R4,#10000		
472	025226	001347		BNE	25:		
473	025230			ESCAPE	SUB		
	025230	104410				TRAP	C\$ESCAPE
	025232	000102				.WORD	L10044
474							
475	025234	005002		CLR	R2		
476	025236	005304		DEC	R4		
477	025240	110402		MOVB	R4,R2		
478	025242	004737	016326	JSR	PC,CHKTSSR		
479	025246	010465	000000	MOV	R4,TSDB(R5)		
480	025252	004737	016326	JSR	PC,CHKTSSR		
481	025256	016501	000000	MOV	TSBA(R5),R1		
482	025262	120102		CMPB	R1,R2		
483	025264	001404		BEQ	45:		
487	025266			ERRHRD	ERRNO,TSBAM2,EXPREC		
	025266	104456				TRAP	C\$ERHRD
	025270	000623				.WORD	403
	025272	026242				.WORD	TSBAM2
	025274	015464				.WORD	EXPREC
488	025276			4			

TEST 4: RAM TEST

SEQ 0133

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025276 104406
489 025300 116501 00^001
490 025304 010402
491 025306 000302
492 025310 060402
493 025312 120102
494 025314 001404
498 025316
    025316 104456
    025320 000624
    025322 026152
    025324 015464
499 025326
    025326 104406
500 025330 005304
501 025332 002342
502
503 025334
    025334
    025334 104403
504

    MOV      TSBAH(R5),R1
    MOV      R4,R2
    SWAB     R2
    ADD      R4,R2
    CMPB     R1,R2
    BEQ      504
    ERRHRD   ERRNO,M2901,EXPREC

504:  CKLOOP

    DEC      R4
    JGE      404
    ENDSUB

;HIGH BYTE READ OF TSBA
;DATA PATTERN WRITTEN
;HIGH TO LOW
;TOTAL OF BYTES IN LOW BYTE
;SUM OF BYTES WRITTEN TO TSDB = TSBAH
;BR IF OK, THEY SHOULD BE
;2901 PROBLEM ADDER

    TRAP     C$CLP1
    .WORD    404
    .WORD    M2901
    .WORD    EXPREC

;SCOPE LOOP

;DROP DATA COUNTER (PATTERN)
;NOT AT LOC. ZERO YET

    TRAP     C$CLP1

;////////// END SUBTEST //////////
L10044:
    TRAP     C$ESUB

```

[illegible]

TEST 4: RAM TEST

```

559 025506          ERRHRD  ERRNO,TSBAM3,EXPREC      ;CHARACTERISTICS DATA NOT CORRECT
      025506 104456          TRAP C$ERHRD
      025510 000627          .WORD 407
      025512 026324          .WORD TSBAM3
      025514 015464          .WORD EXPREC
560 025516 012702 000377    43$: MOV #000377,R2      ;SET ALL ONES WORD
561 025522 010465 000000    MOV R4,TSDB(R5)          ;LOAD UP RAM ADDRESS POINTER
562 025526 004737 016326    JSR PC,CHKTSSR           ;WAIT FOR READY, NON-AMBIGUOUS
563 025532 110265 000000    MOV R2,TSDB(R5)          ;WRITE DATA INTO RAM
564 025536 004737 016326    JSR PC,CHKTSSR           ;WAIT FOR READY, NON-AMBIGUOUS
565 025542 016501 000000    MOV TSBA(R5),R1          ;READ RAM CONTENTS BACK
566 025546 120102          CMPB R1,R2                ;CHECK WITH DATA WRITTEN
567 025550 001404          BEQ 45$                    ;BR IF OK, DATA IN = DATA OUT
571 025552          ERRHRD  ERRNO,TSBAM2,EXPREC      ;WRITTEN DATA NOT = TO READ
      025552 104456          TRAP C$ERHRD
      025554 000630          .WORD 408
      025556 026242          .WORD TSBAM2
      025560 015464          .WORD EXPREC
572 025562          45$: CKLOOP                      ;SCOPE LOOP
      025562 104406          TRAP C$CLP1
573 025564 004737 016326    JSR PC,CHKTSSR           ;WAIT FOR READY, NON-AMBIGUOUS
574 025570 010465 000000    MOV R4,TSDB(R5)          ;WORD WRITE TO SET UP ADDRESS
575 025574 004737 016326    JSR PC,CHKTSSR           ;WAIT FOR READY, NON-AMBIGUOUS
576 025600 116501 000001    MOV TSBAH(R5),R1         ;HIGH BYTE READ OF TSBA
577 025604 010403          MOV R4,R3                  ;DATA PATTERN WRITTEN
578 025606 000303          SWAB R3                     ;HIGH TO LOW
579 025610 060403          ADD R4,R3                    ;TOTAL OF BYTES IN LOW BYTE
580 025612 120103          CMPB R1,R3                  ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
581 025614 001404          BEQ 50$                      ;BR IF OK, THEY SHOULD BE
585 025616          ERRHRD  ERRNO,M2901,EXPREC      ;^901 PROBLEM ADDER
      025616 104456          TRAP C$ERHRD
      025620 000631          .WORD 409
      025622 026152          .WORD M2901
      025624 015464          .WORD EXPREC
586 025626          50$: CKLOOP                      ;SCOPE LOOP
      025626 104406          TRAP C$CLP1
587 025630 005304          DEC R4                       ;DROP RAM ADDRESS POINTER
588 025632 002312          BGE 40$                     ;NOT AT LOC. ZERO YET
589                                     ;
590 025634          ENDSUB                             ;////////// END SUBTEST ////////////
      025634          L10045:
      025634 104403          TRAP C$ESUB
591

```

TEST 4: RAM TEST

[illegible]

TEST 4: RAM TEST

```

641 026006 016501 000000      MOV      TSBA(R5),R1      ;PICK UP RAM CONTENTS
642 026012 120102      CMPB      R1,R2      ;IS MEMORY STILL ALL ONES
643 026014 001404      BEQ        43$      ;BR, IF OK (ALL ONES)
647 026016      ERRHRD      ERRNO,TSBAM3,EXPREC      ;MEMORY CHANGED AFTER ALL ONES WRITE
                                TRAP      C$ERHRD
                                .WORD      412
                                .WORD      TSBAM3
                                .WORD      EXPREC
                                026016 104456
                                026020 000634
                                026022 026324
                                026024 015464
648 026026 005002      43$:      CLR        R2      ;SET UP NEW EXPECTED
649 026030 010465      MOV      R4,TSDB(R5)      ;LOAD UP RAM ADDRESS POINTER
650 026034 004737      JSR      PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
651 026040 110265      MOVB      R2,TSDB(R5)      ;WRITE DATA INTO RAM
652 026044 004737      JSR      PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
653 026050 016501      MOV      TSBA(R5),R1      ;READ RAM CONTENTS BACK
654 026054 120102      CMPB      R1,R2      ;CHECK WITH DATA WRITTEN
655 026056 001404      BEQ        45$      ;BR IF OK, DATA IN = DATA OUT
659 026060      ERRHRD      ERRNO,TSBAM2,EXPREC      ;WRITTEN DATA NOT = TO READ
                                TRAP      C$ERHRD
                                .WORD      413
                                .WORD      TSBAM2
                                .WORD      EXPREC
                                026060 104456
                                026062 000635
                                026064 026242
                                026066 015464
660 026070      CKLOOP      ;SCOPE LOOP
                                TRAP      C$CLP1
                                026070 104406
661 026072 004737      JSR      PC,CHKTSSR      ;WAIT FOR READY, NON-AMBIGUOUS
662 026076 116501      MOVB      TSBAH(R5),R1      ;HIGH BYTE READ OF TSBA
663 026102 010203      MOV      R2,R3      ;DATA PATTERN WRITTEN
664 026104 000303      SWAB      R3      ;HIGH TO LOW
665 026106 060203      ADD      R2,R3      ;TOTAL OF BYTES IN LOW BYTE
666 026110 120103      CMPB      R1,R3      ;SUM OF BYTES WRITTEN TO TSDB = TSBAH
667 026112 001404      BEQ        50$      ;BR IF OK, THEY SHOULD BE
671 026114      ERRHRD      ERRNO,M2901,EXPREC      ;2901 PROBLEM ADDER
                                TRAP      C$ERHRD
                                .WORD      414
                                .WORD      M2901
                                .WORD      EXPREC
                                026114 104456
                                026116 000636
                                026120 026152
                                026122 015464
672 026124      50$:      CKLOOP      ;SCOPE LOOP
                                TRAP      C$CLP1
                                026124 104406
673 026126 005304      DEC      R4      ;DROP RAM ADDRESS POINTER
674 026130 001315      BNE      40$      ;NOT AT LOC. ZERO YET
675      ENDSUB
676 026132      ;///////////////// END SUBTEST ///////////////////
                                L10046:
                                TRAP      C$ESUB
                                026132 104403
677      JSR      PC,TSTLOOP      ;DO WE NEED TO ITERATE TEST ?
678 026134 004737      BCC      63$      ;BRANCH IF NOT
679 026140 103002      JMP      T4LOOP      ;EXECUTE AGAIN
680 026142 000137      63$:      EXIT      TST      ;ALL DONE THIS TEST
681 026146      TRAP      C$EXIT
                                .WORD      L10043
                                026146 104432
                                026150 000256
682      ;*
683      ;LOCAL TEXT MESSAGES FOR TEST
684      ;*
685
686 026152 040 124 123 M2901: .ASCIIZ ' TSBA High Byte Not Sum of Last TSDB Write (2901 Error)'
687 026242 040 127 162 TSBAM2: .ASCIIZ ' Write to TSDB Not Equal to Read of TSBA Low Byte'
688 026324 127 162 151 TSBAM3: .ASCIIZ ' Write To RAM Location Modified Another Location'

```

TEST 4: RAM TEST

SEQ 0138

689 026404 122 101 115 TST4ID: .ASCIZ 'RAM Verification'
690 .EVEN
691 026426 ENDTST
026426
026426 104401
692

L10043: TRAP C\$ETST

TEST 5: SECOND INITIALIZATION TEST

```

694 .SBTTL TEST 5: SECOND INITIALIZATION TEST
695
696 ;
697 ; THIS TEST VERIFIES THE SAME ELEMENTS AS DID INITIALIZATION TEST
698 ; #1 AND ALSO CHECKS THAT CERTAIN PARTS OF RAM IS CLEARED TO ZERO
699 ; AND THAT 2901 REGISTERS 10 AND 11 ARE ALSO CLEARED TO ZERO.
700 ; THIS IS A CONFIDENCE CHECK OF A PART OF THE SELF-TEST SEQUENCE
701 ; (I.E., THAT IT IS REALLY BEING EXECUTED). FOR EACH OF TWO
702 ; SUBTESTS (ONE FOR INITIALIZING VIA A BUS INIT, THE OTHER FOR
703 ; INITIALIZING BY WRITING INTO THE TSSR), THE FOLLOWING SEQUENCE
704 ; IS PERFORMED:
705
706 ;
707 ; 1. EACH RAM LOCATION AND 2901 REGISTERS 10 AND 11 ARE SET
708 ; TO ALL 1'S BY USING WRITES INTO THE TSDB REGISTER (LOW
709 ; BYTE AND MAINTENANCE MODE WORD WRITES).
710 ;
711 ; 2. THE CONTROLLER IS INITIALIZED AND THE VARIOUS CHECKS ON
712 ; THE TSSR DESCRIBED IN INITIALIZATION TEST #1 ARE
713 ; PERFORMED.
714 ;
715 ; 3. #1'S (377 OCTAL) ARE WRITTEN INTO THE LOW BYTE OF TSDB,
716 ; WHICH SHOULD CAUSE RAM LOCATION 0 TO BE WRITTEN TO ALL
717 ; 1'S SINCE 2901 REGISTERS 10 AND 11, SPECIFYING THE RAM
718 ; ADDRESS, SHOULD BE 0. RAM LOCATION 0 IS VERIFIED BY
719 ; WRITING A WORD OF ZEROS INTO THE TSDB. THE RESULTING
720 ; LOW BYTE OF TSBA SHOULD CONTAIN ALL 1'S.
721 ;
722 ; 4. THE ENTIRE RAM IS SCANNED. LOCATION 0 SHOULD CONTAIN
723 ; ALL 1'S AND THE REMAINING LOCATIONS, EXCEPT FOR THE
724 ; MESSAGE BUFFER IMAGE AREA, SHOULD CONTAIN 0.
725 ; DISCREPANCIES ARE REPORTED. AN ERROR AT THIS POINT IS
726 ; MOST LIKELY DUE TO A ROM, PIPELINE OR SEQUENCER PROBLEM
727 ; OR A TIMING PROBLEM.
728 ;
729 ;
730 ;
731 026430 BGNTST
732 026430
733
734 026430 012700 027402 MOV #TSTSID,RO ;ASCII MESSAGE TO IDENTIFY TEST
735 026434 004737 016500 JSR PC,TSTSETUP ;DO INITIAL TEST SETUP
736 026440 012737 000024 002214 MOV #20.,LOOPCNT ;PERFORM 20 ITERATIONS
737 026446
738 026446 005037 002220 TSLOOP: CLR FATFLG ;CLEAR THE FATAL ERROR FLAG
739 026452
740 026452 BGNSUB ;////////// BEGIN SUBTEST ////////////
741 026452 104402 TS.1: TRAP C#BSUB
742
743 026454 004737 015764 JSR PC,SOFINIT ;DO A SOFT TO START
744 026460 103404 BCS 10$ ;BRANCH IF O.K.
745 026462 104455 ERRDF ERRNO,SFERR,SFMSG ;REPORT ERROR AND DROP DRIVE
746 026462 000765 TRAP C#ERDF
747 026464 003646 .WORD 501
748 026470 012024 .WORD SFIERR
749 026472 012702 177777 10$: MOV #1,R2 ;ALL ONE DATA PATTERN

```

TEST 5: SECOND INITIALIZATION TEST

751	026476	005004		CLR	R4	;STARTING RAM ADDRESS	
752	026500	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
753	026504	105065	000000	15\$: CLR	TSDB(R5)	;SET MAINTENANCE MODE	
754	026510	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
755	026514	010465	000000	MOV	R4,TSDB(R5)	;SET THE NEXT RAM ADDRESS	
756	026520	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
757	026524	110265	000000	MOVB	R2,TSDB(R5)	;LOAD TEST DATA	
758	026530	005204		INC	R4	;NEXT ADDRESS TO TEST	
759	026532	020427	007777	CMP	R4,#7777	;COMPARE TO LAST ADDRESS	
760	026536	003762		BLE	15\$	;BRANCH TILL ALL DATA WRITTEN	
761	026540			BRESET		;ISSUE A BUS RESET	
	026540	104433				TRAP	C\$RESET
762	026542	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
763	026546	016501	000002	MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR	
764	026552	010102		MOV	R1,R2	;CONTENTS OF TSSR	
765	026554	042702	176277	BIC	#C<HIADDR!OFL>,R2	;THESE BITS MAY BE SET	
766	026560	052702	002200	BIS	#SSR!NBA,R2	;READY AND NEW DATA SHOULD BE SET	
767	026564	020102		CMP	R1,R2	;COMPARE EXPECTED TO RECEIVED	
768	026566	001406		BEQ	20\$	;BRANCH IF COMPARE	
772	026570			ERRDF	ERRNO,SFHERR,SFFMSG	;REPORT A FATAL ERROR	
	026570	104455				TRAP	C\$ERDF
	026572	000766				.WORD	502
	026574	003701				.WORD	SFHERR
	026576	012072				.WORD	SFFMSG
773	026600	005237	002220	INC	FATFLG	;SET FATAL ERROR FLAG	
774	026604			20\$: CKLOOP		;LOOP ON ERROR IF FLAG SET	
	026604	104406				TRAP	C\$CLP1
775	026606			ESCAPE	SUB	;EXIT IF FATAL ERROR DETECTED	
	026606	104410				TRAP	C\$ESCAPE
	026610	000170				.WORD	L10050-
776	026612	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET	
777	026616	105065	000000	CLR	TSDB(R5)	;PUT BACK INTO MAINTENANCE MODE	
778	026622	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
779	026626	005065	000000	CLR	TSDB(R5)	;SET ADDRESS BACK TO 0000	
780	026632	012702	000377	MOV	#377,R2		
781	026636	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
782	026642	110265	000000	MOVB	R2,TSDB(R5)	;SHOULD POINT TO RAM 0	
783	026646	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
784	026652	005065	000000	CLR	TSDB(R5)	;SELECT LOCATION 0	
785	026656	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
786	026662	116501	000000	MOVB	TSBA(R5),R1	;READ RAM LOCATION SPECIFIED	
787	026666	120102		CMPB	R1,R2	;LOCATION SHOULD BE 377 OCTAL	
788	026670	001406		BEQ	25\$	;BR IF OK	
789	026672			ERRDF	ERRNO,TSADDR,EXPREC	;WASN'T POINTING TO CORRECT LOC.	
	026672	104455				TRAP	C\$ERDF
	026674	000766				.WORD	502
	026676	027470				.WORD	TSADDR
	026700	015464				.WORD	EXPREC
790	026702	005237	002220	INC	FATFLG	;SET THE FATAL ERROR FLAG	
791	026706			25\$: CKLOOP		;SCOPE LOOP	
	026706	104406				TRAP	C\$CLP1
792	026710			ESCAPE	SUB	;NO MORE CHECKS IF FATAL ERROR	
	026710	104410				TRAP	C\$ESCAPE
	026712	000066				.WORD	L10050-
793	026714	012704	000310	MOV	#310,R4	;START WITH LOC 310	
794	026720	005002		CLR	R2	;MEMORY EXPECTED SHOULD BE 000000	
795	026722	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR READY, NON AMBIGUOUS	

TEST 5: SECOND INITIALIZATION TEST

796	026726	010465	000000	30\$:	MOV	R4,TSDB(R5)	;SELECT LOCATION SPECIFIED	
797	026732	004737	016326		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
798	026736	116501	000000		MOVB	TSBA(R5),R1	;READ LOC CONTENTS	
799	026742	120102			CMPB	R1,R2	;CHECK MEMORY FOR 000000	
800	026744	001406			BEQ	40\$	;BRANCH IF DATA OKAY	
801	026746				ERRDF	ERRNO,TSMEM,SFFMSG	;MEMORY NOT ZERO AFTER INIT.	
	026746	104455					TRAP	C\$ERDF
	026750	000766					.WORD	502
	026752	027432					.WORD	TSMEM
	026754	012072					.WORD	SFFMSG
802	026756	005237	002220		INC	FATFLG	;SET THE FATAL ERROR FLAG	
803	026762			40\$:	CKLOOP			
	026762	104406					TRAP	C\$CLP1
804	026764				ESCAPE	SUB	;EXIT ON FATAL ERROR	
	026764	104410					TRAP	C\$ESCAPE
	026766	000012					.WORD	L10050-
805	026770	005204			INC	R4	;LOOK AT NEXT RAM LOC.	
806	026772	020427	000400		CMP	R4,#400	;AT TOP OF RAM ADDRESS SPACE	
807	026776	001353			BNE	30\$	;BRANCH TILL ALL MEMORY TESTED	
808								
809	027000				ENDSUB		;////////// END SUBTEST //////////	
	027000						L10050:	
	027000	104403					TRAP	C\$ESUB
810								
811	027002	005737	002220		TST	FATFLG	;IS FATAL ERROR FLAG SET ?	
812	027006	001404			BEQ	50\$	;BRANCH IF NOT	
813	027010	004737	017172		JSR	PC,CKDROP	;NO LOOP, TRY TO DROP DEVICE	
814	027014	005037	002220		CLR	FATFLG	;CLEAR THE FATAL ERROR FLAG	
815	027020			50\$:				

TEST 5: SECOND INITIALIZATION TEST

817	027020		BGNSUB		////////// BEGIN SUBTEST //////////
	027020				T5.2:
	027020	104402			TRAP C\$BSUB
818					
819	027022	004737	015764	JSR	PC,SOFINIT ;DO A SOFT TO START
820	027026	103404		BCS	10\$;BRANCH IF O.K.
824	027030			ERRDF	ERRNO,SFIERR,SFIMSG ;REPORT ERROR AND DROP DRIVE
	027030	104455			TRAP C\$ERDF
	027032	000767			.WORD 503
	027034	003646			.WORD SFIERR
	027036	012024			.WORD SFIMSG
825	027040	012702	177777	10\$: MOV	\$-1,R2 ;ALL ONE DATA PATTERN
826	027044	005004		CLR	R4 ;STARTING RAM ADDRESS
827	027046	004737	016326	JSR	PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
828	027052	105065	000000	15\$: CLRB	TSDB(R5) ;SET MAINTENANCE MODE
829	027056	004737	016326	JSR	PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
830	027062	010465	000000	MOV	R4,TSDB(R5) ;SET THE NEXT RAM ADDRESS
831	027066	004737	016326	JSR	PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
832	027072	110265	000000	MOVB	R2,TSDB(R5) ;LOAD TEST DATA
833	027076	005204		INC	R4 ;NEXT ADDRESS TO TEST
834	027100	020427	007777	CMP	R4,\$7777 ;COMPARE TO LAST ADDRESS
835	027104	003762		BLE	15\$;BRANCH TILL ALL DATA WRITTEN
836	027106	005065	000002	CLR	TSSR(R5) ;ISSUE A SOFT RESET
837	027112	004737	016326	JSR	PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
838	027116	016501	000002	MOV	TSSR(R5),R1 ;GET THE CONTENTS OF TSSR
839	027122	010102		MOV	R1,R2 ;CONTENTS OF TSSR
840	027124	042702	176277	BIC	\$C<HIADDR!OFL>,R2 ;THESE BITS MAY BE SET
841	027130	052702	002200	BIS	\$SSR!NBA,R2 ;READY AND NEW DATA SHOULD BE SET
842	027134	020102		CMP	R1,R2 ;COMPARE EXPECTED TO RECEIVED
843	027136	001406		BEQ	20\$;BRANCH IF COMPARE
847	027140			ERRDF	ERRNO,SFHERR,SFFMSG ;REPORT A FATAL ERROR
	027140	104455			TRAP C\$ERDF
	027142	000770			.WORD 504
	027144	003701			.WORD SFHERR
	027146	012072			.WORD SFFMSG
848	027150	005237	002220	20\$: INC	FATFLG ;SET FATAL ERROR FLAG
849	027154			CKLOOP	;LOOP ON ERROR IF FLAG SET
	027154	104406			TRAP C\$CLP1
850	027156			ESCAPE	SUB ;EXIT IF FATAL ERROR DETECTED
	027156	104410			TRAP C\$ESCAPE
	027160	000170			.WORD L10051
851	027162	004737	016326	JSR	PC,CHKTSSR ;WAIT FOR SSR TO SET
852	027166	105065	000000	CLRB	TSDB(R5) ;PUT BACK INTO MAINTENANCE MODE
853	027172	004737	016326	JSR	PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
854	027176	005065	000000	CLR	TSDB(R5) ;SET ADDRESS BACK TO 0000
855	027202	012702	000377	MOV	\$377,R2 ;
856	027206	004737	016326	JSR	PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
857	027212	110265	000000	MOVB	R2,TSDB(R5) ;SHOULD POINT TO RAM 0
858	027216	004737	016326	JSR	PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
859	027222	005065	000000	CLR	TSDB(R5) ;SELECT LOCATION 0
860	027226	004737	016326	JSR	PC,CHKTSSR ;WAIT FOR READY, NON-AMBIGUOUS
861	027232	116501	000000	MOVB	TSBA(R5),R1 ;READ RAM LOCATION SPECIFIED
862	027236	120102		CMPB	R1,R2 ;LOCATION SHOULD BE 377 OCTAL
863	027240	001406		BEQ	25\$;BR IF OK
864	027242			ERRDF	ERRNO,TSADDR,EXPREC ;WASN'T POINTING TO CORRECT LOC.
	027242	104455			TRAP C\$ERDF
	027244	000770			.WORD 504

TEST 5: SECOND INITIALIZATION TEST

PC	PC+1	PC+2	PC+3	PC+4	PC+5	PC+6	PC+7	PC+8	PC+9	PC+10	PC+11	PC+12	PC+13	PC+14	PC+15	PC+16	PC+17	PC+18	PC+19	PC+20	PC+21	PC+22	PC+23	PC+24	PC+25	PC+26	PC+27	PC+28	PC+29	PC+30	PC+31	PC+32	PC+33	PC+34	PC+35	PC+36	PC+37	PC+38	PC+39	PC+40	PC+41	PC+42	PC+43	PC+44	PC+45	PC+46	PC+47	PC+48	PC+49	PC+50	PC+51	PC+52	PC+53	PC+54	PC+55	PC+56	PC+57	PC+58	PC+59	PC+60	PC+61	PC+62	PC+63	PC+64	PC+65	PC+66	PC+67	PC+68	PC+69	PC+70	PC+71	PC+72	PC+73	PC+74	PC+75	PC+76	PC+77	PC+78	PC+79	PC+80	PC+81	PC+82	PC+83	PC+84	PC+85	PC+86	PC+87	PC+88	PC+89	PC+90	PC+91	PC+92	PC+93	PC+94	PC+95	PC+96	PC+97	PC+98	PC+99	PC+100	PC+101	PC+102	PC+103	PC+104	PC+105	PC+106	PC+107	PC+108	PC+109	PC+110	PC+111	PC+112	PC+113	PC+114	PC+115	PC+116	PC+117	PC+118	PC+119	PC+120	PC+121	PC+122	PC+123	PC+124	PC+125	PC+126	PC+127	PC+128	PC+129	PC+130	PC+131	PC+132	PC+133	PC+134	PC+135	PC+136	PC+137	PC+138	PC+139	PC+140	PC+141	PC+142	PC+143	PC+144	PC+145	PC+146	PC+147	PC+148	PC+149	PC+150	PC+151	PC+152	PC+153	PC+154	PC+155	PC+156	PC+157	PC+158	PC+159	PC+160	PC+161	PC+162	PC+163	PC+164	PC+165	PC+166	PC+167	PC+168	PC+169	PC+170	PC+171	PC+172	PC+173	PC+174	PC+175	PC+176	PC+177	PC+178	PC+179	PC+180	PC+181	PC+182	PC+183	PC+184	PC+185	PC+186	PC+187	PC+188	PC+189	PC+190	PC+191	PC+192	PC+193	PC+194	PC+195	PC+196	PC+197	PC+198	PC+199	PC+200	PC+201	PC+202	PC+203	PC+204	PC+205	PC+206	PC+207	PC+208	PC+209	PC+210	PC+211	PC+212	PC+213	PC+214	PC+215	PC+216	PC+217	PC+218	PC+219	PC+220	PC+221	PC+222	PC+223	PC+224	PC+225	PC+226	PC+227	PC+228	PC+229	PC+230	PC+231	PC+232	PC+233	PC+234	PC+235	PC+236	PC+237	PC+238	PC+239	PC+240	PC+241	PC+242	PC+243	PC+244	PC+245	PC+246	PC+247	PC+248	PC+249	PC+250	PC+251	PC+252	PC+253	PC+254	PC+255	PC+256	PC+257	PC+258	PC+259	PC+260	PC+261	PC+262	PC+263	PC+264	PC+265	PC+266	PC+267	PC+268	PC+269	PC+270	PC+271	PC+272	PC+273	PC+274	PC+275	PC+276	PC+277	PC+278	PC+279	PC+280	PC+281	PC+282	PC+283	PC+284	PC+285	PC+286	PC+287	PC+288	PC+289	PC+290	PC+291	PC+292	PC+293	PC+294	PC+295	PC+296	PC+297	PC+298	PC+299	PC+300	PC+301	PC+302	PC+303	PC+304	PC+305	PC+306	PC+307	PC+308	PC+309	PC+310	PC+311	PC+312	PC+313	PC+314	PC+315	PC+316	PC+317	PC+318	PC+319	PC+320	PC+321	PC+322	PC+323	PC+324	PC+325	PC+326	PC+327	PC+328	PC+329	PC+330	PC+331	PC+332	PC+333	PC+334	PC+335	PC+336	PC+337	PC+338	PC+339	PC+340	PC+341	PC+342	PC+343	PC+344	PC+345	PC+346	PC+347	PC+348	PC+349	PC+350	PC+351	PC+352	PC+353	PC+354	PC+355	PC+356	PC+357	PC+358	PC+359	PC+360	PC+361	PC+362	PC+363	PC+364	PC+365	PC+366	PC+367	PC+368	PC+369	PC+370	PC+371	PC+372	PC+373	PC+374	PC+375	PC+376	PC+377	PC+378	PC+379	PC+380	PC+3
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TEST 6: COMMAND REJECT

.SBITL TEST 6: COMMAND REJECT

THIS TEST VERIFIES THAT ALL COMMANDS OTHER THAN WRITE CHARACTERISTICS ARE REJECTED DUE TO THE NEED BUFFER ADDRESS (NBA) BIT BEING SET IN TSSR, AND THAT THE TSBA AND TSSR REGISTERS ARE LEFT IN THE PROPER STATE AFTER EACH COMMAND IS REJECTED. THIS TEST CHECKS MICROPROCESSOR SEQUENCING, BASIC COMMAND DECODING AND DATA DMA HANDLING. THIS TEST CONTAINS TWO SUBTESTS: SUBTEST 1 SEQUENCES THROUGH ALL COMMAND WORDS (OTHER THAN WRITE CHARACTERISTICS) WITH THE INTERRUPT ENABLE (IE) BIT CLEAR AND VERIFIES THAT AN INTERRUPT IS NOT GENERATED BY THE REJECTED COMMAND; SUBTEST 2 PERFORMS SIMILARLY TO SUBTEST 1 BUT SETS THE IE BIT IN EACH COMMAND WORD AND VERIFIES THAT AN INTERRUPT IS GENERATED WHEN THE COMMAND IS REJECTED. SUBTEST 1 SETS UP THE INTERRUPT SERVICE ROUTINE TO FLAG UNEXPECTED INTERRUPTS. THE COMMAND WORD IN THE COMMAND BUFFER IS INITIALIZED TO 100000 (OCTAL) AND THE REMAINING THREE WORDS IN THE COMMAND BUFFER ARE SET TO KNOWN UNIQUE PATTERNS. THEN THE FOLLOWING SEQUENCE IS PERFORMED:

1. INITIALIZE THE CONTROLLER BY WRITING INTO THE TSSR; PROPER INITIAL CONDITIONS ARE VERIFIED.
2. TSDB IS WRITTEN WITH ADDRESS OF THE COMMAND BUFFER TO START PROCESSING.
3. THE PROGRAM WAITS FOR SSR TO SET; IF SSR DOES NOT SET, AN ERROR REPORT IS ISSUED AND THE TEST IS ABORTED.
4. THE CONTENTS OF TSSR ARE CHECKED. TSSR IS CORRECT IF IT CONTAINS EITHER OCTAL 102206 OR 102306 (BIT 6 DEPENDS UPON THE STATE OF THE TAPE TRANSPORT).
5. THE CONTENTS OF TSBA ARE CHECKED. TSBA SHOULD CONTAIN THE INITIAL COMMAND BUFFER ADDRESS (LOADED IN STEP 2) PLUS 10 (OCTAL); I.E., TSBA SHOULD POINT TO THE WORD JUST AFTER THE COMMAND PACKET (NOTE THAT 4 COMMAND PACKET WORDS ARE ALWAYS FETCHED).
6. USING THE MAINTENANCE MODE WRAPAROUND FUNCTIONS, THE COMMAND IMAGE BLOCK IN THE M7196'S RAM (LOCATIONS 201-210 (OCTAL)) ARE CHECKED; THE IMAGE SHOULD CONTAIN A COPY OF THE FOUR COMMAND PACKET WORDS AS SET UP IN CPU MEMORY.
7. THE COMMAND WORD IN THE COMMAND BUFFER IS INCREMENTED TO THE NEXT PATTERN NOT CONTAINING WRITE CHARACTERISTICS OR IE. THE REMAINING THREE WORD OF THE COMMAND BUFFER ARE SEQUENCED WITH PSEUDO-RANDOM DATA. IF THE COMMAND WORD HAS NOT REACHED ITS MAXIMUM VALUE (177777+1), THE TEST SEQUENCE IS REPEATED.

SUBTEST 2 IS IDENTICAL TO SUBTEST 1, EXCEPT THAT THE PROGRAM

Address	Offset	Hex Data	Label	Operation	Comments
962					CAUSES THE IE BIT TO BE SET IN EACH COMMAND WORD AND THEN
963					VERIFIES THAT AN INTERRUPT OCCURS.
964					
965	027534			BGNTST	
	027534				
970	027534	012700 030763		MOV #TST6ID,R0	T6:: ASCII MESSAGE TO IDENTIFY TEST
971	027540	004737 016500		JSR PC,TSTSETUP	DO INITIAL TEST SETUP
972	027544	012737 000024	002214	MOV #20.,LOOPCNT	PERFORM 20 ITERATIONS
973	027552				
974	027552		T6LOOP:	BGNSUB	////////// BEGIN SUBTEST //////////
	027552				T6.1:
975	027552	104402			TRAP C#BSUB
976	027554			SETPRI #PRI00	LOWER PRIORITY TO ALLOW INTERRUPTS
	027554	012700 000000			MOV #PRI00,R0
	027560	104441			TRAP C#SPRI
977	027562	012704 030440		MOV #T6PACKET,R4	GET THE ADDRESS OF COMMAND PACKET
978	027566	012703 002750		MOV #TSTBLK,R3	BLOCK OF TEST DATA
979	027572	012314	5:	MOV (R3),.(R4)	INSERT THE NEXT TEST DATA WORD
980	027574			BGNSEG	***** BEGIN SEGMENT *****
	027574	104404			TRAP C#BSEG
981	027576	004737 015764		JSR PC,SOFINIT	DO SOFT INIT OF CONTROLLER
982	027602	103405		BCS 10:	BR IF SOFT INIT = OK
986	027604	010001		MOV R0,R1	SAVE CONTENTS OF TSSR
987	027606			ERRDF ERNO,SFIERR,SFIMSG	DEVICE FATAL ERROR DURING INIT
	027606	104455			TRAP C#ERDF
	027610	001131			.WORD 601
	027612	003646			.WORD SFIERR
	027614	012024			.WORD SFIMSG
988	027616	005037 002220	10:	CLR FATFLG	CLEAR FATAL ERROR FLAG
989	027622	005037 002222		CLR INTRECV	CLEAR INTERRUPT RECEIVED FLAG
990	027626	004737 016326		JSR PC,CHKTSSR	WAIT FOR READY, NON-AMBIGUOUS
991	027632	042714 000200		BIC #BIT7,(R4)	DISABLE INTERRUPTS
992	027636	010465 000000		MOV R4,TSD8(R5)	SET THE PACKET ADDRESS
993	027642	004737 016240		JSR PC,WAITF	WAIT FOR SSR TO SET
994	027646	103407		BCS 15:	BR IF CARRY SET (GOOD RETURN)
995	027650	010001		MOV R0,R1	SAVE CONTENTS OF TSSR
999	027652			ERRDF ERNO,T6SSR,PKTSSR	DEVICE FATAL SSR FAILED TO SET
	027652	104455			TRAP C#ERDF
	027654	001132			.WORD 602
	027656	030475			.WORD T6SSR
	027660	012036			.WORD PKTSSR
1000	027662	005237 002220		INC FATFLG	SET FATAL ERROR FLAG
1001	027666		15:	CKLOOP	LOOP ON ERROR, IF FLAG SET
	027666	104406			TRAP C#CLP1
1002	027670			ESCAPE SUB	BY PASS SUBTEST IF FATAL ERROR
	027670	104410			TRAP C#ESCAPE
	027672	000170			.WORD L10053
1003	027674	005737 002222		TST INTRECV	DID AN INTERRUPT OCCUR ?
1004	027700	001404		BEQ 22:	BRANCH IF NOT
1008	027702			ERRHRD ERNO,T6INT,PKTSSR	
	027702	104456			TRAP C#ERHRD
	027704	001133			.WORD 603
	027706	030553			.WORD T6INT
	027710	012036			.WORD PKTSSR
1009	027712	012702 102206	22:	MOV #SC!NBA!SSR!TSREJ,R2	EXPECTED CONTENTS OF TSSR
1010	027716	004737 016326		JSR PC,CHKTSSR	WAIT FOR READY, NON AMBIGUOUS

TEST 6: COMMAND REJECT

1011	027722	016501	000002		MOV	TSSR(R5),R1		;GET THE CONTENTS OF TSSR
1012	027726	032701	000100		BIT	#OFL,R1		;IS OFF-LINE BIT SET ?
1013	027732	001402			BEQ	25:		;BRANCH IF NOT OFF-LINE
1014	027734	052702	000100		BIS	#OFL,R2		;SET OFF-LINE IN EXPECTED DATA
1015	027740	020201		25:	CMP	R2,R1		;DOES EXPECTED MATCH RECEIVED ?
1016	027742	001404			BEQ	30:		;OKAY IF MATCH
1020	027744				ERRHRD	ERRNO,T6NBA,PKTSSR		;NBA NOT SET TO REJECT
	027744	104456					TRAP	C\$ERHRD
	027746	001134					.WORD	604
	027750	030450					.WORD	T6NBA
	027752	012036					.WORD	PKTSSR
1021	027754			30:	CKLOOP			;LOOP ON ERROR ?
	027754	104406					TRAP	C\$CLP1
1022	027756	004737	016326		JSR	PC,CHKTSSR		;WAIT FOR READY, NON-AMBIGUOUS
1023	027762	016501	000000		MOV	TSBA(R5),R1		;GET TSBA REGISTER CONTENTS
1024	027766	010402			MOV	R4,R2		;START OF THE PACKET
1025	027770	062702	000010		ADD	#10,R2		;EXPECT TSBA TO PACKET + 10
1026	027774	020102			CMP	R1,R2		;COMPARE EXPECTED TO RECEIVED
1027	027776	001404			BEQ	35:		;ERROR IF NOT EQUAL
1031	030000				ERRHRD	ERRNO,T6TSBA,EXPREC		;PRINT THE ERROR & EXPD/RECV
	030000	104456					TRAP	C\$ERHRD
	030002	001135					.WORD	605
	030004	030711					.WORD	T6TSBA
	030006	015464					.WORD	EXPREC
1032								
1033								
1034	030010	004737	011104	35:	JSR	PC,CKRAM		;SEE IF DATA IN RAM IS CORRECT
1035	030014	103404			BIS	40:		;BRANCH IF PACKET IN RAM IS CORRECT
1039	030016				ERRHRD	ERRNO,PKTRAM,RAMERR		;REPORT THE RAM ERROR(S)
	030016	104456					TRAP	C\$ERHRD
	030020	001136					.WORD	606
	030022	004741					.WORD	PKTRAM
	030024	015500					.WORD	RAMERR
1040	030026			40:	ENDSEG			;***** END SEGMENT *****
	030026						10000:	
	030026	104405					TRAP	C\$ESEG
1041	030030	011300			MOV	(R3),R0		;PACKET COMMAND WORD
1042	030032	042700	177740		BIC	#177740,R0		;GET BITS 0-4
1043	030036	020027	000004		CMP	R0,#4		;DON'T TEST WRITE CHARACTERISTICS
1044	030042	001002			BNE	45:		;BRANCH IF OK
1045	030044	062703	000002		ADD	#2,R3		;GET NEXT WORD FROM DATA TABLE
1046	030050	020327	003056	45:	CMP	R3,#TBLEND		;REACHED END OF TABLE ?
1047	030054	103002			BHIS	50:		;BRANCH IF END OF TAB
1048	030056	000137	027572		JMP	5:		;CONTINUE TEST WITH NEW DATA
1049								
1050	030062			50:	ENDSUB			;////////// END SUBTEST //////////
	030062						L10053:	
	030062	104403					TRAP	C\$ESUB
1051								
1052	030064	005737	002220		TST	FATFLG		;ANY FATAL ERRORS ?
1053	030070	001402			BEQ	60:		;BRANCH IF NOT
1054	030072	004737	017172		JSR	PC,CKDROP		;TRY TO DROP THE UNIT

TEST 6: COMMAND REJECT

1056	030076			60:	BGNSUB	////////// BEGIN SUBTEST //////////
	030076					T6.2:
	030076	104402				TRAP C\$BSUB
1057						
1058	030100				SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	030100	012700	000000			MOV #PRI00,R0
	030104	104441				TRAP C\$SPRI
1059	030106	012704	030440		MOV #T6PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET
1060	030112	012703	002750		MOV #TSTBLK,R3	;START OF TEST DATA
1061	030116	012314		5:	MOV (R3)+,(R4)	;PLACE NEXT DATA WORD IN PACKET
1062	030120				BGNSEG	;***** BEGIN SEGMENT *****
	030120	104404				TRAP C\$BSEG
1063	030122	004737	015764		JCR PC,SOFINIT	;DO SOFT INIT OF CONTROLLER
1064	030126	103405			BCS 10:	;BR IF SOFT INIT = OK
1068	030130	010001			MOV R0,R1	;SAVE CONTENTS OF TSSR
1069	030132				ERRDF ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT
	030132	104455				TRAP C\$ERDF
	030134	001137				.WORD 607
	030136	003646				.WORD SFIERR
	030140	012024				.WORD SFIMSG
1070	030142	005037	002220	10:	CLR FATFLG	;CLEAR FATAL ERROR FLAG
1071	030146	005037	002222		CLR INTRECV	;CLEAR INTERRUPT RECEIVED FLAG
1072	030152	004737	016326		JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
1073	030156	052714	000200		BIS #BIT7,(R4)	;ENABLE INTERRUPTS
1074	030162	010465	000000		MOV R4,TSD8(R5)	;SET THE PACKET ADDRESS
1075	030166	004737	016240		JSR PC,WAITF	;WAIT FOR SSR TO SET
1076	030172	103407			BCS 15:	;BR IF CARRY SET (GOOD RETURN)
1077	030174	010001			MOV R0,R1	;SAVE CONTENTS OF TSSR
1081	030176				ERRDF ERRNO,T6SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET
	030176	104455				TRAP C\$ERDF
	030200	001140				.WORD 608
	030202	030475				.WORD T6SSR
	030204	012036				.WORD PKTSSR
1082	030206	005237	002220		INC FATFLG	;SET FATAL ERROR FLAG
1083	030212			15:	CKLOOP	;LOOP ON ERROR, IF FLAG SET
	030212	104406				TRAP C\$CLP1
1084	030214				ESCAPE SUB	;BY-PASS SUBTEST IF FATAL ERROR
	030214	104410				TRAP C\$ESCAPE
	030216	000170				.WORD L10054
1085	030220	005737	002222		TST INTRECV	;DID AN INTERRUPT OCCUR ?
1086	030224	001004			BNE 22:	;BRANCH IF YES
1090	030226				ERRHRD ERRNO,T6NINT,PKTSSR	;REPORT ERROR IF NO INTERRUPT
	030226	104456				TRAP C\$ERRHRD
	030230	001141				.WORD 609
	030232	030631				.WORD T6NINT
	030234	012036				.WORD PKTSSR
1091	030236	012702	102206	22:	MOV #SC!NBA!SSR!TSREJ,R2	;EXPECTED CONTENTS OF TSSR
1092	030242	004737	016326		JSR PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS
1093	030246	016501	000002		MOV TSSR(R5),R1	;GET THE CONTENTS OF TSSR
1094	030252	032701	000100		BIT #OFL,R1	;IS OFF-LINE BIT SET ?
1095	030256	001402			BEQ 25:	;BRANCH IF NOT OFF-LINE
1096	030260	052702	000100		BIS #OFL,R2	;SET OFF-LINE IN EXPECTED DATA
1097	030264	020201		25:	CMP R2,R1	;DOES EXPECTED MATCH RECEIVED ?
1098	030266	001404			BEQ 30:	;OKAY IF MATCH
1102	030270				ERRHRD ERRNO,T6NBA,PKTSSR	;NBA NOT SET TO REJECT
	030270	104456				TRAP C\$ERRHRD
	030272	001142				.WORD 610

TEST 6: COMMAND REJECT

Line	Address	Hex	Dec	Label	Instruction	Comment	Trap	Word
	030274	030450						T6NBA
	030276	012036						PKTSSR
1103	030300			30\$:	CKLOOP	;LOOP ON ERROR ?		
	030300	104406					TRAP	C\$CLP1
1104	030302	004737	016326		JSR	PC,CHKTSSR		
1105	030306	016501	000000		MOV	TSBA(R5),R1		
1106	030312	010402			MOV	R4,R2		
1107	030314	062702	000010		ADD	#10,R2		
1108	030320	020102			CMP	R1,R2		
1109	030322	001404			BEQ	35\$		
1113	030324				ERRHRD	ERRNO,T6TSBA,EXPREC		
	030324	104456					TRAP	C\$ERHRD
	030326	001143					.WORD	611
	030330	030711					.WORD	T6TSBA
	030332	015464					.WORD	EXPREC
1114								
1115								
1116	030334	004737	011104	35\$:	JSR	PC,CKRAM		
1117	030340	103404			BCS	40\$		
1121	030342				ERRHRD	ERRNO,PKTRAM,RAMERR		
	030342	104456					TRAP	C\$ERHRD
	030344	001144					.WORD	612
	030346	004741					.WORD	PKTRAM
	030350	015500					.WORD	RAMERR
1122	030352			40\$:	ENDSEG	;***** END SEGMENT *****		
	030352					10000\$:	TRAP	C\$ESEG
	030352	104405						
1123	030354	011300			MOV	(R3),R0		
1124	030356	042700	177740		BIC	#177740,R0		
1125	030362	020027	000004		CMP	R0,#4		
1126	030366	001002			BNE	45\$		
1127	030370	062703	000002		ADD	#2,R3		
1128	030374	020327	003056	45\$:	CMP	R3,#TBLEND		
1129	030400	103002			BHIS	50\$		
1130	030402	000137	030116		JMP	5\$		
1131								
1132	030406			50\$:	ENDSUB	;***** END SUBTEST *****		
	030406					L10054:	TRAP	C\$ESUB
	030406	104403						
1133	030410	005737	002220		TST	FATFLG		
1134	030414	001402			BEQ	60\$		
1135	030416	004737	017172		JSR	PC,CKDROP		
1136	030422	004737	016446	60\$:	JSR	PC,TSTLOOP		
1137	030426	103002			BCC	62\$		
1138	030430	000137	027552		JMP	T6LOOP		
1139	030434			62\$:	EXIT	TST		
	030434	104432					TRAP	C\$EXIT
	030436	000344					.WORD	L10052
1140								
1141								
1142								
1143								
1144								
1148	030440			T6PACKET:		;COMMAND PACKET FOR TEST		

TEST 6: COMMAND REJECT

1152 030446 052525

.WORD 052525

1153

1154

1155

1156

1157

1158

; *
; LOCAL TEXT MESSAGES FOR TEST
;

1159 030450 103

157

155

T6NBA: .ASCIZ 'Command Not Rejected'

1160 030475 103

157

156

T6SSR: .ASCIZ 'Contents of TSSR Incorrect After Write Packet'

1161 030553 125

156

145

T6INT: .ASCIZ 'Unexpected Interrupt Received On Write Packet'

1162 030631 105

170

160

T6NINT: .ASCIZ 'Expected Interrupt Not Received On Write Packet'

1163 030711 111

156

143

T6TSBA: .ASCIZ 'Incorrect TSBA Address After Packet Write'

1164 030763 103

157

155

TST6ID: .ASCIZ 'Command Reject'

1165

.EVEN

1166 031002

ENDTST

031002

L10052:

031002 104401

TRAP

C\$ETST

TEST 7: WRITE CHARACTERISTICS

1222	031076	005037	002220	10:	CLR	FATFLG	;CLEAR FATAL ERROR FLAG	
1223	031102	005037	002222		CLR	INTRECV	;CLEAR INTERRUPT RECEIVED FLAG	
1224	031106	010465	000000		MOV	R4,TSDB(R5)	;SET THE PACKET ADDRESS	
1225	031112	004737	016326		JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET	
1226	031116	103407			BCS	15:	;BR IF CARRY SET (GOOD RETURN)	
1227	031120	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR	
1231	031122				ERRDF	ERRNO,T7SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET	
	031122	104455					TRAP	C\$ERDF
	031124	001276					.WORD	702
	031126	033741					.WORD	T7SSR
	031130	012036					.WORD	PKTSSR
1232	031132	005237	002220	15:	INC	FATFLG	;SET FATAL ERROR FLAG	
1233	031136				CKLOOP		;LOOP ON ERROR, IF FLAG SET	
	031136	104406					TRAP	C\$CLP1
1234	031140				ESCAPE	SEG	;BY-PASS SUBTEST IF FATAL ERROR	
	031140	104410					TRAP	C\$ESCAPE
	031142	000152					.WORD	10000\$
1235	031144	005737	002222		TST	INTRECV	;DID AN INTERRUPT OCCUR ?	
1236	031150	001404			BEQ	22:	;BRANCH IF NOT	
1240	031152				ERRHRD	ERRNO,T7INT,PKTSSR		
	031152	104456					TRAP	C\$ERHRD
	031154	001277					.WORD	703
	031156	034121					.WORD	T7INT
	031160	012036					.WORD	PKTSSR
1241	031162	016501	000002	22:	MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR	
1242	031166	012702	000200		MOV	#SSR,R2	;EXPECTED CONTENTS OF TSSR	
1243	031172	032701	000100		BIT	#OFL,R1	;IS OFF-LINE BIT SET ?	
1244	031176	001402			BEQ	25:	;BRANCH IF NOT OFF-LINE	
1245	031200	052702	000100		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA	
1246	031204	020201		25:	CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?	
1247	031206	001404			BEQ	30:	;OKAY IF MATCH	
1251	031210				ERRHRD	ERRNO,T7NBA,PKTSSR	;NBA NOT ZERO	
	031210	104456					TRAP	C\$ERHRD
	031212	001300					.WORD	704
	031214	033300					.WORD	T7NBA
	031216	012036					.WORD	PKTSSR
1252	031220			30:	CKLOOP		;LOOP ON ERROR ?	
	031220	104406					TRAP	C\$CLP1
1253	031222	004737	016326		JSR	PC,CHKTSSR	;WAIT FOR READY, NON-AMBIGUOUS	
1254	031226	016501	000000		MOV	TSBA(R5),R1	;GET TSBA REGISTER CONTENTS	
1255	031232	012702	033166		MOV	#T7BFR,R2	;START OF THE DATA BUFFER	
1256	031236	032762	000200	000012	BIT	#BIT7,XST2(R2)	;IS EXTENDED FEATURES BIT SET ?	
1257	031244	001404			BEQ	32:	;BRANCH IF EXTENDED FEATURES OFF	
1258	031246	005237	002224		INC	EXTFEA	;SET EXTENDED FEATURES FOR SUBTEST 6	
1259	031252	062702	000002		ADD	#2,R2	;EXTRA WORD IF SPECIAL FEATURES	
1260	031256	062702	000016	32:	ADD	#14.,R2	;EXPECTED CONTENTS OF TSBA	
1261	031262	020102			CMP	R1,R2	;COMPARE EXPECTED TO RECEIVED	
1262	031264	001404			BEQ	35:	;ERROR IF NOT EQUAL	
1266	031266				ERRHRD	ERRNO,T7TSBA,EXPREC	;PRINT THE ERROR & EXPD/RECV	
	031266	104456					TRAP	C\$ERHRD
	031270	001301					.WORD	705
	031272	034210					.WORD	T7TSBA
	031274	015464					.WORD	EXPREC
1267								
1268								
1269	031276	004737	011104	35:	JSR	PC,CKRAM	;SEE IF DATA IN RAM IS CORRECT	
1270	031302	103404			BCS	40:	;BRANCH IF PACKET IN RAM IS CORRECT	

TEST 7: WRITE CHARACTERISTICS

1290					;*
1291					;
1292					;TEST 7, SUBTEST 2
1293					;
1294					;CHECK THAT UNUSED BITS BEING SET CAUSES
1295					;WRITE CHARACTERISTICS COMMAND TO BE REJECTED
1296					;
1297					;-
1298					
1299	031350	BGNSUB			;/!!!!!!!!!!!!!! BEGIN SUBTEST !!!!!!!!!!!!!!!
	031350				T7.2:
	031350	104402			TRAP C\$BSUB
1300					
1301	031352	SETPRI	#PRI00		;LOWER PRIORITY TO ALLOW INTERRUPTS
	031352	012700	000000		MOV #PRI00,R0
	031356	104441			TRAP C\$SPRI
1302	031360	012703	033206		;START OF TEST DATA FOR SUBTEST
1303	031364	012704	033140	5\$:	MOV #T7PACKET,R4 ;GET THE ADDRESS OF COMMAND PACKET
1304	031370	004737	034322		JSR PC,T7REST ;RESTORE PACKET TO STARTING VALUES
1305					
1306	031374	BGNSEG			;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
	031374	104404			TRAP C\$BSEG
1307					
1308	031376	004737	015764		JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER
1309	031402	103405			BCS 10\$;RR IF SOFT INIT = OK
1313	031404	010001			MOV R0,R1 ;SAVE CONTENTS OF TSSR
1314	031406				ERRDF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
	031406	104455			TRAP C\$ERDF
	031410	001303			.WORD 707
	031412	003646			.WORD SFIERR
	031414	012024			.WORD SFIMSG
1315	031416	005037	002222	10\$:	CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
1316	031422	010400			MOV R4,R0 ;START OF THE COMMAND PACKET
1317	031424	061300			ADD (R3),R0 ;OFFSET TO THE DATA WORD TO TEST
1318	031426	056310	000002		BIS 2(R3),(R0) ;SET THE DATA BITS TO BE TESTED
1319	031432	010465	000000		MOV R4,TSDB(R5) ;SET THE PACKET ADDRESS
1320	031436	004737	016240		JSR PC,WAITF ;WAIT FOR SSR TO SET
1321	031442	103405			BCS 15\$;BR IF CARRY SET (GOOD RETURN)
1322	031444	010001			MOV R0,R1 ;SAVE CONTENTS OF TSSR
1326	031446				ERRDF ERRNO,T7SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
	031446	104455			TRAP C\$ERDF
	031450	001304			.WORD 708
	031452	033741			.WORD T7SSR
	031454	012036			.WORD PKTSSR
1327	031456			15\$:	CKLOOP ;LOOP ON ERROR, IF FLAG SET
	031456	104406			TRAP C\$CLP1
1328	031460				ESCAPE SEG ;BY-PASS CHECKS IF FATAL ERROR
	031460	104410			TRAP C\$ESCAPE
	031462	000116			.WORD 10000\$-.
1329	031464	005737	002222		TST INTRECV ;DID AN INTERRUPT OCCUR ?
1330	031470	001404			BEQ 22\$;BRANCH IF NOT
1334	031472				ERRHRD ERRNO,T7INT,PKTSSR
	031472	104456			TRAP C\$ERHRD
	031474	001305			.WORD /09
	031476	034121			.WORD T7INT
	031500	012036			.WORD PKTSSR
1335	031502	016501	000002	22\$:	MOV TSSR(R5),R1 ;GET THE CONTENTS OF TSSR

TEST 7: WRITE CHARACTERISTICS

Line	Address	Offset	Hex	Label	Operation	Comments	Trap	Value
1336	031506	012702	102206		MOV	#SC!SSR!TSREJ!NBA,R2		
1337	031512	032701	000100		BIT	#OFL,R1		
1338	031516	001402			BEQ	25\$		
1339	031520	052702	000100		BIS	#OFL,R2		
1340	031524	020201		25\$:	CMP	R2,R1		
1341	031526	001414			BEQ	30\$		
1342	031530	010100			MOV	R1,R0		
1343	031532				XOR	R2,R0		
1344	031542	020027	002000		CMP	R0,#NBA		
1345	031546	001404			BEQ	30\$		
1349	031550				ERRHRD	ERRNO,T72REJ,PKTSSR		
	031550	104456					TRAP	C\$ERHRD
	031552	001306					.WORD	710
	031554	033353					.WORD	T72REJ
	031556	012036					.WORD	PKTSSR
1350	031560			30\$:	CKLOOP			
	031560	104406						
1351	031562	032701	002000		BIT	#NBA,R1		
1352	031566	001004			BNE	35\$		
1356	031570				ERRHRD	ERRNO,T72NBA,PKTSSR		
	031570	104456					TRAP	C\$ERHRD
	031572	001307					.WORD	711
	031574	033222					.WORD	T72NBA
	031576	012036					.WORD	PKTSSR
1357	031600			35\$:				
1358	031600				ENDSEG			
	031600							
	031600	104405						
1359								
1360	031602	062703	000004		ADD	#4,R3		
1361	031606	020327	033222		CMP	R3,#T72DONE		
1362	031612	103002			BHIS	57\$		
1363	031614	000137	031364		JMP	5\$		
1364								
1365	031620			57\$:	ENDSUB			
	031620							
	031620	104403						

TEST 7: WRITE CHARACTERISTICS

1367				;*
1368				;
1369				;TEST 7, SUBTEST 3
1370				;
1371				;CHECK THE WRITE CHARACTERISTICS COMMAND IS REJECTED
1372				;IF ISSUED WITH AN INVALID DATA BLOCK BYTE COUNT
1373				;
1374				;-
1375				
1376	031622	BGNSUB		;/!!!!!!!!!!!!!! BEGIN SUBTEST !!!!!!!!!!!!!!!
	031622			T7.3:
	031622	104402		TRAP C\$BSUB
1377				
1378	031624	SETPRI	#PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	031624	012700	000000	MOV #PRI00,R0
	031630	104441		TRAP C\$SPRI
1379	031632	012703	000001	;STARTING BYTE COUNT
1380	031636	012704	033140	;GET THE ADDRESS OF COMMAND PACKET
1381	031642	004737	034322	;RESTORE PACKET TO STARTING VALUES
1382				
1383	031646	BGNSEG		;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
	031646	104404		TRAP C\$BSEG
1384				
1385	031650	004737	015764	JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER
1386	031654	103405		BCS 10\$;BR IF SOFT INIT = OK
1390	031656	010001		MOV R0,R1 ;SAVE CONTENTS OF TSSR
1391	031660			ERRDF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
	031660	104455		TRAP C\$ERDF
	031662	001310		.WORD 712
	031664	003646		.WORD SFIERR
	031666	012024		.WORD SFIMSG
1392	031670	005037	002222	10\$: CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
1393	031674	010364	000006	MOV R3,PKBCNT(R4) ;INSERT THE BYTE COUNT FOR TEST
1394	031700	010465	000000	MOV R4,TSDB(R5) ;SET THE PACKET ADDRESS
1395	031704	004737	016240	JSR PC,WAITF ;WAIT FOR SSR TO SET
1396	031710	103405		BCS 15\$;BR IF CARRY SET (GOOD RETURN)
1397	031712	010001		MOV R0,R1 ;SAVE CONTENTS OF TSSR
1401	031714			ERRDF ERRNO,T7SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
	031714	104455		TRAP C\$ERDF
	031716	001311		.WORD 713
	031720	033741		.WORD T7SSR
	031722	012036		.WORD PKTSSR
1402	031724			15\$: CKLOOP ;LOOP ON ERROR, IF FLAG SET
	031724	104406		TRAP C\$CLP1
1403	031726			ESCAPE SEG ;BY-PASS SUBTEST IF FATAL ERROR
	031726	104410		TRAP C\$ESCAPE
	031730	000116		.WORD 10000\$-
1404	031732	005737	002222	TST INTRECV ;DID AN INTERRUPT OCCUR ?
1405	031736	001404		BEQ 22\$;BRANCH IF NOT
1409	031740			ERRHRD ERRNO,T7INT,PKTSSR
	031740	104456		TRAP C\$ERHRD
	031742	001312		.WORD 714
	031744	034121		.WORD T7INT
	031746	012036		.WORD PKTSSR
1410	031750	016501	000002	22\$: MOV TSSR(R5),R1 ;GET THE CONTENTS OF TSSR
1411	031754	012702	102206	MOV #SC!SSR!TSREJ!NBA,R2 ;EXPECTED CONTENTS OF TSSR
1412	031760	032701	000100	BIT #OFL,R1 ;IS OFF LINE BIT SET ?

TEST 7: WRITE CHARACTERISTICS

1413	031764	001402			BEQ	25\$	;BRANCH IF NOT OFF-LINE
1414	031766	052702	000100		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA
1415	031772	020201		25\$:	CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?
1416	031774	001414			BEQ	30\$	;OKAY IF MATCH
1417	031776	010100			MOV	R1,R0	;DATA FROM TSSR
1418	032000				XOR	R2,R0	;FIND BITS IN ERROR
1419	032010	020027	002000		CMP	R0,#NBA	;IS NBA ONLY BIT IN ERROR ?
1420	032014	001404			BEQ	30\$	;DON'T PRINT ERROR IF NBA ONLY BAD BIT
1424	032016				ERRHRD	ERRNO,T73REJ,PKTSSR	;COMMAND NOT REJECTED
	032016	104456					TRAP C\$ERHRD
	032020	001313					.WORD 715
	032022	033452					.WORD T73REJ
	032024	012036					.WORD PKTSSR
1425	032026			30\$:	CKLOOP		;LOOP ON ERROR ?
	032026	104406					TRAP C\$CLP1
1426	032030	032701	002000		BIT	#NBA,R1	;IS NBA BIT SET ?
1427	032034	001004			BNE	35\$	;OKAY IF NBA SET
1431	032036				ERRHRD	ERRNO,T72NBA,PKTSSR	;NBA NOT SET
	032036	104456					TRAP C\$ERHRD
	032040	001314					.WORD 716
	032042	033222					.WORD T72NBA
	032044	012036					.WORD PKTSSR
1432	032046			35\$:			
1433	032046				ENDSEG		;<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<<<
	032046						10000\$:
	032046	104405					TRAP C\$ESEG
1434							
1435	032050	005203			INC	R3	;NEXT BYTE COUNT
1436	032052	020327	000006		CMP	R3,#6	;TESTED ALL INVALID ?
1437	032056	002002			BGE	57\$	;BRANCH IF TEST DONE
1438	032060	000137	031636		JMP	5\$	;BRANCH TILL BACK TO ZERO
1439							
1440	032064			57\$:	ENDSUB		;////////// END SUBTEST //////////
	032064						L10060:
	032064	104403					TRAP C\$ESUB

TEST 7: WRITE CHARACTERISTICS

```

1442      ;
1443      ;
1444      ;TEST 7, SUBTEST 4
1445      ;
1446      ;SUBTEST TO VERIFY THAT A WRITE CHARACTERISTICS COMMAND IS
1447      ;REJECTED IF AN ILLEGAL DATA BLOCK ADDRESS IS ISSUED.
1448      ;
1449      ;
1450      ;-
1451      032066      BGNSUB      ;//////////////// BEGIN SUBTEST //////////////////
1452      032066      104402      ;T7.4:
1453      032066      104402      TRAP      C18SUB
1454      032070      SETPRI      @PRI00      ;LOWER PRIORITY TO ALLOW INTERRUPTS
1455      032070      012700      000000      MOV      @PRI00,R0
1456      032074      104441      TRAP      C1SPRI
1457      032076      012703      033206      ;START OF TEST DATA FOR SUBTEST
1458      032102      012704      033140      ;GET THE ADDRESS OF COMMAND PACKET
1459      032106      004737      034322      ;RESTORE PACKET TO STARTING VALUES
1460      032112      004737      015764      JSR      PC,SOFINIT      ;DO SOFT INIT OF CONTROLLER
1461      032116      103405      BCS      101      ;BR IF SOFT INIT = OK
1462      032120      010001      MOV      R0,R1      ;SAVE CONTENTS OF TSSR
1463      032122      032122      104455      ERDIF      ERRNO,SFIERR,SFIMSG      ;DEVICE FATAL ERROR DURING INIT
1464      032122      104455      TRAP      C1ERDIF
1465      032124      001315      .WORD      717
1466      032126      003646      .WORD      SFIERR
1467      032130      012024      .WORD      SFIMSG
1468      032132      005037      002222      101:      CLR      INTRECV      ;CLEAR INTERRUPT RECEIVED FLAG
1469      032136      052737      000001      033150      BIS      #1,T7DATA      ;MAKE ADDRESS ODD
1470      032144      010465      000000      MOV      R4,TSD8(R5)      ;SET THE PACKET ADDRESS
1471      032150      004737      016240      JSR      PC,WAITF      ;WAIT FOR SSR TO SET
1472      032154      103405      BCS      151      ;BR IF CARRY SET (GOOD RETURN)
1473      032156      010001      MOV      R0,R1      ;SAVE CONTENTS OF TSSR
1474      032160      032160      104455      ERDIF      ERRNO,T7SSR,PKTSSR      ;DEVICE FATAL SSR FAILED TO SET
1475      032160      104455      TRAP      C1ERDIF
1476      032162      001316      .WORD      718
1477      032164      033741      .WORD      T7SSR
1478      032166      012036      .WORD      PKTSSR
1479      032170      032170      104406      151:      CKLOOP      ;LOOP ON ERROR, IF FLAG SET
1480      032172      104410      ESCAPE      SUB      ;BY-PASS SUBTEST IF FATAL ERROR
1481      032174      000116      TRAP      C1CLP1
1482      032176      005737      002222      TST      INTRECV      ;DID AN INTERRUPT OCCUR ?
1483      032202      001404      BEQ      221      ;BRANCH IF NOT
1484      032204      104456      ERDIF      ERRNO,T7INT,PKTSSR      ;BRANCH IF NOT
1485      032204      104456      TRAP      C1ERDIF
1486      032206      001317      .WORD      719
1487      032210      034121      .WORD      T7INT
1488      032212      012036      .WORD      PKTSSR
1489      032214      016501      000002      221:      MOV      TSSR(R5),R1      ;GET THE CONTENTS OF TSSR
1490      032220      012702      102206      MOV      @SC:SSR:TSREJ:NBA,R2      ;EXPECTED CONTENTS OF TSSR
1491      032224      032701      000100      BIT      @OFL,R1      ;IS OFF LINE BIT SET ?
1492      032230      001402      BEQ      251      ;BRANCH IF NOT OFF-LINE
1493      032232      052702      000100      BIS      @OFL,R2      ;SET OFF LINE IN EXPECTED DATA

```

TEST 7: WRITE CHARACTERISTICS

1489	032236	020201		254:	CMP	R2,R1		;DOES EXPECTED MATCH RECEIVED ?
1490	032240	001414			BEQ	304		;OKAY IF MATCH
1491	032242	010100			MOV	R1,R0		;DATA FROM TSSR
1492	032244				XOR	R2,R0		;FIND BITS IN ERROR
1493	032254	020027	002000		CMP	R0,#NBA		;IS NBA ONLY BIT IN ERROR ?
1494	032260	001404			BEQ	304		;DON'T PRINT ERROR IF NBA ONLY BAD BIT
1498	032262				ERRHRD	ERRNO,T74REJ,PKTSSR		;COMMAND NOT REJECTED
	032262	104456						TRAP C\$ERHRD
	032264	001320						.WORD 720
	032266	033545						.WORD T74REJ
	032270	012036						.WORD PKTSSR
1499	032272			304:	CKLOOP			;LOOP ON ERROR ?
	032272	104406						TRAP C\$CLP1
1500	032274	032701	002000		BIT	#NBA,R1		;IS NBA BIT SET ?
1501	032300	001004			BNE	354		;OKAY IF NBA SET
1505	032302				ERRHRD	ERRNO,T72NBA,PKTSSR		;NBA NOT SET
	032302	104456						TRAP C\$ERHRD
	032304	001321						.WORD 721
	032306	033222						.WORD T72NBA
	032310	012036						.WORD PKTSSR
1506								
1507	032312			354:	ENDSUB			;////////// END SUBTEST //////////
	032312							L10061:
1508	032312	104403						TRAP C\$ESUB

1510						
1511						
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1515						
1516						
1517						
1518						
1519	032314			BGNSUB		/ BEGIN SUBTEST /
	032314					T7.5:
	032314	104402				TRAP C#BSUB
1520						
1521	032316			SETPRI #PRI00		;LOWER PRIORITY TO ALLOW INTERRUPTS
	032316	012700	000000			MOV #PRI00,R0
	032322	104441				TRAP C#SPRI
1522	032324	012703	000001	MOV #1,R3		;STARTING BUFFER LENGTH
1523	032330	012704	033140	MOV #T7PACKET,R4		;GET THE ADDRESS OF COMMAND PACKET
1524	032334	004737	034322	JSR PC,T7REST		;RESTORE PACKET TO STARTING VALUES
1525						
1526	032340			BGNSEG		> BEGIN SEGMENT >
	032340	104404				TRAP C#BSEG
1527						
1528	032342	004737	015764	JSR PC,SOFINIT		;DO SOFT INIT OF CONTROLLER
1529	032346	103405		BCS 10#		;BR IF SOFT INIT = OK
1533	032350	010001		MOV R0,R1		;SAVE CONTENTS OF TSSR
1534	032352			ERRDF ERNO,SFIERR,SFIMSC		;DEVICE FATAL ERROR DURING INIT
	032352	104455				TRAP C#ERDF
	032354	001322				.WORD 722
	032356	003646				.WORD SFIERR
	032360	012024				.WORD SFIMSG
1535	032362	005037	002222	CLR INTRECV		;CLEAR INTERRUPT RECEIVED FLAG
1536	032366	010337	033154	MOV R3,T7DATA.4		;INSERT THE BAD MESSAGE LENGTH
1537	032372	010465	000000	MOV R4,TSD8(R5)		;SET THE PACKET ADDRESS
1538	032376	004737	016240	JSR PC,WAITF		;WAIT FOR SSR TO SET
1539	032402	103405		BCS 15#		;BR IF CARRY SET (GOOD RETURN)
1540	032404	010001		MOV R0,R1		;SAVE CONTENTS OF TSSR
1544	032406			ERRDF ERNO,T7SSR,PKTSSR		;DEVICE FATAL SSR FAILED TO SET
	032406	104455				TRAP C#ERDF
	032410	001323				.WORD 723
	032412	033741				.WORD T7SSR
	032414	012036				.WORD PKTSSR
1545	032416			CKLOOP		;LOOP ON ERROR, IF FLAG SET
	032416	104406				TRAP C#CLP1
1546	032420			ESCAPE SEG		;BY-PASS SUBTEST IF FATAL ERROR
	032420	104410				TRAP C#ESCAPE
	032422	000116				.WORD 10000#
1547	032424	005737	002222	TST INTRECV		;DID AN INTERRUPT OCCUR ?
1548	032430	001404		BEQ 22#		;BRANCH IF NOT
1552	032432			ERRHRD ERNO,T7INT,PKTSSR		
	032432	104456				TRAP C#ERHRD
	032434	001324				.WORD 724
	032436	034121				.WORD T7INT
	032440	012036				.WORD PKTSSR
1553	032442	016501	000002	MOV TSSR(R5),R1		;GET THE CONTENTS OF TSSR
1554	032446	012702	102206	MOV #SC!SSR!TSREJ!NBA,R2		;EXPECTED CONTENTS OF TSSR
1555	032452	032701	000100	BIT #OFL,R1		;IS OFF LINE BIT SET ?

TEST 7: WRITE CHARACTERISTICS

Line	Address	Offset	Label	Operation	Register	Comment
1556	032456	001402		BEQ	25:	;BRANCH IF NOT OFF LINE
1557	032460	052702	000100	BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA
1558	032464	020201		CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?
1559	032466	001414		BEQ	30:	;OKAY IF MATCH
1560	032470	010100		MOV	R1,R0	;DATA FROM TSSR
1561	032472			XOR	R2,R0	;FIND BITS IN ERROR
1562	032502	020027	002000	CMP	R0,#NBA	;IS NBA ONLY BIT IN ERROR ?
1563	032506	001404		BEQ	30:	;DON'T PRINT ERROR IF NBA ONLY BAD BIT
1567	032510			ERRHRD	ERRNO,T75REJ,PKTSSR	;COMMAND NOT REJECTED
	032510	104456				TRAP C\$ERHRD
	032512	001325				.WORD 725
	032514	033643				.WORD T75REJ
	032516	012036				.WORD PKTSSR
1568	032520			30:	CKLOOP	;LOOP ON ERROR ?
	032520	104406				TRAP C\$CLP1
1569	032522	032701	002000	BIT	#NBA,R1	;IS NBA BIT SET ?
1570	032526	001004		BNE	35:	;OKAY IF NBA SET
1574	032530			ERRHRD	ERRNO,T72NBA,PKTSSR	;NBA NOT SET
	032530	104456				TRAP C\$ERHRD
	032532	001326				.WORD 726
	032534	033222				.WORD T72NBA
	032536	012036				.WORD PKTSSR
1575	032540			35:		
1576	032540			ENDSEG		;***** END SEGMENT *****
	032540					10000:
	032540	104405				TRAP C\$ESEG
1577						
1578	032542	005203		INC	R3	;NEXT BUFFER LENGTH
1579	032544	020327	000016	CMP	R3,#14.	;HAVE ALL BAD VALUES BEEN TESTED ?
1580	032550	002002		BGE	57:	;BRANCH IF ALL TESTED
1581	032552	000137	032330	JMP	5:	;BRANCH TILL BACK TO ZERO
1582						
1583	032556			57:	ENDSUB	;***** END SUBTEST *****
	032556					L10062:
	032556	104403				TRAP C\$ESUB

[illegible]

TEST 7: WRITE CHARACTERISTICS

Address	Offset	Hex	Dec	Label	Instruction	Comment	Trap	Word
1631	032720	000156			TST	INTRECV		10000\$-
1632	032722	005737	002222		BEQ	22\$		
1636	032730	001404			ERRHRD	ERRNO,T7INT,PKTSSR		
	032730	104456					TRAP	C\$ERHRD
	032732	001331					.WORD	729
	032734	034121					.WORD	T7INT
	032736	012036					.WORD	PKTSSR
1637	032740	016501	000002	22\$:	MOV	TSSR(R5),R1		
1638	032744	012702	000200		MOV	#SSR,R2		
1639	032750	032701	000100		BIT	#OFL,R1		
1640	032754	001402			BEQ	25\$		
1641	032756	052702	000100		BIS	#OFL,R2		
1642	032762	020201		25\$:	CMP	R2,R1		
1643	032764	001404			BEQ	30\$		
1647	032766				ERRHRD	ERRNO,T7NBA,PKTSSR		
	032766	104456					TRAP	C\$ERHRD
	032770	001332					.WORD	730
	032772	033300					.WORD	T7NBA
	032774	012036					.WORD	PKTSSR
1648	032776			30\$:	CKLOOP			
	032776	104406					TRAP	C\$CLP1
1649	033000	004737	016326		JSR	PC,CHKTSSR		
1650	033004	016501	000000		MOV	TSBA(R5),R1		
1651	033010	012702	033166		MOV	#T7BFR,R2		
1652	033014	032762	000200	000012	BIT	#BIT7,XST2(R2)		
1653	033022	001402			BEQ	32\$		
1654	033024	062702	000002		ADD	#2,R2		
1655	033030	062702	000016		ADD	#14.,R2		
1656	033034	020102		32\$:	CMP	R1,R2		
1657	033036	001404			BEQ	35\$		
1661	033040				ERRHRD	ERRNO,T7TSBA,EXPREC		
	033040	104456					TRAP	C\$ERHRD
	033042	001333					.WORD	731
	033044	034210					.WORD	T7TSBA
	033046	015464					.WORD	EXPREC
1662								
1663								
1664	033050	012704	033150	35\$:	MOV	#T7DATA,R4		
1665	033054	004737	011214		JSR	PC,CKRAM2		
1666	033060	103404			BCS	40\$		
1670	033062				ERRHRD	ERRNO,PKTRAM,RAMERR		
	033062	104456					TRAP	C\$ERHRD
	033064	001334					.WORD	732
	033066	004741					.WORD	PKTRAM
	033070	015500					.WORD	RAMERR
1671								
1672	033072	012704	033140	40\$:	MOV	#T7PACKET,R4		
1673	033076				ENDSEG			
	033076	104405					TRAP	C\$ESEG
1674								
1675	033100	012364	000006		MOV	(R3),PKBCNT(R4)		
1676	033104	020327	003056		CMP	R		

TEST 7: WRITE CHARACTERISTICS

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1680 033116          55:  ENDSUB          ;///////////////// END SUBTEST ///////////////////
      033116          L10063:          TRAP      C$ESUB
      033116 104403
1681
1682 033120 005737 002220          TST      FATFLG          ;ANY FATAL ERRORS ?
1683 033124 001402          BEQ      60:          ;BRANCH IF NOT
1684 033126 004737 017172          JSR      PC,CKDROP        ;TRY TO DROP THE UNIT
1685 033132          60:  EXIT      TST          ;ALL DONE THIS TEST
1686 033132          TRAP      C$EXIT
      033132 104432          .WORD      L10055-.
      033134 001234
1687
1688
1689 ;*
1690 ;LOCAL STORAGE FOR THIS TEST
1691 ;
1692
1693          033140
1695 033140          T7PACKET:          .-<+.10>E177770
1696 033140 100004          .WORD      100004          ;COMMAND PACKET FOR TEST
1697 033142 033150          .WORD      T7DATA          ;WRITE CHARACTERISTICS COMMAND, WITH ACK
1698 033144 000000          .WORD      0              ;ADDRESS OF CHARACTERISTICS BLOCK
1699 033146 000010          .WORD      8.              ;STARTING VALUE OF BLOCK SIZE
1700
1701 033150          T7DATA:          .WORD      T7BFR          ;CHARACTERISTICS DATA BLOCK
1702 033150 033166          .WORD      0              ;ADDRESS OF MESSAGE BUFFER
1703 033152 000000          .WORD      14.             ;LENGTH OF MESSAGE BUFFER
1704 033154 000016          .WORD      0              ;EXTFEA EXTRA WORD
1705 033156 000000          T7SP:   .WORD      0              ;SPACE
1706 033160 000000          .WORD      0,0             ;MESSAGE BUFFER
1707
1708 033162 000000 000000          T7BFR: .BLKW      8.
1709 033166
1710
1711 ;*
1712 ;
1713 ;TEST DATA FOR SUBTEST TWO
1714 ;
1715 ;DATA HAS FORMAT:
1716 ;
1717 ;      1ST WORD      OFFSET TO TEST WORD IN PACKET
1718 ;      2ND WORD      BITS TO SET FOR TEST
1719 ;
1720 ;-
1721
1722 033206          T72DATA:          .WORD      0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13
1723 033206 000000 037140          .WORD      2,BIT0
1724 033212 000002 000001          .WORD      4,BIT6!BIT15
1725 033216 000004 100100          T72DONE=.
1726 033222
1727
1728
1729 ;*
1730 ;LOCAL TEXT MESSAGES FOR TEST
1731 ;
1732
1733 033222          116      102      101  T72NBA: .ASCIZ  'NBA Not Set On Rejected WRITE CHARACTERISTICS'
1734 033300          127      122      111  T7NBA:  .ASCIZ  'WRITE CHARACTERISTICS Command Not Accepted'

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TEST 7: WRITE CHARACTERISTICS

1735	033353	127	122	111	T72REJ: .ASCIIZ	'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
1736	033452	127	122	111	T73REJ: .ASCIIZ	'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
1737	033545	127	122	111	T74REJ: .ASCIIZ	'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
1738	033643	127	122	111	T75REJ: .ASCIIZ	'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
1739	033741	103	157	156	T7SSR: .ASCIIZ	'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
1740	034030	105	170	160	T7NINT: .ASCIIZ	'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
1741	034121	125	156	145	T7INT: .ASCIIZ	'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
1742	034210	111	156	143	T7TSBA: .ASCIIZ	'Incorrect TSBA Address After WRITE CHARACTERISTICS'
1743	034273	127	162	151	TST7ID: .ASCIIZ	'Write Characteristics'
1744						.EVEN
1745						

TEST 7: WRITE CHARACTERISTICS

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1754 034322
1755 034322
1756 034326 012701 033140
1757 034332 012721 100004
1758 034336 012721 033150
1759 034342 005021
1760 034344 012721 000010
1761 034350 012721 033166
1762 034354 005021
1763 034356 012721 000020
1764 034362 005021
1765 034364 005011
1766 034366 000207
1767 034370
      034370
      034370 104401
1768

```

```

;+
;
;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
;-
T7REST:
      SAVREG
      MOV     #T7PACKET,R1      ;SAVE THE REGISTERS
      ;START OF THE PACKET
      MOV     #100004,(R1)+     ;WRITE CHARACTERISTICS WITH ACK
      MOV     #T7DATA,(R1)+    ;ADDRESS OF CHAR DATA BLOCK
      CLR     (R1)+             ;EXTENDED ADDRESS
      MOV     #8,(R1)+         ;SIZE OF DATA BLOCK IN BYTES
      MOV     #T7BFR,(R1)+     ;ADDRESS OF MESSAGE BUFFER
      CLR     (R1)+
      MOV     #16,(R1)+        ;LENGTH OF MESSAGE BUFFER
      CLR     (R1)+
      RTS     PC                ;RETURN
      ENDTST

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L10055: TRAP C$ETST

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TEST 8: VOLUME CHECK

.SBTTL TEST 8: VOLUME CHECK

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THIS TEST VERIFIES THAT THE VOLUME CHECK (VCK) BIT, A FLAG HELD WITHIN THE M7196 AND APPEARING IN XSTO, IS SET BY INITIALIZE AND CLEARED BY EXECUTING A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT SET. IT IS ALSO VERIFIED THAT A WRITE CHARACTERISTICS COMMAND WITH THE CVC BIT CLEAR DOES NOT AFFECT THE STATE OF THE VOLUME CHECK BIT. THE ACTUAL FUNCTION OF VOLUME CHECK, THAT OF PREVENTING OR ALLOWING A TAPE MOTION COMMAND DEPENDING UPON WHETHER VOLUME CHECK IS SET OR CLEAR, IS NOT CHECKED BY THIS TEST; THIS FUNCTIONALITY IS CHECKED IN THE INDIVIDUAL TESTS OF TAPE MOTION COMMANDS.

THE TEST PROCEEDS AS FOLLOWS:

1. THE CONTROLLER IS INITIALIZED BY WRITING INTO THE TSSR.
2. A WRITE CHARACTERISTICS COMMAND IS ISSUED (WITH CVC=0) AND XSTO IN THE RETURNED MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
3. THE PREVIOUS STEP IS REPEATED TO VERIFY THAT VCK DOES NOT CHANGE (REMAINS AT 0).
4. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=1 AND THE VCK BIT IN XSTO IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD BE CLEAR (0).
5. A WRITE CHARACTERISTICS COMMAND IS ISSUED WITH CVC=0 AND THE VCK BIT IN XSTO IN THE MESSAGE BUFFER IS EXAMINED; THE VCK BIT SHOULD REMAIN CLEAR (0).

1804 034372

BGNTST

034372

1809 034372 012700 035257

MOV #TST8ID,R0

T8::
;ASCII MESSAGE TO IDENTIFY TEST

1810 034376 004737 016500

JSR PC,TSTSETUP

;DO INITIAL TEST SETUP

1811 034402 012737 000024 002214

MOV #20.,LOOPCNT

;PERFORM 20 ITERATIONS

1812 034410

T8LOOP:

1813

1814 034410 012704 035000

MOV #T8PACKET,R4

;PACKET FOR WRITE CHARACTERISTICS

1815 034414 004737 015764

5\$:

JSR PC,SOFINIT

;DO SOFT INIT OF CONTROLLER

1816 034420 103405

BCS 10\$

;BR IF SOFT INIT = OK

1820 034422 010001

MOV R0,R1

;SAVE CONTENTS OF TSSR

1821 034424

ERRDF

ERRNO,SFIERR,SFIMSG

;DEVICE FATAL ERROR DURING INIT

034424 104455

TRAP C\$ERDF

034426 001441

.WORD 801

034430 003646

.WORD SFIERR

034432 012024

.WORD SFIMSG

1822 034434 042714 040000

10\$:

BIC #BIT14,(R4)

;CLEAR THE CVC BIT

1823 034440 010465 000000

MOV R4,TSD8(R5)

;SET THE PACKET ADDRESS FOR WRITE CHAR

1824 034444 004737 016326

JSR PC,CHKTSSR

;WAIT FOR SSR TO SET

1825 034450 103405

BCS 15\$

;BR IF CARRY SET (GOOD RETURN)

1826 034452 010001

MOV R0,R1

;SAVE CONTENTS OF TSSR

1830 034454

ERRDF

ERRNO,T8SSR,PK*SSR

;DEVICE FATAL SSR FAILED TO SET

034454 104455

TRAP C\$ERDF


```

;IS VOLUME CHECK CLEAR IN XSTO ?
;OKAY IF VOLUME CHECK IS CLEARED
;CONTENTS OF TSSR FOR ERROR REPORT
;VOLUME CHECK NOT CLEARED
                                .WORD      L10064-.
                                TRAP        C$ERHRD
                                .WORD      807
                                .WORD      T8VCK
                                .WORD      PKTMES

;LOOP ON ERROR ?
                                TRAP        C$CLP1

;CLEAR THE CVC BIT
;SET THE PACKET ADDRESS FOR WRITE CHAR
;WAIT FOR SSR TO SET
;BR IF CARRY SET (GOOD RETURN)
;SAVE CONTENTS OF TSSR
;DEVICE FATAL SSR FAILED TO SET
                                TRAP        C$ERDF
                                .WORD      808
                                .WORD      T8SSR
                                .WORD      PKTSSR

;LOOP ON ERROR, IF FLAG SET
                                TRAP        C$CLP1

;EXIT IF FATAL ERROR
                                TRAP        C$ESCAPE
                                .WORD      L10064-.

;IS VOLUME CHECK CLEAR IN XSTO ?
;OKAY IF VOLUME CHECK IS CLEARED
;CONTENTS OF TSSR FOR ERROR REPORT
;VOLUME CHECK NOT CLEARED
                                TRAP        C$ERHRD
                                .WORD      809
                                .WORD      T8VCK
                                .WORD      PKTMES

;LOOP ON ERROR ?
                                TRAP        C$CLP1

;SHOULD WE DO ITERATIONS ?
;BRANCH IF NOT
;LOOP UNTIL COUNT EXPIRED
;ALL DONE THIS TEST
                                TRAP        C$EXIT
                                .WORD      L10064.

;COMMAND PACKET FOR TEST
;WRITE CHARACTERISTICS COMMAND
;ADDRESS OF CHARACTERISTICS BLOCK

;STARTING VALUE OF COUNTER

;CHARACTERISTICS DATA BLOCK
;ADDRESS OF MESSAGE BUFFER

```


TEST 8: VOLUME CHECK

```

1919 035014 000020      .WORD 16.      ;LENGTH OF MESSAGE BUFFER
1920 035016 000000 000000      .WORD 0,0
1921
1922 035022      T8BFR: .BLKW 8.      ;MESSAGE BUFFER
1923
1924
1925      ;*
1926      ;LOCAL TEXT MESSAGES FOR TEST
1927      ;*
1928
1929 035042      126      157      154 T8VCK: .ASCIZ 'Volume Check Bit Not Cleared'
1930 035077      126      157      154 T8NVCK: .ASCIZ 'Volume Check Bit (VCK) Not Clear After Initialize (XST0)'
1931 035170      103      157      156 T8SSR: .ASCIZ 'Contents of TSSR Incorrect After Write Characteristics'
1932 035257      126      157      154 T8TID: .ASCIZ 'Volume Check'
1933      .EVEN
1934 035274      ENDTST
      035274
      035274 104401

```

L10064: TRAP C\$ETST

TEST 9: COMPLETION INTERRUPT

1936				.SBTTL TEST 9: COMPLETION INTERRUPT	
1937					
1938					
1939				:	
1940				:	THIS TEST VERIFIES THAT AN INTFRRUPT IS GENERATED AT THE
1941				:	COMPLETION OF THE WRITE CHARACTERISTICS COMMAND IF THE INTERRUPT
1942				:	ENABLE (IE) BIT IN THE COMMAND HEADER WORD IS SET. THIS TEST
1943				:	CHECKS THE FUNCTIONING OF THE INTERRUPT LOGIC AND BASIC
1944				:	PROCESSING OF THE IE BIT.
1945				:	
1946				:	THE SEQUENCES OF TEST 7 ARE REPEATED, EXCEPT THAT THE INTERRUPT
1947				:	SERVICE ROUTINE IS SET UP TO EXPECT INTERRUPTS AND EACH WRITE
1948				:	CHARACTERISTICS COMMAND IS ISSUED WITH THE IE BIT SET (1). IT
1949				:	IS VERIFIED, WHERE APPROPRIATE, THAT THE IE STATUS BIT IN XSTO
1950				:	OF ANY MESSAGE PACKET IS SET AND THAT A COMPLETION INTERRUPT IS
1951				:	GENERATED. FINALLY, A SEQUENCE OF TWO COMMANDS ARE ISSUED, THE
1952				:	FIRST WITH IE=1 AND THE SECOND WITH IE=0. IT IS VERIFIED THAT
1953				:	NO INTERRUPT IS GENERATED AFTER THE SECOND COMMAND AND THAT THE
1954				:	IE BIT IN XSTO IS 0.
1955	035276			:	BGNTST
	035276				T9::
1960	035276	005037	002224		;CLEAR EXTENDED FEATURES SWITCH
1961	035302	012700	040311		;ASCII MESSAGE TO IDENTIFY TEST
1962	035306	004737	016500		;DO INITIAL TEST SETUP
1963	035312	012737	000024	002214	;PERFORM 20 ITERATIONS
1964	035320			T9LOOP:	
1965	035320			BGNSUB	////////// BEGIN SUBTEST //////////
	035320				T9.1:
	035320	104402			TRAP C#BSUB
1966					
1967	035322	004737	040336		;SET PACKET TO INITIAL VALUES
1968	035326			JSR PC,T9REST	;LOWER PRIORITY TO ALLOW INTERRUPTS
	035326	012700	000000	SETPRI #PRI00	MOV #PRI00,R0
	035332	104441			TRAP C#SPRI
1969	035334	012703	002762		;START OF TEST DATA
1970	035340	012704	037240		;GET THE ADDRESS OF COMMAND PACKET
1971	035344	012764	000010	000006	;START WITH MINIMUM ALLOWABLE VALUE
1972	035352			51:	
1973	035352			BGNSEG	;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
	035352	104404			TRAP C#BSEG
1974					
1975	035354	004737	015764		;DO SOFT INIT OF CONTROLLER
1976	035360	103405		JSR PC,SOFINIT	;BR IF SOFT INIT = OK
1980	035362	010001		BCS 101	;SAVE CONTENTS OF TSSR
1981	035364			MOV R0,R1	;DEVICE FATAL ERROR DURING INIT
	035364	104455		ERRDF ERNO,SFIERR,SFIMSG	TRAP C#ERDF
	035366	001605			.WORD 901
	035370	003646			.WORD SFIERR
	035372	012024			.WORD SFIMSG
1982	035374	005037	002220	101:	;CLEAR FATAL ERROR FLAG
1983	035400	005037	002222		;CLEAR INTERRUPT RECEIVED FLAG
1984	035404	010465	000000		;SET THE PACKET ADDRESS
1985	035410	004737	016326		;WAIT FOR SSR TO SET
1986	035414	103407		JSR PC,CHKTSSR	;BR IF CARRY SET (GOOD RETURN)
1987	035416	010001		BCS 151	;SAVE CONTENTS OF TSSR
1991	035420			MOV R0,R1	;DEVICE FATAL SSR FAILED TO SET
	035420	104455		ERRDF ERNO,T9SSR,PKTSSR	TRAP C#ERDF

Year	Address	Offset	Label	Instruction	Comment	Symbol	Value
	035422	001606				.WORD	902
	035424	037757				.WORD	T9SSR
	035426	012036				.WORD	PKTSSR
1992	035430	005237	002220	INC	FATFLG		
1993	035434		15:	CKLOOP			
	035434	104406					
1994	035436			ESCAPE	SEG		
	035436	104410					
	035440	000056					
1995	035442	005737	002222	TST	INTRECV		
1996	035446	001004		BNE	22:		
2000	035450			ERRHRD	ERRNO, T9NINT, PKTSSR		
	035450	104456					
	035452	001607				TRAP	C\$ERHRD
	035454	040046				.WORD	903
	035456	012036				.WORD	T9NINT
2001	035460	016501	000002	MOV	TSSR(R5), R1		
2002	035464	012702	000200	MOV	#SSR, R2		
2003	035470	032701	000100	BIT	#OFL, R1		
2004	035474	001402		BEQ	25:		
2005	035476	052702	000100	BIS	#OFL, R2		
2006	035502	020201		CMP	R2, R1		
2007	035504	001404		BEQ	30:		
2011	035506			ERRHRD	ERRNO, T9NBA, PKTSSR		
	035506	104456					
	035510	001610				TRAP	C\$ERHRD
	035512	037316				.WORD	904
	035514	012036				.WORD	T9NBA
2012	035516					.WORD	PKTSSR
2013	035516		30:	ENDSEG			
	035516						
	035516	104405					
2014							
2015	035520	012364	000006	MOV	(R3), PKBCNT(R4)		
2016	035524	020327	003056	CMP	R3, #TBLEND		
2017	035530	103002		BHIS	55:		
2018	035532	000137	035352	JMP	5:		
2019							
2020	035536						
	035536		55:	ENDSUB			
	035536	104403					
2021							
2022	035540	005737	002220	TST	FATFLG		
2023	035544	001402		BEQ	60:		
2024	035546	004737	017172	JSR	PC, CKDROP		
2025	035552	033727	037274	BIT	T9BFR+12, #BIT7		
2026	035560	001402		BEQ	70:		
2027	035562	005237	002224	INC	EXTFEA		
2028	035566						
			70:				

TEST 9: COMPLETION INTERRUPT

PC	PC+1	PC+2	PC+3	PC+4	PC+5	PC+6	PC+7	PC+8	PC+9	PC+10	PC+11	PC+12	PC+13	PC+14	PC+15	PC+16	PC+17	PC+18	PC+19	PC+20	PC+21	PC+22	PC+23	PC+24	PC+25	PC+26	PC+27	PC+28	PC+29	PC+30	PC+31	PC+32	PC+33	PC+34	PC+35	PC+36	PC+37	PC+38	PC+39	PC+40	PC+41	PC+42	PC+43	PC+44	PC+45	PC+46	PC+47	PC+48	PC+49	PC+50	PC+51	PC+52	PC+53	PC+54	PC+55	PC+56	PC+57	PC+58	PC+59	PC+60	PC+61	PC+62	PC+63	PC+64	PC+65	PC+66	PC+67	PC+68	PC+69	PC+70	PC+71	PC+72	PC+73	PC+74	PC+75	PC+76	PC+77	PC+78	PC+79	PC+80	PC+81	PC+82	PC+83	PC+84	PC+85	PC+86	PC+87	PC+88	PC+89	PC+90	PC+91	PC+92	PC+93	PC+94	PC+95	PC+96	PC+97	PC+98	PC+99	PC+100	PC+101	PC+102	PC+103	PC+104	PC+105	PC+106	PC+107	PC+108	PC+109	PC+110	PC+111	PC+112	PC+113	PC+114	PC+115	PC+116	PC+117	PC+118	PC+119	PC+120	PC+121	PC+122	PC+123	PC+124	PC+125	PC+126	PC+127	PC+128	PC+129	PC+130	PC+131	PC+132	PC+133	PC+134	PC+135	PC+136	PC+137	PC+138	PC+139	PC+140	PC+141	PC+142	PC+143	PC+144	PC+145	PC+146	PC+147	PC+148	PC+149	PC+150	PC+151	PC+152	PC+153	PC+154	PC+155	PC+156	PC+157	PC+158	PC+159	PC+160	PC+161	PC+162	PC+163	PC+164	PC+165	PC+166	PC+167	PC+168	PC+169	PC+170	PC+171	PC+172	PC+173	PC+174	PC+175	PC+176	PC+177	PC+178	PC+179	PC+180	PC+181	PC+182	PC+183	PC+184	PC+185	PC+186	PC+187	PC+188	PC+189	PC+190	PC+191	PC+192	PC+193	PC+194	PC+195	PC+196	PC+197	PC+198	PC+199	PC+200	PC+201	PC+202	PC+203	PC+204	PC+205	PC+206	PC+207	PC+208	PC+209	PC+210	PC+211	PC+212	PC+213	PC+214	PC+215	PC+216	PC+217	PC+218	PC+219	PC+220	PC+221	PC+222	PC+223	PC+224	PC+225	PC+226	PC+227	PC+228	PC+229	PC+230	PC+231	PC+232	PC+233	PC+234	PC+235	PC+236	PC+237	PC+238	PC+239	PC+240	PC+241	PC+242	PC+243	PC+244	PC+245	PC+246	PC+247	PC+248	PC+249	PC+250	PC+251	PC+252	PC+253	PC+254	PC+255	PC+256	PC+257	PC+258	PC+259	PC+260	PC+261	PC+262	PC+263	PC+264	PC+265	PC+266	PC+267	PC+268	PC+269	PC+270	PC+271	PC+272	PC+273	PC+274	PC+275	PC+276	PC+277	PC+278	PC+279	PC+280	PC+281	PC+282	PC+283	PC+284	PC+285	PC+286	PC+287	PC+288	PC+289	PC+290	PC+291	PC+292	PC+293	PC+294	PC+295	PC+296	PC+297	PC+298	PC+299	PC+300	PC+301	PC+302	PC+303	PC+304	PC+305	PC+306	PC+307	PC+308	PC+309	PC+310	PC+311	PC+312	PC+313	PC+314	PC+315	PC+316	PC+317	PC+318	PC+319	PC+320	PC+321	PC+322	PC+323	PC+324	PC+325	PC+326	PC+327	PC+328	PC+329	PC+330	PC+331	PC+332	PC+333	PC+334	PC+335	PC+336	PC+337	PC+338	PC+339	PC+340	PC+341	PC+342	PC+343	PC+344	PC+345	PC+346	PC+347	PC+348	PC+349	PC+350	PC+351	PC+352	PC+353	PC+354	PC+355	PC+356	PC+357	PC+358	PC+359	PC+360	PC+361	PC+362	PC+363	PC+364	PC+365	PC+366	PC+367	PC+368	PC+369	PC+370	PC+371	PC+372	PC+373	PC+374	PC+375	PC+376	PC+377	PC+378	PC+379	PC+380	PC+381	PC+382	PC+383	PC+384	PC+385	PC+386	PC+387	PC+388	PC+389	PC+390	PC+391	PC+392	PC+393	PC+394	PC+395	PC+396	PC+397	PC+398	PC+399	PC+400	PC+401	PC+402	PC+403	PC+404	PC+405	PC+406	PC+407	PC+408	PC+409	PC+410	PC+411	PC+412	PC+413	PC+414	PC+415	PC+416	PC+417	PC+418	PC+41
----	------	------	------	------	------	------	------	------	------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	--------	-------

Address	Offset	Hex	Dec	Label	Instruction	Comment	Trap	Code
2076	035724	012702	102206		MOV	#SC!SSR!TSREJ!NBA,R2		
2077	035730	032701	000100		BIT	#OFL,R1		
2078	035734	001402			BEQ	25:		
2079	035736	052702	000100		BIS	#OFL,R2		
2080	035742	020201		25:	CMP	R2,R1		
2081	035744	001404			BEQ	30:		
2085	035746				ERRHRD	ERRNO,T92REJ,PKTSSR		
	035746	104456					TRAP	C\$ERHRD
	035750	001614					.WORD	908
	035752	037371					.WORD	T92REJ
	035754	012036					.WORD	PKTSSR
2086	035756			30:				
2087	035756				ENDSEG			
	035756							
	035756	104405					TRAP	C\$ESEG
2088								
2089	035760	062703	000004		ADD	#4,R3		
2090	035764	020327	037316		CMP	R3,#T92DONE		
2091	035770	103002			BHIS	59:		
2092	035772	000137	035602		JMP	5:		
2093								
2094	035776			59:	ENDSUB			
	035776							
	035776	104403					TRAP	C\$ESUB

2096				;*
2097				;
2098				;TEST 9, SUBTEST 3
2099				;
2100				;CHECK THE WRITE CHARACTERISTICS COMMAND IS REJECTED
2101				;IF ISSUED WITH AN INVALID DATA BLOCK BYTE COUNT
2102				;
2103				;-
2104				
2105	036000		BGNSUB	;/!!!!!!!!!!!!!! BEGIN SUBTEST !!!!!!!!!!!!!!!
	036000			T9.3:
	036000	104402		TRAP C\$BSUB.
2106				
2107	036002		SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS.
	036002	012700	000000	MOV #PRI00,R0
	036006	104441		TRAP C\$SPRI
2108	036010	012703	000001	;STARTING BYTE COUNT
2109	036014	012704	037240	;GET THE ADDRESS OF COMMAND PACKET
2110	036020	004737	040336	;RESTORE PACKET TO STARTING VALUES
2111				
2112	036024		BGNSEG	;>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>
	036024	104404		TRAP C\$BSEG
2113				
2114	036026	004737	015764	JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER
2115	036032	103405		BCS 10\$;BR IF SOFT INIT = OK
2119	036034	010001		MOV R0,R1 ;SAVE CONTENTS OF TSSR
2120	036036		ERRDF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT	
	036036	104455		TRAP C\$ERDF
	036040	001615		.WORD 909
	036042	003646		.WORD SFIERR
	036044	012024		.WORD SFIMSG
2121	036046	005037	002222	10\$: CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
2122	036052	010364	000006	MOV R3,PKBcnt(R4) ;INSERT THE BYTE COUNT FOR TEST
2123	036056	010465	000000	MOV R4,TSDb(R5) ;SET THE PACKET ADDRESS
2124	036062	004737	016240	JSR PC,WAITF ;WAIT FOR SSR TO SET
2125	036066	103405		BCS 15\$;BR IF CARRY SET (GOOD RETURN)
2126	036070	010001		MOV R0,R1 ;SAVE CONTENTS OF TSSR
2130	036072		ERRDF ERRNO,T9SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET	
	036072	104455		TRAP C\$ERDF
	036074	001616		.WORD 910
	036076	037757		.WORD T9SSR
	036100	012036		.WORD PKTSSR
2131	036102			15\$: CKLOOP ;LOOP ON ERROR, IF FLAG SET
	036102	104406		TRAP C\$CLP1
2132	036104		FESCAPE SEG ;BY-PASS SUBTEST IF FATAL ERROR	
	036104	104410		TRAP C\$ESCAPE
	036106	000056		.WORD 10000\$.
2133	036110	005737	002222	TST INTRECV ;DID AN INTERRUPT OCCUR ?
2134	036114	001004		BNE 22\$;BRANCH IF YES
2138	036116		ERRHRD ERRNO,T9NINT,PKTSSR	
	036116	104456		TRAP C\$ERHRD
	036120	001617		.WORD 911
	036122	040046		.WORD T9NINT
	036124	012036		.WORD PKTSSR
2139	036126	016501	000002	22\$: MOV TSSR(R5),R1 ;GET THE CONTENTS OF TSSR
2140	036132	012702	102206	MOV #SC!SSR!TSREJ!NBA,R2 ;EXPECTED CONTENTS OF TSSR
2141	036136	032701	000100	BIT #OFL,R1 ;IS OFF LINE BIT SET ?

TEST 9: COMPLETION INTERRUPT

[illegible]

TEST 9: COMPLETION INTERRUPT

```

2160
2161
2162
2163
2164
2165
2166
2167
2168
2169 036204          BGNSUB                      ;//////////////// BEGIN SUBTEST //////////////////
      036204          T9.4:                      TRAP      C$BSUB
      036204 104402
2170
2171 036206          SETPRI  #PRI00              ;LOWER PRIORITY TO ALLOW INTERRUPTS
      036206 012700 000000                      MOV      #PRI00,R0
      036212 104441                      TRAP      C$SPRI
2172 036214 012703 037302                      ;START OF TEST DATA FOR SUBTEST
2173 036220 012704 037240                      ;GET THE ADDRESS OF COMMAND PACKET
2174 036224 004737 040336                      ;RESTORE PACKET TO STARTING VALUES
2175
2176
2177 036230 004737 015764                      JS      PC,SOFINIT
2178 036234 103405                      BCS     10$
2182 036236 010001                      MOV     R0,R1
2183 036240          ERRDF  ERRNO,SFIERR,SFIMSG  ;DO SOFT INIT OF CONTROLLER
      036240 104455                      ;BR IF SOFT INIT - OK
      036242 001621                      ;SAVE CONTENTS OF TSSR
      036244 003646                      ;DEVICE FATAL ERROR DURING INIT
      036246 012024                      TRAP      C$ERDF
2184 036250 005037 002222                      .WORD    913
2185 036254 052737 000001 037250 10$: CLR      INTRECV          ;CLEAR INTERRUPT RECEIVED FLAG
2186 036262 010465 000000                      BIS      #1,T9DATA
2187 036266 004737 016240                      MOV     R4,TSDB(R5)
2188 036272 103405                      JSR     PC,WAITF
2189 036274 010001                      BCS     15$
2193 036276          MOV     R0,R1
      036276 104455                      ;SAVE CONTENTS OF TSSR
      036300 001622                      ;DEVICE FATAL SSR FAILED TO SET
      036302 037757                      TRAP      C$ERDF
      036304 012036                      .WORD    914
2194 036306          15$: CKLOOP                      ;LOOP ON ERROR, IF FLAG SET
      036306 104406                      TRAP      C$CLP1
2195 036310          ESCAPE SUB                      ;BY-PASS SUBTEST IF FATAL ERROR
      036310 104410                      TRAP      C$ESCAPE
      036312 000056                      .WORD    L10071
2196 036314 005737 002222                      TST     INTRECV
2197 036320 001004                      BNE     22$
2201 036322          ERRHRD  ERRNO,T9NINT,PKTSSR  ;DID AN INTERRUPT OCCUR ?
      036322 104456                      ;BRANCH IF YES
      036324 001623                      TRAP      C$ERHRD
      036326 040046                      .WORD    915
      036330 012036                      .WORD    T9NINT
2202 036332 016501 000002                      .WORD    PKTSSR
2203 036336 012702 102206                      22$: MOV     TSSR(R5),R1
2204 036342 032701 000100                      MOV     #SC!SSR!TSREJ!NBA,R2
2205 036346 001402                      BIT     #OFL,R1
2206 036350 052702 000100                      BEQ     25$
      036350          BIS      #OFL,R2
      036350          ;GET THE CONTENTS OF TSSR
      036350          ;EXPECTED CONTENTS OF TSSR
      036350          ;IS OFF-LINE BIT SET ?
      036350          ;BRANCH IF NOT OFF-LINE
      036350          ;SET OFF-LINE IN EXPECTED DATA

```


TEST 9: COMPLETION INTERRUPT

SEQ 0177

2207 036354 020201
2208 036356 001404
2212 036360
036360 104456
036362 001624
036364 037563
036366 012036
2213 036370
2214
2215 036370
036370
036370 104403
2216

25\$: CMP R2,R1
BEQ 30\$
ERRHRD ERRNO,T94REJ,PKTSSR

30\$:

ENDSUB

; DOES EXPECTED MATCH RECEIVED ?
; OKAY IF MATCH
; COMMAND NOT REJECTED

TRAP C\$ERHRD
.WORD 916
.WORD T94REJ
.WORD PKTSSR

; //////////// END SUBTEST ////////////
L10071:

TRAP C\$ESUB

TEST 9: COMPLETION INTERRUPT

2218					;*
2219					;
2220					;TEST 9, SUBTEST 5
2221					;
2222					;CHECK THAT WRITE CHARACTERISTICS COMMAND IS REJECTED IF THE
2223					;MESSAGE BUFFER LENGTH SPECIFIES AN INVALID COUNT (LESS THAN 14)
2224					;
2225					;-
2226					
2227	036372			BGNSUB	;/!!!!!!!!!!!!!! BEGIN SUBTEST !!!!!!!!!!!!!!!
	036372				T9.5:
	036372	104402			TRAP C\$BSUB
2228					
2229	036374			SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	036374	012700	000000		MOV #PRI00,R0
	036400	104441			TRAP C\$SPRI
2230	036402	012703	000001		;STARTING BUFFER LENGTH
2231	036406	012704	037240	5\$:	MOV #T9PACKET,R4
2232	036412	004737	040336		JSR PC,T9REST
2233					;GET THE ADDRESS OF COMMAND PACKET
2234	036416				;RESTORE PACKET TO STARTING VALUES
	036416	104404		BGNSEG	;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
2235					TRAP C\$BSEG
2236	036420	004737	015764		
2237	036424	103405		JSR PC,SOFINIT	;DO SOFT INIT OF CONTROLLER
2241	036426	010001		BCS 10\$	;BR IF SOFT INIT = OK
2242	036430			MOV R0,R1	;SAVE CONTENTS OF TSSR
	036430	104455		ERRDF ERRNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT
	036432	001625			TRAP C\$ERDF
	036434	003646			.WORD 917
	036436	012024			.WORD SFIERR
2243	036440	005037	002222	10\$:	.WORD SFIMSG
2244	036444	010337	037254		
2245	036450	010465	000000		CLR INTRECV
2246	036454	004737	016240		MOV R3,T9DATA+4
2247	036460	103405			MOV R4,TSDB(R5)
2248	036462	010001			JSR PC,WAITF
2252	036464				BCS 15\$
	036464	104455			MOV R0,R1
	036466	001626			ERRDF ERRNO,T9SSR,PKTSSR
	036470	037757			;CLEAR INTERRUPT RECEIVED FLAG
	036472	012036			;INSERT THE BAD MESSAGE LENGTH
2253	036474			15\$:	;SET THE PACKET ADDRESS
	036474	104406			;WAIT FOR SSR TO SET
2254	036476				;BR IF CARRY SET (GOOD RETURN)
	036476	104410			;SAVE CONTENTS OF TSSR
	036500	000056			;DEVICE FATAL SSR FAILED TO SET
2255	036502	005737	002222		TRAP C\$ERDF
2256	036506	001004			.WORD 918
2260	036510				.WORD T9SSR
	036510	104456			.WORD PKTSSR
	036512	001627			
	036514	040046			CKLOOP
	036516	012036			;LOOP ON ERROR, IF FLAG SET
2261	036520	016501	000002	22\$:	TRAP C\$CLP1
2262	036524	012702	102206		ESCAPE SEG
2263	036530	032701	000100		;BY-PASS SUBTEST IF FATAL ERROR
					TRAP C\$ESCAPE
					.WORD 10000\$.
					TST INTRECV
					;DID AN INTERRUPT OCCUR ?
					BNE 22\$
					;BRANCH IF YES
					ERRHRD ERRNO,T9NINT,PKTSSR
					TRAP C\$ERHRD
					.WORD 919
					.WORD T9NINT
					.WORD PKTSSR
					MOV TSSR(R5),R1
					;GET THE CONTENTS OF TSSR
					MOV #SC!SSR!TSREJ!NBA,R2
					;EXPECTED CONTENTS OF TSSR
					BIT #OFL,R1
					;IS OFF LINE BIT SET ?

TEST 9: COMPLETION INTERRUPT

Address	Offset	Hex	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op41
---------	--------	-----	-------	----	-----	-----	-----	-----	-----	-----	-----	-----	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	-------	------

TEST 9: COMPLETION INTERRUPT

2282				;*
2283				;
2284				;TEST 9, SUBTEST 6
2285				;
2286				;THIS SUBTEST IS EXECUTED ONLY IF THE EXTENDED
2287				;FEATURES MODE IS ENABLED (AS DETERMINED BY EXAMINING
2288				;XST2 AFTER A PREVIOUS EXECUTION OF WRITE CHARACTERISTICS).
2289				;IT VERIFIES THAT A FIFTH CHARACTERISTICS DATA WORD IS FETCHED
2290				;IF THE BYTE COUNT PARAMETER IN THE COMMAND PACKET IS 10 DECIMAL
2291				;OR GREATER.
2292				;
2293				;-
2294	036576			BGNSUB
	036576			;/:::::::::::: BEGIN SUBTEST ::::::::::::::/
	036576	104402		T9.6:
2295	036600	005737	002224	TRAP C\$BSUB
2296	036604	001002		;IS EXTENDED FEATURES SOFT. SW SET?
2297	036606	000137	037026	;BR, IF SOFTWARE SWITCH IS SET (ON)
2298	036612	004737	040336	;EXIT SUBTEST
2299				;SET PACKET TO START-UP VALUES
2300	036616			
	036616	012700	000000	;LOWER PRIORITY TO ALLOW INTERRUPTS
	036622	104441		MOV #PRI00,R0
2301	036624	012703	002762	TRAP C\$SPRI
2302	036630	012704	037240	
2303	036634	012764	000012 000006	MOV #TSTBLK+10.,R3 ;START OF TEST DATA
2304	036642			MOV #T9PACKET,R4 ;GET THE ADDRESS OF COMMAND PACKET
2305	036642			MOV #10.,PKBCNT(R4) ;START WITH EXTENDED FEATURES VALUE
	036642	104404		5\$: BGNSEG ;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
2306				TRAP C\$BSEG
2307	036644	004737	015764	
2308	036650	103405		JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER
2312	036652	010001		BCS 10\$;BR IF SOFT INIT = OK
2313	036654			MOV R0,R1 ;SAVE CONTENTS OF TSSR
	036654	104455		ERRDF ERNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
	036656	001631		TRAP C\$ERDF
	036660	003646		.WORD 921
	036662	012024		.WORD SFIERR
2314	036664	005037	002220	.WORD SFIMSG
2315	036670	005037	002222	10\$: CLR FATFLG ;CLEAR FATAL ERROR FLAG
2316	036674	010465	000000	CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
2317	036700	004737	016326	MOV R4,TSD8(R5) ;SET THE PACKET ADDRESS
2318	036704	103407		JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
2319	036706	010001		BCS 15\$;BR IF CARRY SET (GOOD RETURN)
2323	036710			MOV R0,R1 ;SAVE CONTENTS OF TSSR
	036710	104455		ERRDF ERNO,T9SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
	036712	001632		TRAP C\$ERDF
	036714	037757		.WORD 922
	036716	012036		.WORD T9SSR
2324	036720	005237	002220	.WORD PKTSSR
2325	036724			15\$: INC FATFLG ;SET FATAL ERROR FLAG
	036724	104406		CKLOOP ;LOOP ON ERROR, IF FLAG SET
2326	036726			ESCAPE SEG ;BY-PASS SUBTEST IF FATAL ERROR
	036726	104410		TRAP C\$CLP1
	036730	000056		.WORD C\$ESCAPE
2327	036732	005737	002222	.WORD 10000\$-
2328	036736	001004		TST INTRECV ;DID AN INTERRUPT OCCUR ?
				BNE 22\$;BRANCH IF YES

TEST 9: COMPLETION INTERRUPT

[illegible]

TEST 9: COMPLETION INTERRUPT

```

2354
2355
2356
2357
2358
2359
2360
2361
2362
2363 037030          BGNSUB                      ;////////// BEGIN SUBTEST ////////////
      037030          T9.7:                      TRAP      C$BSUB
      037030 104402
2364 037032          SETPRI  #PRI00              ;LOWER PRIORITY TO ALLOW INTERRUPTS
      037032 012700 000000                      MOV        #PRI00,R0
      037036 104441                      TRAP      C$SPRI
2366 037040 012704 037240          MOV      #T9PACKET,R4      ;GET THE ADDRESS OF COMMAND PACKET
2367 037044 004737 040336          JSR      PC,T9REST        ;SET UP A VALID PACKET
2368 037050 004737 015764          JSR      PC,SOFINIT       ;DO SOFT INIT OF CONTROLLER
2369 037054 103405                      BCS      10$          ;BR IF SOFT INIT = OK
2373 037056 010001                      MOV      R0,R1        ;SAVE CONTENTS OF TSSR
2374 037060          ERRDF  ERRNO,SFIERR,SFIMSG          ;DEVICE FATAL ERROR DURING INIT
      037060 104455                      TRAP      C$ERDF
      037062 001635                      .WORD    925
      037064 003646                      .WORD    SFIERR
      037066 012024                      .WORD    SFIMSG
2375 037070 005037 002222          10$:  CLR      INTRECV      ;CLEAR INTERRUPT RECEIVED FLAG
2376 037074 052714 000200          BIS      #BIT7,(R4)        ;ENABLE INTERRUPTS
2377 037100 010465 000000          MOV      R4,TSDB(R5)        ;SET THE PACKET ADDRESS
2378 037104 004737 016326          JSR      PC,CHKTSSR        ;WAIT FOR SSR TO SET
2379 037110 103405                      BCS      15$          ;BR IF CARRY SET (GOOD RETURN)
2380 037112 010001                      MOV      R0,R1        ;SAVE CONTENTS OF TSSR
2384 037114          ERRDF  ERRNO,T9SSR,PKTSSR          ;DEVICE FATAL SSR FAILED TO SET
      037114 104455                      TRAP      C$ERDF
      037116 001636                      .WORD    926
      037120 037757                      .WORD    T9SSR
      037122 012036                      .WORD    PKTSSR
2385 037124          15$:  CKLOOP                  ;LOOP ON ERROR, IF FLAG SET
      037124 104406                      TRAP      C$CLP1
2386 037126          ESCAPE  SUB                  ;BY-PASS SUBTEST IF FATAL ERROR
      037126 104410                      TRAP      C$ESCAPE
      037130 000102                      .WORD    L10074-.
2387 037132 005737 002222          TST      INTRECV          ;DID AN INTERRUPT OCCUR ?
2388 037136 001004          BNE      22$          ;BRANCH IF YES
2392 037140          ERRHRD  ERRNO,T9NINT,PKTSSR
      037140 104456                      TRAP      C$ERHRD
      037142 001637                      .WORD    927
      037144 040046                      .WORD    T9NINT
      037146 012036                      .WORD    PKTSSR
2393 037150          22$:  CKLOOP                  ;LOOP ON ERROR ?
      037150 104406                      TRAP      C$CLP1
2394
2395 037152 005037 002222          CLR      INTRECV          ;CLEAR INTERRUPT RECEIVED FLAG
2396 037156 042714 000200          BIC      #BIT7,(R4)        ;DISABLE INTERRUPTS
2397 037162 010465 000000          MOV      R4,TSDB(R5)        ;SET THE PACKET ADDRESS
2398 037166 004737 016326          JSR      PC,CHKTSSR        ;WAIT FOR SSR TO SET
2399 037172 103405                      BCS      25$          ;BR IF CARRY SET (GOOD RETURN)

```

TEST 9: COMPLETION INTERRUPT

```

2400 037174 010001      MOV      R0,R1      ;SAVE CONTENTS OF TSSR
2404 037176      ERRDF   ERRNO,T9SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
                                TRAP      C$ERRDF
                                .WORD     928
                                .WORD     T9SSR
                                .WORD     PKTSSR
2405 037206      251:    CKLOOP      ;LOOP ON ERROR, IF FLAG SET
                                TRAP      C$CLP1
2406 037210      ESCAPE  SUB      ;BY PASS SUBTEST IF FATAL ERROR
                                TRAP      C$ESCAPE
                                .WORD     L10074
2407 037214 005737 002222      TST      INTRECV      ;DID AN INTERRUPT OCCUR ?
2408 037220 001404      BEQ      301:                ;BRANCH IF NOT
2412 037222      ERRHRD  ERRNO,T9INT,PKTSSR
                                TRAP      C$ERHRD
                                .WORD     929
                                .WORD     T9INT
                                .WORD     PKTSSR
2413 037232      301:    ENDSUB
                                ;////////// END SUBTEST //////////
                                L10074:
2414 037232      104403      TRAP      C$ESUB
2415 037234      EXIT      TST      ;ALL DONE THIS TEST
2416 037234 104432      TRAP      C$EXIT
2417 037236 001152      .WORD     L10065-
2418
2419      ;*
2420      ;LOCAL STORAGE FOR THIS TEST
2421      ;
2425 037240      T9PACKET:
2426 037240 100204      .WORD     100204      ;COMMAND PACKET FOR TEST
2427 037242 037250      .WORD     T9DATA      ;WRITE CHAR COMMAND, WITH IE, ACK
2428 037244 000000      .WORD     0          ;ADDRESS OF CHARACTERISTICS BLOCK
2429 037246 000010      .WORD     8          ;STARTING VALUE OF BLOCK SIZE
2430
2431 037250      T9DATA:
2432 037250 037262      .WORD     T9BFR      ;CHARACTERISTICS DATA BLOCK
2433 037252 000000      .WORD     0          ;ADDRESS OF MESSAGE BUFFER
2434 037254 000016      .WORD     14         ;LENGTH OF MESSAGE BUFFER
2435 037256 000000 000000      .WORD     0,0
2436
2437 037262      T9BFR:   .BLKW     8          ;MESSAGE BUFFER
2438
2439      ;*
2440      ;
2441      ;TEST DATA FOR SUBTEST TWO
2442      ;
2443      ;DATA HAS FORMAT:
2444      ;
2445      ;      1ST WORD      OFFSET TO TEST WORD IN PACKET
2446      ;      2ND WORD      BITS TO SET FOR TEST
2447      ;
2448      ;
2449
2450 037302      T92DATA:

```

TEST 9: COMPLETION INTERRUPT

```

2451 037302 000000 037140 .WORD 0,BIT5!BIT6!BIT9!BIT10!BIT11!BIT12!BIT13
2452 037306 000002 000001 .WORD 2,BIT0
2453 037312 000004 100100 .WORD 4,BIT6!BIT15
2454 037316 T92DONE=.
2455
2456
2457
2458 ;*
2459 ;LOCAL TEXT MESSAGES FOR TEST
2460 ;-
2461 037316 127 122 111 T9NBA: .ASCIZ 'WRITE CHARACTERISTICS Command Not Accepted'
2462 037371 127 122 111 T92REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Non-Zero Unused Fields'
2463 037470 127 122 111 T93REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Data Count'
2464 037563 127 122 111 T94REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Block Address'
2465 037661 127 122 111 T95REJ: .ASCIZ 'WRITE CHARACTERISTICS Not Rejected With Invalid Buffer Length'
2466 037757 103 157 156 T9SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
2467 040046 105 170 160 T9NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
2468 040137 125 156 145 T9INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
2469 040226 111 156 143 T9TSBA: .ASCIZ 'Incorrect TSBA Address After WRITE CHARACTERISTICS'
2470 040311 103 157 155 T9T9ID: .ASCIZ 'Completion Interrupt'
2471 .EVEN
2472

```


TEST 9: COMPLETION INTERRUPT

```

2474
2475
2476
2477
2478
2479
2480
2481 040336
2482 040336
2483 040342 012701 037240
2484 040346 012721 100204
2485 040352 012721 037250
2486 040356 005021
2487 040360 012721 000010
2488 040364 012721 037262
2489 040370 005021
2490 040372 012721 000016
2491 040376 005021
2492 040400 005011
2493 040402 005037 037262
2494 040406 000207
2495 040410
      040410
      040410 104401

```

```

; *
;
; ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
;
; -
T9REST:
      SAVREG
      MOV     #T9PACKET,R1      ;SAVE THE REGISTERS
      MOV     #100204,(R1).     ;START OF THE PACKET
      MOV     #T9DATA,(R1).     ;WRITE CHARACTERISTICS WITH ACK, IE
      MOV     #T98FR,(R1).      ;ADDRESS OF CHAR DATA BLOCK
      CLR     (R1).             ;EXTENDED ADDRESS
      MOV     #8,(R1).          ;SIZE OF DATA BLOCK IN BYTES
      MOV     #T98FR,(R1).      ;ADDRESS OF MESSAGE BUFFER
      CLR     (R1).             ;LENGTH OF MESSAGE BUFFER
      MOV     #14,(R1).
      CLR     (R1).
      CLR     (R1)
      CLR     T98FR             ;CLEAR 1ST LOC IN MESSAGE BUFFER
      RTS     PC                ;RETURN
      ENDTST

```

```

L10065: TRAP C$ETST

```

TEST 10: BASIC PACKET PROTOCOL

2497			.SBTTL TEST 10: BASIC PACKET PROTOCOL	
2498				
2499			: THIS TEST VERIFIES BASIC OPERATION OF THE MESSAGE BUFFER RELEASE	
2500			: COMMAND, THE FUNCTION OF THE ACK BIT IN THE COMMAND HEADER WORD,	
2501			: AND THE REGISTER MODIFICATION REFUSED (RMR) LOGIC.	
2502			:	
2503			:	
2504			:	
2505			:*	
2506			;TEST 10 SUBTEST 1	
2507			:	
2508			;CHECKS THAT THE MESSAGE BUFFER RELEASE COMMAND WORKS	
2509			;PROPERLY AND THAT NO INTERRUPT IS GENERATED EVEN	
2510			;IF THE "IE" BIT IS SET IN THE COMMAND PACKET	
2511			:	
2512	040412		BGNTST	
	040412			T10::
2517	040412	012700 043337	MOV #TST1OID,R0	;ASCII MESSAGE TO IDENTIFY TEST
2518	040416	004737 016500	JSR PC,TSTSETUP	;DO INITIAL TEST SETUP
2519	040422	012737 000024 002214	MOV #20.,LOOPCNT	;PERFORM 20 ITERATIONS
2520	040430		T10LOOP:	
2521				
2522	040430		BGNSUB	;////////// BEGIN SUBTEST //////////
	040430			T10.1:
	040430	104402		TRAP C\$BSUB
2523				
2524	040432	004737 043366	JSR PC,T10RST	;SET PACKET TO INITIAL VALUES
2525	040436		SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	040436	012700 000000		MOV #PRI00,R0
	040442	104441		TRAP C\$SPRI
2526	040444	012704 042530	MOV #T10PACKET,R4	;GET THE ADDRESS OF COMMAND PACKET
2527	040450	012764 000010 000006	MOV #8.,PKBCNT(R4)	;START WITH MINIMUM ALLOWABLE VALU
2528	040456		5\$:	
2529	040456		BGNSEG	;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
	040456	104404		TRAP C\$BSEG
2530				
2531	040460	004737 015764	JSR PC,SOFINIT	;DO SOFT INIT OF CONTROLLER
2532	040464	103405	BCS 10\$	;BR IF SOFT INIT = OK
2536	040466	010001	MOV R0,R1	;SAVE CONTENTS OF TSSR
2537	040470		ERRDF ERNO,SFIERR,SFIMSG	;DEVICE FATAL ERROR DURING INIT
	040470	104455		TRAP C\$ERDF
	040472	001751		.WORD 1001
	040474	003646		.WORD SFIERR
	040476	012024		.WORD SFIMSG
2538	040500	005037 002220	10\$: CLR FATFLG	;CLEAR FATAL ERROR FLAG
2539	040504	005037 002222	CLR INTRECV	;CLEAR INTERRUPT RECEIVED FLAG
2540	040510	010465 000000	MOV R4,TSD8(R5)	;SET THE PACKET ADDRESS
2541	040514	004737 016326	JSR PC,CHKTSSR	;WAIT FOR SSR TO SET
2542	040520	103407	BCS 15\$	;BR IF CARRY SET (GOOD RETURN)
2543	040522	010001	MOV R0,R1	;SAVE CONTENTS OF TSSR
2547	040524		ERRDF ERNO,T10SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET
	040524	104455		TRAP C\$ERDF
	040526	001752		.WORD 1002
	040530	043070		.WORD T10SSR
	040532	012036		.WORD PKTSSR
2548	040534	005237 002220	15\$: INC FATFLG	;SET FATAL ERROR FLAG
2549	040540		CKLOOP	;LOOP ON ERROR, IF FLAG SET

TEST 10: BASIC PACKET PROTOCOL

2550	040540	104406		ESCAPE	SEG		TRAP	C\$CLP1
	040542						BY-PASS SUBTEST IF FATAL ERROR	
	040542	104410					TRAP	C\$ESCAPE
	040544	000056					.WORD	10000\$.
2551	040546	005737	002222	TST	INTRECV		DID AN INTERRUPT OCCUR ?	
2552	040552	001004		BNE	22\$		BRANCH IF YES	
2556	040554			ERRHRD	ERRNO,T10NINT,PKTSSR			
	040554	104456					TRAP	C\$ERHRD
	040556	001753					.WORD	1003
	040560	043157					.WORD	T10NINT
	040562	012036					.WORD	PKTSSR
2557	040564	016501	000002	22\$:	MOV	TSSR(R5),R1	GET THE CONTENTS OF TSSR	
2558	040570	012702	000200		MOV	\$SSR,R2	EXPECTED CONTENTS OF TSSR	
2559	040574	032701	000100		BIT	\$OFL,R1	IS OFF LINE BIT SET ?	
2560	040600	001402			BEQ	25\$	BRANCH IF NOT OFF-LINE	
2561	040602	052702	000100		BIS	\$OFL,R2	SET OFF-LINE IN EXPECTED DATA	
2562	040606	020201		25\$:	CMP	R2,R1	DOES EXPECTED MATCH RECEIVED ?	
2563	040610	001404			BEQ	30\$	OKAY IF MATCH	
2567	040612				ERRHRD	ERRNO,T10NBA,PKTSSR	NBA NOT ZERO	
	040612	104456					TRAP	C\$ERHRD
	040614	001754					.WORD	1004
	040616	042731					.WORD	T10NBA
	040620	012036					.WORD	PKTSSR
2568	040622			30\$:				
2569	040622				ENDSEG		END SEGMENT	
	040622						10000\$:	
	040622	104405					TRAP	C\$ESEG
2570	040624				BGNSEG		BEGIN SEGMENT	
	040624	104404					TRAP	C\$BSEG
2571	040626	005037	002222		CLR	INTRECV	CLEAR INTERRUPT RECEIVED FLAG	
2573	040632	012737	025252	042552	MOV	\$025252,T10BFR	WIPE OUT MESSAGE BUFFER AREA	
2574	040640	012714	100212		MOV	\$100212,(R4)	SET COMMAND PACKET TO MESS BUF REL	
2575	040644	010465	000000		MOV	R4,TSD8(R5)	SET THE PACKET ADDRESS	
2576	040650	004737	016326		JSR	PC,CHKTSSR	WAIT FOR SSR TO SET	
2577	040654	103407			BCS	45\$	BR IF CARRY SET (GOOD RETURN)	
2578	040656	010001			MOV	R0,R1	SAVE CONTENTS OF TSSR	
2582	040660				ERRDF	ERRNO,T10SSR,PKTSSR	DEVICE FATAL SSR FAILED TO SET	
	040660	104455					TRAP	C\$ERDF
	040662	001755					.WORD	1005
	040664	043070					.WORD	T10SSR
	040666	012036					.WORD	PKTSSR
2583	040670	005237	002220		INC	FATFLG	SET FATAL ERROR FLAG	
2584	040674			45\$:	CKLOOP		LOOP ON ERROR, IF FLAG SET	
	040674	104406					TRAP	C\$CLP1
2585	040676	005737	002222		TST	INTRECV	DID AN INTERRUPT OCCUR ?	
2586	040702	001404			BEQ	52\$	BRANCH IF NO	
2590	040704				ERRHRD	ERRNO,T10INT,PKTSSR		
	040704	104456					TRAP	C\$ERHRD
	040706	001756					.WORD	1006
	040710	043250					.WORD	T10INT
	040712	012036					.WORD	PKTSSR
2591	040714	016501	000002	52\$:	MOV	TSSR(R5),R1	GET THE CONTENTS OF TSSR	
2592	040720	012702	000200		MOV	\$SSR,R2	EXPECTED CONTENTS OF TSSR	
2593	040724	032701	000100		BIT	\$OFL,R1	IS OFF LINE BIT SET ?	
2594	040730	001402			BEQ	55\$	BRANCH IF NOT OFF LINE	
2595	040732	052702	000100		BIS	\$OFL,R2	SET OFF LINE IN EXPECTED DATA	

TEST 10: BASIC PACKET PROTOCOL

2596	040736	020201		55\$:	CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
2597	040740	001404			BEQ	60\$	;OKAY IF MATCH		
2601	040742				ERRHRD	ERRNO,T10NNBA,PKTSSR	;NBA SET		
	040742	104456						TRAP	C\$ERHRD
	040744	001757						.WORD	1007
	040746	043013						.WORD	T10NNBA
	040750	012036						.WORD	PKTSSR
2602	040752			60\$:					
2603	040752	013701	042552		MOV	T10BFR,R1	;PICK UP THE 1ST WORD OF MESSAGE BUFFER		
2604	040756	012702	025252		MOV	#025252,R2	;SET UP EXPECTED DATA		
2605	040762	020102			CMP	R1,R2	;WAS ANY MESSAGE REC'D		
2606	040764	001404			BEQ	70\$	;BR, IF OK (EQUAL)		
2610	040766				ERRHRD	ERRNO,T10MBF,EXPREC	;MESSAGE BUFFER WAS MODIFIED		
	040766	104456						TRAP	C\$ERHRD
	040770	001760						.WORD	1008
	040772	042634						.WORD	T10MBF
	040774	015464						.WORD	EXPREC
2611	040776			70\$:					
2612	040776	005737	002220		TST	FATFLG	;ANY FATAL ERRORS		
2613	041002	001403			BEQ	80\$	;BR, IF NO FATAL ERRORS		
2614	041004	004737	017172		JSR	PC,CKDROP	;TRY TO DROP THE UNIT		
2615	041010				ENDSEG		;<<<<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<<<<		
	041010						10001\$:		
	041010	104405						TRAP	C\$ESEG
2616	041012			80\$:					
2617	041012				ENDSUB		;////////// END SUBTEST //////////		
	041012						L10076:		
2618	041012	104403						TRAP	C\$ESUB

TEST 10: BASIC PACKET PROTOCOL

Line	Address	Hex	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op417	Op418	Op419	Op420	Op421	Op422	Op423	Op424	Op425	Op426	Op427	Op428	Op429	Op430	Op431	Op432	Op433	Op434	Op435	Op436	Op437	Op438	Op439	Op440	Op441	Op442	Op443	Op444	Op445	Op446	Op447	Op448	Op449	Op450	Op451	Op452	Op453	Op454	Op455	Op456	Op457	Op458	Op459	Op460	Op461	Op462	Op463	
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Line	Address	Offset	Label	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op417	Op418	Op419	Op420	Op421	Op422	Op423	Op424	Op425	Op426	Op427	Op428	Op429	Op430	Op431	Op432	Op433	Op434	Op435	Op436	Op437	Op438	Op439	Op440	Op441	Op442	Op443	Op444	Op445	Op446	Op447	Op448	Op449	Op450	Op451	Op452	Op453	Op454	Op455	Op456	Op457	Op458	Op459	Op460	Op461	Op462	Op463	
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SEQ 0191

Line	Address	Offset	Label	Instruction	Comment
2718	041360			ERRHRD	ERRNO, T10MBF, EXPREC
	041360	104456			
	041362	001770			
	041364	042634			
	041366	015464			
2719					
2720	041370				
2721	041370	005737	002220		
2722	041374	001402		TST	FATFLG
2723	041376	004737	017172	BEQ	80\$
2724	041402			JSR	PC, CKDROP
2725	041402				
	041402			ENDSEG	
	041402	104405			
2726	041404			ENDSUB	
	041404				
	041404	104403			

TEST 10: BASIC PACKET PROTOCOL

2728					;*
2729					;TEST 10 SUBTEST 3
2730					;
2731					;CHECKS THAT THE CPU GIVES UP OWNERSHIP OF THE MESSAGE BUFFER
2732					;AFTER THE MESSAGE BUFFER RELEASE, AND THAT FOLLOWING COMMANDS
2733					;WORK CORRECTLY
2734					;
2735					;
2736	041406				BGNSUB
	041406				;/!!!!!!!!!!!!!! BEGIN SUBTEST !!!!!!!!!!!!!!!
	041406	104402			T10.3:
2737					TRAP C\$BSUB
2738	041410	004737	043366		JSR PC,T10RST
2739	041414				SETPRI #PRI00
	041414	012700	000000		MOV #PRI00,R0
	041420	104441			TRAP C\$SPRI
2740	041422	012704	042530		MOV #T10PACKET,R4
2741	041426	012764	000010	000006	MOV #8.,PKBCNT(R4)
2742	041434			5\$:	;
2743	041434				BGNSEG
	041434	104404			;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
2744					TRAP C\$BSEG
2745	041436	004737	015764		JSR PC,SOFINIT
2746	041442	103405			BCS 10\$
2750	041444	010001			MOV R0,R1
2751	041446				ERRDF ERRNO,SFIERR,SFIMSG
	041446	104455			TRAP C\$ERDF
	041450	001771			.WORD 1017
	041452	003646			.WORD SFIERR
	041454	012024			.WORD SFIMSG
2752	041456	005037	002220	10\$:	CLR FATFLG
2753	041462	005037	002222		CLR INTRECV
2754	041466	010465	000000		MOV R4,TSDB(R5)
2755	041472	004737	016326		JSR PC,CHKTSSR
2756	041476	103407			BCS 15\$
2757	041500	010001			MOV R0,R1
2761	041502				ERRDF ERRNO,T10SSR,PKTSSR
	041502	104455			TRAP C\$ERDF
	041504	001772			.WORD 1018
	041506	043070			.WORD T10SSR
	041510	012036			.WORD PKTSSR
2762	041512	005237	002220		INC FATFLG
2763	041516			15\$:	CKLOOP
	041516	104406			SET TRAP C\$CLP1
2764	041520				ESCAPE SEG
	041520	104410			BY-PASS SUBTEST IF FATAL ERROR
	041522	000056			TRAP C\$ESCAPE
2765	041524	005737	002222		TST INTRECV
2766	041530	001004			BNE 22\$
2770	041532				ERRHRD ERRNO,T10NINT,PKTSSR
	041532	104456			TRAP C\$ERHRD
	041534	001773			.WORD 1019
	041536	043157			.WORD T10NINT
	041540	012036			.WORD PKTSSR
2771	041542	016501	000002	22\$:	MOV TSSR(R5),R1
2772	041546	012702	000200		MOV #SSR,R2
2773	041552	032701	000100		BIT #OFL,R1
					GET THE CONTENTS OF TSSR
					EXPECTED CONTENTS OF TSSR
					IS OFF-LINE BIT SET ?

TEST 10: BASIC PACKET PROTOCOL

2774	041556	001402		BEG	25\$	;BRANCH IF NOT OFF-LINE		
2775	041560	052702	000100	BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA		
2776	041564	020201		CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
2777	041566	001404		BEQ	30\$	;OKAY IF MATCH		
2781	041570			ERRHRD	ERRNO,T10NBA,PKTSSR	;NBA NOT ZERO		
	041570	104456					TRAP	C\$ERHRD
	041572	001774					.WORD	1020
	041574	042731					.WORD	T10NBA
	041576	012036					.WORD	PKTSSR
2782	041600							
2783	041600			ENDSEG		; <<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<		
	041600					10000\$:		
	041600	104405					TRAP	C\$ESEG
2784	041602			BGNSEG		; >>>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>>>>		
	041602	104404					TRAP	C\$BSEG
2785	041604	004737	043366	JSR	PC,T10IRST	;RESET PACKETS AND COMMANDS		
2786	041610	005037	002222	CLR	INTRECV	;CLEAR INTERRUPT RECEIVED FLAG		
2787	041614	012737	025252	MOV	#025252,T10BFR	;WIPE OUT MESSAGE BUFFER AREA		
2788	041622	012714	100212	MOV	#100212,(R4)	;SET COMMAND PACKET TO MESS BUF REL		
2789	041626	010465	000000	MOV	R4,TSD8(R5)	;SET THE PACKET ADDRESS		
2790	041632	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET		
2791	041636	103407		BCS	45\$	;BR IF CARRY SET (GOOD RETURN)		
2792	041640	010001		MOV	R0,R1	;SAVE CONTENTS OF TSSR		
2796	041642			ERRDF	ERRNO,T10SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET		
	041642	104455					TRAP	C\$ERDF
	041644	001775					.WORD	1021
	041646	043070					.WORD	T10SSR
	041650	012036					.WORD	PKTSSR
2797	041652	005237	002220	INC	FATFLG	;SET FATAL ERROR FLAG		
2798	041656			CKLOOP		;LOOP ON ERROR, IF FLAG SET		
	041656	104406					TRAP	C\$CLP1
2799	041660	005737	002222	TST	INTRECV	;DID AN INTERRUPT OCCUR ?		
2800	041664	001404		BEQ	52\$	;BRANCH IF NO		
2804	041666			ERRHRD	ERRNO,T10INT,PKTSSR			
	041666	104456					TRAP	C\$ERHRD
	041670	001776					.WORD	1022
	041672	043250					.WORD	T10INT
	041674	012036					.WORD	PKTSSR
2805	041676	016501	000002	MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR		
2806	041702	012702	000200	MOV	#SSR,R2	;EXPECTED CONTENTS OF TSSR		
2807	041706	032701	000100	BIT	#OFL,R1	;IS OFF-LINE BIT SET ?		
2808	041712	001402		BEQ	55\$	;BRANCH IF NOT OFF-LINE		
2809	041714	052702	000100	BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA		
2810	041720	020201		CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
2811	041722	001404		BEQ	60\$	;OKAY IF MATCH		
2815	041724			ERRHRD	ERRNO,T10NNBA,PKTSSR	;NBA NOT SET		
	041724	104456					TRAP	C\$ERHRD
	041726	001777					.WORD	1023
	041730	043013					.WORD	T10NNBA
	041732	012036					.WORD	PKTSSR
2816	041734							
2817	041734	013701	042552	MOV	T10BFR,R1	;PICK UP THE 1ST WORD OF MESSAGE BUFFER		
2818	041740	012702	025252	MOV	#025252,R2	;SET UP EXPECTED DATA		
2819	041744	020102		CMP	R1,R2	;WAS ANY MESSAGE REC'D		
2820	041746	001404		BEQ	70\$	;BR, IF OK (EQUAL)		
2824	041750			ERRHRD	ERRNO,T10MBF,EXPREC	;MESSAGE BUFFER WAS MODIFIED		
	041750	104456						

TEST 10: BASIC PACKET PROTOCOL

041752	002000				.WORD	1024
041754	042634				.WORD	T10MBF
041756	015464				.WORD	EXPREC
2825						
2826	041760	70\$:	CKLOOP		;LOOP ON ERROR IF FLAG SET	
	041760				TRAP	C\$CLP1
2827						
2828	041762	005037	002222	CLR	INTRECV	;CLEAR INTERRUPT RECEIVED FLAG
2829	041766	004737	043366	JSR	PC,T10RST	;RESET THE PACKETS AND COMMANDS
2830	041772	042714	100000	BIC	#100000,(R4)	;CLEAR THE ACK BIT
2831	041776	010465	000000	MOV	R4,TSD8(R5)	;SET THE PACKET ADDRESS
2832	042002	004737	016326	JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET
2833	042006	103407		BCS	75\$	;BR IF CARRY SET (GOOD RETURN)
2834	042010	010001		MOV	R0,R1	;SAVE CONTENTS OF TSSR
2838	042012			ERRDF	ERRNO,T10SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET
	042012	104455			TRAP	C\$ERDF
	042014	002001			.WORD	1025
	042016	043070			.WORD	T10SSR
	042020	012036			.WORD	PKTSSR
2839	042022	005237	002220	INC	FATFLG	;SET FATAL ERROR FLAG
2840	042026			75\$:	CKLOOP	;LOOP ON ERROR, IF FLAG SET
	042026	104406			TRAP	C\$CLP1
2841	042030			ESCAPE	SEG	;BY-PASS SUBTEST IF FATAL ERROR
	042030	104410			TRAP	C\$ESCAPE
	042032	000062			.WORD	10001\$-
2842	042034	005737	002222	TST	INTRECV	;DID AN INTERRUPT OCCUR ?
2843	042040	001006		BNE	82\$	;BRANCH IF YES
2847	042042	016500	000002	MOV	TSSR(R5),R0	;GET TSSR FOR ERROR REPORT
2848	042046			ERRHRD	ERRNO,T10NINT,PKTSSR	
	042046	104456			TRAP	C\$ERHRD
	042050	002002			.WORD	1026
	042052	043157			.WORD	T10NINT
	042054	012036			.WORD	PKTSSR
2849	042056	016501	000002	82\$:	MOV	TSSR(R5),R1
2850	042062	012702	000200	MOV	#SSR,R2	;GET THE CONTENTS OF TSSR
2851	042066	032701	000100	BIT	#OFL,R1	;EXPECTED CONTENTS OF TSSR
2852	042072	001402		BEQ	85\$	;IS OFF-LINE BIT SET ?
2853	042074	052702	000100	BIS	#OFL,R2	;BRANCH IF NOT OFF-LINE
2854	042100	020201		85\$:	CMP	R2,R1
2855	042102	001404		BEQ	90\$	;SET OFF-LINE IN EXPECTED DATA
2859	042104			ERRHRD	ERRNO,T10SSR,PKTSSR	;DOES EXPECTED MATCH RECEIVED ?
	042104	104456				;OKAY IF MATCH
	042106	002003				;NBA NOT ZERO
	042110	043070			TRAP	C\$ERHRD
	042112	012036			.WORD	1027
2860	042114				.WORD	T10SSR
2861	042114				.WORD	PKTSSR
	042114	104405		90\$:	ENDSEG	
	042114	005737	002220			;***** END SEGMENT *****
2862	042116	001403			10001\$:	
2863	042122	004737	017172	TST	FATFLG	TRAP
2864	042124			BEQ	95\$	C\$ESEG
2865				JSR	PC,CKDROP	
2866	042130			BGNSEG		
	042130	104404				;***** BGN SEGMENT *****
2867	042132	005037	002222	95\$:	CLR	INTRECV
2868	042136	004737	043366	JSR	PC,T10RST	;CLEAR INTERRUPT RECEIVED FLAG
						;RESET THE PACKETS AND COMMANDS

TEST 10: BASIC PACKET PROTOCOL

2869	042142	010465	000000		MOV	R4,TSD8(R5)	;SET THE PACKET ADDRESS		
2870	042146	004737	016326		JSR	PC,CHKTSSR	;WAIT FOR SSR TO SET		
2871	042152	103407			BCS	100\$	;BR IF CARRY SET (GOOD RETURN)		
2872	042154	010001			MOV	R0,R1	;SAVE CONTENTS OF TSSR		
2876	042156				ERRDF	ERRNO,T10SSR,PKTSSR	;DEVICE FATAL SSR FAILED TO SET		
	042156	104455						TRAP	C\$ERDF
	042160	002004						.WORD	1028
	042162	043070						.WORD	T10SSR
	042164	012036						.WORD	PKTSSR
2877	042166	005237	002220		INC	FATFLG	;SET FATAL ERROR FLAG		
2878	042172			100\$:	CKLOOP		;LOOP ON ERROR, IF FLAG SET		
2879	042174	104406			ESCAPE	SEG	;BY-PASS SUBTEST IF FATAL ERROR		
	042174	104410						TRAP	C\$CLP1
	042176	000062						.WORD	C\$ESCAPE
2880	042200	005737	002222		TST	INTRECV	;DID AN INTERRUPT OCCUR ?		
2881	042204	001006			BNE	112\$	;BRANCH IF YES		
2885	042206	016500	000002		MOV	TSSR(R5),R0	;GET TSSR FOR ERROR REPORT		
2886	042212				ERRHRD	ERRNO,T10NINT,PKTSSR			
	042212	104456						TRAP	C\$ERHRD
	042214	002005						.WORD	1029
	042216	043157						.WORD	T10NINT
	042220	012036						.WORD	PKTSSR
2887	042222	016501	000002		MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR		
2888	042226	012702	000200		MOV	#SSR,R2	;EXPECTED CONTENTS OF TSSR		
2889	042232	032701	000100		BIT	#OFL,R1	;IS OFF-LINE BIT SET ?		
2890	042236	001402			BEQ	125\$	;BRANCH IF NOT OFF-LINE		
2891	042240	052702	000100		BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA		
2892	042244	020201			CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
2893	042246	001405			BEQ	130\$	;OKAY IF MATCH		
2897	042250				ERRHRD	ERRNO,T10SSR,PKTSSR	;NBA NOT ZERO		
	042250	104456						TRAP	C\$ERHRD
	042252	002006						.WORD	1030
	042254	043070						.WORD	T10SSR
	042256	012036						.WORD	PKTSSR
2898	042260				ENDSEG		;<<<<<<<<<<<< END SEGMENT <<<<<<<<<<<<		
	042260	104405					10002\$:		
2899	042262	005737	002220		TST	FATFLG	;CHECK FATAL FLAG FOR SET	TRAP	C\$ESEG
2900	042266	001402			BEQ	140\$	;BR, IF NOT SET (OK)		
2901	042270	004737	017172		JSR	PC,CKDROP	;DROP DEVICE IF INDICATED		
2902	042274								
2903	042274				ENDSUB		;////////// END SUBTEST //////////		
	042274	104403					L10100:		
2904	042276			30\$:				TRAP	C\$ESUB

Address	Offset	PC	Op	Op2	Op3	Op4	Op5	Op6	Op7	Op8	Op9	Op10	Op11	Op12	Op13	Op14	Op15	Op16	Op17	Op18	Op19	Op20	Op21	Op22	Op23	Op24	Op25	Op26	Op27	Op28	Op29	Op30	Op31	Op32	Op33	Op34	Op35	Op36	Op37	Op38	Op39	Op40	Op41	Op42	Op43	Op44	Op45	Op46	Op47	Op48	Op49	Op50	Op51	Op52	Op53	Op54	Op55	Op56	Op57	Op58	Op59	Op60	Op61	Op62	Op63	Op64	Op65	Op66	Op67	Op68	Op69	Op70	Op71	Op72	Op73	Op74	Op75	Op76	Op77	Op78	Op79	Op80	Op81	Op82	Op83	Op84	Op85	Op86	Op87	Op88	Op89	Op90	Op91	Op92	Op93	Op94	Op95	Op96	Op97	Op98	Op99	Op100	Op101	Op102	Op103	Op104	Op105	Op106	Op107	Op108	Op109	Op110	Op111	Op112	Op113	Op114	Op115	Op116	Op117	Op118	Op119	Op120	Op121	Op122	Op123	Op124	Op125	Op126	Op127	Op128	Op129	Op130	Op131	Op132	Op133	Op134	Op135	Op136	Op137	Op138	Op139	Op140	Op141	Op142	Op143	Op144	Op145	Op146	Op147	Op148	Op149	Op150	Op151	Op152	Op153	Op154	Op155	Op156	Op157	Op158	Op159	Op160	Op161	Op162	Op163	Op164	Op165	Op166	Op167	Op168	Op169	Op170	Op171	Op172	Op173	Op174	Op175	Op176	Op177	Op178	Op179	Op180	Op181	Op182	Op183	Op184	Op185	Op186	Op187	Op188	Op189	Op190	Op191	Op192	Op193	Op194	Op195	Op196	Op197	Op198	Op199	Op200	Op201	Op202	Op203	Op204	Op205	Op206	Op207	Op208	Op209	Op210	Op211	Op212	Op213	Op214	Op215	Op216	Op217	Op218	Op219	Op220	Op221	Op222	Op223	Op224	Op225	Op226	Op227	Op228	Op229	Op230	Op231	Op232	Op233	Op234	Op235	Op236	Op237	Op238	Op239	Op240	Op241	Op242	Op243	Op244	Op245	Op246	Op247	Op248	Op249	Op250	Op251	Op252	Op253	Op254	Op255	Op256	Op257	Op258	Op259	Op260	Op261	Op262	Op263	Op264	Op265	Op266	Op267	Op268	Op269	Op270	Op271	Op272	Op273	Op274	Op275	Op276	Op277	Op278	Op279	Op280	Op281	Op282	Op283	Op284	Op285	Op286	Op287	Op288	Op289	Op290	Op291	Op292	Op293	Op294	Op295	Op296	Op297	Op298	Op299	Op300	Op301	Op302	Op303	Op304	Op305	Op306	Op307	Op308	Op309	Op310	Op311	Op312	Op313	Op314	Op315	Op316	Op317	Op318	Op319	Op320	Op321	Op322	Op323	Op324	Op325	Op326	Op327	Op328	Op329	Op330	Op331	Op332	Op333	Op334	Op335	Op336	Op337	Op338	Op339	Op340	Op341	Op342	Op343	Op344	Op345	Op346	Op347	Op348	Op349	Op350	Op351	Op352	Op353	Op354	Op355	Op356	Op357	Op358	Op359	Op360	Op361	Op362	Op363	Op364	Op365	Op366	Op367	Op368	Op369	Op370	Op371	Op372	Op373	Op374	Op375	Op376	Op377	Op378	Op379	Op380	Op381	Op382	Op383	Op384	Op385	Op386	Op387	Op388	Op389	Op390	Op391	Op392	Op393	Op394	Op395	Op396	Op397	Op398	Op399	Op400	Op401	Op402	Op403	Op404	Op405	Op406	Op407	Op408	Op409	Op410	Op411	Op412	Op413	Op414	Op415	Op416	Op417	Op418	Op419	Op420	Op421	Op422	Op423	Op424	Op425	Op426	Op427	Op428	Op429	Op430	Op431	Op432	Op433	Op434	Op435	Op436	Op437	Op438	Op439	Op440	Op441	Op442	Op443	Op444	Op445	Op446	Op447	Op448	Op449	Op450	Op451	Op452	Op453	Op454	Op455	Op456	Op457	Op458	Op459	Op460	Op461	Op462	Op463	Op464
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Address	Offset	Label	Instruction	Comment	Trap	Code
042452	104456				TRAP	C\$ERRRD
042454	002011				.WORD	1033
042456	043157				.WORD	T10NINT
042460	012036				.WORD	PKTSSR
2956	042462	000002	22\$: MOV TSSR(R5),R1	;GET THE CONTENTS OF TSSR		
2957	042466	110200	MOV #5'R!RMR!SC,R2	;EXPECTED CONTENTS OF TSSR		
2958	042472	032701	BIT #0FL,R1	;IS OFF-LINE BIT SET ?		
2959	042476	001402	BEQ 25\$	;BRANCH IF NOT OFF-LINE		
2960	042500	052702	BIS #0FL,R2	;SET OFF-LINE IN EXPECTED DATA		
2961	042504	020201	25\$: CMP R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
2962	042506	001404	BEQ 30\$	;OKAY IF MATCH		
2966	042510		ERRHRD ERRNO,T10SSR,PKTSSR	;NBA NOT ZERO		
042510	104456				TRAP	C\$ERRRD
042512	002012				.WORD	1034
042514	043070				.WORD	T10SSR
042516	012036				.WORD	PKTSSR
2967	042520		30\$:			
2968	042520		ENDSEG	;***** END SEGMENT *****		
042520				10000\$:		
042520	104405				TRAP	C\$ESEG
2969	042522		ENDSUB	;////////// END SUBTEST //////////		
042522				L10101:		
042522	104403				TRAP	C\$ESUB
2970	042524		EXIT TST	;ALL DONE WITH THIS TEST		
042524	104432				TRAP	C\$EXIT
042526	000764				.WORD	L10075-

TEST 10: BASIC PACKET PROTOCOL

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2972
2973
2974
2975
2979 042530
2980 042530 100204
2981 042532 042540
2982 042534 000000
2983 042536 000010
2984
2985 042540
2986 042540 042552
2987 042542 000000
2988 042544 000016
2989 042546 000000 000000
2990
2991 042552
2992
2993
2994
2995
2996
2997 042572
2998 042572 100204
2999 042574 042602
3000 042576 000000
3001 042600 000010
3002
3003 042602
3004 042602 042614
3005 042604 000000
3006 042606 000016
3007 042610 000000 000000
3008
3009 042614
3010
3011
3012
3013
3014
3015
3016 042634 115 145 163
3017 042731 116 102 101
3018 043013 116 102 101
3019 043070 103 157 156
3020 043157 105 170 160
3021 043250 125 156 145
3022 043337 102 141 163
3023
3024

;
;LOCAL STORAGE FOR THIS TEST
;
T10PACKET:
        .WORD 100204
        .WORD T10DATA
        .WORD 0
        .WORD 8.
;COMMAND PACKET FOR TEST
;WRITE CHAR COMMAND, WITH IE, ACK
;ADDRESS OF CHARACTERISTICS BLOCK
;STARTING VALUE OF BLOCK SIZE

T10DATA:
        .WORD T10BFR
        .WORD 0
        .WORD 14.
        .WORD 0.0
;CHARACTERISTICS DATA BLOCK
;ADDRESS OF MESSAGE BUFFER
;LENGTH OF MESSAGE BUFFER

T10BFR: .BLKW 8.
;MESSAGE BUFFER

;
;
;TEST DATA FOR SUBTEST FOUR
;
T10PKT:
        .WORD 100204
        .WORD T10DATA
        .WORD 0
        .WORD 8.
;COMMAND PACKET FOR TEST
;WRITE CHAR COMMAND, WITH IE, ACK
;ADDRESS OF CHARACTERISTICS BLOCK
;STARTING VALUE OF BLOCK SIZE

T10DATA:
        .WORD T10BFR
        .WORD 0
        .WORD 14.
        .WORD 0.0
;CHARACTERISTICS DATA BLOCK
;ADDRESS OF MESSAGE BUFFER
;LENGTH OF MESSAGE BUFFER

T10BFR: .BLKW 8.
;MESSAGE BUFFER

;
;LOCAL TEXT MESSAGES FOR TEST
;
T10MBF: .ASCIZ 'Message Buffer Modified after MESSAGE BUFFER RELEASE Command'
T10NBA: .ASCIZ 'NBA Not Clear After WRITE CHARACTERISTICS Command'
T10NNBA: .ASCIZ 'NBA Set After MESSAGE BUFFER RELEASE Command'
T10SSR: .ASCIZ 'Contents of TSSR Incorrect After WRITE CHARACTERISTICS'
T10NINT: .ASCIZ 'Expected Interrupt Not Received On WRITE CHARACTERISTICS'
T10INT: .ASCIZ 'Unexpected Interrupt Received On WRITE CHARACTERISTICS'
TST10ID: .ASCIZ 'Basic Packet Protocol'
        .EVEN

```

TEST 10: BASIC PACKET PROTOCOL

```

3026
3027
3028
3029
3030
3031
3032
3033 043366
3034 043366
3035 043372 012701 042530
3036 043376 012721 100204
3037 043402 012721 042540
3038 043406 005021
3039 043410 012721 000010
3040 043414 012721 042552
3041 043420 005021
3042 043422 012721 000016
3043 043426 005021
3044 043430 005011
3045 043432 005037 042552
3046 043436 000207
3047
3048
3049
3050
3051
3052
3053 043440
3054 043440
3055 043444 012701 042572
3056 043450 012721 100204
3057 043454 012721 042602
3058 043460 005021
3059 043462 012721 000010
3060 043466 012721 042614
3061 043472 005021
3062 043474 012721 000016
3063 043500 005021
3064 043502 005011
3065 043504 005037 042614
3066 043510 000207
3067 043512
      043512
      043512 104401

```

```

;
;
; ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
;
;
T10RST:
      SAVREG                ;SAVE THE REGISTERS
      MOV    #T10PACKET,R1  ;START OF THE PACKET
      MOV    #100204,(R1)+  ;WRITE CHARACTERISTICS WITH ACK, IE
      MOV    #T10DATA,(R1)+ ;ADDRESS OF CHAR DATA BLOCK
      CLR    (R1)+          ;EXTENDED ADDRESS
      MOV    #8,(R1)+       ;SIZE OF DATA BLOCK IN BYTES
      MOV    #T10BFR,(R1)+  ;ADDRESS OF MESSAGE BUFFER
      CLR    (R1)+
      MOV    #14,(R1)+      ;LENGTH OF MESSAGE BUFFER
      CLR    (R1)+
      CLR    (R1)
      CLR    T10BFR         ;CLEAR 1ST LOC IN MESSAGE BUFFER
      RTS    PC             ;RETURN
;
;
; ROUTINE TO RESTORE COMMAND PACKET #2 TO START UP (DEFAULT) VALUES
;
;
T10RT2:
      SAVREG                ;SAVE THE REGISTERS
      MOV    #T10PKT,R1     ;START OF THE PACKET
      MOV    #100204,(R1)+  ;WRITE CHARACTERISTICS WITH ACK, IE
      MOV    #T10DTA,(R1)+  ;ADDRESS OF CHAR DATA BLOCK
      CLR    (R1)+          ;EXTENDED ADDRESS
      MOV    #8,(R1)+       ;SIZE OF DATA BLOCK IN BYTES
      MOV    #T10BUFR,(R1)+ ;ADDRESS OF MESSAGE BUFFER
      CLR    (R1)+
      MOV    #14,(R1)+      ;LENGTH OF MESSAGE BUFFER
      CLR    (R1)+
      CLR    (R1)
      CLR    T10BUFR        ;CLEAR 1ST LOC IN MESSAGE BUFFER
      RTS    PC             ;RETURN
      ENDTST

```

L10075: TRAP C\$ETST

Address	Offset	Hex Data	Label	Operation	Comments
3070					.SBTTL TEST 11: NON TAPE MOTION COMMANDS
3071					
3072					
3073					
3074					; THIS TEST VERIFIES PROPER OPERATION OF THE INITIALIZE
3075					; COMMAND. TWO SUBTESTS ARE USED. THE FIRST VERIFIES THAT
3076					; THE COMMAND RUNS TO COMPLETION AND STORES A VALID
3077					; MESSAGE PACKET. THE SECOND VERIFIES THAT NON ZERO
3078					; VALUES IN THE COMMAND MODE FIELD CAUSES COMMAND REJECT.
3079					
3080					
3081					
3082	043514		BGNTST		
3087	043514	012700 045512		MOV #TST1.ID,R0	; ASCII MESSAGE TO IDENTIFY TEST
3088	043520	004737 016500		JSR PC,TSTSETUP	; DO INITIAL TEST SETUP
3089	043524	012737 000024 002214		MOV #20.,LOOPCNT	; PERFORM 20 ITERATIONS
3090	043532		T11LOOP:		
3091	043532		BGNSUB		; BEGIN SUBTEST
3092	043532	104402			
3093	043534		SETPRI	#PRI00	; LOWER PRIORITY TO ALLOW INTERRUPTS
3094	043540	012700 000000		MOV #PRI00,R0	
3095	043542	004737 015764		JSR PC,SOFINIT	; DO SOFT INIT OF CONTROLLER
3099	043546	103405		BCS 3;	; BR IF SOFT INIT = OK
3100	043550	010001		MOV R0,R1	; SAVE CONTENTS OF TSSR
3101	043552		ERRDF	ERRNO,SFIERR,SFIMSG	; DEVICE FATAL ERROR DURING INIT
3102	043552	104455			
3103	043554	002115			
3104	043556	003646			
3105	043560	012024			
3106	043562		3:		
3107	043562	012704 044740		MOV #T11PK2,R4	; WRITE CHARACTERISTICS PACKET
3108	043566	004737 010652		JSR PC,WRTCHR	; ISSUE WRITE CHARACTERISTICS
3109	043572	103404		BCS 4;	; BR, IF COMMAND ISSUED OK
3110	043574		ERRHRD	ERRNO,WRTMSG,SFIMSG	; WRITE CHARACTERISTICS FAILED
3111	043574	104456			
3112	043576	002116			
3113	043600	005052			
3114	043602	012024			
3115	043604		4:		
3116	043604	004737 045544		JSR PC,T11REST	; SET UP PACKET FOR COMMAND
3117	043610	012704 044670		MOV #T11PACKET,R4	; GET THE ADDRESS OF COMMAND PACKET
3118	043614		5:		
3119	043614		BGNSEG		; BEGIN SEGMENT
3120	043614	104404			
3121	043616	005037 002220			
3122	043622	005037 002222			
3123	043626	010465 000000		CLR FATFLG	; CLEAR FATAL ERROR FLAG
3124	043632	004737 016326		CLR INTRECV	; CLEAR INTERRUPT RECEIVED FLAG
3125	043636	103407		MOV R4,TSDB(R5)	; SET THE PACKET ADDRESS
3126	043640	010001		JSR PC,CHKTSSR	; WAIT FOR SSR TO SET
3127	043642			BCS 15;	; BR IF CARRY SET (GOOD RETURN)
3128	043642			MOV R0,R1	; SAVE CONTENTS OF TSSR
3129	043642		ERRDF	ERRNO,T11SSR,P1TSSR	; DEVICE FATAL SSR FAILED TO SET
3130	043642	104455			

Address	Hex	Dec	Label	Op	Op2	Comment	Trap	Word
043644	002117							1103
043646	045234							T11SSR
043650	012036							PKTSSR
3125	043652	005237	002220	INC	FATFLG	;SET FATAL ERROR FLAG		
3126	043656	104406		15\$: CKLOOP		;LOOP ON ERROR, IF FLAG SET	TRAP	C\$CLP1
3127	043660	104410		ESCAPE	SEG	;BY PASS SUBTEST IF FATAL ERROR	TRAP	C\$ESCAPE
	043662	000074					TRAP	10000\$-
3128	043664	005737	002222	TST	INTRECV	;DID AN INTERRUPT OCCUR ?		
3129	043670	001004		BNE	22\$	;BRANCH IF YES		
3133	043672	104456		ERRHRD	ERRNO,T11NINT,PKTSSR			
	043674	002120					TRAP	C\$ERHRD
	043676	045364					.WORD	1104
	043700	012036					.WORD	T11NINT
3134	043702	016501	000002	22\$: MOV	TSSR(R5),R1	;GET THE CONTENTS OF TSSR		
3135	043706	012702	000200	MOV	#SSR,R2	;EXPECTED CONTENTS OF TSSR		
3136	043712	032701	000100	BIT	#OFL,R1	;IS OFF-LINE BIT SET ?		
3137	043716	001402		BEQ	25\$	;BRANCH IF NOT OFF-LINE		
3138	043720	052702	000100	BIS	#OFL,R2	;SET OFF-LINE IN EXPECTED DATA		
3139	043724	020201		25\$: CMP	R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
3140	043726	001404		BEQ	30\$	;OKAY IF MATCH		
3144	043730	-		ERRHRD	ERRNO,T11NBA,PKTSSR	;NBA NOT ZERO		
	043730	104456					TRAP	C\$ERHRD
	043732	002121					.WORD	1105
	043734	045002					.WORD	T11NBA
	043736	012036					.WORD	PKTSSR
3145	043740			30\$: JSR	PC,CKRAM	;CHECK RAM TO MEMORY		
3146	043740	004737	011104	35\$: BCS	59\$	;RAM OK GO ON		
3147	043744	103405		ERRHRD	ERRNO,PKTRAM,RAMERR	;THEY DON'T MATCH		
3151	043746	104456					TRAP	C\$ERHRD
	043750	002122					.WORD	1106
	043752	004741					.WORD	PKTRAM
	043754	015500					.WORD	RAMERR
3152	043756			ENDSEG		;***** END SEGMENT *****		
	043756	104405				10000\$:	TRAP	C\$ESEG
3153								
3154								
3155	043760			59\$: ENDSUB		;***** END SUBTEST *****		
	043760					L10103:		
	043760	104403					TRAP	C\$ESUB
3156								
3157	043762	005737	002220	TST	FATFLG	;ANY FATAL ERRORS ?		
3158	043766	001402		BEQ	60\$	;BRANCH IF NOT		
3159	043770	004737	017172	JSR	PC,CKDROP	;TRY TO DROP THE UNIT		
3160	043774			60\$:				

TEST 11: NON TAPE MOTION COMMANDS

3162				;*
3163				;
3164				;TEST 11, SUBTEST 2
3165				;
3166				;CHECK THAT NON-ZERO MODE BITS BEING SET CAUSES
3167				;INITIALIZE COMMAND TO BE REJECTED
3168				;
3169				;-
3170				
3171	043774		BGNSUB	;////////// BEGIN SUBTEST //////////
	043774			T11.2:
	043774	104402		TRAP C\$BSUB
3172				
3173	043776		SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	043776	012700	000000	MOV #PRI00,R0
	044002	104441		TRAP C\$SPRI
3174	044004		BGNSEG	;>>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
	044004	104404		TRAP C\$BSEG
3175				
3176				
3177	044006	004737	015764	JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER
3178	044012	103405		BCS 3\$;BR IF SOFT INIT = OK
3182	044014	010001		MOV R0,R1 ;SAVE CONTENTS OF TSSR
3183	044016			EPRDF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
	044016	104455		TRAP C\$ERDF
	044020	002123		.WORD 1107
	044022	003646		.WORD SFIERR
	044024	012024		.WORD SFIMSG
3184	044026		3\$:	
3185	044026	012704	04474C	MOV #T11PK2,R4 ;WRITE CHARACTERISTICS PACKET
3186	044032	004737	010652	JSR PC,WRTCHR ;ISSUE WRITE CHARACTERISTICS
3187	044036	103404		BCS 4\$;BR, IF COMMAND ISSUED OK
3191	044040			ERRHRD ERRNO,WRTMSG,SFIMSG ;WRITE CHARACTERISTISC FAILED
	044040	104456		TRAP C\$ERHRD
	044042	002124		.WORD 1108
	044044	005052		.WORD WRTMSG
	044046	012024		.WORD SFIMSG
3192	044050		4\$:	
3193	044050	004737	045544	JSR PC,T11REST ;SET UP PACKET FOR COMMAND
3194	044054	012704	044670	MOV #T11PACKET,R4 ;GET THE ADDRESS OF COMMAND PACKET
3195	044060		5\$:	
3196	044060	005037	002222	CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
3197	044064	052714	007400	BIS #P.MODE,(R4) ;NON-ZERO COMMAND MODE BITS
3198	044070	010465	000000	MOV R4,T3DB(R5) ;SET THE PACKET ADDRESS
3199	044074	004737	016326	JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
3200	044100	103405		BCS 15\$;BR IF CARRY SET (GOOD RETURN)
3201	044102	010001		MOV R0,R1 ;SAVE CONTENTS OF TSSR
3205	044104			EPRDF ERRNO,T11SSR,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
	044104	104455		TRAP C\$ERDF
	044106	002125		.WORD 1109
	044110	045234		.WORD T11SSR
	044112	012036		.WORD PKTSSR
3206	044114		15\$:	
	044114	104406		CKLOOP ;LOOP ON ERROR, IF FLAG SET
3207	044116			ESCAPE SEG ;BY PASS CHECKS IF FATAL ERROR
	044116	104410		TRAP C\$ESCAPE
	044120	000074		.WORD 10000\$.

Address	Hex	Dec	Label	Op	Opnd	Comment	Trap	Code
3208	044122	005737	002222	TST	INTRECV	;DID AN INTERRUPT OCCUR ?		
3209	044126	001004		BNE	22\$	;BRANCH IF YES		
3213	044130			ERRHRD	ERRNO,T11NINT,PKTSSR			
	044130	104456					TRAP	C\$ERHRD
	044132	002126					.WORD	1110
	044134	045364					.WORD	T11NINT
	044136	012036					.WORD	PKTSSR
3214	044140	016501	000002	22\$:	MOV TSSR(R5),R1	;GET THE CONTENTS OF TSSR		
3215	044144	012702	100206		MOV #SC!SSR!TSREJ,R2	;EXPECTED CONTENTS OF TSSR		
3216	044150	032701	000100		BIT #OFL,R1	;IS OFF-LINE BIT SET ?		
3217	044154	001402			BEQ 25\$	;BRANCH IF NOT OFF-LINE		
3218	044156	052702	000100		BIS #OFL,R2	;SET OFF-LINE IN EXPECTED DATA		
3219	044162	020201		25\$:	CMP R2,R1	;DOES EXPECTED MATCH RECEIVED ?		
3220	044164	001404			BEQ 30\$	;OKAY IF MATCH		
3224	044166			ERRHRD	ERRNO,T112REJ,PKTSSR	;COMMAND NOT REJECTED		
	044166	104456					TRAP	C\$ERHRD
	044170	002127					.WORD	1111
	044172	045042					.WORD	T112REJ
	044174	012036					.WORD	PKTSSR
3225	044176			30\$:				
3226	044176	004737	011104	35\$:	JSR PC,CKRAM	;CHECK RAM TO MEMORY		
3227	044202	103405			BCS 59\$	;RAM OK GO ON		
3231	044204			ERRHRD	ERRNO,PKTRAM,RAMERR	;THEY DON'T MATCH		
	044204	104456					TRAP	C\$ERHRD
	044206	002130					.WORD	1112
	044210	004741					.WORD	PKTRAM
	044212	015500					.WORD	RAMERR
3232	044214			ENDSEG		;***** END SEGMENT *****		
	044214					10000\$:		
	044214	104405					TRAP	C\$ESEG
3233								
3234	044216			59\$:	ENDSUB	;***** END SUBTEST *****		
	044216					L10104:		
	044216	104403					TRAP	C\$ESUB

TEST 11: NON TAPE MOTION COMMANDS

Address	Offset	Label	Instruction	Comment
3236			;	
3237			;	
3238			;TEST 11, SUBTEST 3	
3239			;	
3240			;CHECK THAT THE GET STATUS COMMAND IS ACCEPTED	
3241			;	
3242			;	
3243			;	
3244	044220	BGN SUB		;/!!!!!!!!!!!!!! BEGIN SUBTEST !!!!!!!!!!!!!!!
	044220			T11.3:
	044220	104402		TRAP C\$BSUB
3245				
3246	044222	SETPRI	#PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	044222	012700	000000	MOV #PRI00,R0
	044226	104441		TRAP C\$SPRI
3247	044230	BGN SEG		;!!!!!!!!!!!!!!!!!! BEGIN SEGMENT !!!!!!!!!!!!!!!!!!!
	044230	104404		TRAP C\$BSEG
3248	044232	004737	015764	
3249	044236	103405		JSR PC,SOFINIT ;DO SOF INIT OF CONTROLLER
3253	044240	010001		BCS 3\$;BR IF SOF INIT = OK
3254	044242			MOV R0,R1 ;SAVE CONTENTS OF TSSR
	044242	104455		ERRDF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT
	044244	002131		TRAP C\$ERDF
	044246	003646		.WORD 1113
	044250	012024		.WORD SFIERR
	044252			.WORD SFIMSG
3255	044252	012704	044740	3\$: MOV #T11PK2,R4 ;WRITE CHARACTERISTICS PACKET
3256	044252	004737	010652	JSR PC,WRTCHR ;ISSUE WRITE CHARACTERISTICS
3257	044256	103404		BCS 4\$;BR, IF COMMAND ISSUED OK
3262	044264	104456		ERRHRD ERRNO,WRTMSG,SFIMSG ;WRITE CHARACTERISTICS FAILED
	044264	002132		TRAP C\$ERHRD
	044266	005052		.WORD 1114
	044270	012024		.WORD WRTMSG
	044272			.WORD SFIMSG
3263	044274	004737	045544	4\$: JSR PC,T11REST ;SET UP PACKET FOR COMMAND
3264	044300	012704	044670	MOV #T11PACKET,R4 ;GET THE ADDRESS OF COMMAND PACKET
3266	044304	005037	002222	5\$: CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
3267	044310	010465	000000	10\$: MOV R4,TSDB(R5) ;SET THE PACKET ADDRESS
3268	044314	004737	016326	JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
3270	044320	103405		BCS 15\$;BR IF CARRY SET (GOOD RETURN)
3271	044322	010001		MOV R0,R1 ;SAVE CONTENTS OF TSSR
3275	044324	104455		ERRDF ERRNO,T11SR2,PKTSSR ;DEVICE FATAL SSR FAILED TO SET
	044324	002133		TRAP C\$ERDF
	044326	045310		.WORD 1115
	044330	012036		.WORD T11SR2
	044332			.WORD PKTSSR
3276	044334	104406		15\$: CKLOOP ;LOOP ON ERROR, IF FLAG SET
	044334	104410		TRAP C\$CLP1
3277	044336	000074		ESCAPE SL3 ;BY-PASS SUBTEST IF FATAL ERROR
	044336	001004		TRAP C\$ESCAPE
	044340			.WORD 10000\$
3278	044342	005737	002222	TST INTRECV ;DID AN INTERRUPT OCCUR ?
3279	044346			BNE 22\$;BRANCH IF YES
3283	044350	104456		ERRHRD ERRNO,T11NINT,PKTSSR
	044350			TRAP C\$ERHRD

TEST 11: NON-TAPE MOTION COMMANDS

Address	Offset	Hex Data	Label	Instruction	Comment	Trap	Code
044352	002134						1116
044354	045364					.WORD	T11NINT
044356	012036					.WORD	PKTSSR
3284	044360	016501	000002	22\$: MOV	TSSR(R5),R1		
3285	044364	012702	000200				
3286	044370	032701	000100				
3287	044374	001402					
3288	044376	052702	000100				
3289	044402	020201		25\$: CMP	R2,R1		
3290	044404	001404					
3294	044406			ERRHRD	ERRNO,T113REJ,PKTSSR		
	044406	104456				TRAP	C\$ERHRD
	044410	002135				.WORD	1117
	044412	045123				.WORD	T113REJ
	044414	012036				.WORD	PKTSSR
3295	044416			30\$: JSR	PC,CKRAM		
3296	044416	004737	011104	35\$: BCS	59\$		
3297	044422	103405					
3301	044424			ERRHRD	ERRNO,PKTRAM,RAMERR		
	044424	104456				TRAP	C\$ERHRD
	044426	002136				.WORD	1118
	044430	004741				.WORD	PKTRAM
	044432	015500				.WORD	RAMERR
3302	044434			ENDSEG			
	044434						
	044434	104405					
3303							
3304							
3305	044436			59\$: ENDSUB			
	044436						
	044436	104403					

TEST 11: NON TAPE MOTION COMMANDS

3307				;*
3308				;
3309				;TEST 11, SUBTEST 4
3310				;
3311				;SUBTEST TO VERIFY THAT A GET STATUS COMMAND IS
3312				;REJECTED IF A NON-ZERO COMMAND MODE FIELD IS USED
3313				;
3314				;-
3315				
3316	044440		BGNSUB	;/!!!!!!!!!!!!!! BEGIN SUBTEST !!!!!!!!!!!!!!!
	044440			T11.4:
	044440	104402		TRAP C\$BSUB
3317				
3318	044442		SETPRI #PRI00	;LOWER PRIORITY TO ALLOW INTERRUPTS
	044442	012700		MOV #PRI00,R0
	044446	104441		TRAP C\$SPRI
3319	044450		BGNSEG	;>>>>>>>>> BEGIN SEGMENT >>>>>>>>>
	044450	104404		TRAP C\$BSEG
3320	044452	004737	015764	JSR PC,SOFINIT ;DO SOFT INIT OF CONTROLLER
3321	044456	103405		BCS 3\$;BR IF SOFT INIT = OK
3325	044460	010001		MOV R0,R1 ;SAVE CONTENTS OF TSSR
3326	044462		ERRDF ERRNO,SFIERR,SFIMSG ;DEVICE FATAL ERROR DURING INIT	
	044462	104455		TRAP C\$ERDF
	044464	002137		.WORD 1119
	044466	003646		.WORD SFIERR
	044470	012024		.WORD SFIMSG
3327	044472		3\$:	
3328	044472	012704	044740	MOV #T11PK2,R4 ;WRITE CHARACTERISTICS PACKET
3329	044476	004737	010652	JSR PC,WRTCHR ;ISSUE WRITE CHARACTERISTICS
3330	044502	103404		BCS 4\$;BR, IF COMMAND ISSUED OK
3334	044504		ERRHRD ERRNO,WRTMSG,SFIMSG ;WRITE CHARACTERISTICSC FAILED	
	044504	104456		TRAP C\$ERHRD
	044506	002140		.WORD 1120
	044510	005052		.WORD WRTMSG
	044512	012024		.WORD SFIMSG
3335	044514		4\$:	
3336	044514	004737	045544	JSR PC,T11REST ;SET UP PACKET FOR COMMAND
3337	044520	012704	044670	MOV #T11PACKET,R4 ;GET THE ADDRESS OF COMMAND PACKET
3338	044524		5\$:	
3339	044524	005037	002222	CLR INTRECV ;CLEAR INTERRUPT RECEIVED FLAG
3340	044530	052714	007000	BIS #007000,(R4) ;SET TO NON-ZERO MODE
3341	044534	010465	000000	MOV R4,TSDB(R5) ;SET THE PACKET ADDRESS
3342	044540	004737	016326	JSR PC,CHKTSSR ;WAIT FOR SSR TO SET
3343	044544	103405		BCS 15\$;BR IF CARRY SET (GOOD RETURN)
3344	044546	010001		MOV R0,R1 ;SAVE CONTENTS OF TSSR
3348	044550		ERRDF ERRNO,T11SR2,PKTSSR ;DEVICE FATAL SSR FAILED TO SET	
	044550	104455		TRAP C\$ERDF
	044552	002141		.WORD 1121
	044554	045310		.WORD T11SR2
	044556	012036		.WORD PKTSSR
3349	044560		15\$:	
	044560	104406		CKLOOP ;LOOP ON ERROR, IF FLAG SET
3350	044562		ESCAPE SUB	TRAP C\$CLP1
	044562	104410		;BY PASS SUBTEST IF FATAL ERROR
	044564	000076		TRAP C\$FESCAPE
				.WORD L10106-
3351	044566	005737	002222	TST INTRECV ;DID AN INTERRUPT OCCUR ?
3352	044572	001004		BNE 22\$;BRANCH IF YES

TEST 11: NON-TAPE MOTION COMMANDS

Address	Offset	Value	Label	Instruction	Comment	Trap	Code
3356	044574			ERRHRD	ERRNO,T11NINT,PKTSSR		
	044574	104456				TRAP	C\$ERHRD
	044576	002142				.WORD	1122
	044600	045364				.WORD	T11NINT
	044602	012036				.WORD	PKTSSR
3357	044604	016501	000002	22\$:	MOV TSSR(R5),R1	;GET THE CONTENTS OF TSSR	
3358	044610	012702	100206		MOV #SC!SSR!TSREJ,R2	;EXPECTED CONTENTS OF TSSR	
3359	044614	032701	000100		BIT #0FL,R1	;IS OFF-LINE BIT SET ?	
3360	044620	001402			BEQ 25\$	;BRANCH IF NOT OFF-LINE	
3361	044622	052702	000100		BIS #0FL,R2	;SET OFF-LINE IN EXPECTED DATA	
3362	044626	020201		25\$:	CMP R2,R1	;DOES EXPECTED MATCH RECEIVED ?	
3363	044630	001404			BEQ 30\$	;OKAY IF MATCH	
3367	044632				ERRHRD	ERRNO,T114REJ,PKTSSR	;COMMAND NOT REJECTED
	044632	104456				TRAP	C\$ERHRD
	044634	002143				.WORD	1123
	044636	045153				.WORD	T114REJ
	044640	012036				.WORD	PKTSSR
3368	044642			30\$:			
3369							
3370	044642	004737	011104	35\$:	JSR PC,CKRAM	;CHECK RAM TO MEMORY	
3371	044646	103404			BCS 59\$	;RAM OK GO ON	
3375	044650				ERRHRD	ERRNO,PKTRAM,RAMERR	;THEY DON'T MATCH
	044650	104456				TRAP	C\$ERHRD
	044652	002144				.WORD	1124
	044654	004741				.WORD	PKTRAM
	044656	015500				.WORD	RAMERR
3376	044660			59\$:			
3377	044660				ENDSEG	;<<<<<<<<<<<<<<<< END SEGMENT<<<<<<<<<<<<<<<<	
	044660					1000C\$:	
	044660	104405				TRAP	C\$ESEG
3378	044662				ENDSUB	;////////// END SUBTEST \\\\\\\\\\\\\\\\\\\	
	044662					L10106:	
3379	044662	104403				TRAP	C\$ESUB

TEST 11: NON-TAPE MOTION COMMANDS

```

3381 044664          EXIT    TST          ;ALL DONE THIS TEST
      044664 104432
      044666 000762          TRAP      COEXIT
                                .WORD    L10102-.
3382
3383
3384      ;
3385      ;LOCAL STORAGE FOR THIS TEST
3386      ;
3390 044670          T11PACKET:          ;COMMAND PACKET FOR TEST
3391 044670 100204          .WORD    100204          ;WRITE CHAR COMMAND, WITH IE, ACK
3392 044672 044700          .WORD    T11DATA          ;ADDRESS OF CHARACTERISTICS BLOCK
3393 044674 000000          .WORD    0
3394 044676 000010          .WORD    8.          ;STARTING VALUE OF BLOCK SIZE
3395
3396 044700          T11DATA:          ;CHARACTERISTICS DATA BLOCK
3397 044700 044712          .WORD    T11BFR          ;ADDRESS OF MESSAGE BUFFER
3398 044702 000000          .WORD    0
3399 044704 000016          .WORD    14.          ;LENGTH OF MESSAGE BUFFER
3400 044706 000000 000000          .WORD    0.0
3401
3402 044712          T11BFR: .BLKW  8.          ;MESSAGE BUFFER
3403
3404
3406          044740
3408 044740          T11PK2:  .-<..10>E177770
3409 044740 100204          .WORD    100204          ;COMMAND PACKET FOR TEST
3410 044742 044750          .WORD    T11DTA          ;WRITE CHAR COMMAND, WITH IE, ACK
3411 044744 000000          .WORD    0          ;ADDRESS OF CHARACTERISTICS BLOCK
3412 044746 000010          .WORD    8.          ;STARTING VALUE OF BLOCK SIZE
3413
3414 044750          T11DTA:          ;CHARACTERISTICS DATA BLOCK
3415 044750 044762          .WORD    T11BF2          ;ADDRESS OF MESSAGE BUFFER
3416 044752 000000          .WORD    0
3417 044754 000016          .WORD    14.          ;LENGTH OF MESSAGE BUFFER
3418 044756 000000 000000          .WORD    0.0
3419
3420 044762          T11BF2: .BLKW  8.          ;MESSAGE BUFFER
3421
3422
3423
3424
3425      ;
3426      ;LOCAL TEXT MESSAGES FOR TEST
3427      ;
3428 045002 111 116 111 T11NBA: .ASCIZ 'INITIALIZE Command Not Accepted'
3429 045042 111 116 111 T112REJ: .ASCIZ 'INITIALIZE Not Rejected With Non-Zero Mode Field'
3430 045123 107 105 124 T113REJ: .ASCIZ 'GET STATUS Not Accepted'
3431 045153 107 105 124 T114REJ: .ASCIZ 'GET STATUS Not Rejected With Non-Zero Mode Field'
3432 045234 103 157 156 T11SSR: .ASCIZ 'Contents of TSSR Incorrect After INITIALIZE'
3433 045310 103 157 156 T11SR2: .ASCIZ 'Contents of TSSR Incorrect After GET STATUS'
3434 045364 105 170 160 T11NINT: .ASCIZ 'Expected Interrupt Not Received On INITIALIZE'
3435 045442 111 156 143 T11TSBA: .ASCIZ 'Incorrect TSBA Address After INITIALIZE'
3436 045512 116 157 156 TST11ID: .ASCIZ 'Non-Tape Motion Commands'
3437
3438          .EVEN

```


TEST 11: NON TAPE MOTION COMMANDS

```

3440
3441
3442
3443
3444
3445
3446
3447
3448 045544
3449 045544
3450 045550 012701 044670
3451 045554 012721 100213
3452 045560 005021
3453 045562 005021
3454 045564 005021
3455 045566 005021
3456 045570 005021
3457 045572 005021
3458 045574 005021
3459 045576 005011
3460 045600 005037 044712
3461 045604 000207
3462
3463
3464
3465
3466
3467
3468
3469 045606
3470 045606
3471 045612 012701 044670
3472 045616 012721 100217
3473 045622 005021
3474 045624 005021
3475 045626 005021
3476 045630 005021
3477 045632 005021
3478 045634 005021
3479 045636 005021
3480 045640 005011
3481 045642 005037 044712
3482 045646 000207
3483 045650
    045650
    045650 104401
3484 045652

;
;
;ROUTINE TO RESTORE COMMAND PACKET TO START-UP (DEFAULT) VALUES
;INITIALIZE COMMAND
;
;
T11REST:
    SAVREG
    MOV     #T11PACKET,R1
    MOV     #100213,(R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     T11BFR
    RTS     PC
;SAVE THE REGISTERS
;START OF THE PACKET
;INITIALIZE WITH ACK, IE
;ADDRESS OF CHAR DATA BLOCK
;EXTENDED ADDRESS
;SIZE OF DATA BLOCK IN BYTES
;ADDRESS OF MESSAGE BUFFER
;LENGTH OF MESSAGE BUFFER
;CLEAR 1ST LOC IN MESSAGE BUFFER
;RETURN

;
;
;ROUTINE TO RESTORE COMMAND PACKET TO START UP (DEFAULT) VALUES
;GET STATUS COMMAND
;
;
T11RT2:
    SAVREG
    MOV     #T11PACKET,R1
    MOV     #100217,(R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     (R1)
    CLR     T11BFR
    RTS     PC
    ENDTST
;SAVE THE REGISTERS
;START OF THE PACKET
;GET STATUS WITH ACK, IE
;ADDRESS OF CHAR DATA BLOCK
;EXTENDED ADDRESS
;SIZE OF DATA BLOCK IN BYTES
;ADDRESS OF MESSAGE BUFFER
;LENGTH OF MESSAGE BUFFER
;CLEAR 1ST LOC IN MESSAGE BUFFER
;RETURN

L10102:
    TRAP    C$ETST
ENDMOD

```

TEST 11: NON TAPE MOTION COMMANDS

```

1          .TITLE    TSV6 - PARAMETER CODING
7
12
18
19 045652          BGNMOD    TSV6
19 045652          TSV6::
20
21          .SBTTL    HARDWARE PARAMETER CODING SECTION
22
23          ;**
24          ; THE HARDWARE PARAMETER CODING SECTION CONTAINS MACROS
25          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
26          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
27          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
28          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
29          ; WITH THE OPERATOR.
30          ;--
31 045652          BGNHRD
31 045652    000010    .WORD L10107-L#HARD/2
31 045654          L#HARD::
32
33 045654          GPRMA    HPM1,0,0,160010,177776,YES    ;GET TSBA/TSDB REGISTER ADDRESS.
33 045654    000031    .WORD    T#CODE
33 045656    045674    .WORD    HPM1
33 045660    160010    .WORD    T#LOLIM
33 045662    177776    .WORD    T#HILIM
34 045664          GPRMA    HPM2,2,0,0,776,YES    ;GET VECTOR ADDRESS.
34 045664    001031    .WORD    T#CODE
34 045666    045730    .WORD    HPM2
34 045670    000000    .WORD    T#LOLIM
34 045672    000776    .WORD    T#HILIM
35          ;GPRMD    HPM3,4,0,340,0,7,YES    ;GET INTERRUPT PRIORITY.
36 045674          ENDRD
36          .EVEN
36          045674
37 045674    104    105    126    L10107:    HPM1:    .ASCIZ    'DEVICE ADDRESS (TSBA/TSDB) '
38 045730    111    116    124    HPM2:    .ASCIZ    'INTERRUPT VECTOR '
39 045754    111    116    124    HPM3:    .ASCIZ    'INTERRUPT PRIORITY '
40          .EVEN

```


SOFTWARE PARAMETER CODING SECTION

```

42          .SBTTL  SOFTWARE PARAMETER CODING SECTION
43
44          ;**
45          ; THE SOFTWARE PARAMETER CODING SECTION CONTAINS MACROS
46          ; THAT ARE USED BY THE SUPERVISOR TO BUILD P-TABLES.  THE
47          ; MACROS ARE NOT EXECUTED AS MACHINE INSTRUCTIONS BUT ARE
48          ; INTERPRETED BY THE SUPERVISOR AS DATA STRUCTURES.  THE
49          ; MACROS ALLOW THE SUPERVISOR TO ESTABLISH COMMUNICATIONS
50          ; WITH THE OPERATOR.
51          ;--
52 046004      BGNSFT
53 046004      .WORD L10110-L$SOFT/2
54 046006      L$SOFT::
55          ; GPRML  SPM1,0,-1,YES          ; GET TRANSPORT TEST FLAG.
56          ; GPRML  SPM4,2,-1,YES          ; GET ITERATION CONTROL.
57 046006      .WORD  T$CODE
58 046010      .WORD  SPM4
59 046012      .WORD  -1
60          ; GPRMD  SPM6,4,D,7777,0,7777,YES      ; GET LOCAL ERROR LIMIT
61          ; GPRMD  SPM7,6,D,7777,0,7777,YES      ; GET GLOBAL ERROR LIMIT
62          ENDSFT
63          .EVEN
64
65          L10110:
66          SPM1:  .ASCIZ  'ENABLE TRANSPORT TESTS '
67          SPM4:  .ASCIZ  'INHIBIT ITERATIONS '
68          SPM6:  .ASCIZ  'PER TEST ERROR LIMIT '
69          SPM7:  .ASCIZ  'PER UNIT ERROR LIMIT '
70          .SBTTL  PATCH AREA
71
72          ;
73          ; FINALLY A GENEROUS PATCH AREA.
74          ;
75          ; AND AN ADJUSTMENT TO ACCOUNT FOR THE "LASTAD BIT7" HACK
76          ; DESCRIBED IN "SUPPRG.MEM" (FOR REV C).
77          ;
78          PATCH::
79          .BLKW  32.
80          .-.!377+1
81          LASTAD          ;SET LAST USED ADDRESS.
82          .EVEN
83          .WORD  0
84          .WORD  0
85          L$LAST::
86          ENDMOD
87          .END

```

SYMBOL TABLE

ADDSR	012116	G	C#AU	= 000052	DEVDR0	023414	FRESIZ	003122	G	INTFLA	016135		
ADR	= 000020	G	C#AUTO	= 000061	DEVNRD	023333	FUSI	004113		INTMAS	016134		
AMBTSS	006631		C#BRK	= 000022	DEVNXR	023251	F#AU	= 000015		INTR	016206	G	
ASSEMB	= 000010		C#BSEG	= 000004	DEVONL	023201	F#AUTO	= 000020		INTREC	002222	G	
A1716	= 000003		C#BSUB	= 000002	DEVSUM	023144	F#BGN	= 000040		INTVEC	016136		
BADDAT	003152	G	C#CEFG	= 000045	DFPTBL	002154	F#CLEA	= 000007		INTX	004274		
BADSSR	015670	G	C#CLCK	= 000062	DIAGMC	= 000000	F#DU	= 000016		INVERT	021176	G	
BDVPCR	= 177520	G	C#CLEA	= 000012	DICEA	= 000001	F#END	= 000041		IOCKI	= 000200		
BENBSW	002226	G	C#CLOS	= 000035	DSBINT	016174	F#HARD	= 000004		IOKSTP	= 000001		
BIE	= 040000		C#CLP1	= 000006	DUAD12	004637	F#HW	= 000013		IPRI	002210	G	
BIT0	= 000001	G	C#CVEC	= 000036	DUFLG	003106	F#INIT	= 000006		ISR	= 000100	G	
BIT00	= 000001	G	C#DCLN	= 000044	DUMMY	003056	F#JMP	= 000050		IVEC	002206	G	
BIT01	= 000002	G	C#DODU	= 000051	EF.CON	= 000036	F#MOD	= 000000		IXE	= 004000	G	
BIT02	= 000004	G	C#DRPT	= 000024	EF.NEW	= 000035	F#MSG	= 000011		I#AU	= 000041		
BIT03	= 000010	G	C#DU	= 000053	EF.PWR	= 000034	F#PROT	= 000021		I#AUTO	= 000041		
BIT04	= 000020	G	C#EDIT	= 000003	EF.RES	= 000037	F#PWR	= 000017		I#CLN	= 000041		
BIT05	= 000040	G	C#ERDF	= 000055	EF.STA	= 000040	F#RPT	= 000012		I#DU	= 000041		
BIT06	= 000100	G	C#ERHR	= 000056	EMAXDU	016767	F#SEG	= 000003		I#HRD	= 000041		
BIT07	= 000200	G	C#ERRO	= 000060	EN	= 000000	F#SOFT	= 000005		I#INIT	= 000041		
BIT08	= 000400	G	C#ERSF	= 000054	ENAIN	016142	F#SRV	= 000010		I#MOD	= 000041		
BIT09	= 001000	G	C#ERSO	= 000057	ENVIRN	020620	F#SUB	= 000002		I#MSG	= 000041		
BIT1	= 000002	G	C#ESCA	= 000010	EPRTSW	002176	F#SW	= 000014		I#PROT	= 000040		
BIT10	= 002000	G	C#ESEG	= 000005	EPRT1	006354	F#TEST	= 000001		I#PTAB	= 000041		
BIT11	= 004000	G	C#ESUB	= 000003	EPRT2	006354	GDDAT	003154	G	I#PWR	= 000041		
BIT12	= 010000	G	C#ETST	= 000001	ERCH	011723	GERRMA	002172	G	I#RPT	= 000041		
BIT13	= 020000	G	C#EXIT	= 000032	ERRHI	002234	GETPAT	020164	G	I#SEG	= 000041		
BIT14	= 040000	G	C#GETB	= 000026	ERRK	016746	GETSEL	020246	G	I#SETU	= 000041		
BIT15	= 100000	G	C#GETW	= 000027	ERRLO	002236	G#CNT0	= 000200		I#SFT	= 000041		
BIT2	= 000004	G	C#GMAN	= 000043	ERRNO	= 002144	G#DELM	= 000372		I#SRV	= 000041		
BIT3	= 000010	G	C#GPHR	= 000042	ERRVEC	= 000004	G#DISP	= 000003		I#SUB	= 000041		
BIT4	= 000020	G	C#GPLO	= 000030	ERTABE	003372	G#EXCP	= 000400		I#TST	= 000041		
BIT5	= 000040	G	C#GPRI	= 000040	ERTABL	003172	G#HILI	= 000002		J#JMP	= 000167		
BIT6	= 000100	G	C#INIT	= 000011	ESUM	016750	G#LOLI	= 000001		KIPAR0	= 172340		
BIT7	= 000200	G	C#INLP	= 000020	EVL	= 000004	G#NO	= 000000		KIPAR1	= 172342		
BIT8	= 000400	G	C#MANI	= 000050	EXBCNT	= 000010	G#OFFS	= 000400		KIPAR2	= 172344		
BIT9	= 001000	G	C#MEM	= 000031	EXPBRE	015472	G#OFISI	= 000376		KIPAR3	= 172346		
BOE	= 000400	G	C#MSG	= 000023	EXPD	002230	G#PRMA	= 000001		KIPAR4	= 172350		
BRINIT	004453		C#OPEN	= 000034	EXPGOT	004527	G#PRMD	= 000002		KIPAR5	= 172352		
BSELO	= 000000		C#PNTB	= 000014	EXPGT2	004563	G#PRML	= 000000		KIPAR6	= 172354		
BSEL1	= 000001		C#PNTF	= 000017	EXPMMSG	002320	G#RADA	= 000140		KIPAR7	= 172356		
CHKAMB	016034		C#PNTS	= 000016	EXPREC	015464	G#RAD8	= 000000		KIPDR0	= 172300		
CHKMAN	020470	G	C#PNTX	= 000015	EXTA	005766	G#RADL	= 000040		KIPDR1	= 172302		
CHKTSS	016326		C#QIO	= 000377	EXTEND	005764	G#RADL	= 000120		KIPDR2	= 172304		
CKDROP	017172		C#RDBU	= 000007	EXTFEA	002224	G#RADO	= 000020		KIPDR3	= 172306		
CKEMAX	017072		C#REFG	= 000047	E#END	= 002100	G#XFER	= 000004		KIPDR4	= 172310		
CKMSG	011350	G	C#RESE	= 000033	E#LOAD	= 000035	G#YES	= 000010		KIPDR5	= 172312		
CKMSG2	011470	G	C#REVI	= 000003	FATERR	= 000060	HIADDR	= 001400		KIPDR6	= 172314		
CKRAM	011104	G	C#RFLA	= 000021	FATFLG	002220	HOE	= 100000	G	KIPDR7	= 172316		
CKRAM2	011214	G	C#RPT	= 000025	FERCH	011712	HPM1	045674		KTENAB	003130	G	
CHDPKT	021250	G	C#SEFG	= 000046	FIFEXP	012160	HPM2	045730		KTFLG	003126	G	
CHPMEM	017650		C#SPRI	= 000041	FIF1MS	012232	HPM3	045754		KTINIT	021044		
CONFIG	017240		C#SVEC	= 000037	FIF2MS	012301	IBE	= 010000	G	KTOFF	017264		
COUNT	002306	G	C#TPRI	= 000013	FILLME	017412	IDU	= 000040	G	KTON	017246		
CSRADD	002204	G	DATA	002310	G	FNOINT	004211	IER	= 020000	G	LEKMA	002170	G
CTAB	003160	G	DATASC	020222		FORCER	002174	IFALT	004252		LISTAL	= 000001	
CTABE	003172	G	DEBUGH	011622		FREE	003120	INCERK	017034		LOE	= 040000	G
CTABM	003160	G	DEVcnt	002216	G	FREEHI	003124	INTCPC	016140		LOOPCN	002214	G

SYMBOL TABLE

LOOPCO	013116		L10001	002174		L10073	037026		NXR	003734		PRI05	=	000240	G
LOOPFL	003156	G	L10002	005762		L10074	037232		NXRERR	005732	G	PRI06	=	000300	G
LOT	=	000010	G	L10003	012034		L10075	043512	NXRX	003773		PRI07	=	000340	G
L\$ACP	002110	G	L10004	012052		L10076	041012		NXTU	022040		PRMESS		014232	
L\$APT	002036	G	L10005	012070		L10077	041404		OFL	=	000100	PRMNO		002316	G
L\$AU	022370	G	L10006	012076		L10100	042274		ONEFIL	=	000000	PRMSG0		014542	G
L\$AUT	002070	G	L10007	012114		L10101	042522		O\$APTS	=	000000	PRMSG1		014722	
L\$AUTO	022574	G	L10010	012132		L10102	045650		O\$AU	=	000001	PRMSG2		015025	
L\$CCP	002106	G	L10011	012156		L10103	043760		O\$BGNR	=	000001	PROASC		014410	
L\$CLEA	022654	G	L10012	012230		L10104	044216		O\$BGNS	=	000001	PR1ASC		014455	
L\$CO	002032	G	L10013	012400		L10105	044436		O\$DU	=	000001	PST32W		003146	G
L\$DEPO	002011	G	L10014	013114		L10106	044662		O\$ERRT	=	000000	PUNIT		022322	
L\$DESC	003404	G	L10015	013742		L10107	045674		O\$GNSW	=	000001	PW.D11	=	000021	
L\$DESP	002076	G	L10016	013764		L10110	046014		O\$POIN	=	000001	PW.D13	=	000022	
L\$DEVP	002060	G	L10017	015470		MEMADD	013744	G	O\$SETU	=	000000	PW.D22	=	000020	
L\$DISP	002124	G	L10020	015476		MEMCK	021266	G	PASRPT	022072		PW.NOP	=	000000	
L\$DLY	002116	G	L10021	015504		MENASC	020437		PATCH	046154	G	PW.NO1	=	000023	
L\$DTP	002040	G	L10022	015516		MENERR	020364		PATDAT	020220		PW.RDE	=	000024	
L\$DTYP	002034	G	L10023	015540		MENRES	020466		PC.ERA	=	002400	PW.RDR	=	000001	
L\$DU	022466	G	L10024	015566		MMVEC	=	000250	PC.IER	=	002000	PW.RDS	=	000005	
L\$DUT	002072	G	L10025	015726		MSA.FR	=	000006	PC.NOO	=	001000	PW.RFI	=	000003	
L\$DVTY	003376	G	L10026	016236		MSA.NO	=	000000	PC.REL	=	000000	PW.WCT	=	000006	
L\$EF	002052	G	L10030	022320		MSA.NR	=	000004	PC.REW	=	000400	PW.WFI	=	000004	
L\$ENVI	002044	G	L10031	022464		MSA.VO	=	000002	PKBCNT	=	000006	PW.WFM	=	000007	
L\$ETP	002102	G	L10032	022572		MSGEXP	012134	G	PKHI	=	000004	PW.WMI	=	000010	
L\$EXP1	002046	G	L10033	022652		MSGLOO	013054	G	PKLOW	=	000002	PW.WNP	=	000011	
L\$EXP4	002064	G	L10034	022700		MSGSTA	012340	G	PKTADD	007550		PW.WTR	=	000002	
L\$EXP5	002066	G	L10035	023142		MSGSUB	013732	G	PKTFRM	007512		P.ACK	=	100000	
L\$HARD	045654	G	L10036	023702		MS.ATT	=	000006	PKTGET	012054	G	P.CMD	=	000037	
L\$HIME	002120	G	L10037	023550		MS.EXT	=	000200	PKTMES	012100	G	P.CONT	=	000012	
L\$HPCP	002016	G	L10040	023632		MS.RSD	=	000001	PKTRAM	004741	G	P.CVC	=	040000	
L\$HPTP	002022	G	L10041	024400		MS.RSF	=	000020	PKTSSR	012036	G	P.FMT	=	000140	
L\$HW	002154	G	L10042	025072		MS.RST	=	000010	PNT	=	001000	P.FORM	=	000011	
L\$ICP	002104	G	L10043	026426		M2901	026152		PRAMPK	013766		P.GETS	=	000017	
L\$INIT	021546	G	L10044	025334		M8186	005550		PRASC	014513		P.IE	=	000200	
L\$LADP	002026	G	L10045	025634		M8189	005641		PRBEXP	015460		P.INIT	=	000013	
L\$LAST	046404	G	L10046	026132		NBA	=	002000	PRBMSG	015326		P.MODE	=	007400	
L\$LOAD	002100	G	L10047	027532		NEWPAS	022026		PRBREC	015462		P.OPP	=	020000	
L\$LUN	002074	G	L10050	027000		NODEV	003110	G	PRBTOT	015413		P.POSI	=	000010	
L\$MREV	002050	G	L10051	027350		NOINIT	004331		PRBYTE	015112	G	P.READ	=	000001	
L\$NAME	002000	G	L10052	031002		NOINTR	004215		PRI	=	002000	P.SWB	=	010000	
L\$PRIO	002042	G	L10053	030062		NOITS	002166	G	PRIADD	010154		P.WRIT	=	000005	
L\$PROT	021536	G	L10054	030406		NOMAN	020524		PRIAO	010224		P.WRTC	=	000004	
L\$PRT	002112	G	L10055	034370		NOMEM	005454		PRIBXO	007606	G	P.WRTS	=	000006	
L\$REPP	002062	G	L10056	031334		NP.IR	=	000200	PRIEQU	010054		QVP		002202	G
L\$REV	002010	G	L10057	031620		NP.LOO	=	000040	PRIPKT	007364	G	RAMASC		014146	
L\$RPT	022702	G	L10060	032064		NP.OUT	=	000100	PRIRAM	010062		RAMDAT		002240	G
L\$SOFT	046006	G	L10061	032312		NP.WRP	=	000020	PRITAD	010270		RAMERR		015500	G
L\$SPC	002056	G	L10062	032556		NSI	004146		PRITSS	006020		RAMEXP		015520	G
L\$SPCP	002020	G	L10063	033116		NSINIT	004403		PRITO	010352		RAMFOR		010112	
L\$SPTP	002024	G	L10064	035274		NUL	004523		PRIT1	010415		RAMSIZ		002300	G
L\$STA	002030	G	L10065	040410		NULCR	004524		PRI XOR	007736	G	RAMTAD		015506	G
L\$SW	002164	G	L10066	035536		NXM	=	004000	PRI00	=	000000	RCVHIA		002302	G
L\$TEST	002114	G	L10067	035776		NXMFLG	003132	G	PRI01	=	000040	RCVLOA		002304	G
L\$TIML	002014	G	L10070	036202		NXMHI	003136	G	PRI02	=	000100	RDERR		005202	
L\$UNIT	002012	G	L10071	036370		NXMLO	003134	G	PRI03	=	000140	RECMSG		002464	G
L10000	002162		L10072	036574		NXMTST	021442		PRI04	=	000200				

SYMBOL TABLE

RECV	002232	G	S1.IEO=	010000	TST6ID	030763	T1.1	023504	T4	025074	G	
REGSAV	020130		S1.IFM=	001000	TST7ID	034273	T1.2	023564	T4LOOP	025114		
RETErr	005366		S1.IHE=	000400	TST8ID	035257	T10	040412	T4.1	025074		
REWIND	011004	G	S1.IID=	004000	TST9ID	040311	T10BFR	042552	T4.2	025336		
RMCHBE=	000167		S1.IIR=	020000	TSV2	002000	G	T10BUF	042614	T4.3	025636	
RMCHEN=	000200		S1.IZR=	040000	TSV3	002174	G	T10DAT	042540	T5	026430	G
RMMSG8=	000215		S1.PAR=	100000	TSV4	021536	G	T10DTA	042602	T5ADDR	027470	
RMMSG8=	000234		S2.ATI=	000010	TSV5	023464	G	T10INT	043250	T5LOOP	026446	
RMPKTB=	000201		S2.BTI=	000004	TSV6	045652	G	T10L00	040430	T5MEM	027432	
RMPKTE=	000210		S2.DIM=	000200	TTIBFR=	177562	G	T10MBF	042634	T5.1	026452	
RMR	= 010000		S2.ILW=	000100	TTICSR=	177560	G	T10NBA	042731	T5.2	027020	
RWPACK	011100		S2.INR=	000020	TTIVEC=	000060	G	T10NIN	043157	T6	027534	G
SC	= 100000		S2.OUT=	000040	T#ARGC=	000003		T10NNB	043013	T6INT	030553	
SCE	= 020000		S2.UND=	000003	T#CODE=	001130		T10PAC	042530	T6LOOP	027552	
SCHERR	005274		TBLEND=	003056	T#ERRN=	002144		T10PKT	042572	T6NBA	030450	
SCME	005007		TCOASC	006472	T#EXCP=	000000		T10RST	043366	T6NINT	030631	
SDELAY	010650		TCOCOD	006672	T#FLAG=	000040		T10RT2	043440	T6PACK	030440	
SELASC	020432		TEMP1	003112	T#GMAN=	000000		T10SSR	043070	T6SSR	030475	
SELDAT=	000004		TEMP2	003114	T#HILI=	000776		T10.1	040430	T6TSBA	030711	
SEL2	= 000002		TERCLS=	000016	T#LAST=	000001		T10.2	041014	T6.1	027552	
SETMAP	017306		TESTNO=	000013	T#LOLI=	000000		T10.3	041406	T6.2	030076	
SETU	022124		TEXASC	006431	T#LSYM=	010000		T10.4	042276	T7	031004	G
SFFMSG	012072	G	TFCASC	006533	T#LTNO=	000013		T11	043514	T7BFR	033166	
SFHERR	003701		TIMEXP	015542	T#NEST=	177777		T11BFR	044712	T7DATA	033150	
SFIERR	003646		TIMSGO	015570	T#NSO =	000000		T11BF2	044762	T7INT	034121	
SFIMSG	012024	G	TINERR	012011	T#NS1 =	000005		T11DAT	044700	T7LOOP	031022	
SFPTBL	002164	G	TMPBFR	002630	T#NS2 =	000002		T11DTA	044750	T7NBA	033300	
SIFLAG	003150	G	TNAM	016674	T#NS3 =	000003		T11L00	043532	T7NINT	034030	
SIMSG	011756		TRANST	002164	T#PTNU=	000000		T11NBA	045002	T7PACK	033140	
SKIPT	003374		TSBA	= 000000	T#SAVL=	177777		T11NIN	045364	T7REST	034322	
SOFINI	015764	G	TSBAH =	000001	T#SEGL=	177777		T11PAC	044670	T7SP	033160	
SPACE	010462	G	TSBAM2	026242	T#SEKO=	010000		T11PK2	044740	T7SSR	033741	
SPM1	046014		TSBAM3	026324	T#SUBN=	000004		T11RES	045544	T7TSBA	034210	
SPM4	046044		TSDB =	000000	T#TAGL=	177777		T11RT2	045606	T7.1	031022	
SPM6	046074		TSDBH =	000001	T#TAGN=	010111		T11SR2	045310	T7.2	031350	
SPM7	046124		TSFCOD	007232	T#TEMP=	000000		T11SSR	045234	T7.3	031622	
SRO	= 177572		TSREJ =	000006	T#TEST=	000013		T11TSB	045442	T7.4	032066	
SR1	= 177574		TSSDEF	006602	T#TSTM=	177777		T11.1	043532	T7.5	032314	
SR2	= 177576		TSSR =	000002	T#TSTS=	000001		T11.2	043774	T7.6	032560	
SR3	= 172516		TSSRBI	003476	T#TAU =	010031		T11.3	044220	T72DAT	033206	
SSR	= 000200		TSSRFO	006411	T#TAUT=	010033		T11.4	044440	T72DON=	033222	
STATCO	012402		TSSRH =	000003	T#TCLE=	010034		T112RE	045042	T72NBA	033222	
SVCGBL=	000000		TSSX	004014	T#TDU =	010032		T113RE	045123	T72REJ	033353	
SVCINS=	000000		TSTBLK	002750	T#THAR=	010107		T114RE	045153	T73REJ	033452	
SVCSUB=	000001		TSTCNT	002212	T#THW =	010000		T2	023704	T74REJ	033545	
SVCTAG=	000000		TSTEND	016710	T#THINI=	010030		T2LOOP	023722	T75REJ	033643	
SVCTST=	000001		TSTFLA	002312	T#THMSG=	010025		T2SSR	024300	T8	034372	G
S#LSYM=	010000		TSTL00	016446	T#THPRO=	010027		T2TSBA	024166	T8BFR	035022	
SO.IDB=	000010		TSTPTR	002314	T#THRPT=	010035		T2TSSR	024233	T8DATA	035010	
SO.IFB=	000002		TSTSET	016500	T#HSEG=	010000		T23A	003140	T8LOOP	034410	
SO.IFP=	000001		TST1ID	023662	T#HSOF=	010110		T23B	003142	T8NVCK	035077	
SO.ILD=	000020		TST10I	043337	T#HSRV=	010026		T3	024402	T8PACK	035000	
SO.ION=	000040		TST11I	045512	T#HSUB=	010106		T3BFLG	003144	T8SSR	035170	
SO.IRD=	000100		TST2ID	024352	T#HSW =	010001		T3LOOP	024420	T8VCK	035042	
SO.IRW=	000004		TST3ID	025045	T#HTES=	010102		T3SSR	024774	T9	035276	G
SO.ISP=	000200		TST4ID	026404	T1	023464	G	T3TSBA	024664	T9BFR	037262	
S1.ICE=	002000		TST5ID	027402	T1LOOP	023502		T3TSSR	024730	T9DATA	037250	

SYMBOL TABLE

T9INT	040137	UAM	=	000200	G	WRTCHR	010652	G	XSOONL	=	000100	X2.EXT	=	000200	
T9LOOP	035320	UNITN	=	002200	G	WRTERR	005107		XSOPED	=	000010	X2.OPM	=	100000	
T9NBA	037316	UNREC	=	000006		WRTMSG	005052		XSORLL	=	010000	X2.RCE	=	040000	
T9NINT	040046	USI	=	004117		WSMBK	021260	G	XSORLS	=	040000	X2.REV	=	000077	
T9PACK	037240	WAITF	=	016240	G	XFERAS	015730		XSOITMK	=	100000	X2.SPA	=	035400	
T9REST	040336	WC.IFA	=	000200		XNXM	016366		XSOVCK	=	000020	X2.UNI	=	000007	
T9SSR	037757	WC.IFE	=	000002		XORBFO	007670		XSOVLE	=	004000	X2.WCF	=	002000	
T9TSBA	040226	WC.IG0	=	000001		XORFOR	010006		XSOVLK	=	000004	X3.DCK	=	000010	
T9.1	035320	WC.IRE	=	000010		XST0	=	000006	G	XXCOMM	=	003116	X3.MBZ	=	000006
T9.2	035566	WC.IRW	=	000004		XST1	=	000010	G	X\$ALWA	=	000000	X3.MDE	=	177400
T9.3	036000	WC.IOT	=	000100		XST2	=	000012	G	X\$FALS	=	000040	X3.OPI	=	000100
T9.4	036204	WC.IIT	=	000040		XST3	=	000014	G	X\$OFFS	=	000400	X3.REV	=	000040
T9.5	036372	WC.ISR	=	000020		XST4	=	000016	G	X\$TRUE	=	000020	X3.RIB	=	000001
T9.6	036576	WF.IED	=	000010		XSOBOT	000002		X1.COR	=	020000	X3.SPA	=	000200	
T9.7	037030	WF.IER	=	000004		XSOEOT	000001		X1.DLT	=	100000	X3.TRF	=	000020	
T92DAT	037302	WF.IHI	=	000200		XSOIE	=	000040		X1.MBZ	=	017375	X4.HSP	=	100000
T92DON	037316	WF.IRE	=	000040		XSOILA	=	000400		X1.RBP	=	000400	X4.MBZ	=	017400
T92REJ	037371	WF.IWF	=	000020		XSOILC	=	001000		X1.SPA	=	040000	X4.RCE	=	040000
T93REJ	037470	WF.IWR	=	000100		XSOLET	=	020000		X1.UNC	=	000002	X4.TSM	=	020000
T94REJ	037563	WF.I3R	=	000002		XSOHOT	=	000200		X2.BUF	=	000100	X4.WRC	=	000377
T95REJ	037661	WF.I4R	=	000001		XSONEF	=	002000							

. ABS. 046404 000
000000 001

ERRORS DETECTED: 0

VIRTUAL MEMORY USED: 28880 WORDS (113 PAGES)

DYNAMIC MEMORY: 20060 WORDS (77 PAGES)

ELAPSED TIME: 00:37:00

CNTSAA0.BIC,CNTSAA0.SEQ/-SP=SVC34/ML,TSV1A,TSV22A,TSV3A,TSV4,TSV5A,TSV6