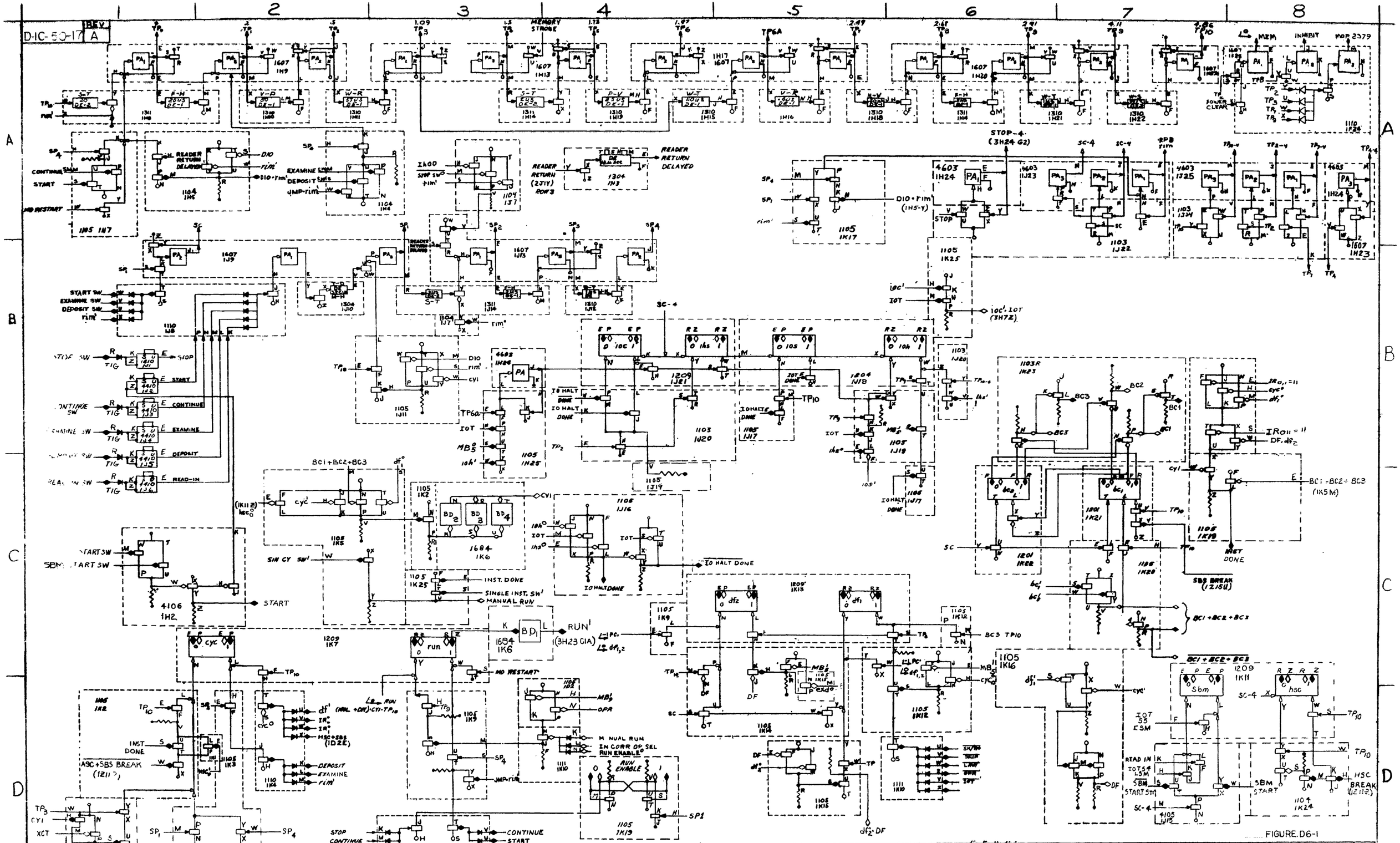
D-IC-50-17
REV. A
MEMORY STROBE
MEM INHIBIT
POR 2579
POWER CLEAR
STOP-4 (3H24 G2)
READER RETURN (2711Y) ROW3
READER RETURN DELAYED
DIO+rim' (1H5-Y)
IOC', IOT (3H7Z)
BC1+BC2+BC3
IR011 = 11
DF, df2
BC1+BC2+BC3 (1K5M)
SBS BREAK (1Z15U)
INST DONE
IO HALT DONE
START
RUN'
(3H23 G1A)
NO RESTART
MANUAL RUN
SINGLE INST. SW'
RUN ENABLE
CONTINUE
STOP
EXAMINE
DEPOSIT
READ IN
FIGURE D6-1
DRAFTSMAN B. LUMPKIN
DATE 4-21-64
CHECKER
DATE
ENGINEER
DATE
PRODUCTION
DATE
digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS
CODE BS
DWG. NO. D-IC-50-17
REV. A
TITLE GENERAL CONTROL FUNCTIONS
PDP-IC-50
ADX-0