

PLEASE REFER TO THE  
CORPORATION AND  
IN CASE OF WHOLE  
SALES MANUFACTURE OR SALE  
BY THE  
EQUIPMENT CORPORATION

POWER AND GROUND TABLE FOR IC's							
	GND	+5V	-5V	+V	-V	+12V	-12V
578 14 PIN	7	14	-	-	-	-	-
578 15 PIN	8	15	-	-	-	-	-
578 18 PIN	9	18	-	-	-	-	-
578 20 PIN	10	20	-	-	-	-	-
578 24 PIN	12	24	-	-	-	-	-
578 28 PIN	14	28	-	-	-	-	-
578 32 PIN	20	40	-	-	-	-	-
7341	1	16	-	-	-	-	-
7341	4	-	-	8	5	-	-
7341	6	1	-	-	-	-	-
8275-5	7	26	-	-	-	-	-
8275	16	9	1	-	-	8	-

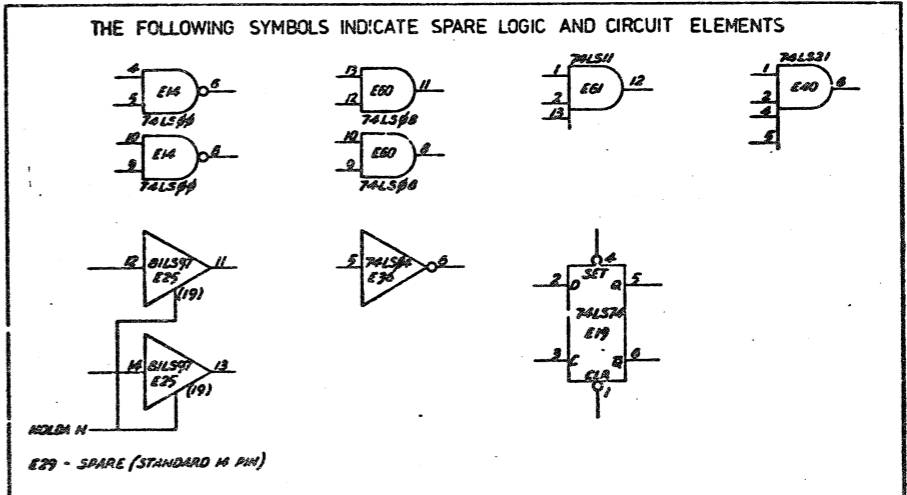
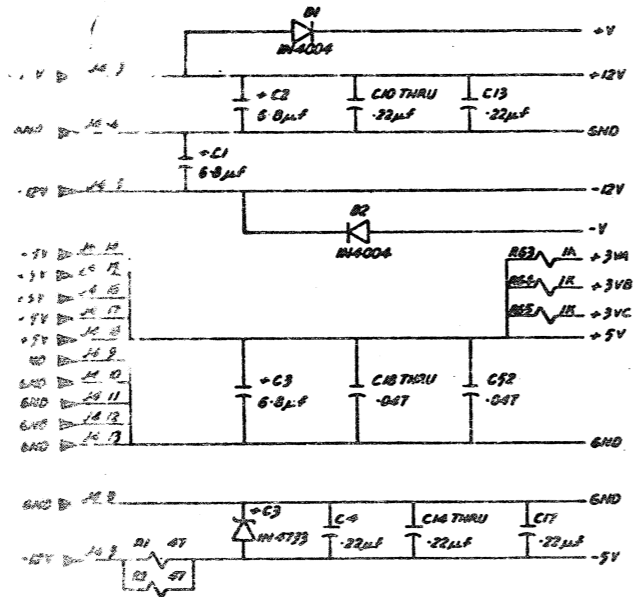
**SIGNAL NAMES HAVE THE FOLLOWING FORMAT**

S# NAME H  
S# NAME L

WHERE:  
S# IS A SHEET NUMBER WHERE SIGNAL ORIGINATES

NAME IS THE SIGNAL MNEMONIC  
H OR L IS THE LEVEL FOR THE SIGNALS TRUE OR LOGICAL  
ONE VALUE H = HIGH ; L = LOW

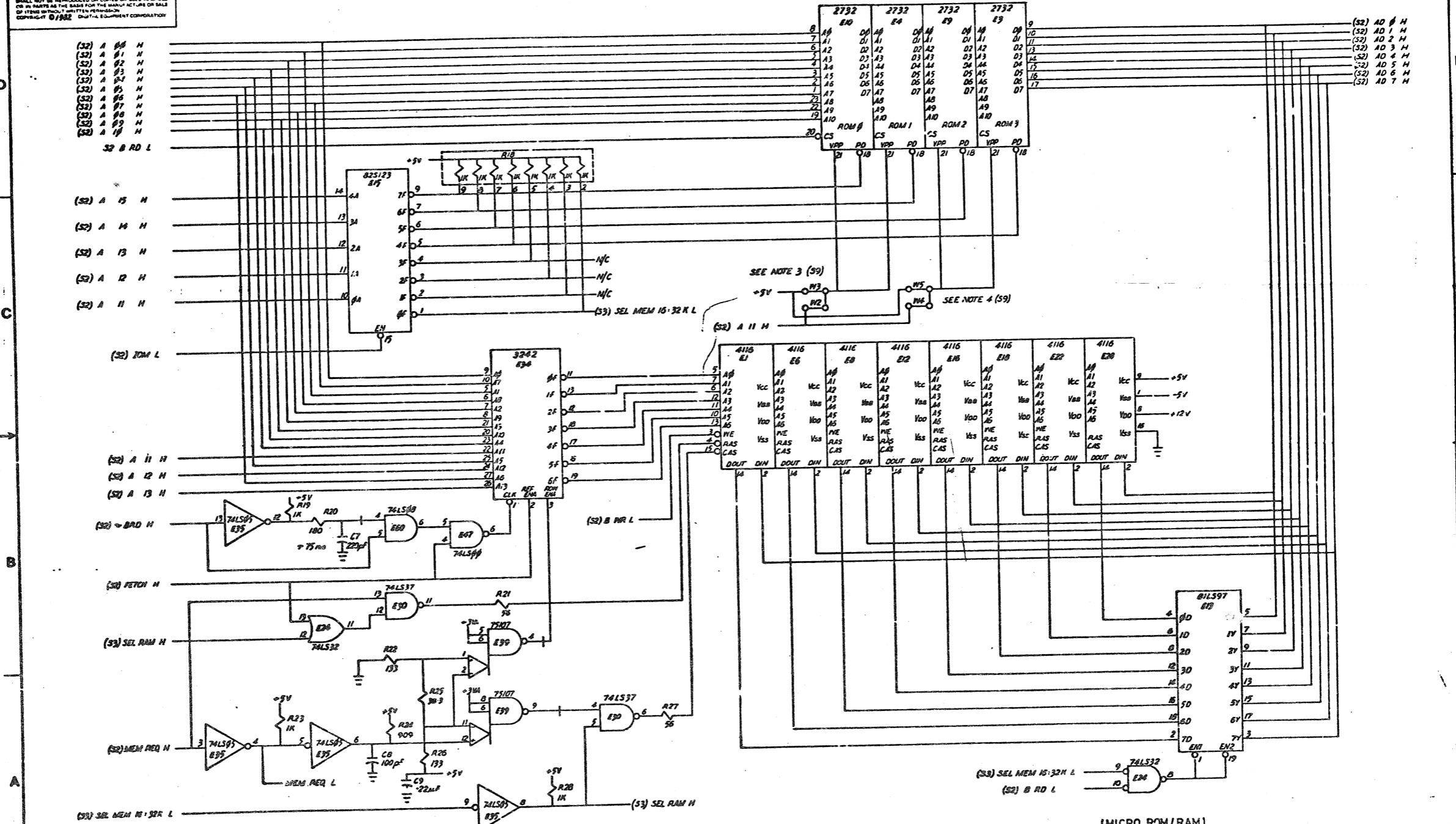
- MNEMONICS USED BY THIS PRINT SET**
- A = MICROPROCESSOR ADDRESS BUS
  - AD = MULTIPLEXED ADDRESS DATA BUS
  - ADENA = ADDRESS BUS ENABLE
  - B = BUFFERED
  - BAD = BUFFERED ADDRESS DATA BUS
  - B# = I/O ADDRESS B# HEX
  - B#-DBSD = BUFFERED 5-0688 MHz CLOCK SIGNAL
  - BB = I/O ADDRESS BB HEX
  - CLM = BAUD RATE TIMING
  - COMM = COMMUNICATION DEVICE
  - DMA = DIRECT MEMORY ACCESS CONTROLLER
  - DSR = DATA SET READY SIGNAL
  - ETR = DATA TERMINAL READY SIGNAL
  - ENA = ENABLE
  - FETCH = INSTRUCTION FETCH CYCLE
  - HOLDA = DMA REQUEST ACKNOWLEDGE
  - HOLD = DMA REQUEST
  - IN = SISO INPUT
  - INIT = INITIALISE
  - INTA = INTERRUPT ACKNOWLEDGE
  - INTRA = INTERRUPT
  - IMM = SELECT MEMORY PAGE
  - MMIN = MAIN COMPOSITE PORT
  - MSMNT = MAINTENANCE
  - MEM = MEMORY
  - N/C = NO CONNECTION
  - OUT = SISO OUTPUT
  - PBRX = PORT B BIT
  - RAM = RANDOM ACCESS MEMORY
  - RD = READ
  - RDY = READY
  - REC = RECEIVE
  - REQ = REQUEST
  - RT = ROUTE THROUGH COMPOSITE PORT
  - SIB = FIRST IN FIRST OUT MEMORY
  - STAT = STATUS
  - STP = VT100 STANDARD TERMINAL PORT
  - TIMING = BAUD RATE TIMING
  - UA = UART ADDRESS BUS
  - USART = UNIVERSAL SY
  - WR = WRITE
  - ZMIT = TRANSMIT
  - ZVX = LOGIC ONE SOURCE



QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST			
DRN	DATE	digital EQUIPMENT AUSTRALIA PTY. LTD.	
CHKD	DATE		
ENQ	DATE		
PROJ. ENQ.	DATE		
PROD.	DATE		
VT1XX-EB CONTROL			
D-UA-5414707-0-0		SIZE CODE	NUMBER
SCALE		DCS	5414707-0-1
SHEET	1 OF 9	DIST.	



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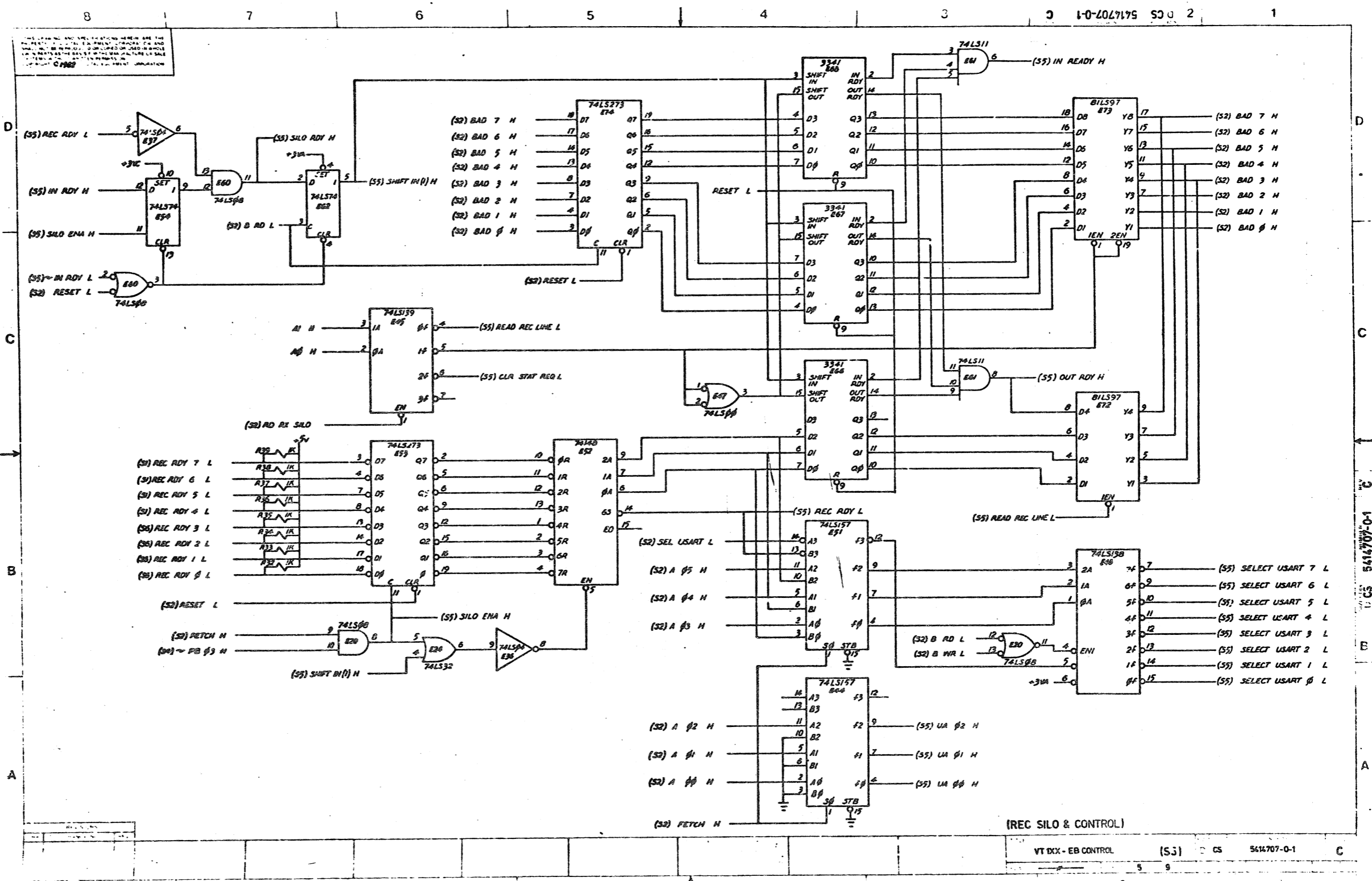


REV	CHANGES	BY	DATE

[MICRO ROM/RAM]		TITLE	VT1XX-EB CONTROL (S3)	SIZE	D	CS	NUMBER	5414707-0-1	REV.	C
SCALE	3	9	2	1						

5414707-0-1  
D [CS] B A

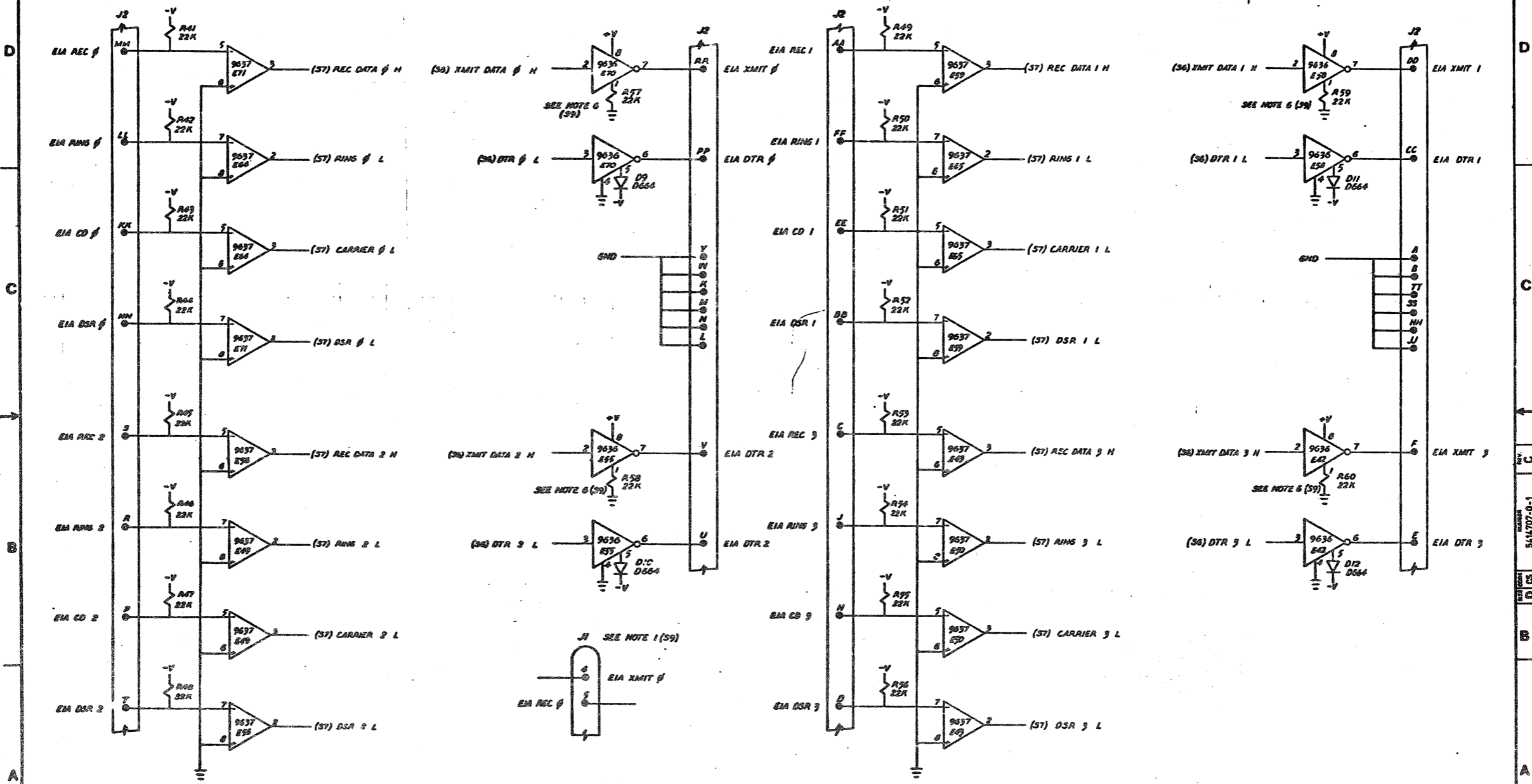




[REC SILO & CONTROL]  
 VT DCX - EB CONTROL (S3) CS 5614707-0-1 C



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REV.	CHANGE NO.	REV.

(RS232-C / RS423 TRANSCEIVERS 0:3)

TITLE	VT1XX-EB CONTROL (S7)	REV.	C
SCALE	SHEET 7 OF 9	DATE	



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- J1 IS THE MAIN AND ROUTE-THROUGH COMPOSITE PORT CONNECTION. IT ALSO CONNECTS ASYNCHRONOUS PORT 8 TO THE HOST VT188.
- W1 IS FOR FACTORY TEST AND SHOULD BE INSTALLED AT ALL TIMES.
- W3 INSTALLED ENABLES E18 AND E4 FOR 2716 (2K X 8) OPERATION. W2 INSTALLED ENABLES E18 AND E4 FOR 2732 (4K X 8) OPERATION. W2 AND W3 ARE MUTUALLY EXCLUSIVE, ONLY ONE CAN BE INSTALLED AT ANY TIME. NORMAL OPERATION IS WITH W2 INSTALLED, I.E. E18 AND E4 SET FOR 4K X 8 OPERATION.
- W5 INSTALLED ENABLES E9 AND E3 FOR 2716 (2K X 8) OPERATION. W4 INSTALLED ENABLES E9 AND E3 FOR 2732 (4K X 8) OPERATION. W4 AND W5 ARE MUTUALLY EXCLUSIVE, ONLY ONE CAN BE INSTALLED AT ANY TIME. NORMAL OPERATION IS WITH W4 INSTALLED, I.E. E9 AND E3 SET FOR 4K X 8 OPERATION.
- THE 54-14713 FOUR CHANNEL EXPANSION MODULE CONNECTS TO J3. ASYNCHRONOUS PORTS 4 THRU 7 ARE LOCATED ON THE 54-14713 MODULE.
- R57, R59, R58 AND R68 ARE THE SIGNAL SLEW RATE RESISTORS FOR PORTS 8, 1, 2, AND 3 RESPECTIVELY. REFER TO OPTION DESCRIPTION FOR CORRECT RESISTOR VALUES.
- MAINTENANCE/STATUS SWITCH IS SHOWN FOR REFERENCE ONLY. SWITCH IS MOUNTED ON E1A CLUSTER PANEL (54-14711). TABLE DESCRIBES SWITCH FUNCTION.

- E33-1 THRU E33-4 ARE THE ASYNCHRONOUS PORT MAPPING SWITCHES.  
 A SWITCH ON (CLOSED) DISABLES THE PORT.  
 A SWITCH OFF (OPEN) ENABLES THE PORT.  
 E33-5 THRU E33-8 ARE THE ASYNCHRONOUS PORT FLOW CONTROL SWITCHES.  
 A SWITCH ON (CLOSED) ENABLES XON/XOFF CONTROL.  
 A SWITCH OFF (OPEN) ENABLES DTR/DSR CONTROL.  
 THE VT1XX-EB OFFLINE DIAGNOSTIC SUBTEST 8 WILL DISPLAY STATUS OF SWITCH PACK E33.
- SWITCH PACK E23 CONTROLS MAIN AND ROUTE-THROUGH COMPOSITE PORT BAUD RATES AND CLOCK SOURCE. THE TABLE BELOW SHOWS CORRELATION BETWEEN SWITCH SETTINGS AND FUNCTION.

MAINTENANCE/STATUS SWITCH FUNCTION	
SWITCH POSITION	FUNCTION
RUN	IF IN "RUN" AT POWER UP OR AT VT1XX-EB INITIALISATION, VT1XX-EB WILL OPERATE IN STAT MUX MODE.
TEST (INIT)	IF IN "TEST" AT POWER UP OR AT VT1XX-EB INITIALISATION, VT1XX-EB WILL EVOKE OFF LINE DIAGNOSTICS.
TEST (OPR)	IF SET TO "TEST" POSITION DURING OPERATIONAL MODE, VT1XX-EB WILL DISPLAY NETWORK STATISTICS ON HOST VT188 SCREEN

BAUD RATE	COMPOSITE PORT BAUD RATE					
	ROUTE-THROUGH			MAIN		
	SW 2	SW 3	SW 4	SW 6	SW 7	SW 8
1200	ON	ON	ON	ON	ON	ON
1800	ON	ON	OFF	ON	ON	OFF
2400	ON	OFF	ON	ON	OFF	ON
3600	ON	OFF	OFF	ON	OFF	OFF
4800	OFF	ON	ON	OFF	ON	ON
7200	OFF	ON	OFF	OFF	ON	OFF
9600	OFF	OFF	ON	OFF	OFF	ON
19200	OFF	OFF	OFF	OFF	OFF	OFF

- NOTE:
- SW1 = ON (CLOSED) SELECTS EXTERNAL TIMING FOR R/T COMP. PORT.
  - SW1 = OFF (OPEN) SELECTS INTERNAL TIMING FOR R/T COMP. PORT.
  - SW5 = ON (CLOSED) SELECTS EXTERNAL TIMING FOR MAIN COMP. PORT.
  - SW5 = OFF (OPEN) SELECTS INTERNAL TIMING FOR MAIN COMP. PORT.

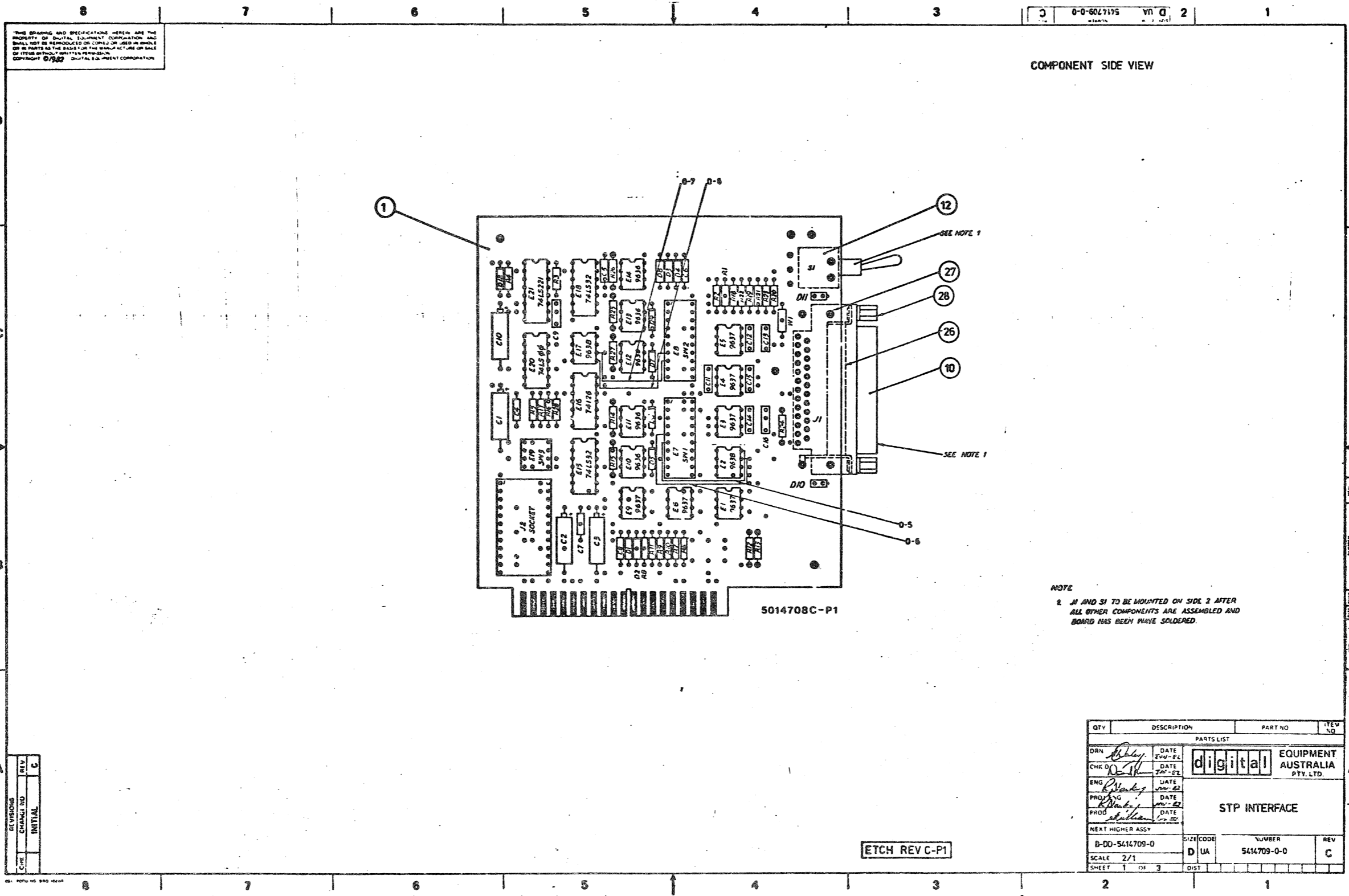
REVISIONS		
CHK	CHANGE NO	REV

(NOTES)

TITLE	VT1XX-EB CONTROL (S9)	SIZE CODE	C CS	NUMBER	5414707-0-1	REV	C
SCALE		SHEET	9 of 9	DIST			

REV C  
 NUMBER 5414707-0-1  
 C CS





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0-0-6021195 YN D 2 1

COMPONENT SIDE VIEW

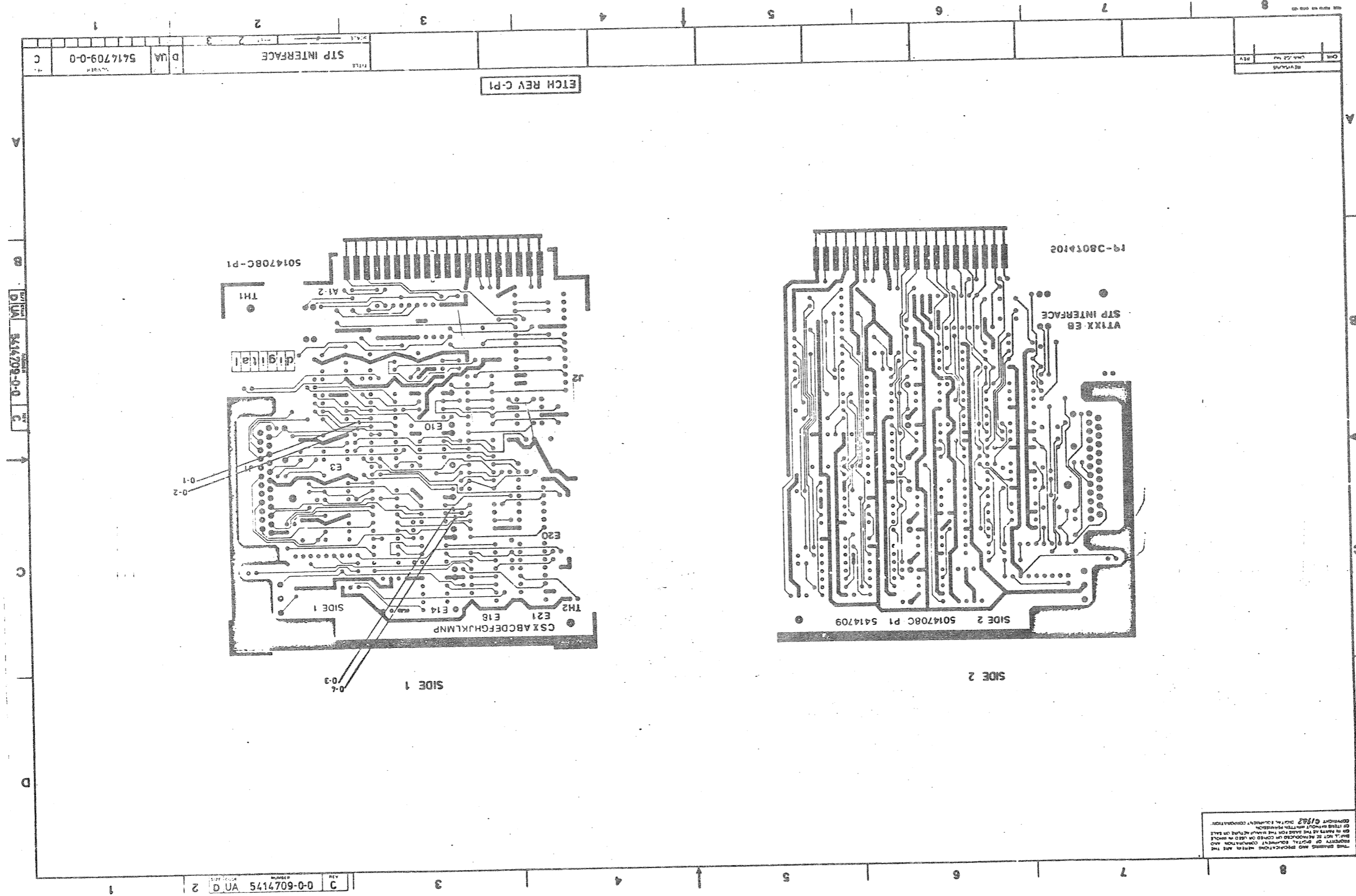
5014708C-P1

NOTE  
1. J1 AND S1 TO BE MOUNTED ON SIDE 2 AFTER ALL OTHER COMPONENTS ARE ASSEMBLED AND BOARD HAS BEEN PLATE SOLDERED.

REV	INITIAL
C	
B	
A	

ETCH REV C-P1

QTY	DESCRIPTION	PART NO	REV
PARTS LIST			
DRN	DATE	<b>digital</b> EQUIPMENT AUSTRALIA PTY. LTD.	
CHKD	DATE		
ENG	DATE		
PROD	DATE		
PHOD	DATE		
STP INTERFACE			
NEXT HIGHER ASSY		SIZE CODE	NUMBER
B-DD-5414709-0		D UA	5414709-0-0
SCALE 2/1		DIST	REV C
SHEET 1 OF 3			



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REV C 0-0-6027145 UA D 2

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**ETCH CUT LAYER 1**

- 0-1 AT E7-6
- 0-2 AT E7-5
- 0-3 AT E17-5
- 0-4 AT E17-6

**WIRE ADD LAYER 1**

- 0-5 FROM E7-5 TO E2-7
- 0-6 FROM E7-6 TO E2-8
- 0-7 FROM E17-5 TO E8-7
- 0-8 FROM E17-6 TO E8-8

REVISIONS		
CHK	CHANGE NO	REV

ETCH REV C-P1

TITLE	STP INTERFACE	SIZE CODE	C UA	NUMBER	5414709-0-0	REV	C
SCALE	SHEET 3 OF 3		DIST	1			

REV C  
5414709-0-0  
C UA

