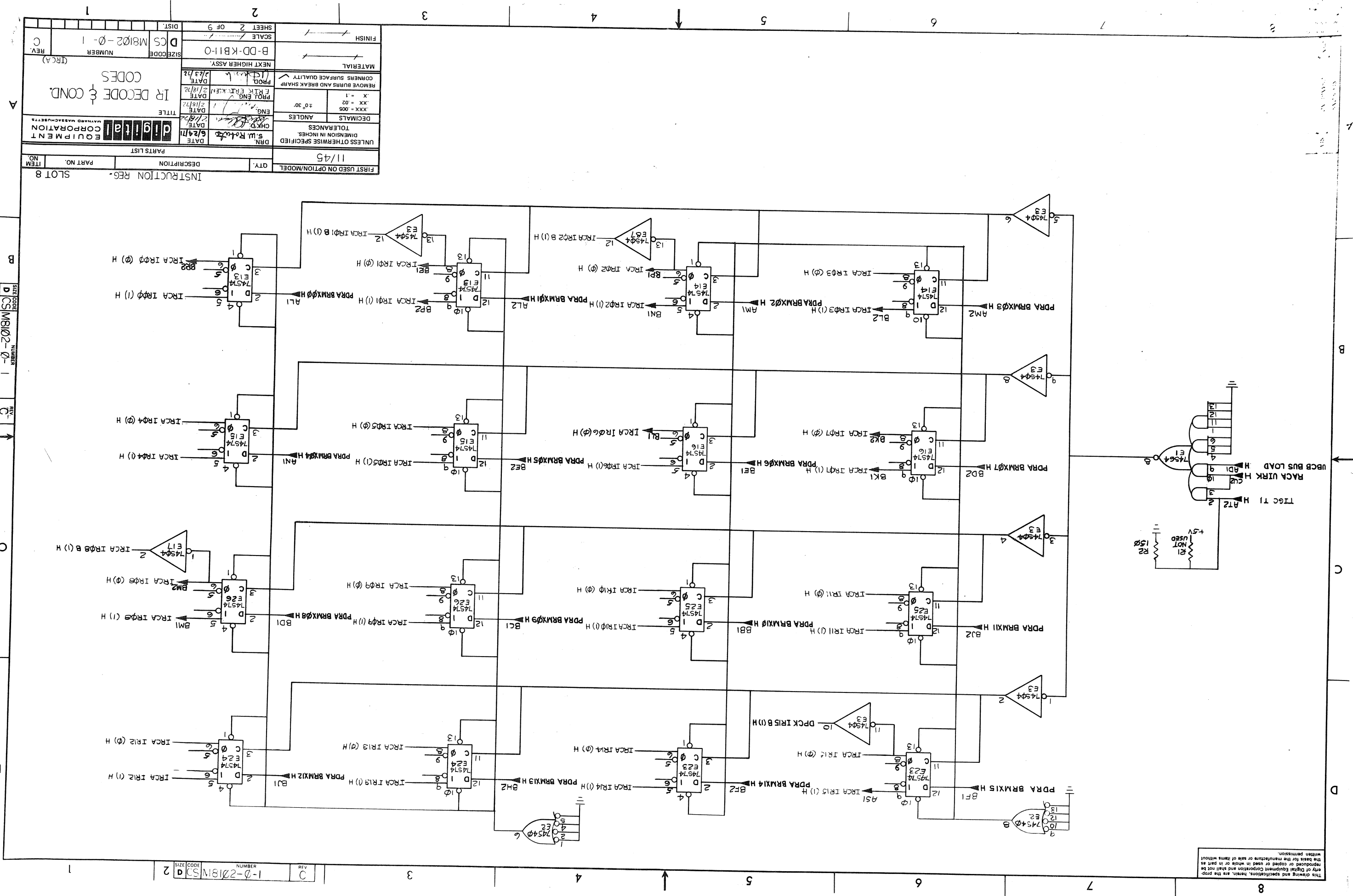
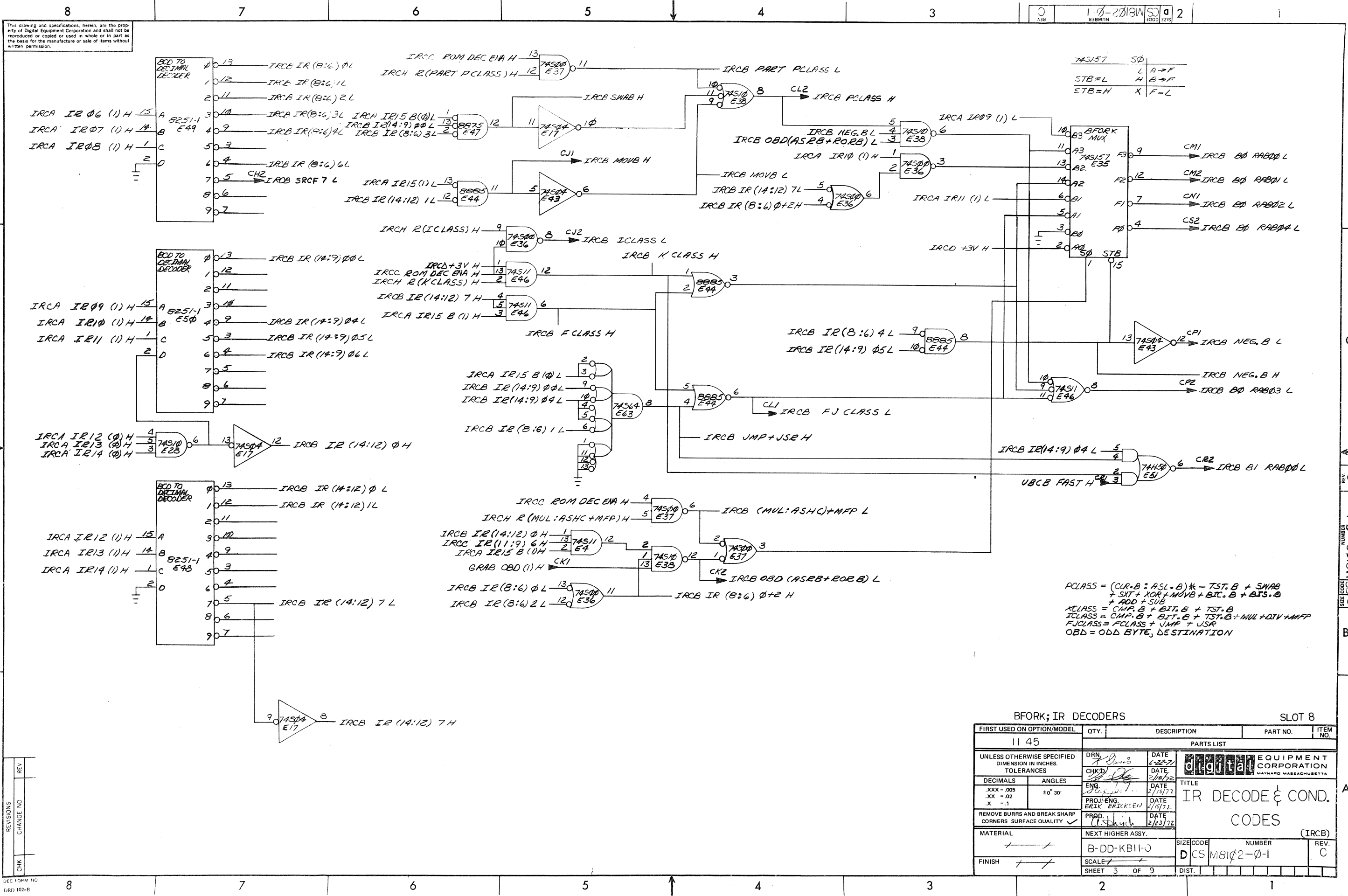
[illegible]

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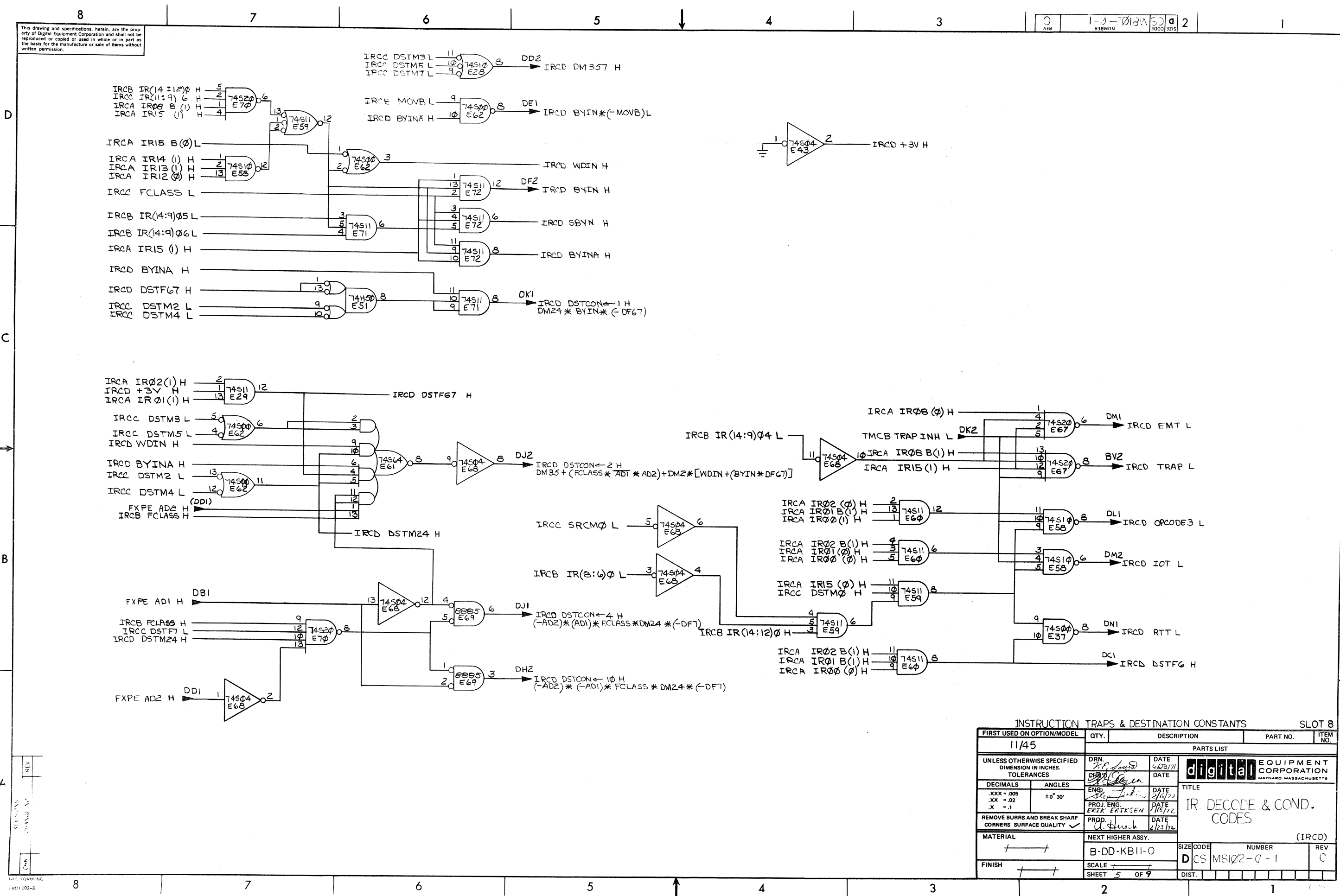


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REV	CHANGE NO	REVISIONS
1	1	1

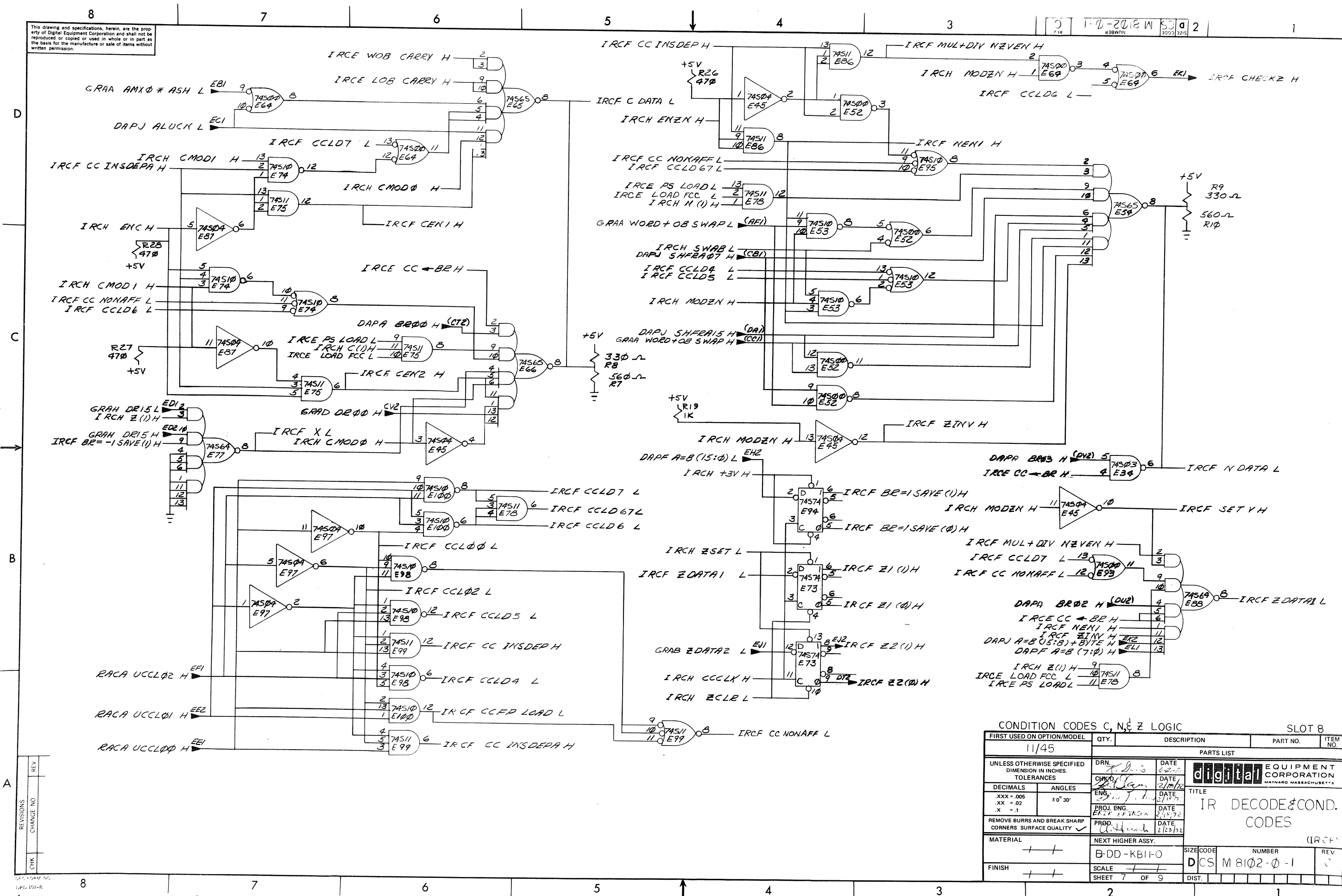
DEC FORM NO 102-B

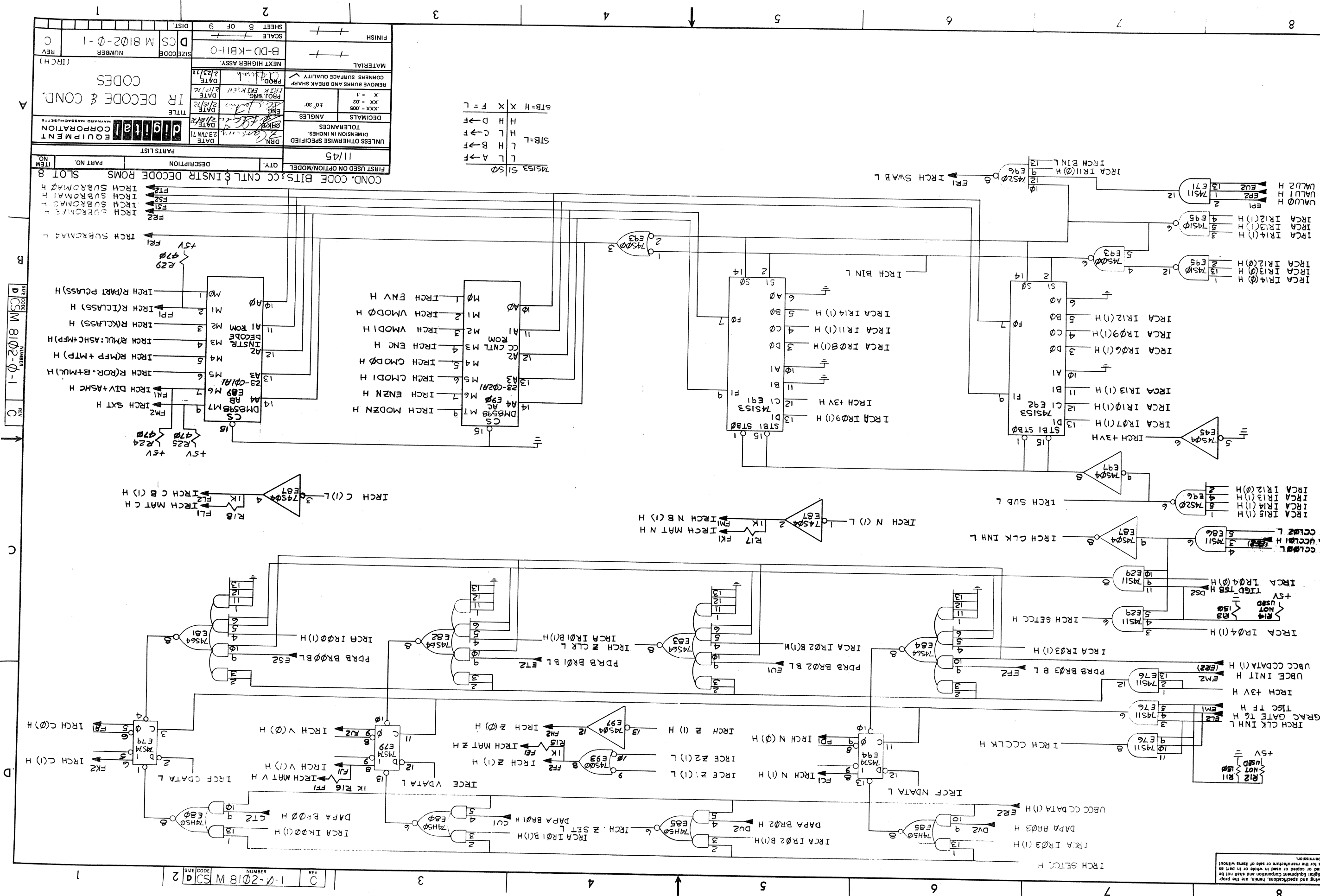
BFORM; IR DECODERS		SLOT 8	
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.
11 45			
PARTS LIST			
UNLESS OTHERWISE SPECIFIED	DRN	DATE	DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
DIMENSION IN INCHES.	CHKD	DATE	
TOLERANCES	ENG	DATE	
DECIMALS	PROJ. ENG.	DATE	
ANGLES	ERIK ERICKSEN	2/15/72	
.XXX = .005			
.XX = .02			
.X = .1			
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD.	DATE	
		2/23/72	
MATERIAL	NEXT HIGHER ASSY.		
FINISH	B-DD-KB11-0	SIZE CODE	NUMBER
		DCS M8102-0-1	REV. C
	SHEET 3 OF 9	DIST.	



INSTRUCTION TRAPS & DESTINATION CONSTANTS				SLOT 8	
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.	
11/45					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				PARTS LIST	
DECIMALS	ANGLES	DATE	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
.XXX = .005	± 0° 30'	DATE	DATE	TITLE	
.XX = .02		DATE	DATE	IR. DECODE & COND. CODES	
.X = .1		DATE	DATE		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY				(IRCD)	
MATERIAL	NEXT HIGHER ASSY.	SIZE CODE	NUMBER	REV	
	B-DD-KB11-0	D	CS	MS102-0-1	C
FINISH	SCALE	SHEET	OF	DIST.	
	5	9			

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1d

INSTRUCTION	OCTAL ADDRESS
ROR.B	00
ROL.B	01
ASR.B	02
ASL.B	03
MARK	04
MFP	05
MTP	06
SXT	07
CLR.B	10
COM.B	11
INC.B	12
DEC.B	13
NEG.B	14
ADC.B	15
SRC.B	16
TST.B	17
SUB	20
MOV.B	21
CMP.B	22
BIT.B	23
BIC.B	24
BIS.B	25
ADD	26
NOT USED	27
MUL	30
DIV	31
ASH	32
ASHC	33
XOR	34
NOT USED	35
NOT USED	36
SDB	37

[illegible][illegible]

CCLD4 (RACC UCCL(2:8)=4)
CCLD5 (RACC UCCL(2:8)=5)
CCLD6 (RACC UCCL(2:8)=6)
CCLD7 (RACC UCCL(2:8)=7)


INSTRUCTION DEPENDENT CONTROL (RACA UCCL (2:0)=1)							
N		Z		V		C	
WORD+ ODD BYTE	BYTE *(EVEN + DSTW0)	WORD	BYTE	WORD	BYTE	WORD	BYTE
* SHFR (15)1	: SHFR (7)1	SHFR(15:0)0	SHFR(7:0)0	SHFR15 \forall AMX 0	SHFR7 \forall AMX0	AMX0	
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	SHFR15 \forall AMX15	SHFR15 \forall AMX7	SHFR7 \forall AMX7	AMX15 AMX7
* SHFR(15)1	: SHFR(7)1	SHFR(15:0)0	SHFR(7:0)0	SHFR15 \forall AMX0	SHFR7 \forall AMX0	AMX0	
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	SHFR15 \forall AMX15	SHFR15 \forall AMX7	SHFR7 \forall AMX7	AMX15 AMX7
NOT INSTR. DEP.		NOT INSTR. DEP.		NOT INSTR. DEP.		NOT INSTR. DEP.	
SHFR(15)1		SHFR(15:0)0		0		NOT AFFECTED	
SHFR(15)1		SHFR(15:0)0		0		NOT AFFECTED	
SHFR(15)1		SHFR(15:0)0		0		NOT AFFECTED	
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	0		ALUCOUT 15	ALUCOUT 7
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	0		ALUCOUT 15	ALUCOUT 7
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	AMX15 * ALU15	AMX7 * ALU7	NOT AFFECTED	
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	AMX15 * ALU15	AMX7 * ALU7	NOT AFFECTED	
* SHFR(15)1	: SHFR(7)1	SHFR(15:0)0	SHFR(7:0)0	A15*B15*ALU15 + A15*B15*ALU15	A7*B7*ALU7 + A7*B7*ALU7	ALUCOUT 15	ALUCOUT 7
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	AMX15 * ALU15	AMX7 * ALU7	ALUCOUT 15	ALUCOUT 7
SHER (15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	AMX15 * ALU15	AMX7 * ALU7	ALUCOUT 15	ALUCOUT 7
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	AMX15 * ALU15	AMX7 * ALU7	ALUCOUT 15	ALUCOUT 7
SHFR(15)1		SHFR(15:0)0		AMX15*BMX15*ALU15 + AMX15*BMX15*ALU15		ALUCOUT 15	
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	0		NOT AFFECTED	
SHFR(15)0	SHFR(7)0	A=B(15:0)	A=B(7:0)	A15*B15*ALU15 + A15*B15*ALU15	A7*B7*ALU7 + A7*B7*ALU7	ALUCOUT 15	ALUCOUT 7
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	0		NOT AFFECTED	
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	0		NOT AFFECTED	
SHFR(15)1	SHFR(7)1	SHFR(15:0)0	SHFR(BYTE)0	0		NOT AFFECTED	
SHFR(15)1		SHFR(15:0)0		AMX15*BMX15*ALU15 + AMX15*BMX15*ALU15		ALUCOUT 15	
NOT AFFECTED		Z(OLD) * SHFR(15:0)0		0		X	
NOT AFFECTED		1		1		1(ALUCN + ALUCN)	
SHFR(15)1		SHFR(15:0)0		0		AMX0	
SHFR(15)1		SHFR(15:0)0		0		0R00	
SHFR(15)1		SHFR(15:0)0		0		NOT AFFECTED	
NOT INSTR. DEP		NOT INSTR. DEP.		NOT INSTR. DEP.		NOT INSTR. DEP.	
SHFR(7)1		SHFR(7:0)0		0		0	
SHFR(15)1		SHFR(15:0)0		0		0	
SHFR(15)1		SHFR(15:0)0		V(OLD) + (SHFR15 \forall AMX15)		AMX15	
NOT AFFECTED		Z(OLD) * SHFR(15:0)0		NOT AFFECTED		NOT AFFECTED	
NOT AFFECTED		NOT AFFECTED		NOT AFFECTED		ALUCOUT 15	

* ODD BYTE SWAPPED TO EVEN DURING COND. CODE CHECKING

NOTE: BYTE INSTRUCTION WITH DSTMØ IS ALWAYS EVEN BYTE

$X = Z(OLD) * \overline{DR15} + (SR = Ø) * DR15$

SWAB INSTR. HAS SAME ROM ADDRESS AS ASL.B

CC CNTL & INSTR DECODE ROM MAPS		SLOT 8	
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO. ITEM NO.
11/45		PARTS LIST	
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRN <i>AK</i>	DATE 1-20-72	 EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS
DECIMALS	CHD <i>AK</i>	DATE 2/15/72	
ANGLES	ENG. <i>AK</i>	DATE 2/15/72	
.XXX ± .005 .XX ± .02 .X ± .1	PROJ. ENG. <i>AK</i>	DATE 2/15/72	
	ENTR. <i>AK</i>	DATE 2/15/72	
REMOVE BURRS AND BREAK SHARP CORNERS. SURFACE QUALITY ✓	PROD. <i>AK</i>	DATE 2/15/72	TITLE IR DECODE & COND. CODES
MATERIAL	NEXT HIGHER ASSY.		(IRCU)
FINISH	B-DD-KB110		SIZE CODE
	SCALE NO. 1		NUMBER
	SHEET 5 OF 5		REV. C
		DIST.	