



CUSTOMER PRINT SET INDEX

THIS IS PRINT SET

--	--	--	--	--

SEQUENCE

SEQUENCE

DRAWING DIRECTORY	B-DD-MF11-L
MODULE UTILIZATION	D-MU-MF11-L-MU
16 BIT 18 MIL MEMORY	A-DD-MM11-L
CIRCUIT SCHEMATIC	C-CS-5409959-0-1
PARTS LIST	A-PL-MF11-L-0
MF11-L/LP OPTION HARNESS	D-IA-7009560-0-0

UNIT ASSEMBLY D-UA-MF11-L-0

UNIT VARIATIONS

PRINT SET TYPE

VARIATION

T I T L E

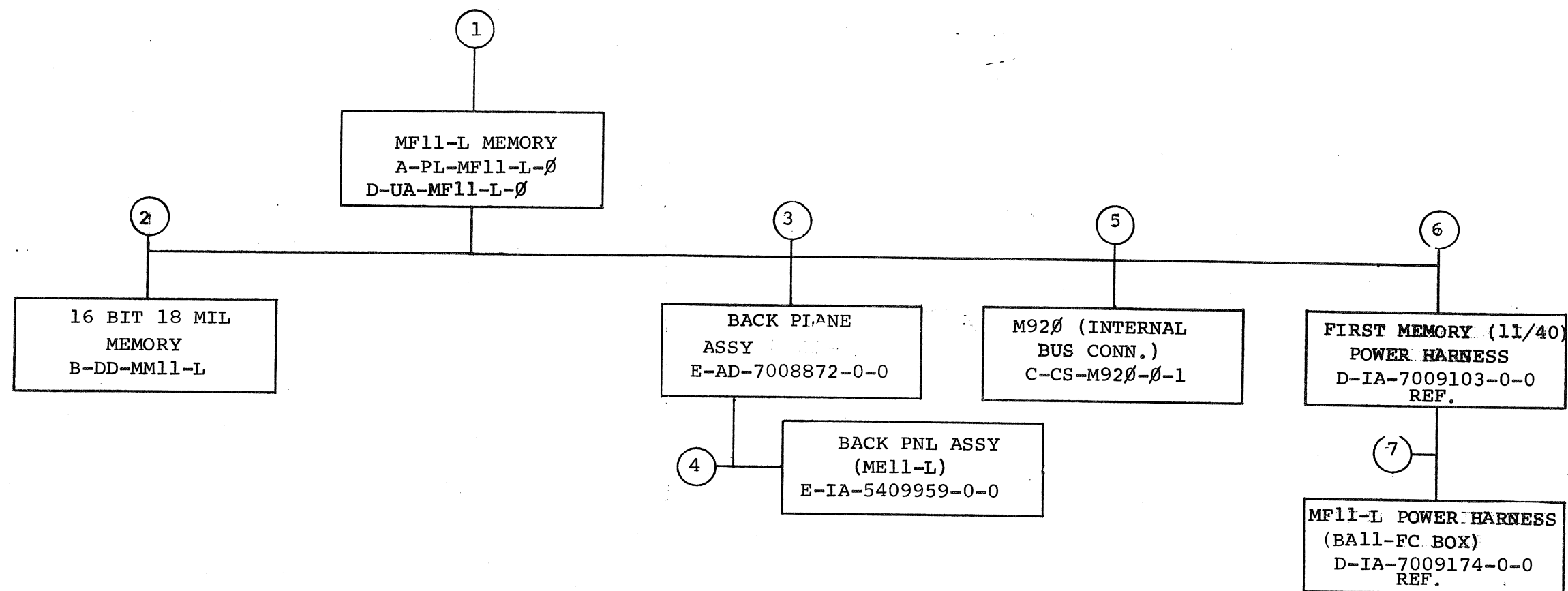
MF11-L

MF11-L MEMORY (8K TO 24K, 16 BIT)

1883

X

[illegible]



TITLE	SHEET	OF	SIZE	CODE	NUMBER	REV
BACK PLANE MF11-L	2	3	B	DD	MF11-L	J

DEC FORM NO.
DRD 100

SHEET

ET OF

4. ELECTROMAGNETIC SHIELD (1700021-02) LOCATED BETWEEN SLOTS Ø3 AND Ø4, AND SLOTS Ø6 AND Ø7 WHEN OPTIONAL MEMORY SEGMENTS ARE ADDED. SHIELDS ARE POSITIONED IN MODULE SECTIONS C,D,E AND F AND HELD IN WITH MODULE HOLDERS
5. MODULE HOLDERS (1209856-0-02) ARE POSITIONED AS NOTED. POSITION AND NUMBER DEPEND UPON INSTALLATION OF OPTIONAL MEMORY SEGMENTS.

DRAWING DIRECTORY

CUSTOMER PRINT SET INDEX

THIS IS PRINT SET

	SEQUENCE
┌	┐
MODULE UTILIZATION	D-MU-MM11-L-1
BLOCK DIAGRAM	D-BD-MM11-L-2
TIMING DIAGRAM	D-TD-MM11-L-3
* MEMORY DRIVERS	E-SC-G231-O-1
* CONTROL & DATA LOOPS	E-CS-G110-O-1
STACK SCHEMATIC	E-CS-H213-O-1
STACK SCHEMATIC	E-CS-H214-O-1
BLOCK DIAGRAM	D-BD-MM11-S-2
8K MEMORY	A-PL-MM11-L-0

SEQUENCE T

MFG. PRINT SET

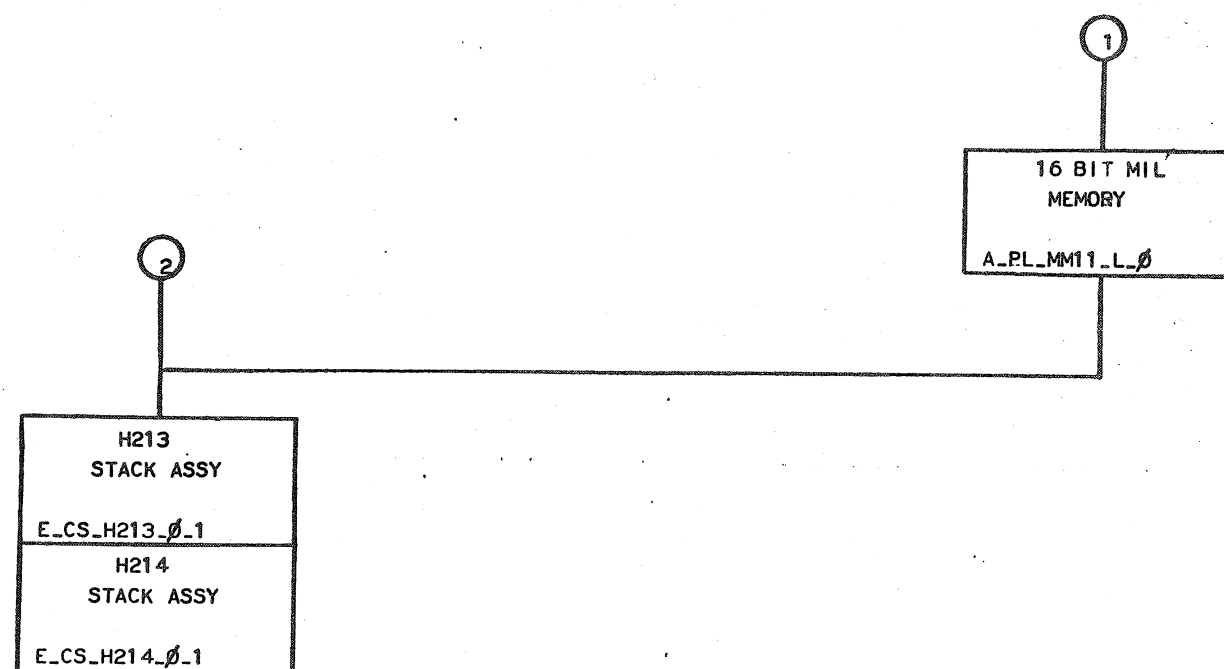
MFG. TEST PRO. FOR MMII/K,L,M,S&SP

ASP-MMII-L-5

[illegible]

* SPECIAL REVISION PRINTS ARE AVAILABLE ON "C" ETCH REV. MODULES.
CARE SHOULD BE TAKEN TO INSURE THAT PROPER PRINTS ARE ORDERED.

REVISIONS													USED ON OPTION/MODEL		DRN.		DATE		TITLE						
DATE	CHG. NO.	REV											MM11_K		CHK'D		DATE		SIZE	CODE	NUMBER				REV
9-72	MM11L-00001	A											MM11_L		J. Kalayhan		1-25-72				B	DD	MM11L-2		
11-72	MM11L-00002	B													J. Kalayhan		1-25-72								
1-73	MISC-00107	C													P. Durant		1-25-72								
2-73	MISC-00111	D													PROD.		1-26-72								
2-73	MISC-00112	E													K. Peters		1-26-72								
2-73	MISC-00113	F													FIELD SERV.		1-26-72								
1-74	MM11L-3	H													W. Gentry		1-26-72								
															SHEET 1	OF 3			DIST						



TITLE	SHEET	SIZE	CODE	NUMBER	REV
16 BIT 18 MIL MEMORY	2 OF 3	B	DD	MM11_L	H.

DRB 107

DFC 16--(325)--1062--3--N871

CUSTOMER PRINT SET										ELECTRICAL										CUSTOMER PRINT SET										MECHANICAL									
MFG SET										MFG SET										MFG SET										MFG SET									
FIND NO.										FIND NO.										FIND NO.										FIND NO.									
DRAWING NO.										DRAWING NO.										DRAWING NO.										DRAWING NO.									
REV										REV										REV										REV									
NO OF SHT										NO OF SHT										NO OF SHT										NO OF SHT									
DESCRIPTION										DESCRIPTION										DESCRIPTION										DESCRIPTION									
OPTION NO.										OPTION NO.										OPTION NO.										OPTION NO.									
X										1	D_MU_MM11_L_1	*	1	MODULE UTILIZATION																				1	A_PL_MM11_L_0	*	1	MEMORY	
X											D_BD_MM11_L_2	*	1	BLOCK DIAGRAM																									
X											D_TD_MM11_L_3	*	1	TIMING DIAGRAM																									
X											E_CS_G231_0_1	#	5	MEMORY DRIVER																					A_PS_3010654_0_0			PURCHASE SPEC	
X											E_CS_G110_0_1	#	5	CONTROL & DATA LOOPS																									
X											D_BD_MM11_S_2	#	2	BLOCK DIAGRAM																									
X											A_PL_MM11_L_0		1	8K MEMORY																				2	B_DD_H214_0_1	#	2	STACK SCHEMATIC	
											A-SP-MM11-1-7	A	4	PRELIM. ENG. SPEC. FOR MM11-K,L																					B_DD_H213_0	#	2	STACK SCHEMATIC	
X										2	E_CS_H213_0_1	#	2	STACK SCHEMATIC																									
X											E_CS_H214_0_1	#	2	STACK SCHEMATIC																					A-PL_G645_0_0		1	STACK BOARD	
											A-SP-G109-0-8		1	G109,G110 CONT & DATA LOOP MFG. SPEC.																									
											A-SP-G231-0-8		1	MEMORY DRIVER MFG. SPEC.																									
											A-SP-MM11-L-4		1	MM11-K,-L,-S & SP MFG. TEST SPEC.																									
									X		A-SP-MM11-L-5		29	MFG. TEST PRO. MM11/K,L,M,S & SP																									

TITLE

16 BIT 18 MIL MEMORY

SHEET 3 OF 3

SIZE CODE

B DD

NUMBER

MM11_L

REV

H

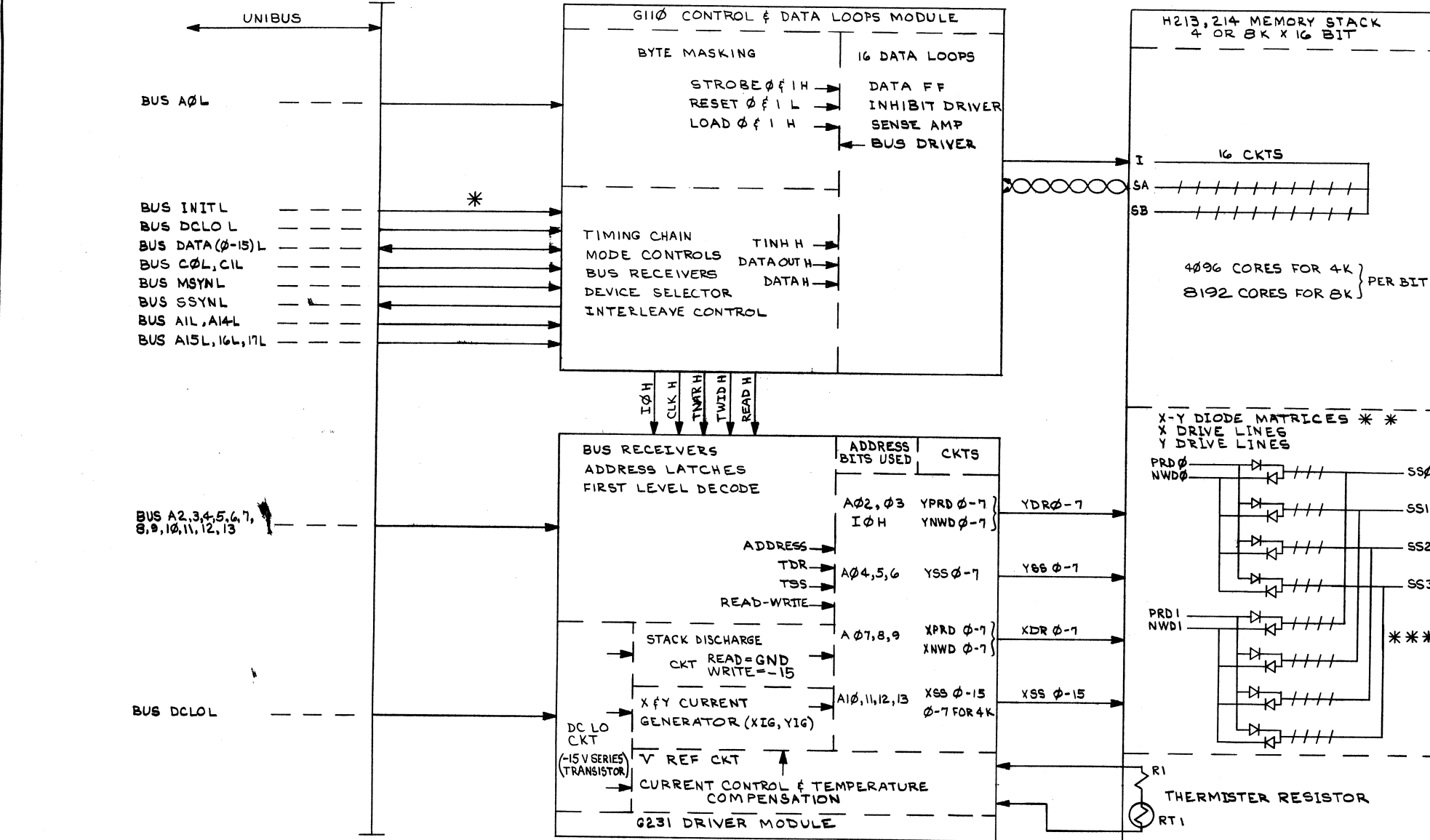
THESE 2 SLOTS ARE UNIBUS WIRED ON ALL 3 CONNECTORS

[illegible]

SENSE-CONTROL
(F, E, D, B, C, A) (HEX @ 1/2)
G110

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION		PART NO.		ITEM NO.
MMII-L							
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN <i>Harley</i>		DATE 1/19/72	 DIGITAL EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS		
		CHK'D <i>J. Seale</i>		DATE 1-23-72			
DECIMALS		ENG <i>P. Duvant</i>		DATE 1-24-72			
ANGLES		PROJ. ENG. <i>P. Duvant</i>		DATE 1-25-72			
.XXX = .005		PROD. <i>R. K. Jeter</i>		DATE 1-26-72	TITLE MODULE UTILIZATION		
.XX = .02							
.X = .1							
±0° 30'							
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓		NEXT HIGHER ASSY.					
MATERIAL		B-DD-MMII-L		SIZE CODE	NUMBER		REV.
—//—				DMU		MMII-L - I	
FINISH		SCALE					
—//—		SHEET					
		OF					
		I					

REVISIONS
CHANGE NO.
CHK



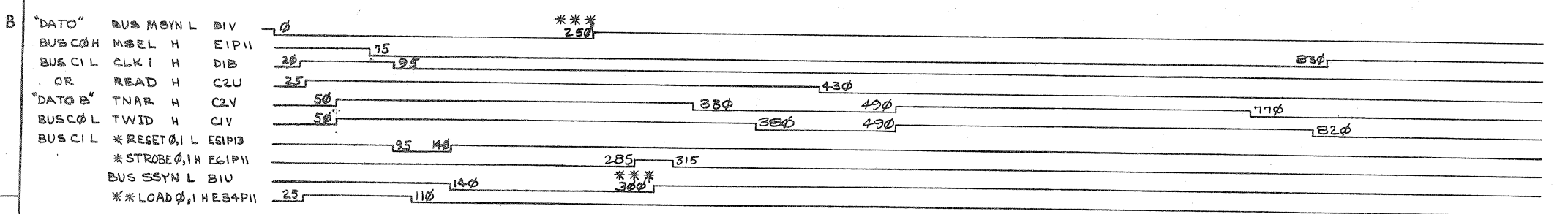
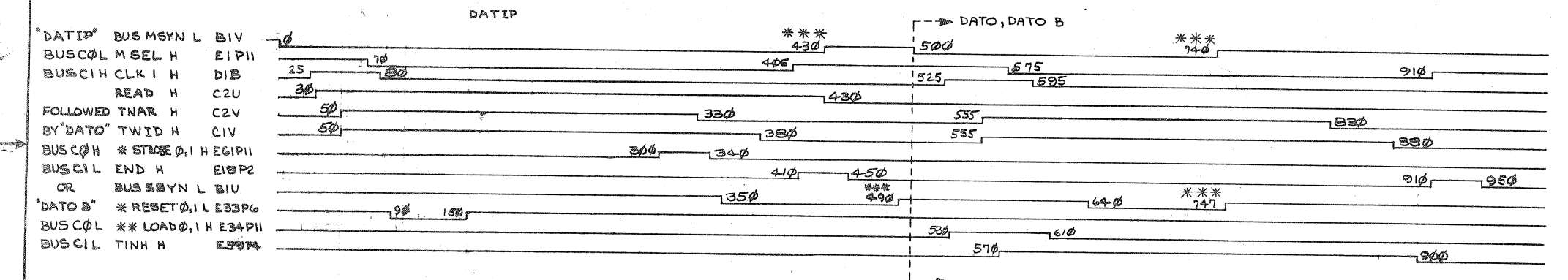
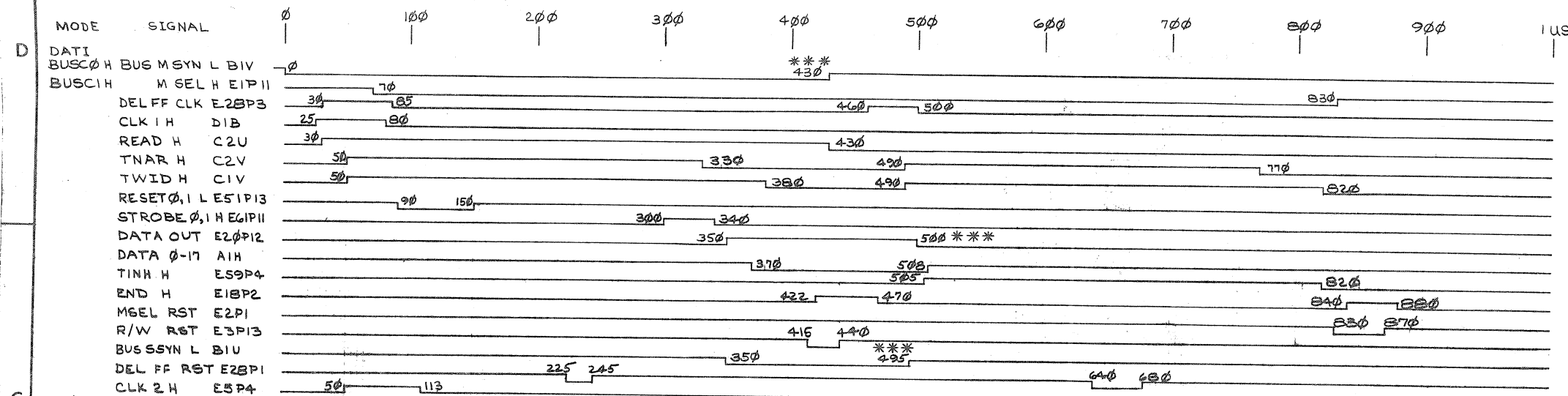
NOTES:
*1. ALL ARROWS SHOW SIGNAL FLOW DIRECTION.
**2. MATRIX SHOWN IS FOR ILLUSTRATION ONLY.
***3. ACTUAL MATRIX HAS { Y AXIS 8PRD, 8NRD, 8SS
X AXIS 4K 8PRD, 8NRD, 8SS
X AXIS 8K 8PRD, 8NRD, 16SS

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MMII-L				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN CHKD ENG. PROJ. ENG. PROD.	DATE 1-25-71 1-25-71 1-25-71 1-26-72	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
DECIMALS	ANGLES	TITLE BLOCK DIAGRAM		
.XXX = .005 .XX = .02 .X = .1	±0° 30'	(MMII-L)		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓		MATERIAL NEXT HIGHER ASSY.		
FINISH		B-DD-MMII-L	SCALE	NUMBER REV.
SHEET 1 OF 1		DIST.	DIST.	DIST.

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3-7-11MMII-L-3 2

- NOTES:
1. ANY SIGNALS NOT SHOWN ON DATIP, DATO OR DATO B ARE AS SHOWN ON DATI TIMING.
 - * 2. RESET L AND STROBE H DO NOT OCCUR IN DATO MODE. THEY ONLY OCCUR FOR THE BYTE NOT BEING ADDRESSED IN DATO B MODE.
 - ** 3. LOAD H OCCURS FOR BOTH BITS IN DATO MODE AND ONLY FOR THE ADDRESSED BYTE IN THE DATO B MODE.
 - *** 4. ACTUAL TIME DEPENDS ON BUS AND PROCESSOR DELAYS.
 5. ALL SIGNALS ON G109 OR G110 MODULE



FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MMII-L				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				
DECIMALS	ANGLES	DATE		
.XXX = .005		12/3/71		
.XX = .02	±0° 30'	DATE		
.X = .1		1-27-72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE		
		1-25-72		
		DATE		
		1-25-72		
		DATE		
		1-27-72		
MATERIAL		NEXT HIGHER ASSY.		
		B-DD-MMII-L		
FINISH		SCAL		
		OF 1		
		DIST.		
		1		

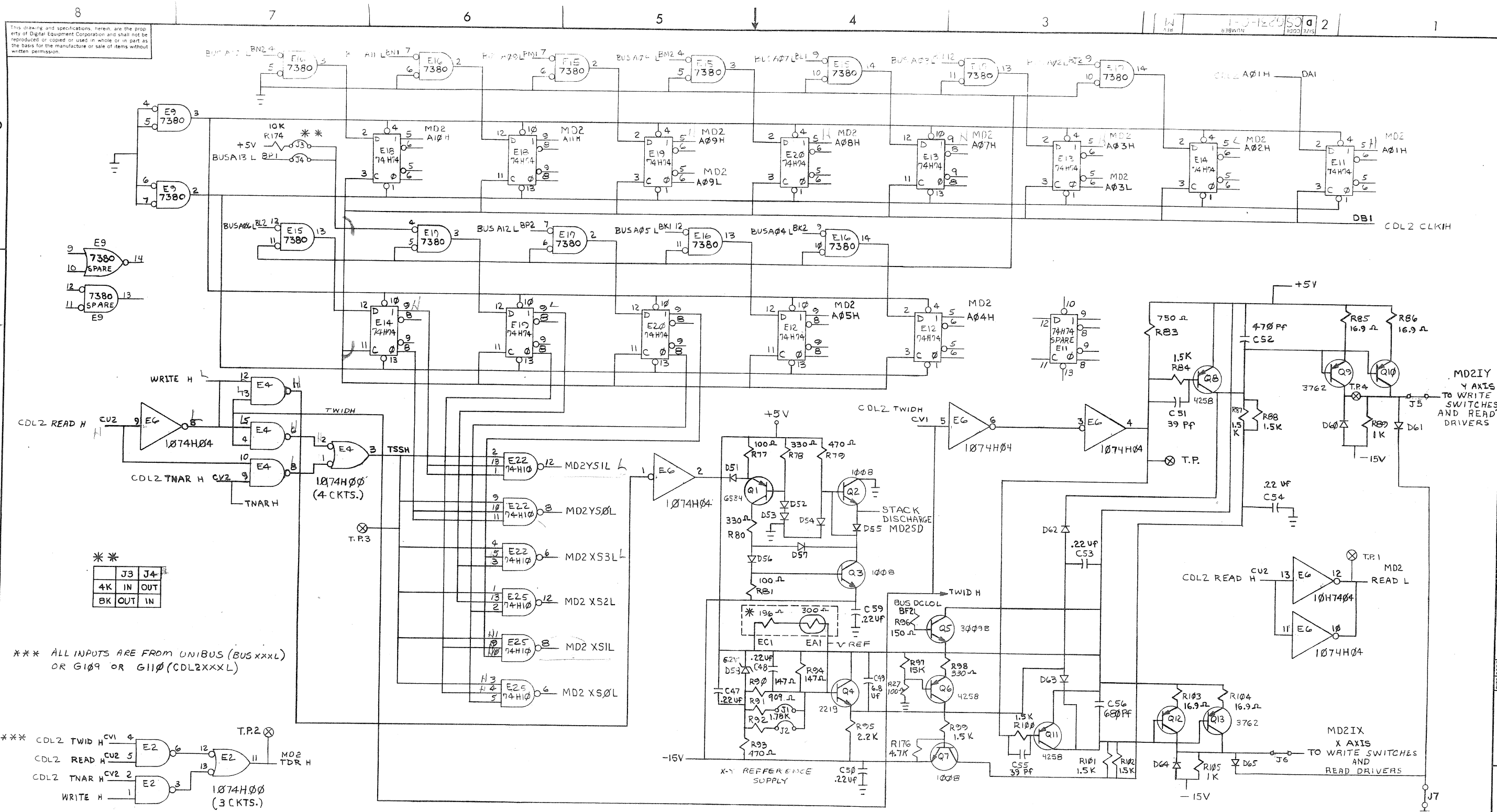
digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE
TIMING
DIAGRAM

(MMII-L, MMII-K)

SIZE CODE
D TD MMII-L-3

NUMBER
REV.



FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DRN CHKD ENG PROJ. ENG. PROD. MATERIAL FINISH	DATE 9-24-71 DATE 1-21-72 DATE 1-25-72 DATE 1-25-72 DATE 1-26-72 NEXT HIGHER ASSY. SCALE SHEET 2 OF 4	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS TITLE PDP-11 MEMORY DRIVER SIZE CODE D CS G231-0-1	REV. M
DECIMALS		ANGLES			
.XXX = .005		±0° 30'			
.XX = .02					
.X = .1					
REMOVE BURRS AND BREAK SHARP CORNERS. SURFACE QUALITY					
DIST.					

*** ALL INPUTS ARE FROM UNIBUS (BUSXXXX) OR G109 OR G110 (CDL2XXXX)

*** CDL2 TWID H CV1 4
CDL2 READ H CV2 5
CDL2 TNR H CV2 2
WRITE H 1

1074H04 (4 CKTS.)

1074H04 (3 CKTS.)

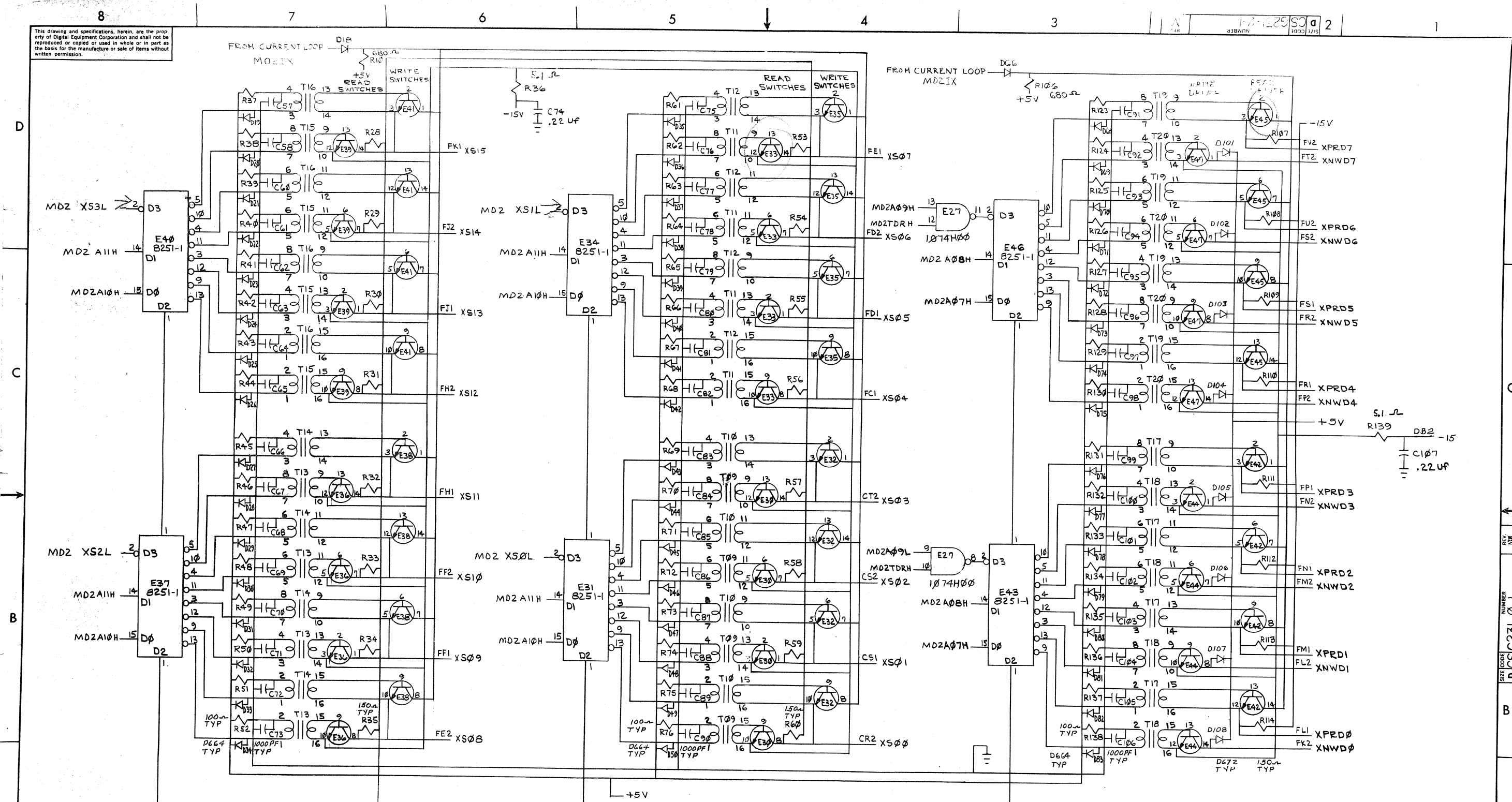
MD2Y SIL L
MD2Y SIL
MD2X SIL L
MD2X SIL
MD2X SIL L
MD2X SIL

MD2IY
TO WRITE SWITCHES AND READ DRIVERS

MD2IX
X AXIS
TO WRITE SWITCHES AND READ DRIVERS

* THIS CIRCUIT IS ON STACK BOARD

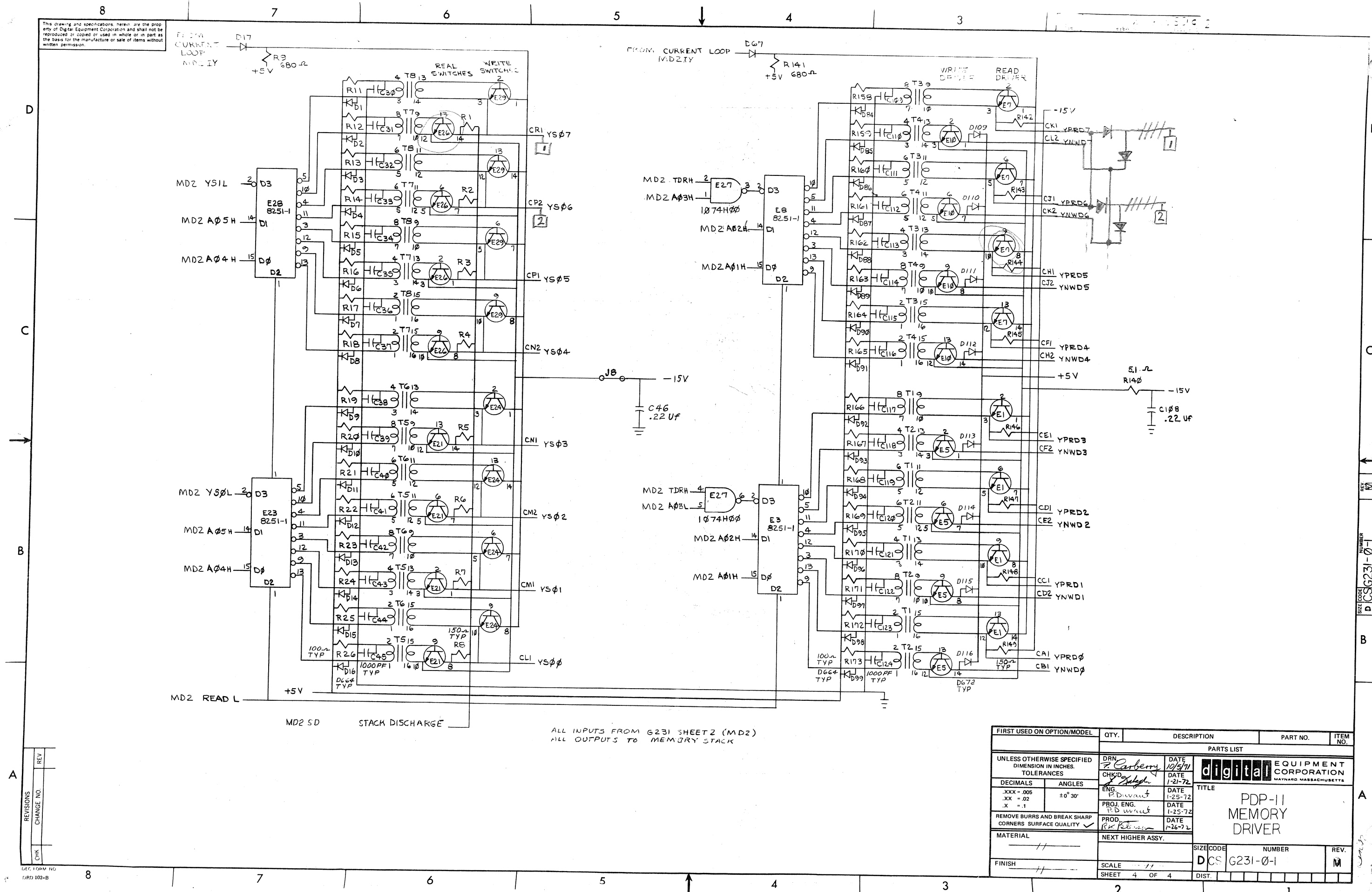
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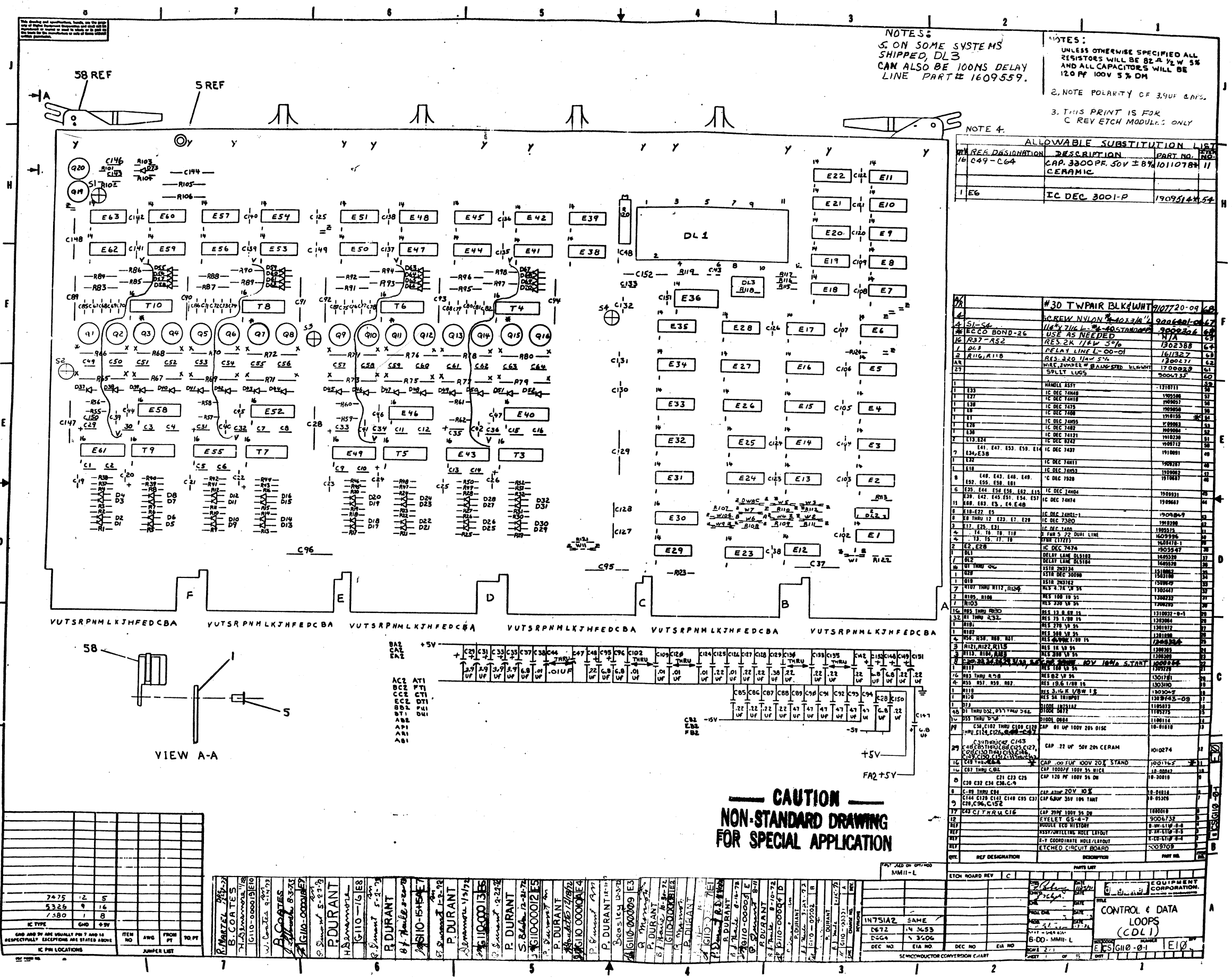


ALL INPUTS FROM G231 SHEET2 (MD2)
ALL OUTPUTS GO TO MEMORY STACK

REV	CHG	NO.
1	1	1

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES	DRN 10-1-71 DATE 1-21-72 CHKD 1-21-72 DATE 1-25-72 PROJ. ENG. P. DUMAT DATE 1-25-72 PROD. R. K. PETERSON DATE 1-26-72	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		
DECIMALS .XXX = .005 .XX = .02 .X = .1	ANGLES ± 0° 30'	TITLE PDP-11 MEMORY DRIVER		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	NEXT HIGHER ASSY.	SIZE CODE DCS G231-0-1	NUMBER	REV. 1
MATERIAL --//--	SCALE SHEET 3 OF 4	DIST.		
FINISH --//--				





NOTES:
1. ON SOME SYSTEMS
SHIPPED, DL3
CAN ALSO BE 100MS DELAY
LINE PART# 1609559.

NOTES:
UNLESS OTHERWISE SPECIFIED ALL
RESISTORS WILL BE 82.4 1/2 W 5%
AND ALL CAPACITORS WILL BE
120 PF 100V 5% DM
2. NOTE POLARITY OF 3.9UF CAPS.
3. THIS PRINT IS FOR
C REV ETCH MODULES ONLY

NOTE 4:

REF. DESIGNATION	DESCRIPTION	PART NO.
C49-C64	CAP. 3300PF. 50V ± 8%	10110784 11
E6	IC DEC 3001-P	19095144 54

QTY.	REF. DESIGNATION	DESCRIPTION	PART NO.
1	DL1	DL3	10110784 11
1	E1	IC DEC 3001-P	19095144 54
1	E2	IC DEC 3001-P	19095144 54
1	E3	IC DEC 3001-P	19095144 54
1	E4	IC DEC 3001-P	19095144 54
1	E5	IC DEC 3001-P	19095144 54
1	E6	IC DEC 3001-P	19095144 54
1	E7	IC DEC 3001-P	19095144 54
1	E8	IC DEC 3001-P	19095144 54
1	E9	IC DEC 3001-P	19095144 54
1	E10	IC DEC 3001-P	19095144 54
1	E11	IC DEC 3001-P	19095144 54
1	E12	IC DEC 3001-P	19095144 54
1	E13	IC DEC 3001-P	19095144 54
1	E14	IC DEC 3001-P	19095144 54
1	E15	IC DEC 3001-P	19095144 54
1	E16	IC DEC 3001-P	19095144 54
1	E17	IC DEC 3001-P	19095144 54
1	E18	IC DEC 3001-P	19095144 54
1	E19	IC DEC 3001-P	19095144 54
1	E20	IC DEC 3001-P	19095144 54
1	E21	IC DEC 3001-P	19095144 54
1	E22	IC DEC 3001-P	19095144 54
1	E23	IC DEC 3001-P	19095144 54
1	E24	IC DEC 3001-P	19095144 54
1	E25	IC DEC 3001-P	19095144 54
1	E26	IC DEC 3001-P	19095144 54
1	E27	IC DEC 3001-P	19095144 54
1	E28	IC DEC 3001-P	19095144 54
1	E29	IC DEC 3001-P	19095144 54
1	E30	IC DEC 3001-P	19095144 54
1	E31	IC DEC 3001-P	19095144 54
1	E32	IC DEC 3001-P	19095144 54
1	E33	IC DEC 3001-P	19095144 54
1	E34	IC DEC 3001-P	19095144 54
1	E35	IC DEC 3001-P	19095144 54
1	E36	IC DEC 3001-P	19095144 54
1	E37	IC DEC 3001-P	19095144 54
1	E38	IC DEC 3001-P	19095144 54
1	E39	IC DEC 3001-P	19095144 54
1	E40	IC DEC 3001-P	19095144 54
1	E41	IC DEC 3001-P	19095144 54
1	E42	IC DEC 3001-P	19095144 54
1	E43	IC DEC 3001-P	19095144 54
1	E44	IC DEC 3001-P	19095144 54
1	E45	IC DEC 3001-P	19095144 54
1	E46	IC DEC 3001-P	19095144 54
1	E47	IC DEC 3001-P	19095144 54
1	E48	IC DEC 3001-P	19095144 54
1	E49	IC DEC 3001-P	19095144 54
1	E50	IC DEC 3001-P	19095144 54
1	E51	IC DEC 3001-P	19095144 54
1	E52	IC DEC 3001-P	19095144 54
1	E53	IC DEC 3001-P	19095144 54
1	E54	IC DEC 3001-P	19095144 54
1	E55	IC DEC 3001-P	19095144 54
1	E56	IC DEC 3001-P	19095144 54
1	E57	IC DEC 3001-P	19095144 54
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1	E59	IC DEC 3001-P	19095144 54
1	E60	IC DEC 3001-P	19095144 54
1	E61	IC DEC 3001-P	19095144 54
1	E62	IC DEC 3001-P	19095144 54
1	E63	IC DEC 3001-P	19095144 54
1	E64	IC DEC 3001-P	19095144 54
1	E65	IC DEC 3001-P	19095144 54
1	E66	IC DEC 3001-P	19095144 54
1	E67	IC DEC 3001-P	19095144 54
1	E68	IC DEC 3001-P	19095144 54
1	E69	IC DEC 3001-P	19095144 54
1	E70	IC DEC 3001-P	19095144 54
1	E71	IC DEC 3001-P	19095144 54
1	E72	IC DEC 3001-P	19095144 54
1	E73	IC DEC 3001-P	19095144 54
1	E74	IC DEC 3001-P	19095144 54
1	E75	IC DEC 3001-P	19095144 54
1	E76	IC DEC 3001-P	19095144 54
1	E77	IC DEC 3001-P	19095144 54
1	E78	IC DEC 3001-P	19095144 54
1	E79	IC DEC 3001-P	19095144 54
1	E80	IC DEC 3001-P	19095144 54
1	E81	IC DEC 3001-P	19095144 54
1	E82	IC DEC 3001-P	19095144 54
1	E83	IC DEC 3001-P	19095144 54
1	E84	IC DEC 3001-P	19095144 54
1	E85	IC DEC 3001-P	19095144 54
1	E86	IC DEC 3001-P	19095144 54
1	E87	IC DEC 3001-P	19095144 54
1	E88	IC DEC 3001-P	19095144 54
1	E89	IC DEC 3001-P	19095144 54
1	E90	IC DEC 3001-P	19095144 54
1	E91	IC DEC 3001-P	19095144 54
1	E92	IC DEC 3001-P	19095144 54
1	E93	IC DEC 3001-P	19095144 54
1	E94	IC DEC 3001-P	19095144 54
1	E95	IC DEC 3001-P	19095144 54
1	E96	IC DEC 3001-P	19095144 54
1	E97	IC DEC 3001-P	19095144 54
1	E98	IC DEC 3001-P	19095144 54
1	E99	IC DEC 3001-P	19095144 54
1	E100	IC DEC 3001-P	19095144 54

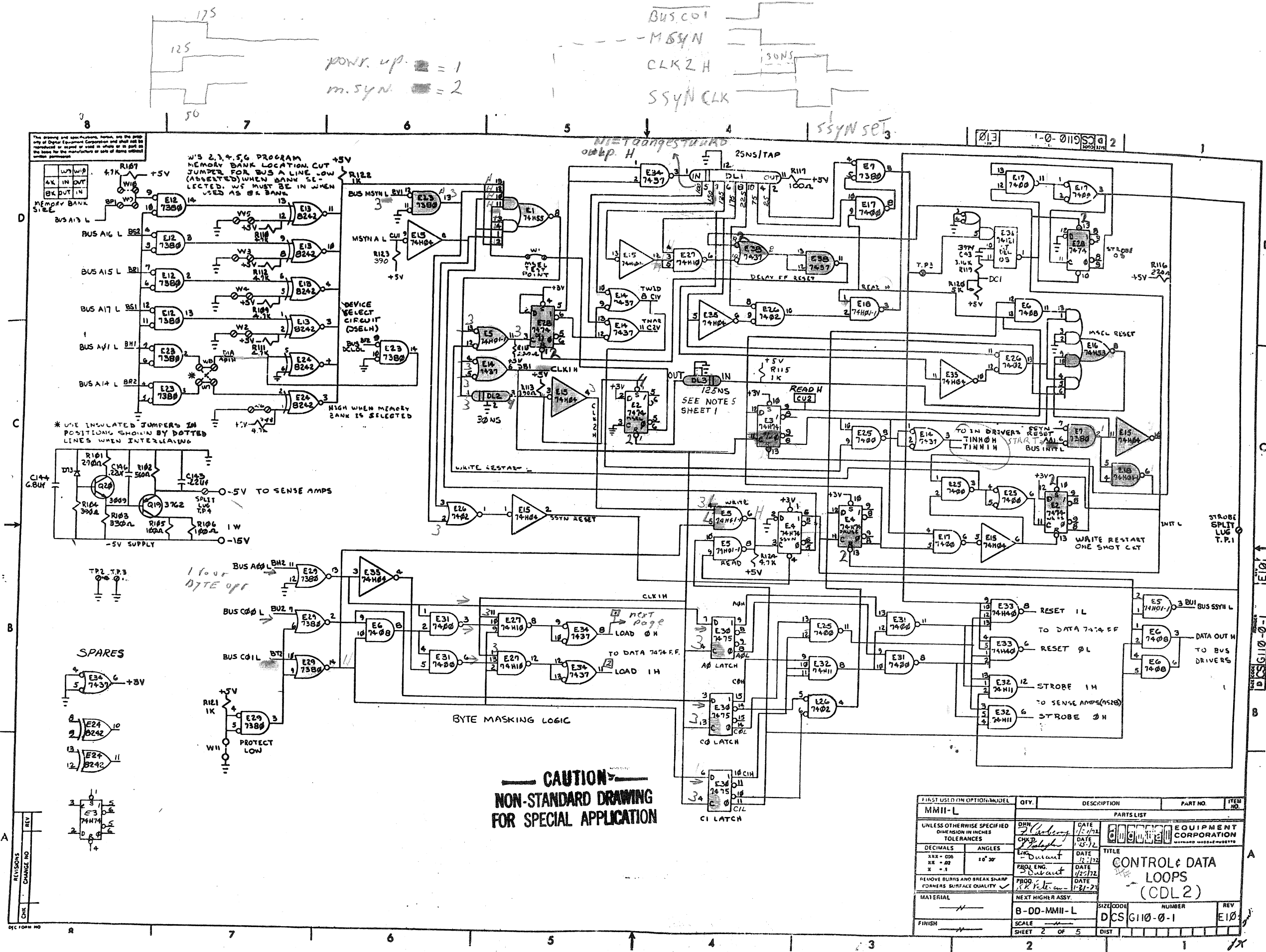
CAUTION
NON-STANDARD DRAWING
FOR SPECIAL APPLICATION

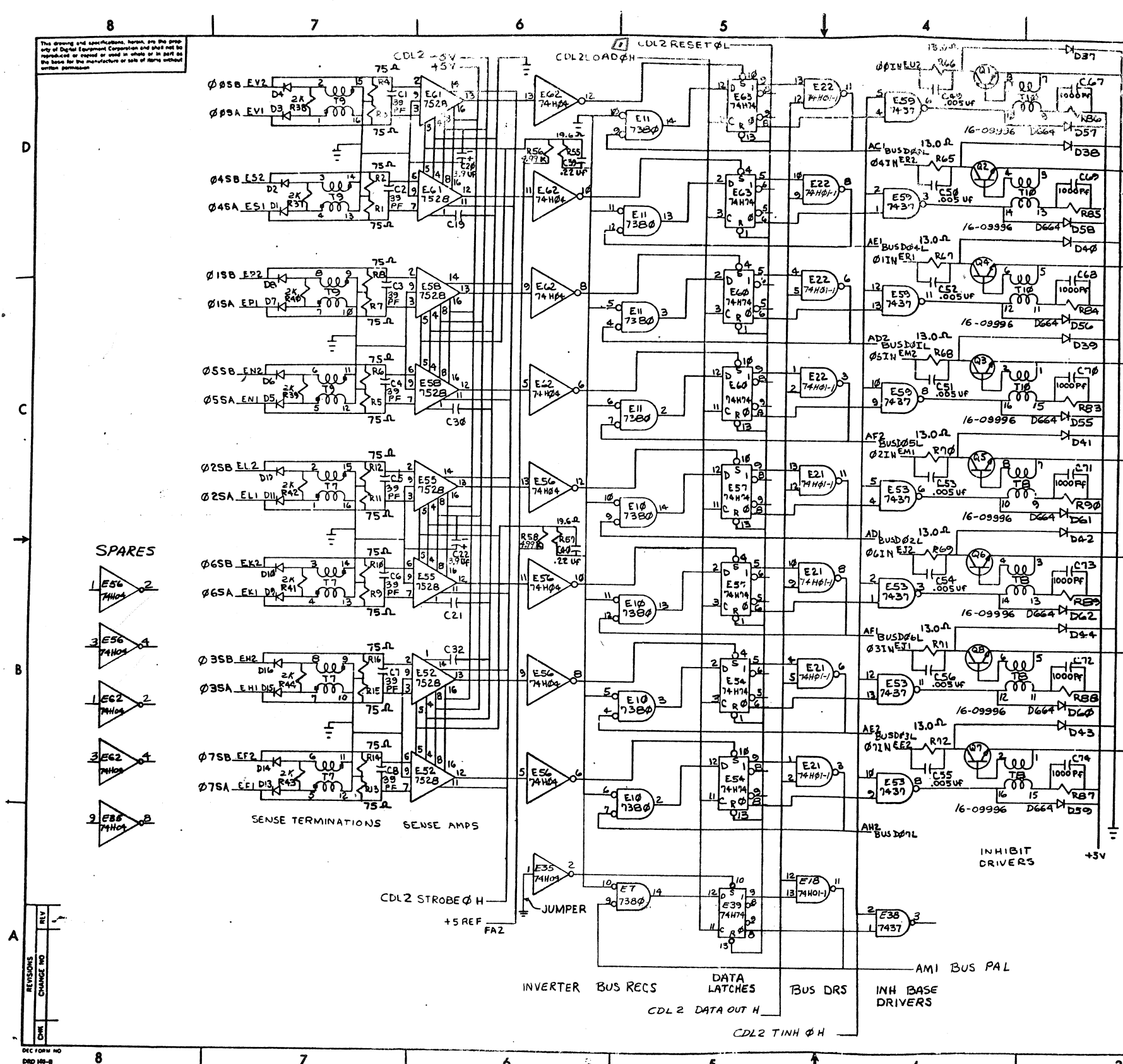
IC TYPE	QTY	REF. NO.	TEST POINT
7475	12	5	
5325	9	16	
5380	1	8	

IC PART NO.	QTY	REF. NO.	TEST POINT
7475	12	5	
5325	9	16	
5380	1	8	

IC PART NO.	QTY	REF. NO.	TEST POINT
7475	12	5	
5325	9	16	
5380	1	8	

$\frac{1}{2} = a$
 $\frac{1}{2} = b$
 $\frac{1}{2} = c$
 $\frac{1}{2} = d$
 $\frac{1}{2} = e$
 $\frac{1}{2} = f$
 $\frac{1}{2} = g$
 $\frac{1}{2} = h$
 $\frac{1}{2} = i$
 $\frac{1}{2} = j$
 $\frac{1}{2} = k$
 $\frac{1}{2} = l$
 $\frac{1}{2} = m$
 $\frac{1}{2} = n$
 $\frac{1}{2} = o$
 $\frac{1}{2} = p$
 $\frac{1}{2} = q$
 $\frac{1}{2} = r$
 $\frac{1}{2} = s$
 $\frac{1}{2} = t$
 $\frac{1}{2} = u$
 $\frac{1}{2} = v$
 $\frac{1}{2} = w$
 $\frac{1}{2} = x$
 $\frac{1}{2} = y$
 $\frac{1}{2} = z$





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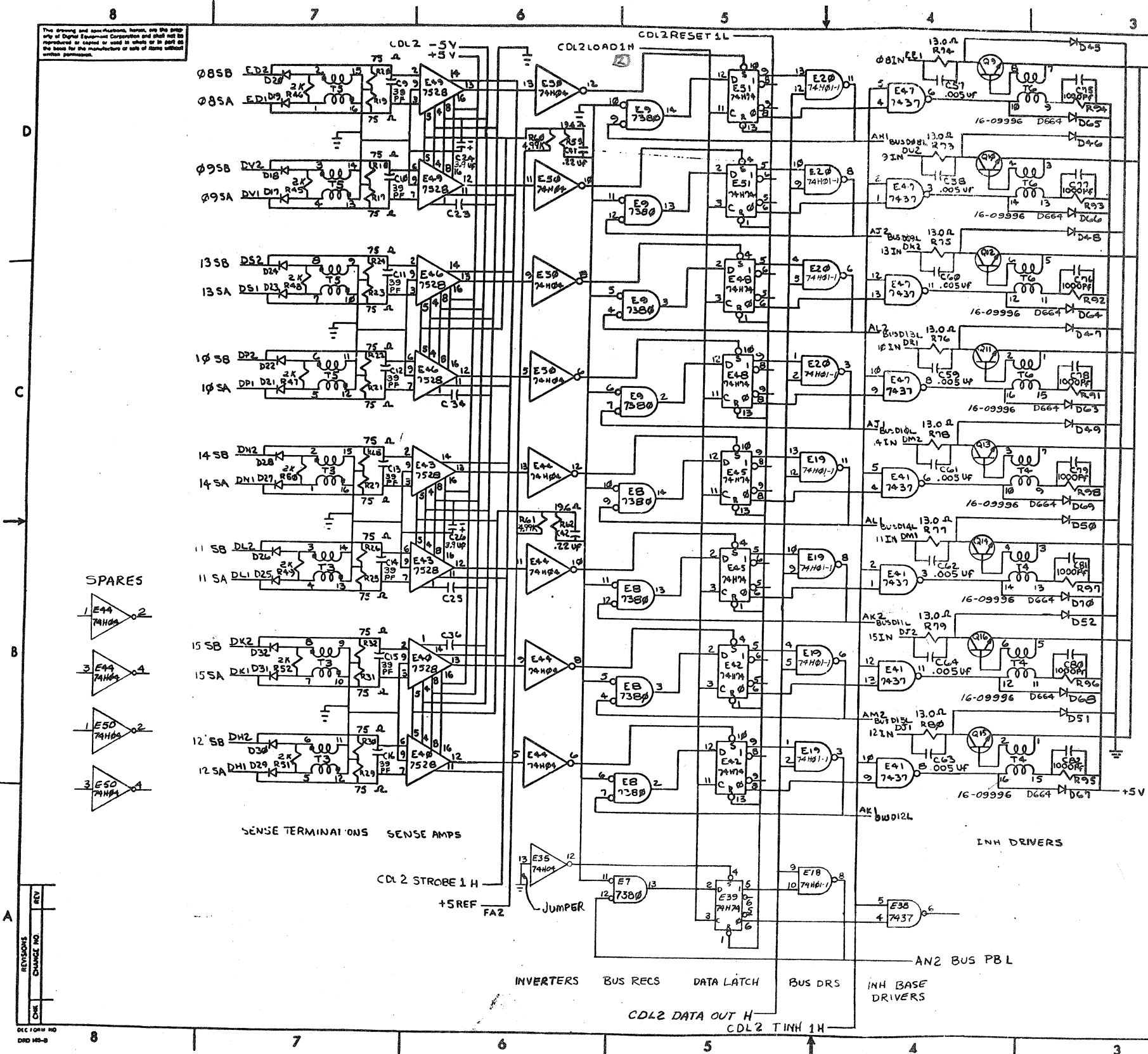
NOTES:

1. ALL SIGNALS FROM UNIBUS (BUS D07L TO BUS D07L), G110 SHEET 2 (CDL2) OR STACK (Q05A,B TO Q75A,B AND Q01N TO Q71N)
2. UNLESS SPECIFIED:
ALL RESISTORS ARE 82Ω 1/2 W 5%
ALL CAPACITORS ARE 120 PF 100V 5%
ALL TRANSFORMERS ARE 16-09478-01
ALL DIODES ARE D672
ALL TRANSISTORS ARE DEC-3734
3. ECO#2 DID THE FOLLOWING:
C1-C8 CHANGED FROM 68 PF TO 39 PF
R37-R44 CHANGED FROM 330Ω TO 2 K
C49-C56 CHANGED FROM .0022μF TO .005μF
4. ECO#3 DID THE FOLLOWING:
R56, R58 CHANGED FROM 5.62K TO 4.99K
C20, C22 CHANGED FROM .22μF TO 3.9μF
5. ECO#13 ADD JUMPER E35PI TO GND.

CAUTION
NON-STANDARD DRAWING
FOR SPECIAL APPLICATION

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MMII-L				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES				
DECIMALS	ANGLES	DATE	EQUIPMENT CORPORATION	
XXX = .005	XX = .02	XX = .1	DATE	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY	PROD.	DATE	TITLE	
MATERIAL	NEXT HIGHER ASSY.	DATE	CONTROL & DATA LOOPS (CDL3)	
FINISH	B-DD-MMII-L	DATE	SIZE/ODD	NUMBER
	SCALE	DATE	DCS G110-0-1	HFV
	SHEET 3 OF 5	DATE		

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- NOTES:
1. ALL SIGNALS FROM UNIBUS (BUS DOBL-DISL) G110 SHEET 2 (CDL2) OR STACK (085A,B TO 155A,B AND 081N TO 151N)
 2. UNLESS SPECIFIED:
ALL RESISTORS ARE 020 1/2 W 5%
ALL DIODES ARE D672
ALL CAPACITORS ARE 120 PF 100V 5%
ALL TRANSFORMERS ARE 16-09996 D664
ALL TRANSISTORS ARE DEC-3734
 3. ECO #2 DID THE FOLLOWING:
C9-C16 CHANGED FROM 60 PF TO 39 PF.
R45-R52 CHANGED FROM 330 1/2 TO 2K
C57-C64 CHANGED FROM .0022 1/2 TO .005 1/2
 4. ECO #8 DID THE FOLLOWING:
R60, R61 CHANGED FROM 5.62K TO 4.99K
C24, C26 CHANGED FROM .22 1/2 TO 3.9 1/2
 5. ECO #13 ADD JUMPER E35P13 TO GND.

CAUTION
NON-STANDARD DRAWING
FOR SPECIAL APPLICATION

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MM11-L				
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES				
DECIMALS	ANGLES	PARTS LIST		
XXX - .001	16° 30'	EQUIPMENT CORPORATION		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		CONTROL & DATA LOOPS (CDL 4)		
MATERIAL	NEXT HIGHER ASSY.	SIZE CODE NUMBER		
FINISH	SCALE	DIST		
	SHEET 4 OF 5	REV. E10		

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0-0-0119 500 2

NOTES:

1. THIS PRINT IS FOR C REV. ETCH MODULES ONLY.

2. THIS CHART IS DESIGNED TO ALLOW THIS G110 CIRCUIT SCHEMATIC TO BE USED WITH ALL PREVIOUS REVISION MODULES

3. CHART IS USED AS FOLLOWS:

A. LOCATE REVISION LETTER STAMPED ON HANDLE

B. FOLLOW THE MODULE STAMPED COLUMN TO FIND APPROPRIATE REVISION

C. NOTICE SYMBOLS TO RIGHT OF REVISION LETTER, THESE SYMBOLS WILL INDICATE ECO REQUIREMENTS FOR THAT MODULE.

4. SYMBOLS:

☐ = REQUIRED BUT REFER TO EFFECTIVITY DATE

☐ = ONE OR THE OTHER IS REQUIRED

☐ = ONE OR THE OTHER IS REQUIRED BUT (C152 MUST BE CHANGED)

* = NOT REQUIRED

E.C.O. MODULE REFERENCE

ECO NUMBER	SUMMARY OF E.C.O.	PRINT CHANGE LOCATION	MODULE STAMPED REVISION	CIRCUIT SCHEM REVISION	E.C.O. REQUIREMENTS																					
					1	2	3	3A	4	5	6	7	8	9	9A	10	10A	11	12	12A	12A	13	15A	16	17	18
1	CHANGE R116, R118 FROM 1K TO 220K. CUT ETCH FROM E2-P3 TO E5-P4. TO E15-P4. ADD JUMPERS FROM E2-P3 TO GND, FROM E5-P4 TO E15-P4. CUT ETCH FROM E2-P4 TO E2-P2. ADD JUMPER FROM E2-P4 TO DL2-P3. CUT ETCH FROM E26-P8 TO DL1-P10. ADD JUMPER FROM E26-P8 TO DL1-P9.		A	A																						
2	CHANGE SENSE TERM. CAPS FROM 68PF TO 39PF. C1-C6 (16 PARTS). CHANGE BALUN RES., R37-R52, FROM 330K TO 2K (16 PARTS). CHANGE 1NH BY PASS CAPS., C47-C64, FROM .0022UF TO .005UF (16 PARTS). CHANGE DL3 FROM 50NS TO 100NS DELAY LINE. CHANGE TERM. RES R115 FOR DL3 FROM 390K TO 3K.	SHEET 1 AREA 2-C SHEET 2 AREA 4-C SHEET 2 AREA 4-C	B	B																						
3	DID NOT AFFECT C REV ETCH MODULES		-	-	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
3A	DID NOT AFFECT C REV ETCH MODULES		-	-	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
4	DL3 NOT TERMINATED PROPERLY. CHANGE R115 FROM 3K TO 1K. CUT ETCH FROM DL3 OUT TO E16-P8. CUT ETCH FROM DL3 IN TO E2-P1. ADD JUMPER FROM DL3 IN TO E16-P8. ADD JUMPER FROM DL3 OUT TO E2-P1.	SHEET 2 AREA 4-C	D	D			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
5	GLUE DOWN DL2 WITH ECCO BOND	SHEET 1 AREA 3-D	E	E			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
6	REVERSE C152 SO THAT + SIDE OF CAPACITOR GOES TO +SV	SHEET 1 AREA 4-E	F	F			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
7	DID NOT AFFECT C REV ETCH MODULES		-	-	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
7	DRILL BLANK BOARD, AND INSTALL (4) STANDOFFS 8SC/2W	SHEET 1 AREA 2-F	E1	E1			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
8	CHANGE R56, R58, R60, R61 FROM 5.62K TO 4.99K	SHEET 36A AREA 2-C	E2	E2			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
8	CHANGE C20, C22, C24, C26, C28, C30, C32, C34, C36 FROM .22UF TO .3UF	SHEET 36A AREA 2-C	E2	E2			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
8	CHANGE C44-C47 FROM .22UF TO .01UF	SHEET 36A AREA 2-C	E2	E2			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
9	INSTALL GND JUMPERS ON SIDE. CHANGE PART NO. OF #18 WIRE FROM 9107360-00 TO 1700029	SHEET 1	H	E3			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
9A	REPLACE HANDLE E-3, CHANGE PRINT FROM H TO E-3		E3	E3			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
10	REMOVE E2B, CUT ETCH FROM E2B-P13 TO E2B-P2		J	E4			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
10A	ADD E2B AND JUMPER E2B-P13 TO E15-P10		J	E4			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
10A	REPLACE HANDLE E-4, CHANGE PRINT FROM J TO E4		E4	E4			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
11	CANCELED		-	-	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
12	PRINT CHANGE ONLY		E5	E5			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
13	ADD 2 JUMPERS SO THAT PAL AND PBL ARE HIGH ON THE BUS		E6	E6			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
13A	CHANGE EFFECTIVITY DATE		-	-	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
14	DID NOT AFFECT C REV ETCH MODULE		-	-	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
15	GENERATE AN ALLOWABLE SUBSTITUTION LIST. CHANGE DL3	SHEET 1 AREA 1-E	E7	E7			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
15A	DELETE 68K RES FROM ALLOWABLE SUBSTITUTION LIST	SHEET 1 AREA 1+2-H	E7	E7			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
16	3 ETCH CUTS, 2 JUMPERS TO E7 PINS 6 X 7	SHEET 1 AREA 2-C	E8	E8			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
17	DID NOT AFFECT C REV ETCH MODULES		-	-	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
18	1 ETCH CUT, REMOVE 1 JUMPER		E9	E9			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
18	INSTALL 30 TWP FOR STROBE QH		E9	E9			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*
19	MOVE E26-8 FROM TAP 9 TO TAP 10 OF DELAY LINE 1.	SHEET 2 AREA D4	E10	E10			*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*	*

CAUTION

NON-STANDARD DRAWING

FOR SPECIAL APPLICATION

FIRST USED ON OPTION/MODEL

MMII-L

UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES

TOLERANCES

DECIMALS

ANGLES

.XXX ± .005

.XX ± .02

.X ± .1

REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY 1

MATERIAL

FINISH

+

+

+

QTY.

1

DATE

12-22-78

CHK'D.

DATE

ENG.

DATE

PROJ. ENG.

DATE

PROD.

DATE

NEXT HIGHER ASSY.

B-DD-MMII-L

SCALE NONE

SHEET 5 OF 5

PARTS LIST

digital EQUIPMENT CORPORATION

MAINTENANCE MASSACHUSETTS

TITLE

CONTROL & DATA LOOPS (CDL5)

SIZE CODE

NUMBER

B-DD-MMII-L

REV.

E10

REV

CHANGE NO

CHK

DEC FORM NO

1007

8

7

6

5

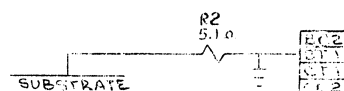
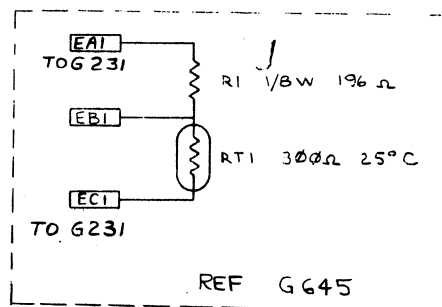
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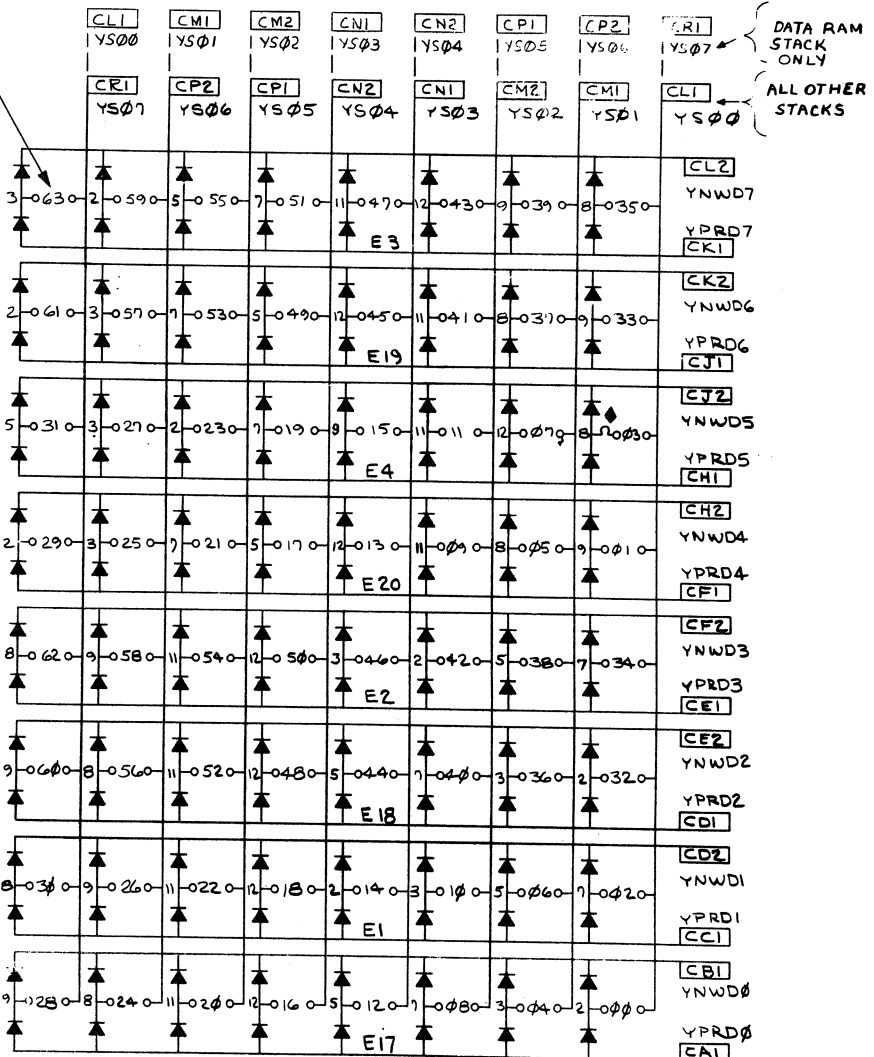
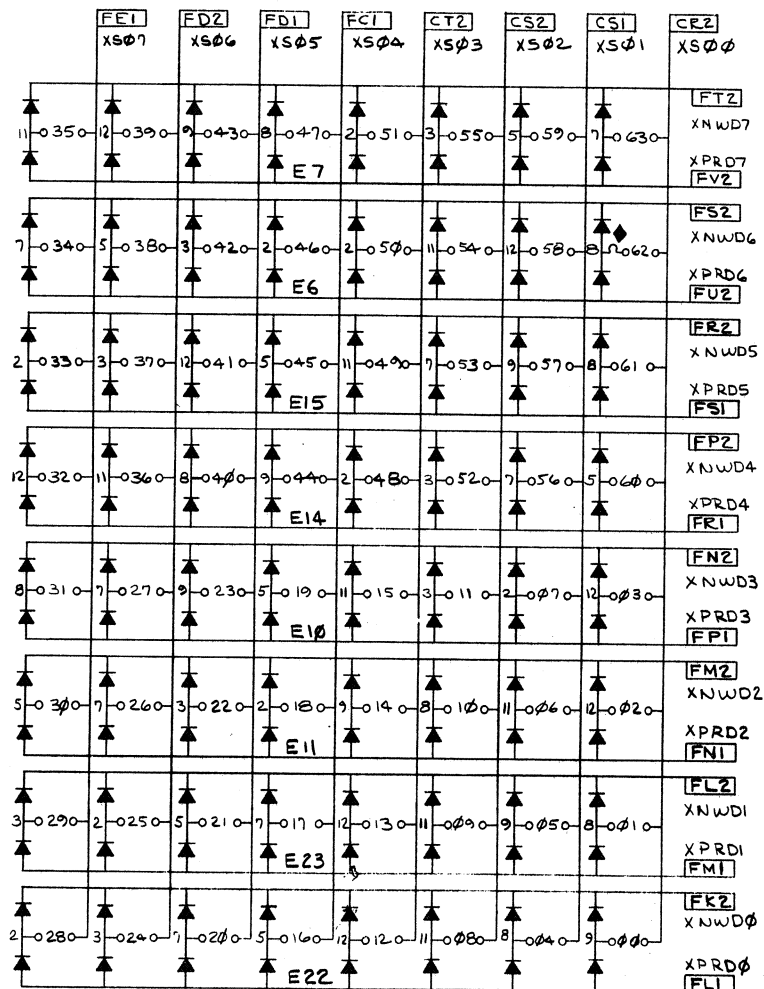


DCSH213-0-1

NOTES

1. UNLESS OTHERWISE SPECIFIED:
IC'S ARE TO BE DEC PART # 1910010
2. INDICATES STACK LINE NUMBER. (TYP)
3. INDICATES CURRENT LOOP.
4. INDICATES MAGNET WIRE TERMINATION (SOLDERED TO P.C. PAD).

SEE NOTE 4



- BIT 0: EU2 OIN, EV2 OSB, EV1 OSA
- BIT 4: ER2 OIN, ES2 OSB, ES1 OSA
- BIT 1: EI1 OIN, EP2 OSB, EP1 OSA
- BIT 5: EM2 OIN, EN2 OSB, EN1 OSA
- BIT 2: EI1 OIN, EL2 OSB, EL1 OSA
- BIT 6: EJ2 OIN, EK2 OSB, EK1 OSA
- BIT 3: EI1 OIN, EH2 OSB, EH1 OSA
- BIT 7: EE2 OIN, EF2 OSB, EF1 OSA
- BIT 8: EE1 OIN, ED2 OSB, ED1 OSA
- BIT 9: DU2 OIN, DV2 OSB, DV1 OSA
- BIT 13: DR2 OIN, DS2 OSB, DS1 OSA
- BIT 0: DI1 OIN, DP2 OSB, DP1 OSA
- BIT 14: DM2 OIN, DN2 OSB, DN1 OSA
- BIT 11: DI1 OIN, DL2 OSB, DL1 OSA
- BIT 15: DV2 OIN, DK2 OSB, DK1 OSA
- BIT 12: DJ1 OIN, DH2 OSB, DH1 OSA

REV	CHANGE NO
1	1

DEC 108M NO
ORD 102-B

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.		
MM11-L						
PARTS LIST						
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES						
DECIMALS	ANGLES	DATE	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS			
.XXX - .005	±0° 30'	12/1/71				
.XX - .02		1-25-72				
.X - .1		1-25-72				
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY						
MATERIAL						
FINISH						
NEXT HIGHER ASSY.		TITLE				
B-DD-MM11-L-0		STACK SCHEMATIC				
SCALE		4K X 16 BIT				
SHEET 2 OF 2		SIZE CODE				
		DASH213-0-1				
		NUMBER				
		REV. F				

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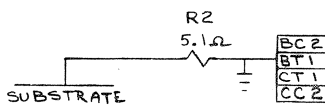
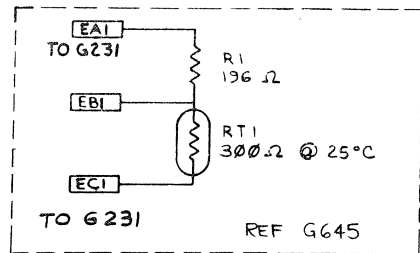
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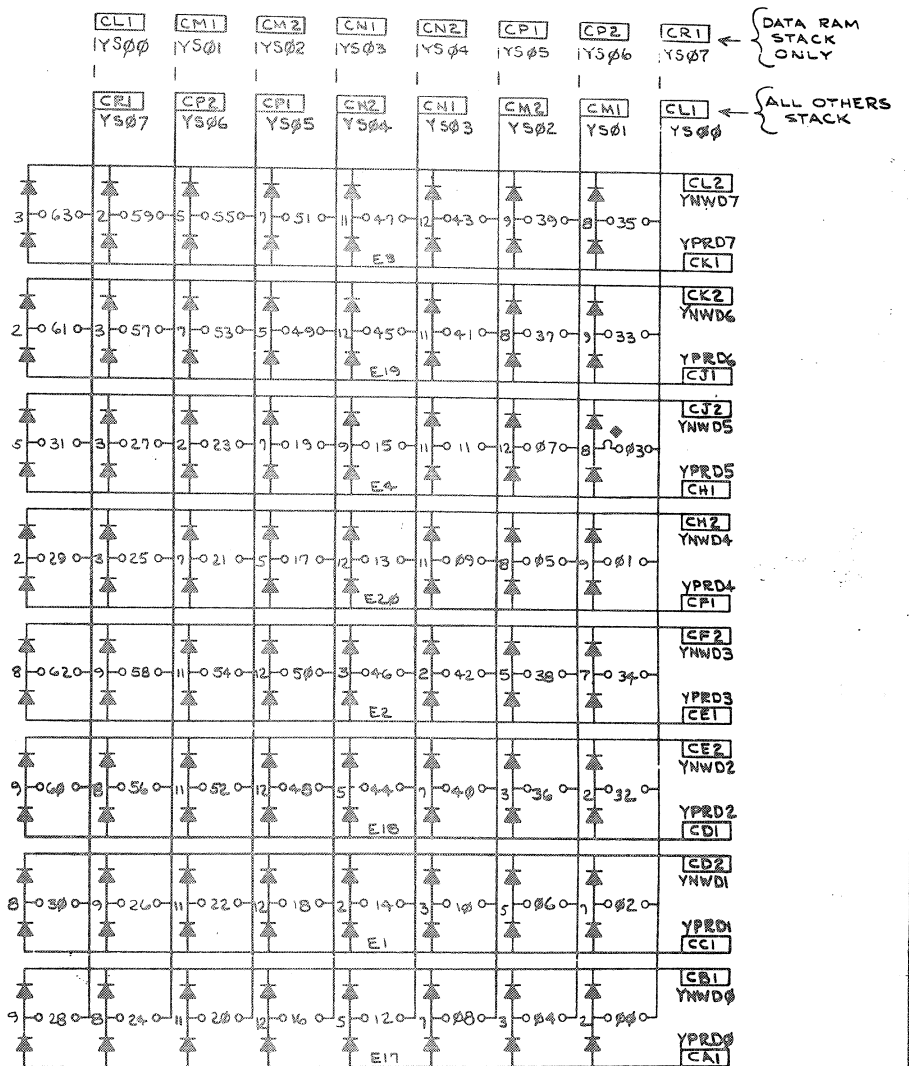
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SEE NOTE 4

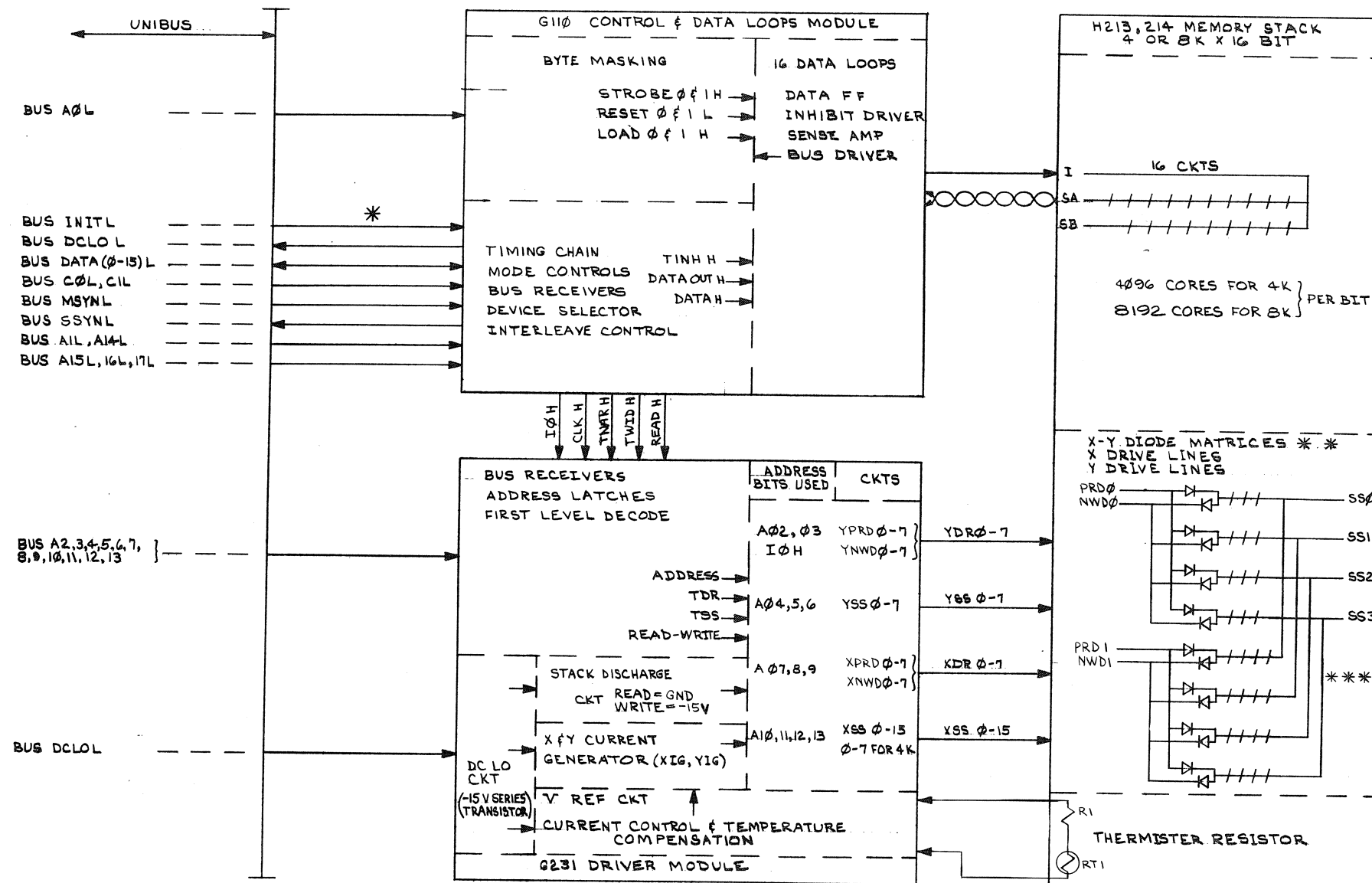
NOTES:
 1. UNLESS OTHERWISE SPECIFIED:
 IC'S E1-E24 ARE TO BE DEC PART #1910010
 2. INDICATES STACK LINE NUMBER. (TYP)
 3. INDICATES CURRENT LOOP.
 4. INDICATES MAGNET WIRE TERMINATION (SOL-
 DERED TO P.C. PAD).
 * 3. FOR H215 & H216 ONLY. P0=PA, P1=PB
 * 6. FOR H216 ONLY.



FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
MM11-L				
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES		DATE 1/23/71	EQUIPMENT CORPORATION CORPORATION 0000000000000000	
DECIMALS	ANGLES	DATE 1/25/72	TITLE STACK SCHEMATIC (H214-8K X 16 BIT) (H215-8K X 18 BIT) (H216-8K X 19 BIT)	
.001 - .005 ± .00	20° 20'	DATE 1/25/72		
.006 - .010 ± .01		DATE 1/26/72		
RESERVE SURFACES AND BREAK SHARP CORROSION SURFACE QUALITY		DATE 1/26/72		
MATERIAL		NEXT NUMBER ASBY:		
		B-DD-MM11-L-0	DCS	H214-0-1
FINISH		SCALE	REV. F	
		SHEET 2 OF 2		

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- NOTES:
- *1. ALL ARROWS SHOW SIGNAL FLOW DIRECTION.
 - **2. MATRIX SHOWN IS FOR ILLUSTRATION ONLY.
 - ***3. ACTUAL MATRIX HAS
- Y AXIS 8PRD, 8NRD, 8SS
X AXIS 4K 8PRD, 8NRD, 8SS
X AXIS 8K 8PRD, 8NRD, 16SS

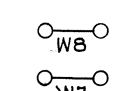


FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
MMII-S					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN. <i>J. Carbery</i>	DATE <i>11/4/72</i>	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS		CHKD. <i>J. Carbery</i>	DATE <i>2-14-72</i>		
ANGLES		ENG. <i>P. Durand</i>	DATE <i>3-30-72</i>		
XXX + .005 XX - .02 X .1		PROJ. ENG. <i>P. Durand</i>	DATE <i>3-31-72</i>		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY ✓		PROD. <i>Jack Horner</i>	DATE <i>6-2-72</i>		
MATERIAL — / —		NEXT HIGHER ASSY. B-DD-MMII-S			
FINISH — / —		SCALE SHEET 1 OF 2			SHEET NUMBER DBD MMII-S-2 REV.
		DIST.			

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MEMORY BANK	MACHINE ADDRESS	W1 *	W5 A13 △	W6 □ A14 OR A81	W4 A15	W3 A16	W2 A17L	W9 4K-8K △	W10	W7-8 INTER LEAVE □	W11 PROTECT
0-4K 4-8K	000000-017776 020000-037776	IN	IN OUT	IN IN	IN IN	IN IN	IN IN				
8-12K 12-16K	040000-057776 060000-077776		IN OUT	OUT OUT	IN IN	IN IN	IN IN				
16-20K 20-24K	100000-117776 120000-137776		IN OUT	IN IN	OUT OUT	IN IN	IN IN				
24-28K 28-32K	140000-157776 160000-177776		IN OUT	OUT OUT	OUT OUT	IN IN	IN IN				
32-36K 36-40K	200000-217776 220000-237776		IN OUT	IN IN	IN IN	OUT OUT	IN IN				
40-44K 44-48K	240000-257776 260000-277776		IN OUT	OUT OUT	IN IN	OUT OUT	IN IN				
48-52K 52-56K	300000-317776 320000-337776		IN OUT	IN IN	OUT OUT	OUT OUT	IN IN				
56-60K 60-64K	340000-357776 360000-377776		IN OUT	OUT OUT	OUT OUT	OUT OUT	IN IN				
64-68K 68-72K	400000-417776 420000-437776		IN OUT	IN IN	IN IN	IN IN	OUT OUT				
72-76K 76-80K	440000-457776 460000-477776		IN OUT	OUT OUT	IN IN	IN IN	OUT OUT				
80-84K 84-88K	500000-517776 520000-537776		IN OUT	IN IN	OUT OUT	IN IN	OUT OUT				
88-92K 92-96K	540000-557776 560000-577776		IN OUT	OUT OUT	OUT OUT	IN IN	OUT OUT				
96-100K 100-104K	600000-617776 620000-637776		IN OUT	IN IN	IN IN	OUT OUT	OUT OUT				
104-108K 108-112K	640000-657776 660000-677776		IN OUT	OUT OUT	IN IN	OUT OUT	OUT OUT				
112-116K 116-120K	700000-717776 720000-737776		IN OUT	IN IN	OUT OUT	OUT OUT	OUT OUT				
120-124K	740000-757776		IN	OUT	OUT	OUT	OUT				
DEVICE USED AS 4K MEMORY △		IN	X	X	X	X	X	IN	OUT		

WHEN USED AS A 4K OR A NON INTER-LEAVED 8K JUMPERS ARE



WHEN USED AS AN 8K INTERLEAVED (2-8K BANKS REQUIRED) JUMPERS ARE



WHEN W11 IS INSTALLED IT MAKES THE MEMORY DO A DATA REGARDLESS OF THE STATE OF THE C00 AND C01 LINES

NOTES:

- *1. W1 IS FOR TEST PURPOSES ONLY
- △2. WHEN USED AS AN 8K BANK, W5 AND W10 MUST BE INSTALLED AND W9 MUST BE OUT. WHEN USED AS A 4K BANK W10 MUST BE OUT, W9 MUST BE IN AND W5 DETERMINES THE BANKS LOCATION ON THE BUS.
- 3. THIS MEMORY CAN ONLY BE INTERLEAVED AS 16K (TWO ADJACENT, CONTIGUOUS ADDRESS 8K BANKS). WHEN NOT INTER-LEAVED (SOLID JUMPERS ON W7 AND W8) THE DEVICE SELECT IS AS SHOWN IN TABLE 1 USING A14. WHEN TWO 8K BANKS ARE INTER-LEAVED W7 AND W8 MUST BE AS SHOWN IN DOTTED LINES IN TABLE 1. ALSO IN TABLE 1, A01 NOW GOES TO THE DEVICE SELECTOR GATE CONTROLLED BY W6. THE TWO BANKS MUST HAVE W6 IN ON ONE BANK AND OUT ON THE OTHER.
4. FIGURE 1 SHOWS THE PHYSICAL LOCATION OF THE JUMPERS ON THE G110 IF THE MODULE WERE LYING ON THE PRINT WITH COMPONENTS UP AND CONNECTORS TOWARD BOTTOM OF PRINT. W7 & W8 ARE AS SHOWN SCHEMATICALLY ON D-CS-G110-0-1

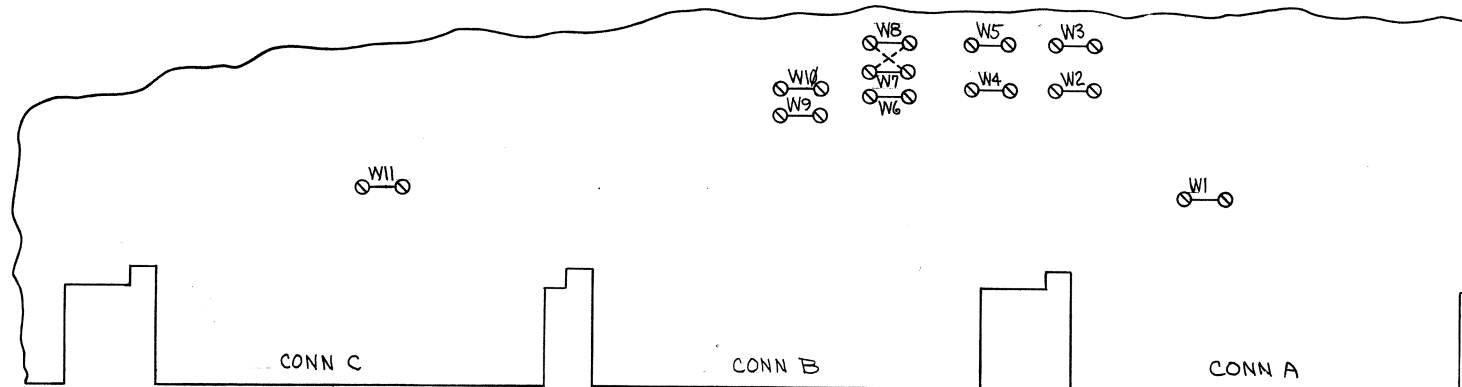


FIGURE 1
G110 JUMPER PHYSICAL LOCATION
SEE NOTE 4

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION		PART NO.	ITEM NO.
MM11-S						
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN	DATE	PARTS LIST		
		5/15/72	digital EQUIPMENT CORPORATION			
		5-25-72	NATYARD MASSACHUSETTS			
DECIMALS	ANGLES	CHK'D	DATE	TITLE		
.XXX = .005	±0° 30'	5-25-72	BLOCK DIAGRAM (DEVICE DECODING)			
.XX = .02		ENG.	DATE			
.X = .1		5-22-72				
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		PROJ. ENG.	DATE			
		6-2-72				
MATERIAL		PROD.	DATE			
		6-2-72				
FINISH		NEXT HIGHER ASSY.				
		B-DD-MM11-S-0				
SCALE		SIZE/CODE	NUMBER	REV.		
		D BD	MM11-S-2			
SHEET 2 OF 2		DIST.				

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS PARTS LIST		
MADE BY F. CARBERRY	CHECKED <i>J. Kellogg</i>	SECTION
DATE 1/5/72	DATE 1-25-72	
ENG P. Duval	PROD <i>R.K. Peterson</i>	ISSUED SECT.
DATE 1/25/72	DATE 1-26-72	

DATE 1/25/72

DATE 1-26-72

ISSUED SECT.

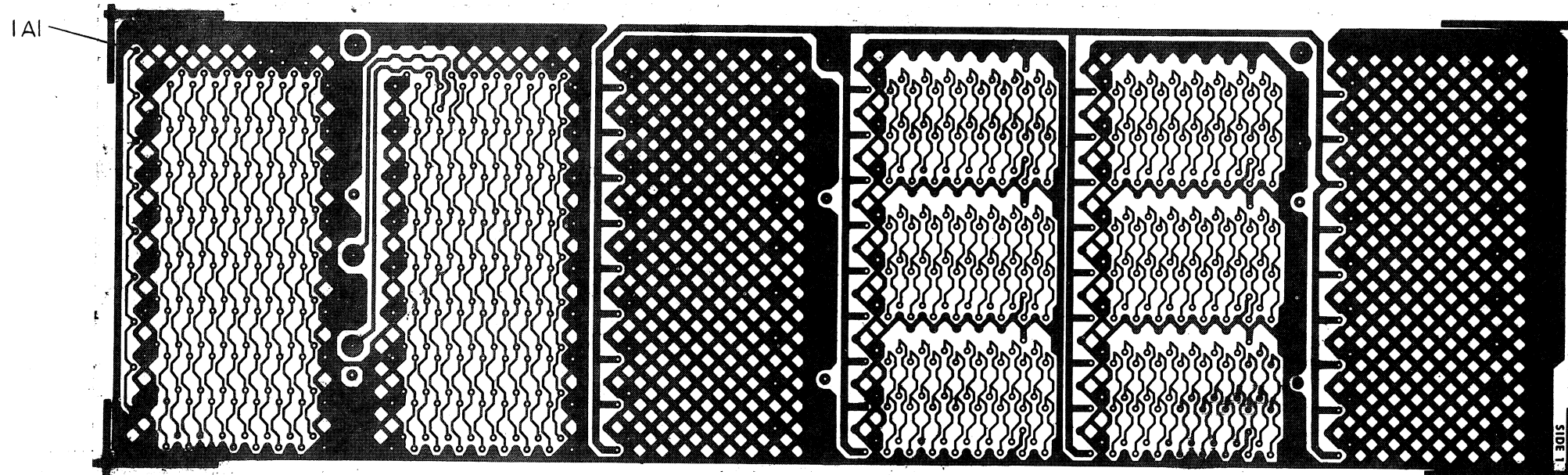
[illegible]

1	B-DD-H213-Ø	H213 MEMORY STACK (4K X 16)
2	E-CS-G231-Ø-1	MEMORY DRIVER
3	E-CS-G11Ø-Ø-1	CONTROL & DATA LOOPS
4	B-DD-H214-Ø	H214 MEMORY STACK (8K X 16)

REV.	ECO NO.
------	---------

DIST.

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BLOCK SIDE (SIDE 1)

REVISIONS		REV.
CHK	CHANGE NO.	
1	5409959-00001	A
2	5409959-00002	B
3	5409959-00003	C
4	5409959-00004	D

DEC FORM NO.
DRC 100-A

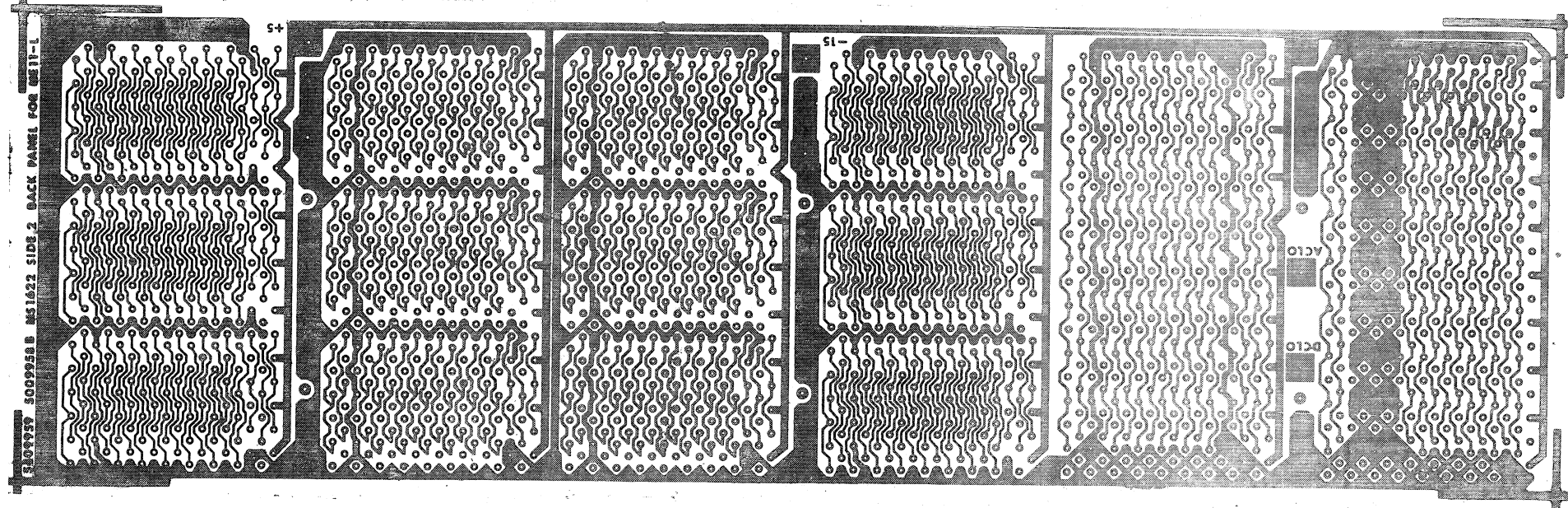
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES				
DECIMALS	ANGLES	DRN. K. J. Naile	DATE 8-23-72	
.XXX = .005	±0° 30'	CHK'D. S. ROTHMAN	DATE 8/31/72	
.XX = .02		ENG. S. ROTHMAN	DATE 8/31/72	
.X = .1		PROJ. ENG. S. ROTHMAN	DATE 8/31/72	
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		PROD. R. PETERSON	DATE 8/31/72	
MATERIAL	NEXT HIGHER ASSY.			
FINISH	SCALE 1/1			
SHEET 1 OF 2		DIST.		

SIZE CODE	NUMBER	REV.
C	5409959-0-1	C

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

TITLE
CIRCUIT SCHEMATIC

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SOLDER SIDE (SIDE 2)

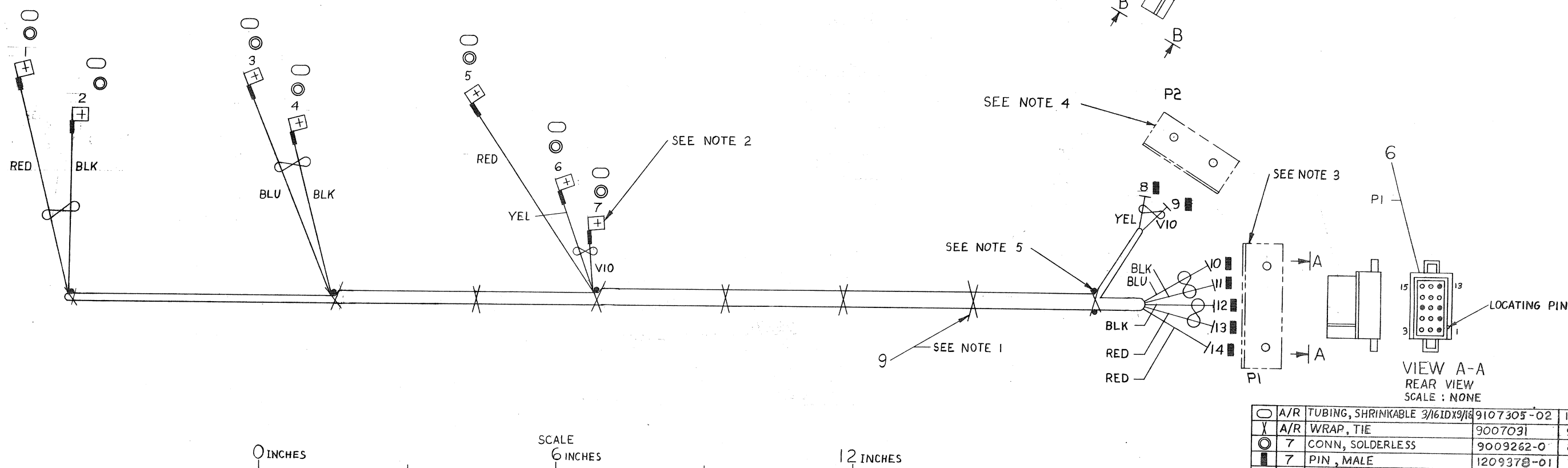
REVISIONS	
CHK	REV.

BRUNING 40-107 15968

DEC FORM NO.
DRC 100-A

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		DRN. B. J. Mark	DATE 8-23-72	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS	ANGLES	CHK'D. S. ROTHMAN	DATE 8/31/72		
.XXX = .005	± 0° 30'	ENG. S. ROTHMAN	DATE 8/31/72		
.XX = .02		PROJ. ENG. S. ROTHMAN	DATE 8/31/72		
.X = .1		PROD. R. PETERSON	DATE 8/31/72		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		NEXT HIGHER ASSY.		TITLE CIRCUIT SCHEMATIC	
MATERIAL		E-AL-7008872-0-0		SIZE CODE C CS	NUMBER 5409959-0-1
FINISH		SCALE 1/1		REV. C	
		SHEET 2 OF 2		DIST.	

1. USE TIE WRAPS ITEM #9 APPROXIMATELY EVERY THREE (3) INCHES WHEN NECESSARY, AND AT EVERY BREAKOUT POINT.
2. ATTACH MALE FASTON DEC # (9008219-0) WITH #4 WOOD SCREWS (7 PLACES).
3. USE CONN. BRKT. #C-MD-9305761-H15-O, MOUNT WITH #6 WOOD SCREWS. USE MATING CONN. 1209350-15.
4. USE CONN. BRKT. #C-MD-9305761-H6-O, MOUNT WITH #6 WOOD SCREWS. USE MATING CONN. 1209350-06.
5. DOT (•) INDICATES NAIL LOCATIONS FOR ASSEMBLY USE ONLY. COVER NAILS WITH SHRINK TUBING TO PREVENT CUTTING HARNESS



DO NOT REDUCE
DO NOT BUILD FROM REDUCED PRINT

SCALE: NONE			
	A/R	TUBING, SHRINKABLE 3/16ID X 1/8	9107305-02
	A/R	WRAP, TIE	9007091
	7	CONN, SOLDERLESS	9009262-0
	7	PIN, MALE	1209378-01
	1	HOUSING, CONN 15 PIN	1209351-15
	1	HOUSING, CONN 6 PIN	1209351-06
	A/R	WIRE #14 AWG RED	9107370-22
	A/R	WIRE #14 AWG TWP BLK/RED	9107440-02
	A/R	WIRE #14 AWG TWP BLK/BLU	9107440-06
	A/R	WIRE #18 AWG TWP YEL/VIO	9107430-47

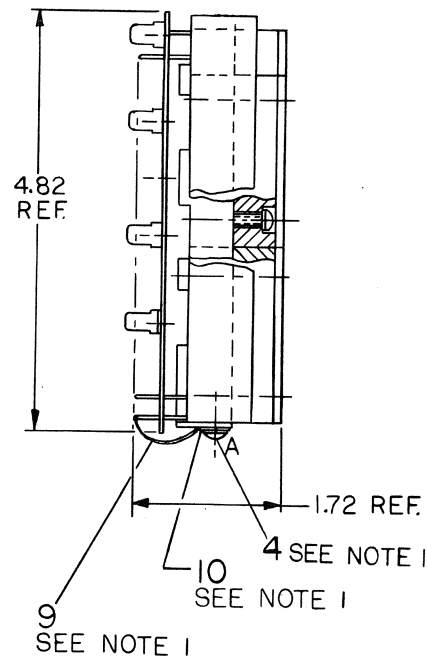
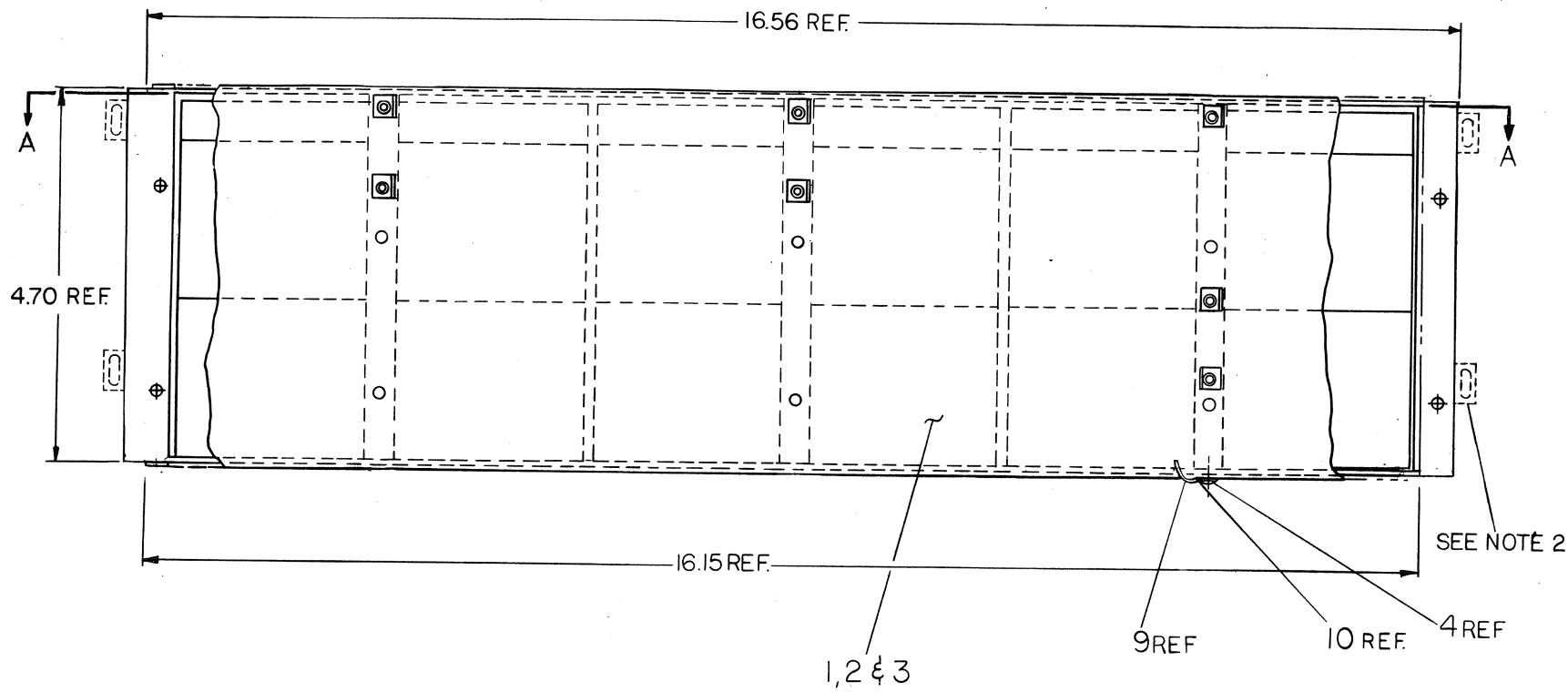
FIRST USED ON OPT / MOD		SYM	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST						
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES			DRN	DATE	EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
			CHKD	DATE		
TOLERANCES			ENG.	DATE		
DECIMALS	ANGLES	PROJ. ENG.	DATE			
.xxx = .005	±0° 30'	PROD.	DATE	TITLE MF11-L/LP POWER HARNESS		
.xx = .02						
.x = .1						
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY √			NEXT HIGHER ASSY.			
MATERIAL						
SEE PARTS LIST						
FINISH			SIZE CODE			
SCALE 1 / 1			NUMBER			
SHEET OF 1			REV.			
DIST.			D IA 7009560-0-0			

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NOTES:

1. WIRE WRAP ONE END OF ITEM #9 TO PIN B0902 AND SOLDER THE OTHER END TO ITEM #10 WHICH IS MOUNTED AS SHOWN.
2. IF LOGIC FRAMES DEC P/N 1210103 OR 1210331 ARE USED, THE MOUNTING EARS ARE TO BE MACHINED OFF FLUSH WITH THE SIDES OF THE CASTING.

SECTION A-A



REVISIONS	
CHK	CHANGE NO.
1	MFII-L-00002
2	ORIGINATED
3	MFII-L-00003
4	MFII-L-00004
5	MFII-L-00005
6	MFII-L-00006
7	MFII-L-00007
8	MFII-L-00008
9	MFII-L-00009
10	MFII-L-00010
11	MFII-L-00011
12	MFII-L-00012
13	MFII-L-00013
14	MFII-L-00014
15	MFII-L-00015
16	MFII-L-00016
17	MFII-L-00017
18	MFII-L-00018
19	MFII-L-00019
20	MFII-L-00020
21	MFII-L-00021
22	MFII-L-00022
23	MFII-L-00023
24	MFII-L-00024
25	MFII-L-00025
26	MFII-L-00026
27	MFII-L-00027
28	MFII-L-00028
29	MFII-L-00029
30	MFII-L-00030
31	MFII-L-00031
32	MFII-L-00032
33	MFII-L-00033
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36	MFII-L-00036
37	MFII-L-00037
38	MFII-L-00038
39	MFII-L-00039
40	MFII-L-00040
41	MFII-L-00041
42	MFII-L-00042
43	MFII-L-00043
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79	MFII-L-00079
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91	MFII-L-00091
92	MFII-L-00092
93	MFII-L-00093
94	MFII-L-00094
95	MFII-L-00095
96	MFII-L-00096
97	MFII-L-00097
98	MFII-L-00098
99	MFII-L-00099
100	MFII-L-00100

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
MFII-L					
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES. TOLERANCES		PARTS LIST			
		DRN. <i>Beasley R</i>	DATE <i>2-28-73</i>	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS TITLE MFII-L MEMORY	
CHK'D. <i>[Signature]</i>	DATE <i>3-5-73</i>				
DECIMALS	ANGLES	ENG. <i>[Signature]</i>	DATE <i>3-14-73</i>		
.XXX - .005 .XX - .02 .X - .1	± 0° 30'	PROJ. ENGR. <i>[Signature]</i>	DATE <i>3-16-73</i>		
REMOVE BURRS AND BREAK SHARP CORNERS. SURFACE QUALITY ☒		PROD. <i>[Signature]</i>	DATE <i>3-16-73</i>		
MATERIAL		NEXT HIGHER ASSY.		SIZE CODE NUMBER REV. D UA MFII-L-0 D	
— # —		B-DD-MFII-L			
FINISH		SCALE 1/1			
— # —		SHEET 1 OF 1		DIST.	