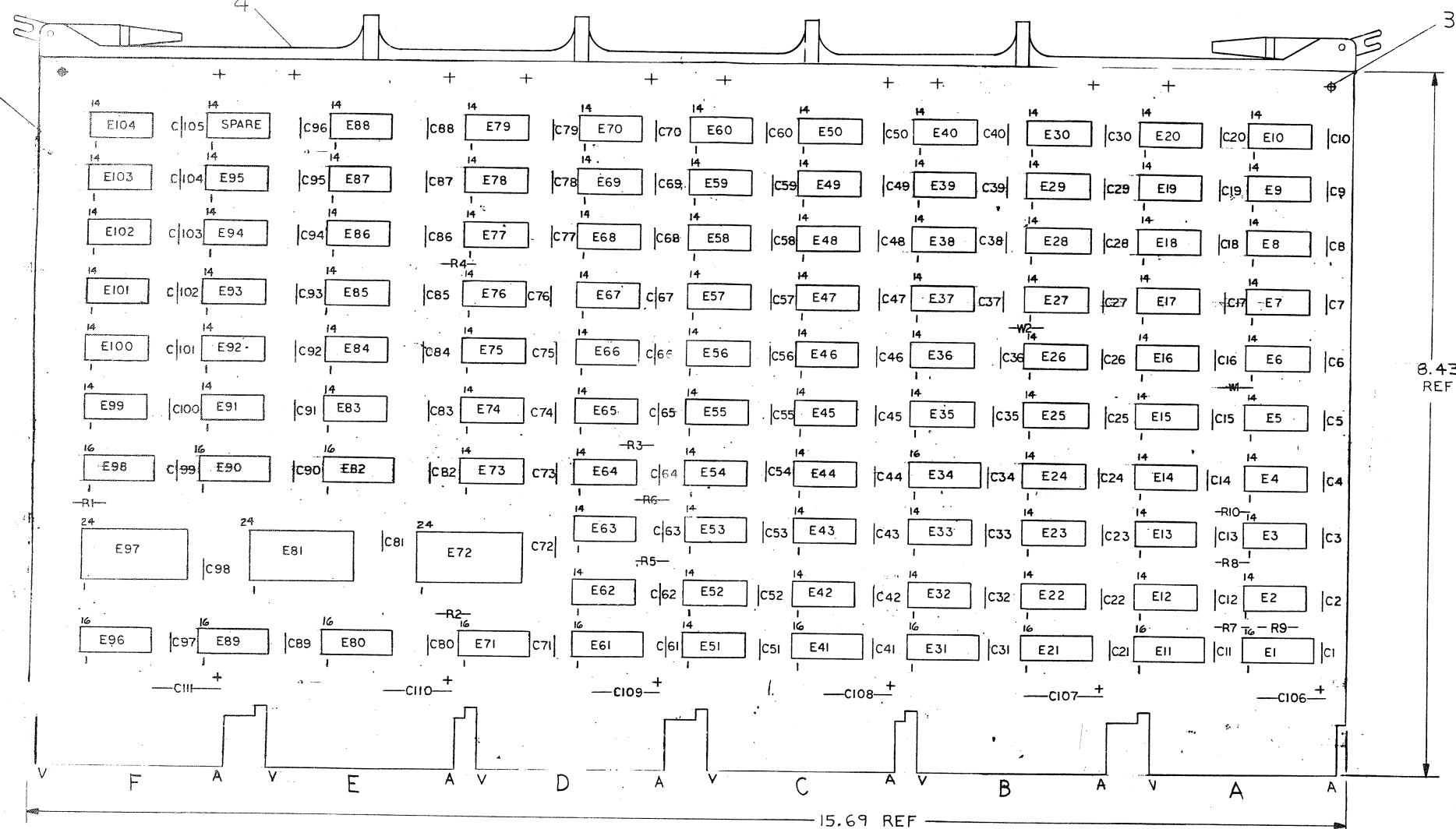


NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KDI-1A PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER.
2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED. OUTPUT SIGNALS WITH MODULE PINS ARE BROUGHT TO THE RIGHT SIDE OF THE PRINT.

3. PROCESSOR SIGNAL PREFIX NOTATION (K2-1 FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.

4. UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS;
CAPACITANCE IS IN PICOFARRADS.



2	WI, W2	INSULATED JUMPER	9009185	32
12		EYELET	9006732	31
1	E1	I.C. DEC 74157	1910655	30
4	E21, E71, E96, E41	I.C. DEC 74175	1910651	29
3	E81, E97, E72	I.C. DEC 74150	1910153	28
1	E82	I.C. DEC 74153	1909937	27
2	E90, E98	I.C. DEC 74151	1909936	26
11	E5, E8, E29, E33, E37, E45 E62, E75, E86, E92, E95	I.C. DEC 74H04	1909931	25
6	E11, E31, E34, E61, E80, E89	I.C. DEC 8251	1909594	24
6	E20, E22, E23, E27, E32, E56	I.C. DEC 8815	1909713	23
7	E14, E36, E51, E79, E83 E84, E59	I.C. DEC 74H11	1909267	22
1	E26	I.C. DEC 74H61	1909065	21
2	E6, E7	I.C. DEC 74H60	1909064	20
3	E39, E17, E57	I.C. DEC 74H53	1909062	19
5	E4, E28, E52, E53, E54	I.C. DEC 74H52	1909061	18
3	E87, E49, E19	I.C. DEC 74H50	1909060	17
5	E24, E67, E68, E69, E70	I.C. DEC 74H30	1909059	16
4	E12, E13, E38, E102	I.C. DEC 74H21	1909058	15
8	E15, E40, E42, E64, E77 E104, E47, E101	I.C. DEC 74H10	1909057	14
14	E2, E3, E18, E35, E43, E48 E58, E73, E76, E78, E85 E88, E93, E99	I.C. DEC 74H00	1909056	13
7	E9, E10, E16, E44, E60 E100, E103	I.C. DEC 7402	1909004	12
7	E25, E30, E46, E55 E65, E66, E94.	I.C. DEC 74H20	1905635	11
1	E50	I.C. DEC 74H40	1905586	10

3	E63,E74,E91	I.C. DEC 7400	1905575	6
10	RI THRU R10	RES. 1K 1/4W $\pm 5\%$	1300365	3
1		HANDLE MODULE	1210711-02	4
105	C1 THRU C105	CAP .01 μ F 100V $\pm 20\%$ DISC	1001610	3
6	C106 THRU C111	CAP 6.8 μ F 35V $\pm 20\%$ STANT	1000067	2
1		ETCHED CIRCUIT BOARD	5009982	1
QTY	REF DESIGNATION	DESCRIPTION	PART NO.	ITEM NO.

[illegible]

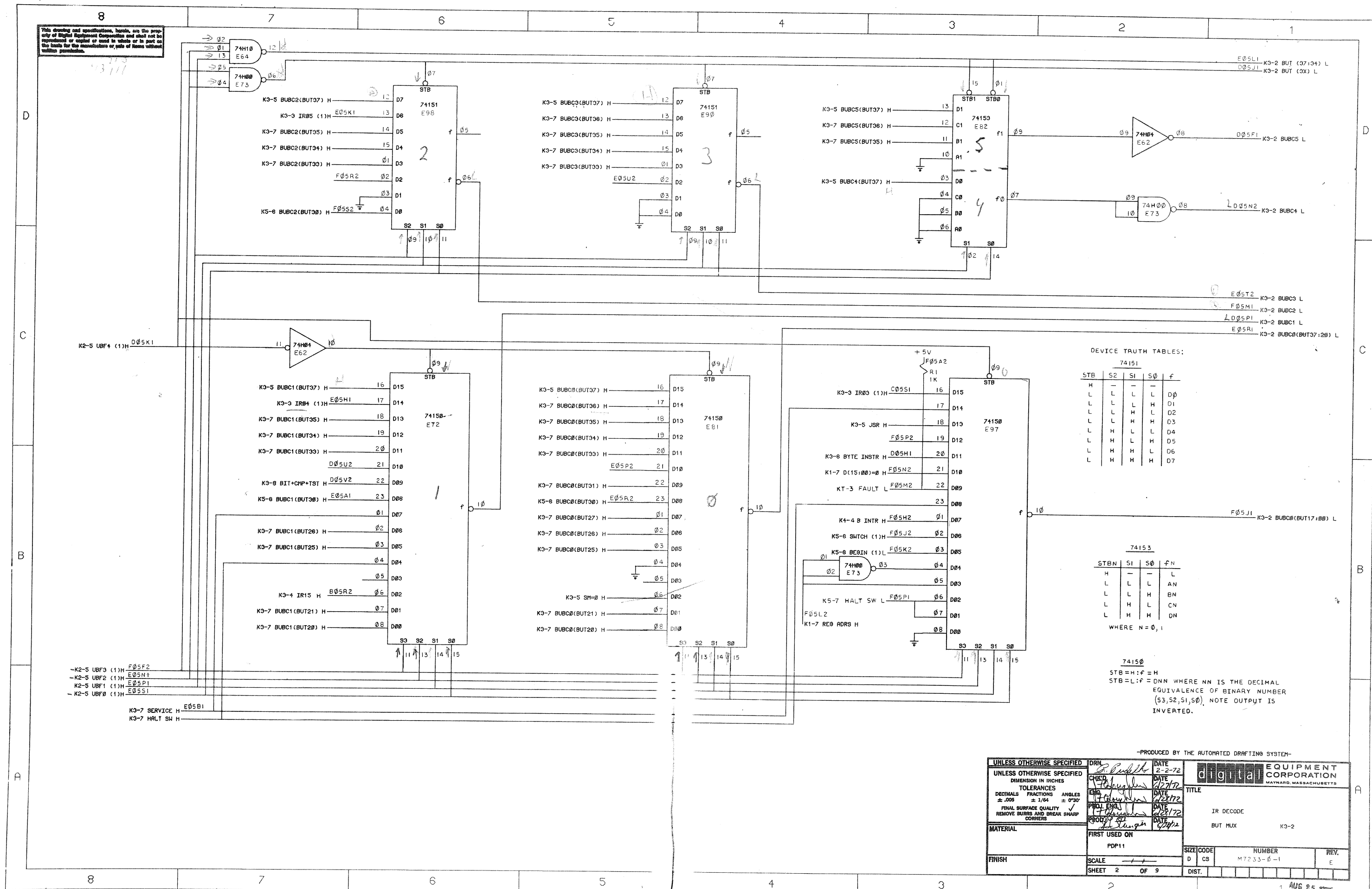
FIRST USED ON OPTION MODEL
PDP 11

DEC NO.	EIA NO.
SEMICONDUCTOR	

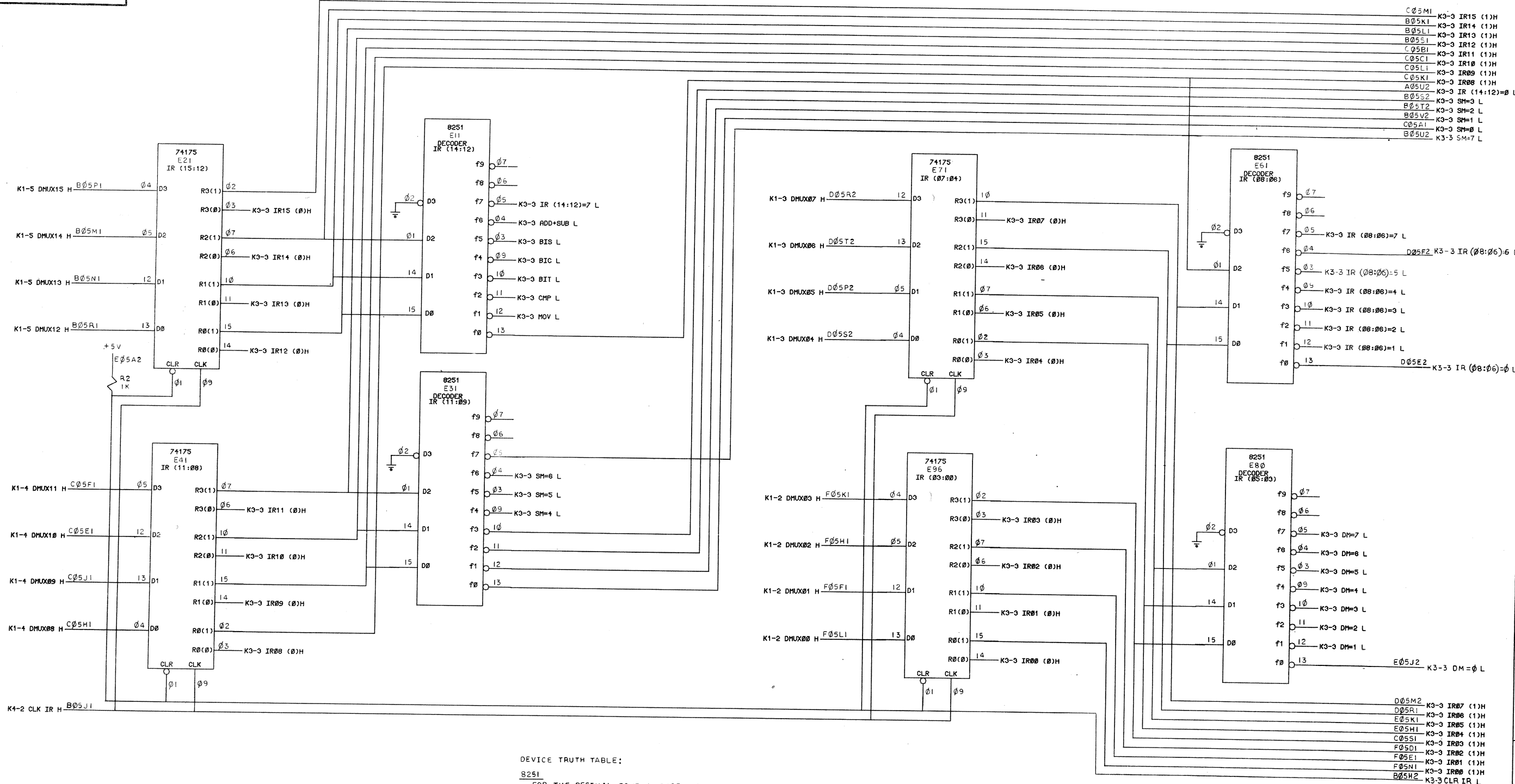
SEMICONDUCTOR CONVERSION CHART

[illegible]

A05C2, A05T1
B05C2, B05T1
C05C2, C05T1
D05C2, D05T1
E05C2, E05T1
F05C2, F05T1



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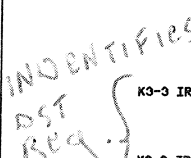
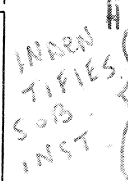


DEVICE TRUTH TABLE:
8251
FOR THE DECIMAL EQUIVALENCE, N, OF THE BINARY
NUMBER (D3,D2,D1,D0), ONLY OUTPUT #N IS ACTIVE
(LOW) FOR 0 ≤ N < 10

UNLESS OTHERWISE SPECIFIED		DATE	2-2-72	
UNLESS OTHERWISE SPECIFIED		CHKD	R. J. R. / 2/2/72	
DIMENSION IN INCHES		DATE	2/2/72	
TOLERANCES		DATE	2/2/72	
DECIMALS	FRACTIONS	ANGLES	± 0°30'	
± .005	± 1/64	± 0°30'		
FINAL SURFACE QUALITY		DATE	2/2/72	
REMOVE BURRS AND BREAK SHARP CORNERS		DATE	2/2/72	
MATERIAL		FIRST USED ON	POP11	
FINISH		SCALE	1" = 1"	
SHEET 3 OF 9		DIST.		

digital EQUIPMENT CORPORATION		MAYNARD, MASSACHUSETTS	
TITLE		IR DECODE	
IR & DECODE		K3-3	
SIZE/CODE	NUMBER	REV	
D CS	M7233-0-1	E	

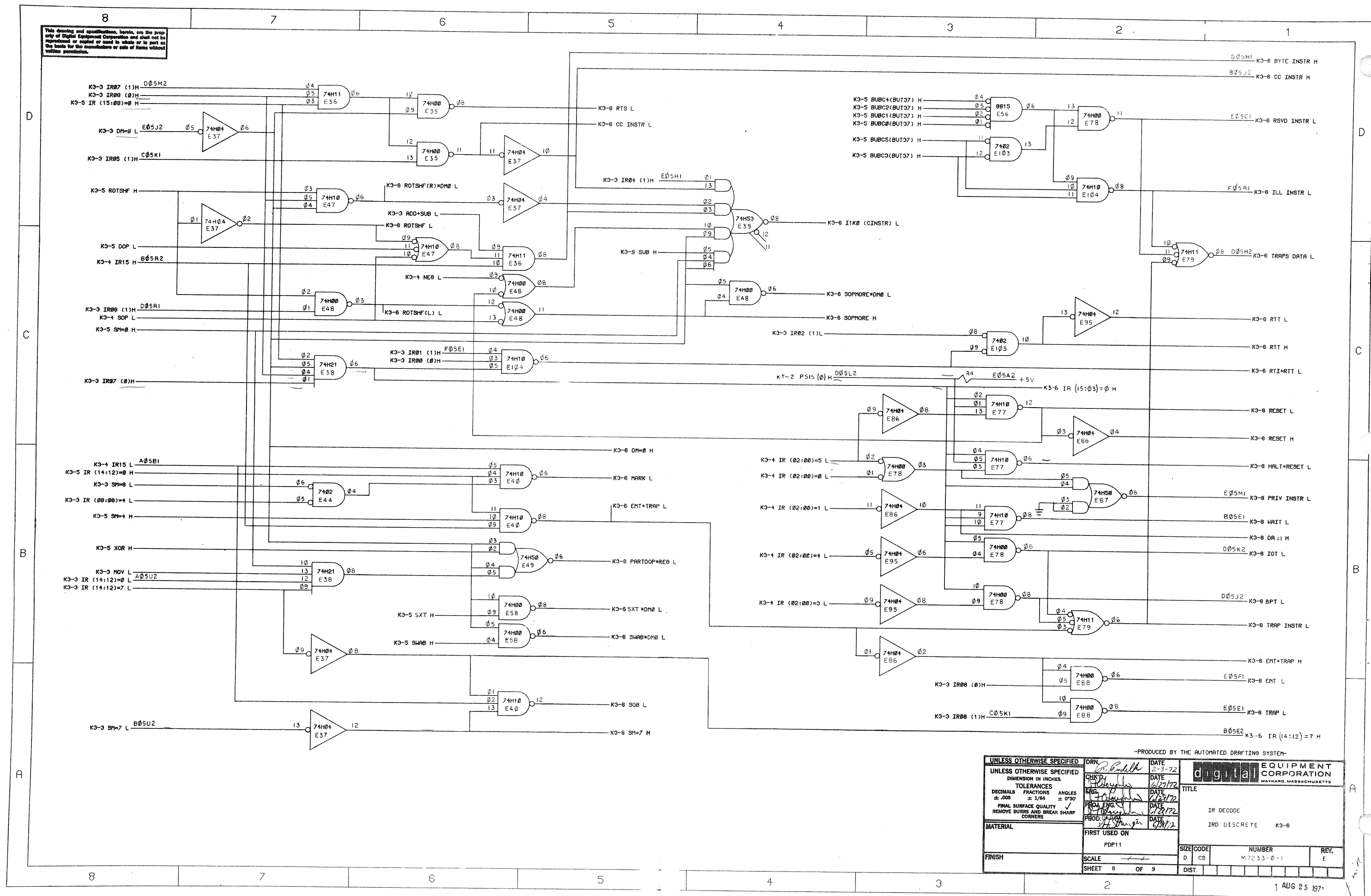
1 AUG 25 1971

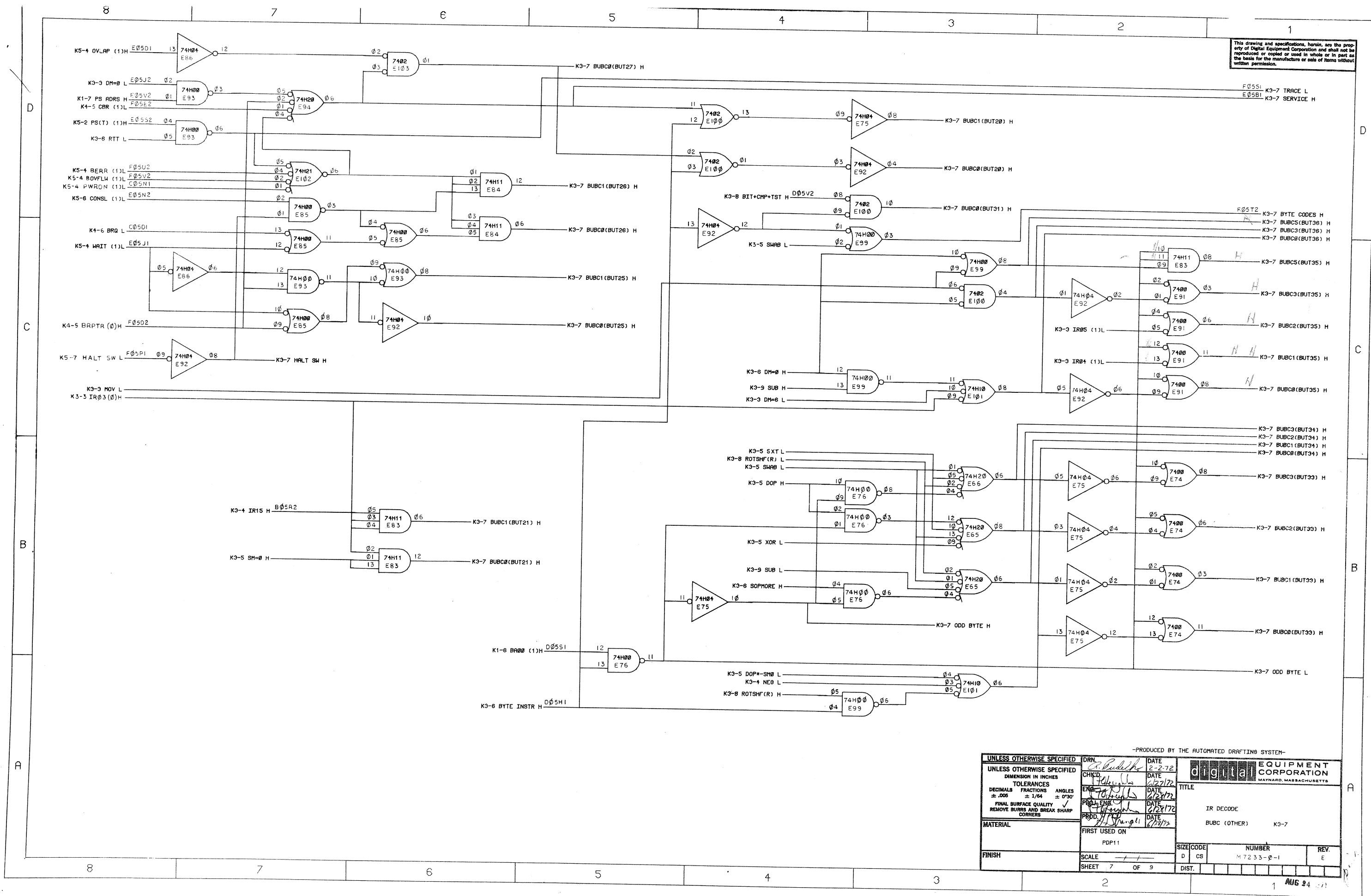


D



1 AUG 25 1971





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D

C

B

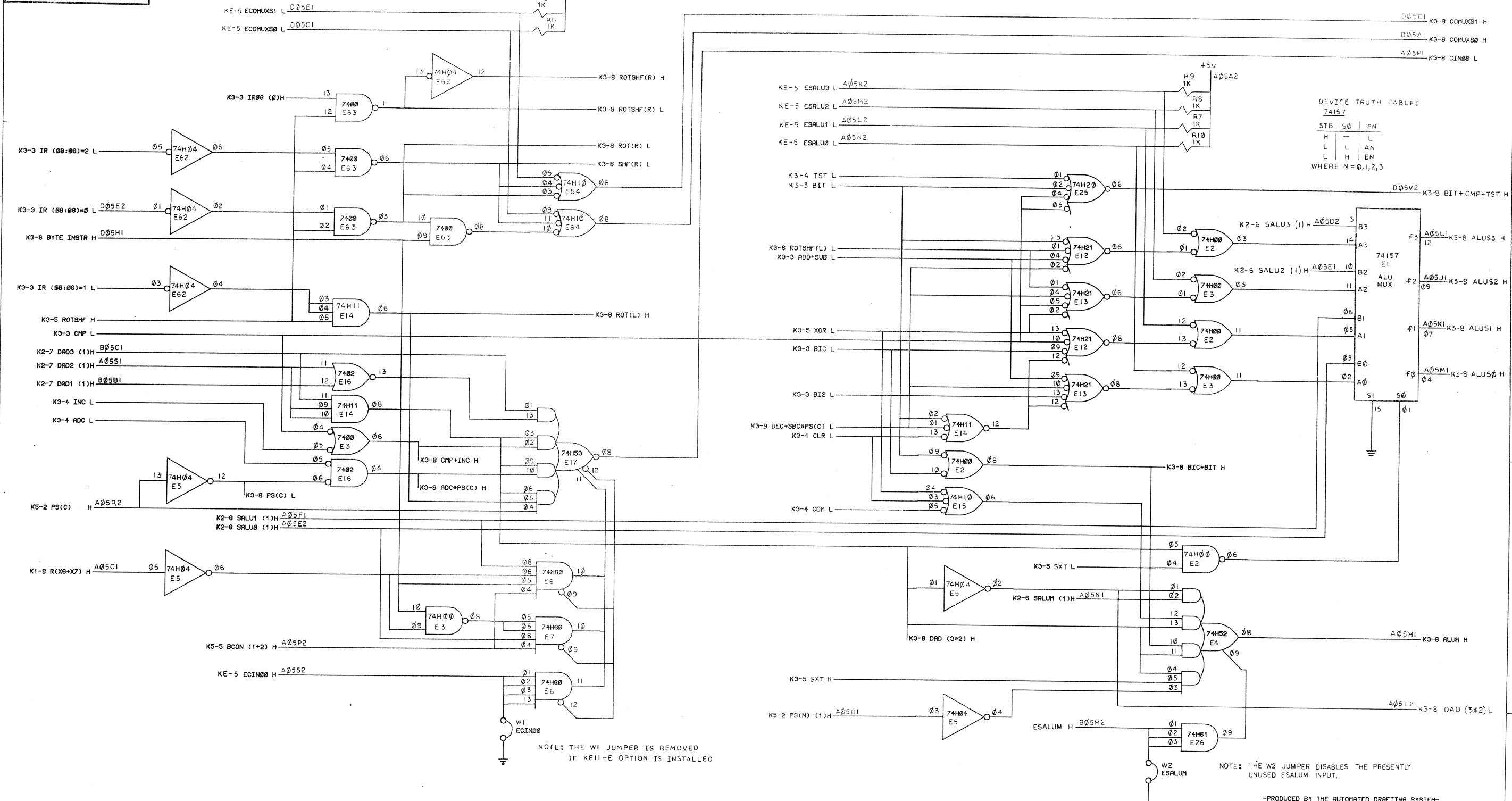
A

D

C

B

A



NOTE: THE W1 JUMPER IS REMOVED IF KE11-E OPTION IS INSTALLED

NOTE: THE W2 JUMPER DISABLES THE PRESENTLY UNUSED ESALUM INPUT.

-PRODUCED BY THE AUTOMATED DRAFTING SYSTEM-

DRN. <i>P. J. Delaney</i>	DATE <i>2-3-72</i>	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS
CHK'D. <i>H. J. Delaney</i>	DATE <i>6/27/72</i>	
ENG. <i>J. J. Delaney</i>	DATE <i>6/28/72</i>	
PRD. <i>J. J. Delaney</i>	DATE <i>6/28/72</i>	
PROJ. <i>J. J. Delaney</i>	DATE <i>6/28/72</i>	
FIRST USED ON		
PDP11		
SCALE <i>1:1</i>	SIZE CODE <i>D CS</i>	NUMBER <i>M7233-0-1</i>
SHEET <i>8</i>	OF <i>9</i>	REV. <i>E</i>
		DIST. <i>1</i>

IR DECODE
ALU CONTROL K3-8

AUG 25 1971

A



AUG 25 1971