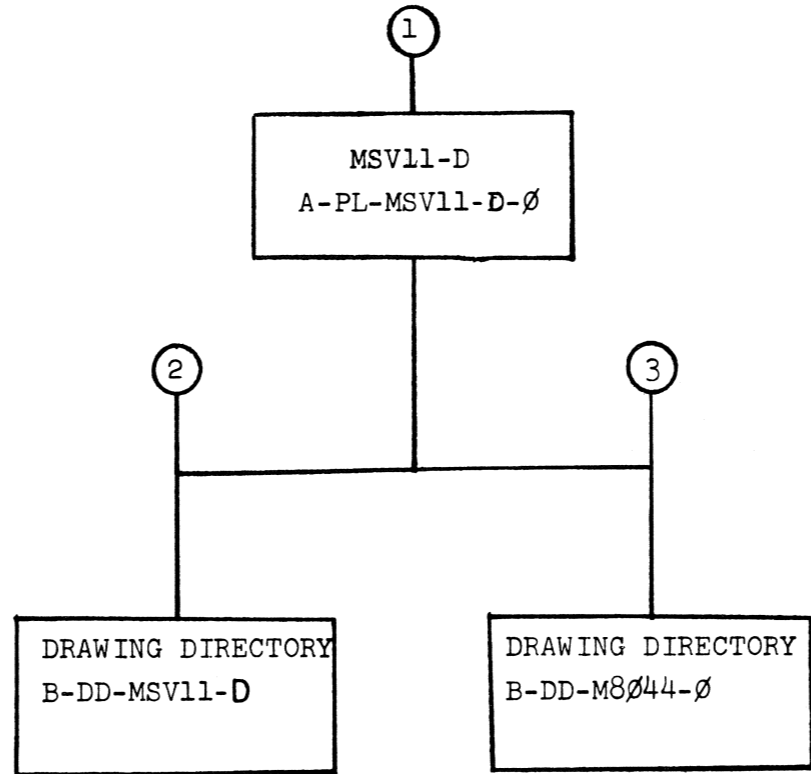


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TITLE DRAWING DIRECTORY MSVII-D (32K X 16) MOS MEMORY SYSTEM	SHEET 2 OF 3	SIZE CODE B DD	NUMBER MSVII-D	REV A
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ML

FIND NO.	DRAWING NO.	DESCRIPTION	TYPE	FIND NO.	DRAWING NO.	DESCRIPTION	TYPE
	MP00566	FIELD MAINTENANCE PRINT SET (MP)	-				
	B-TC-MSV11-D-1	FIELD MAINTENANCE PRINT SET (TC)	-				
1	A-PL-MSV11-D-0	PARTS LIST (MSV11-D)	E/M				
	D-BD-MSV11-D-4	MSV11D/E BLOCK DIAGRAM	E				
	D-FD-MSV11-D-5	MSV11 D/E FLOW DIAGRAM	E				
	D-TD-MSV11-D-6	MSV11 D/E TIMING DIAGRAM	E				
	A-SP-MSV11-D-3	MSV11-D SYSTEM SPECIFICATION	E/M				
	A-PL-MSV11-D-7	MSV11-D SHIPPING LIST	-				
	EK-MSV1D-0P-001	USERS MANUAL	E/M				
	D-06-MSV11-D-10	MSV11-D 32K MOS MEMORY	E				
2	B-DD-MSV11-D	DRAWING DIRECTORY MSV11-D MOS MEMORY SYSTEM	E/M				
3	B-DD-M8044-0	DRAWING DIRECTORY (M8044) MODULE	E/M				

TYPE: E ELECTRICAL
M MECHANICAL
E/M ELECTRO/MECHANICAL

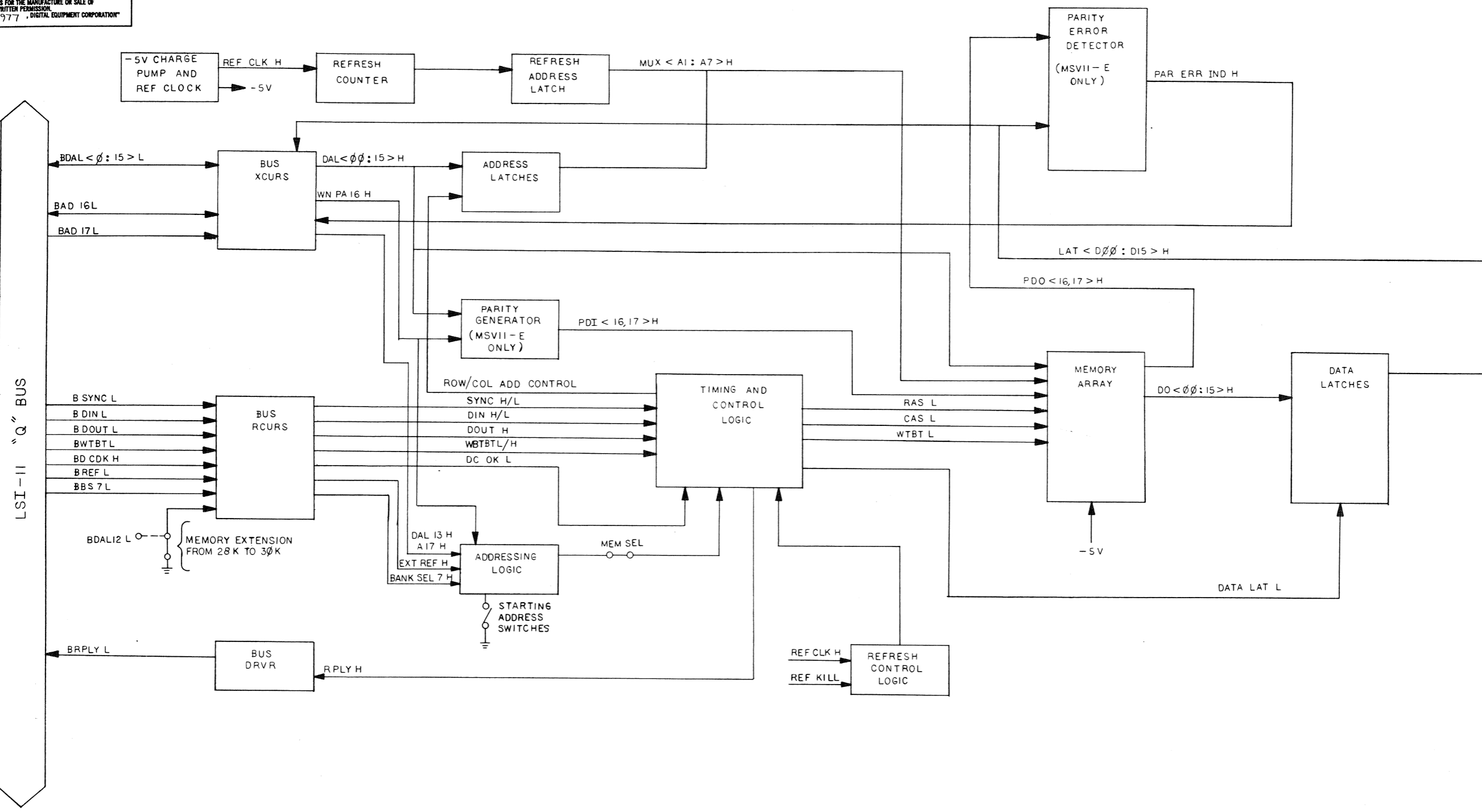


TITLE DRAWING DIRECTORY MOS MEM. MSV11-D (32K X 16) SYSTEM
SHEET 3 OF 3 SIZE B CODE DD NUMBER MSV11-D REV A

DRB 108A

ML

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REV.	
CHANGE NO.	
CHK	

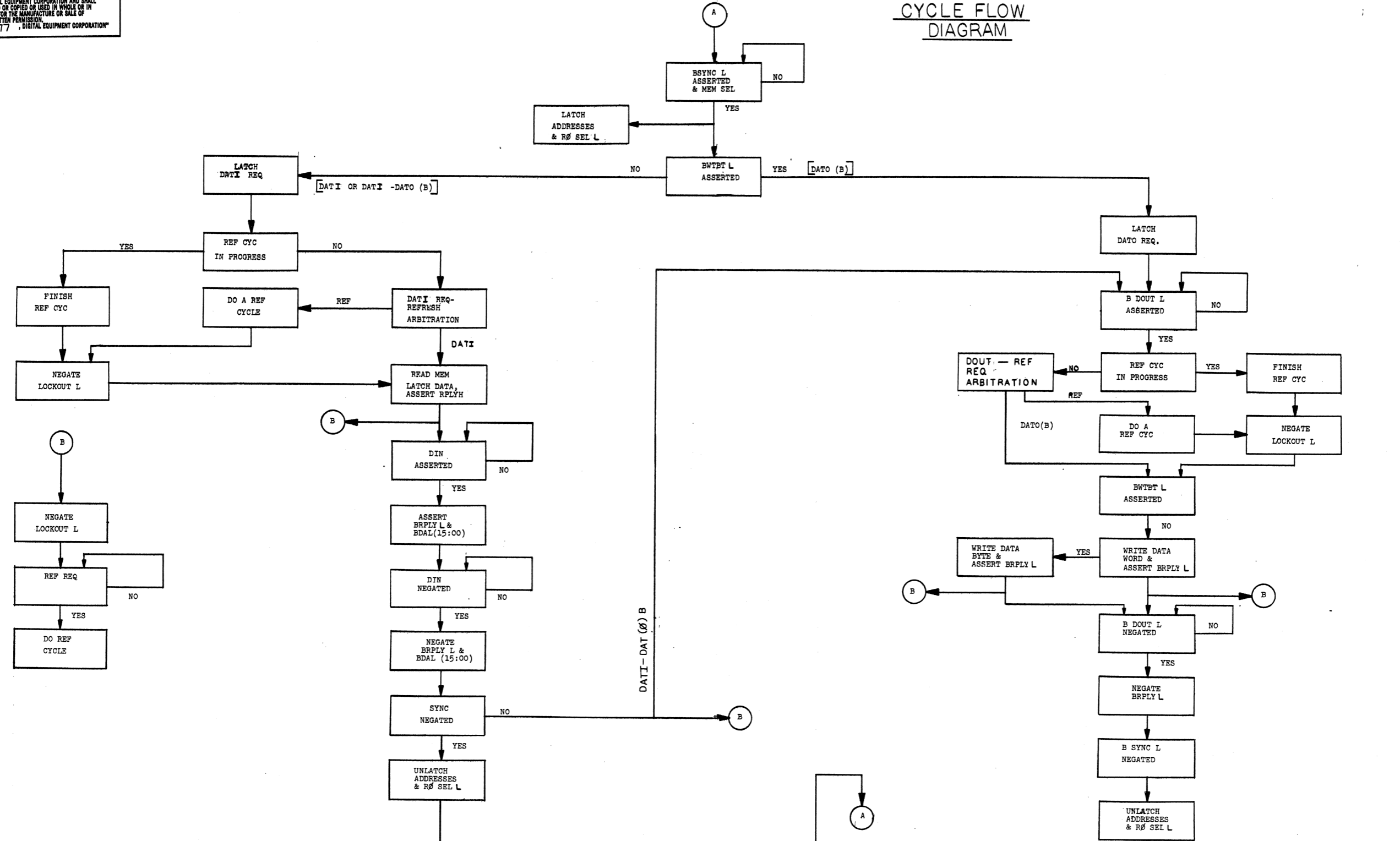
DRN. E. deVries	FIRST USED ON	digital
CHK'D R. GIVEN	2-21-78	
ENG. R. GIVEN	2-21-78	
PROJ. ENG. R. Jim	2-21-78	
PROD. R. M. J.	2-21-78	
NEXT HIGHER ASSY.		
B-DD-MSVII-D/E	SIZE CODE	NUMBER
SCALE	D BD	MSVII-D-4
SHEET	1 OF 1	DIST.

CYCLE FLOW DIAGRAM

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8 7 6 5 4 3 2 1

MSVII-D-5



REVISIONS	REV.
CHANGE NO.	
CHK	

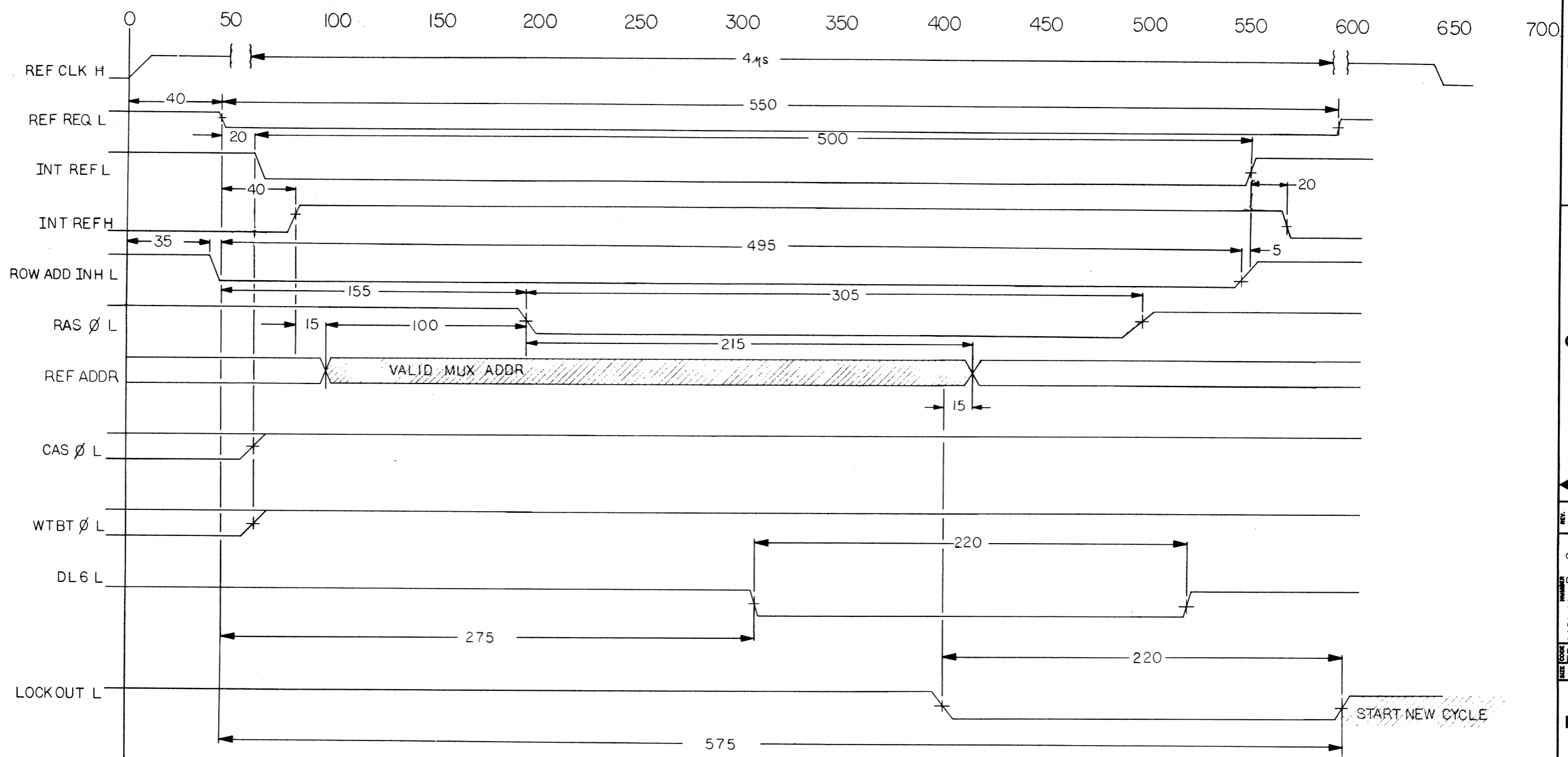
DRN. <i>E. Davis</i>	4-12-77	FIRST USED ON	
CHK'D <i>R. Given</i>	2-21-78		
ENG. <i>R. Given</i>	2-21-78	TITLE	MSVII D/E
PROJ. ENG. <i>P. H. Smith</i>	2-21-78		FLOW DIAGRAM
PROD. <i>R. Math</i>	2-21-78		
NEXT HIGHER ASSY.		SIZE	CODE
B-DD-MSVII-D/E		D	FD
SCALE		NUMBER	5
SHEET	1 OF 1	DIST.	

MSVII-D-5



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REFRESH CYCLE



t_{cycle} = 575_{NS} (TYP); 600_{NS} (MAX)

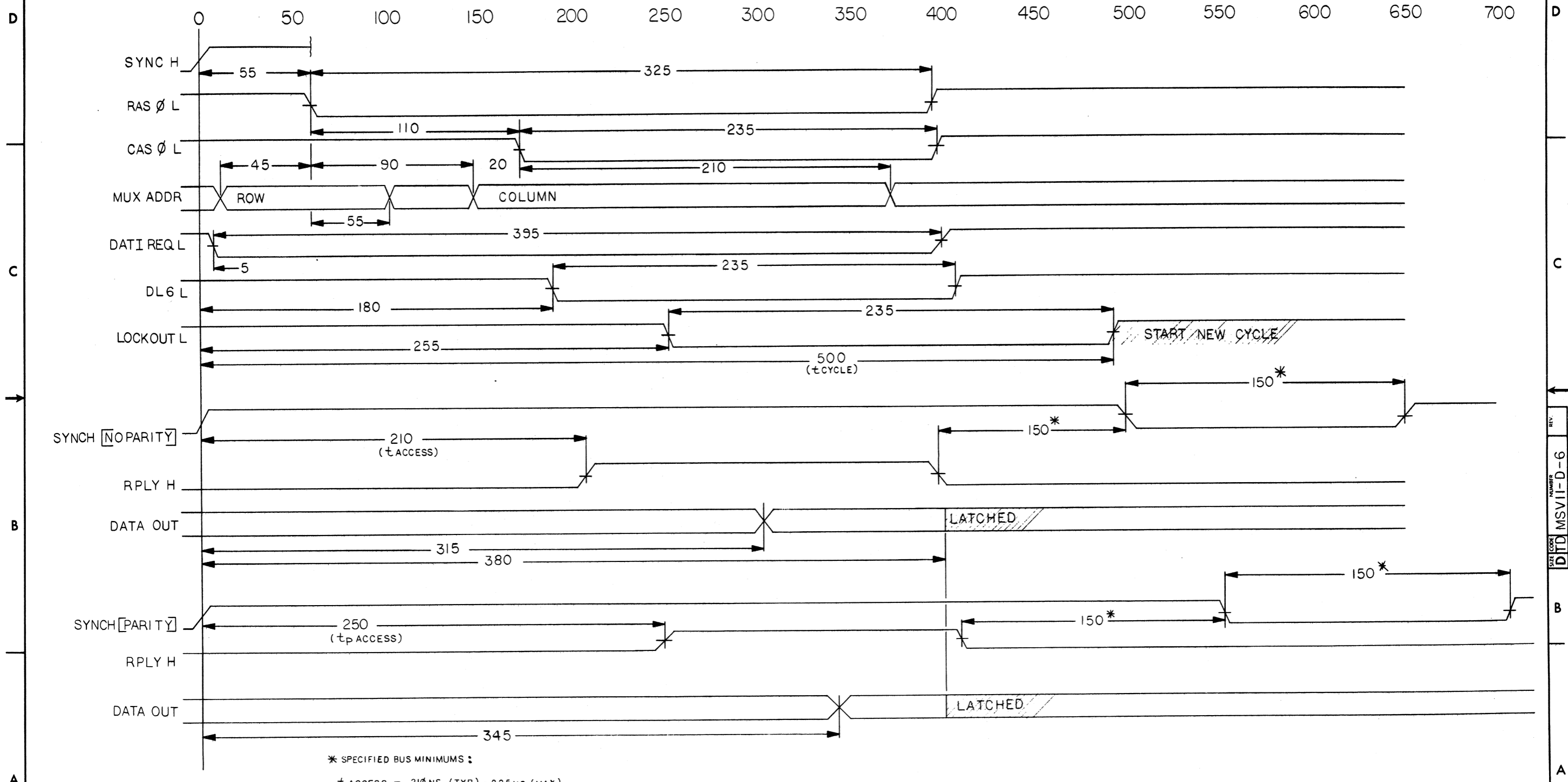
DRN. E. DeVries	17 NOV 77	FIRST USED ON	digital
CHK'D R. GIVEN	2-21-78	TITLE	MSVII D/E TIMING
ENG. R. GIVEN	2-21-78	DIAGRAM	
PROJ. ENG. R. ALLEN	2-21-78	SIZE	D
PROD. R. MATH	2-21-78	CODE	TD
NEXT HIGHER ASSY.		NUMBER	MSVII-D-6
B-DD-MSVII-D/E		REV.	
SCALE		SHEET	1 OF 4
		DIST.	

MAINTENANCE P-1185	REV.
REVISIONS	
CHK	
CHANGE NO.	

REV. 1
D TD MSVII-D-6

DATI CYCLE

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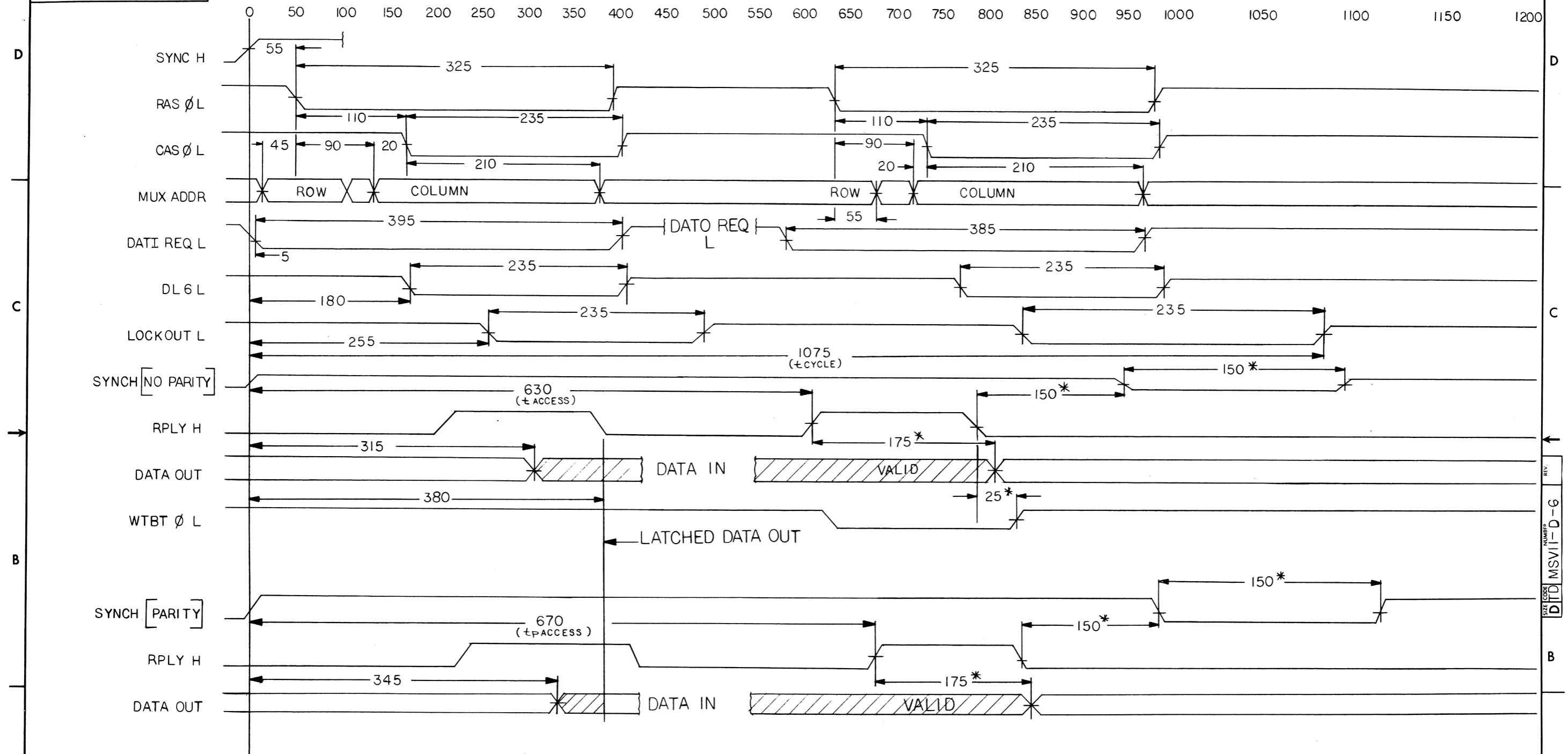
* SPECIFIED BUS MINIMUMS :
 $t_{ACCESS} = 210\text{ NS (TYP); } 225\text{ NS (MAX)}$
 $t_{PACCESS} = 250\text{ NS (TYP); } 265\text{ NS (MAX)}$
 $t_{CYCLE} = 500\text{ NS (TYP); } 520\text{ NS (MAX)}$

REVISIONS		
CHK	CHANGE NO.	REV.

REV. NUMBER DTD MSVII-D-6

DATIO (B) CYCLE

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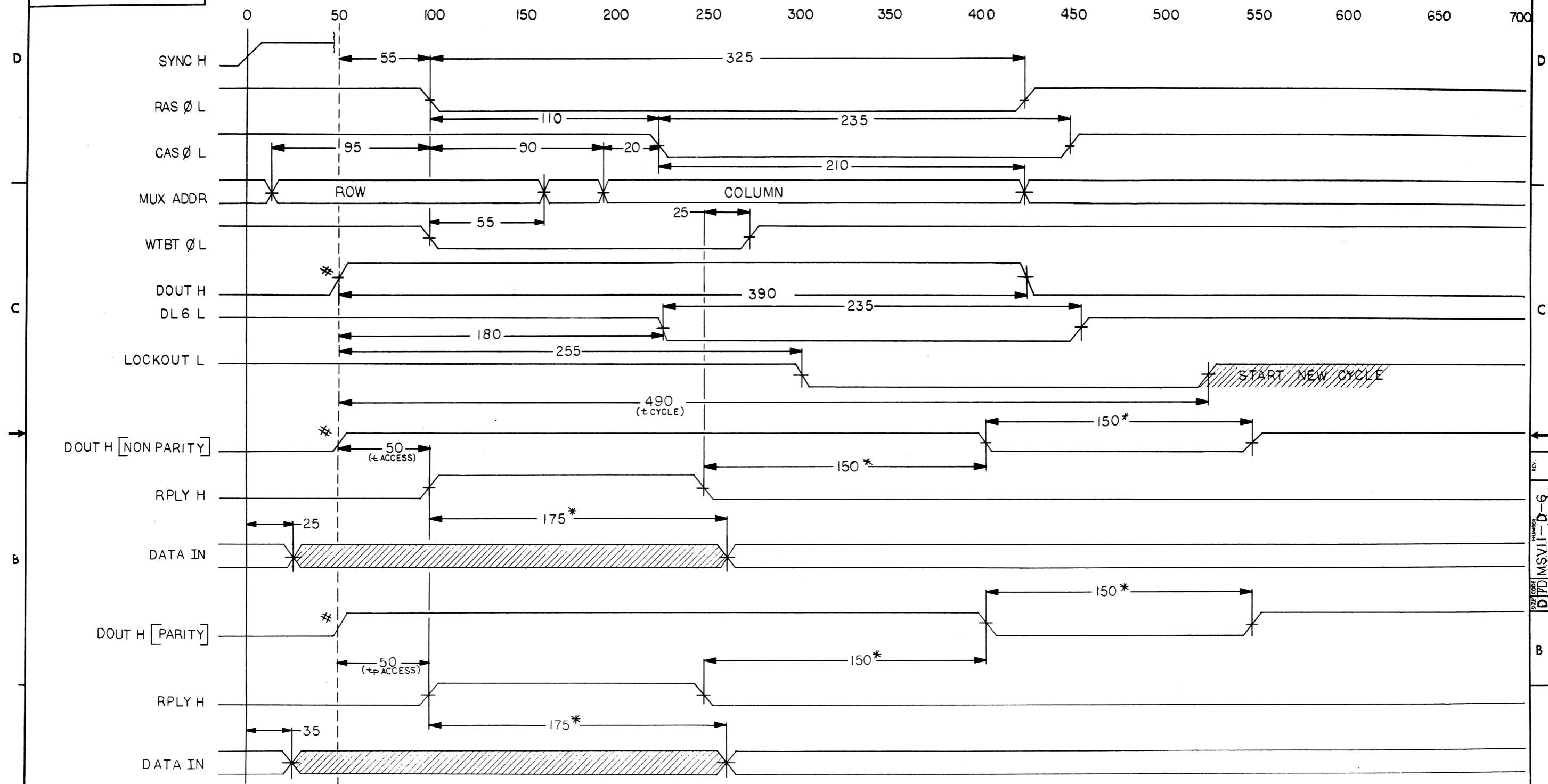


* SPECIFIED BUS MINIMUMS
 $t_{ACCESS} = 630 \text{ ns (TYP); } 650 \text{ ns (MAX)}$
 $t_{pACCESS} = 670 \text{ ns (TYP); } 690 \text{ ns (MAX)}$
 $t_{CYCLE} = 1075 \text{ ns (TYP); } 1100 \text{ ns (MAX)}$
 $t_{pCYCLE} = 1115 \text{ ns (TYP); } 1140 \text{ ns (MAX)}$

REVISIONS		
CHK	CHANGE NO.	REV.

DATA (B) CYCLE

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* SPECIFIED BUS MINIMUMS
 # TIME DEPENDENT ON PROCESSOR
 t_{ACCESS} = 100 NS (TYP); 110 NS (MAX)
 t_{PACCESS} = 100 NS (TYP); 110 NS (MAX)
 t_{CYCLE} = 545 NS (TYP); 565 NS (MAX)
 } SYNC H TO DOUT H SPECIFIED AT A MINIMUM OF 50 NS

REVISIONS		
CHK	CHANGE NO.	REV.