



87

1-0-1682W

2

1

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NOTES:

1. STARTING ADDRESS  
DEC (K) OCTAL

SWITCH SETTING  
(X = OFF)

2. STARTING ADDRESS  
DEC (K) OCTAL

SWITCH SETTING  
(X = OFF)

3. EUB CSR ADDRESS  
OCTAL

MOD. UNIBUS CSR ADDRESS  
OCTAL

SWITCH SETTING  
(X = OFF)

4. OPTION DESIGNATION

PRIMARY VARIATION

SYSTEM DESCRIPTION

5. BUS VOLTAGE

JUMPER

6. BUS VOLTAGE

JUMPER

7. PERIPHERAL PAGE SIZE

JUMPER

8. FUNCTION

JUMPER

9. FUNCTION

JUMPER

10. TYPE

JUMPER

11. FUNCTION

JUMPER

12. IC TYPE

+5V

GND

+12V

-5V

13. SIGNAL NAMES WILL BE REFERENCED AS FOLLOWS:  
[X] SIGNAL NAME [Y]Z

NUMBER OF CONNECTION POINTS ON DESTINATION SHEET.

DESTINATION SHEET NUMBER.

SOURCE SHEET NUMBER.

14. \* INDICATES BATTERY BACK UP (+5V CR)  
● INDICATES .125 PAD ON SIDES 1 AND 2.  
⊙ INDICATES WIRE WRAP PINS AND TEST POINTS.

15. RESISTORS ARE 5% UNLESS OTHERWISE SPECIFIED.

16. SOME .47 UFD AND .01 UFD ARE NOT USED ON THE AA, BA, CA PRIMARY VARIATIONS.

17. FOR -5V TEST OPERATION W22 IS IN AND W23 IS OUT, FOR -15V OR -12V OPERATION W22 IS OUT AND W23 IS IN.

18. W24 AND W25 ARE FOR FUTURE USE.

REVISIONS

CHANGE NO.

REV.

DATE

BY

CHK'D

DATE

BY

PROJ. ENG.

DATE

BY

PROD. ENGR.

DATE

BY

TEST ENG.

DATE

BY

QA

DATE

BY

128K X 18 BIT MOS MEM

11/34

1

2

3

4

5

6

7

8

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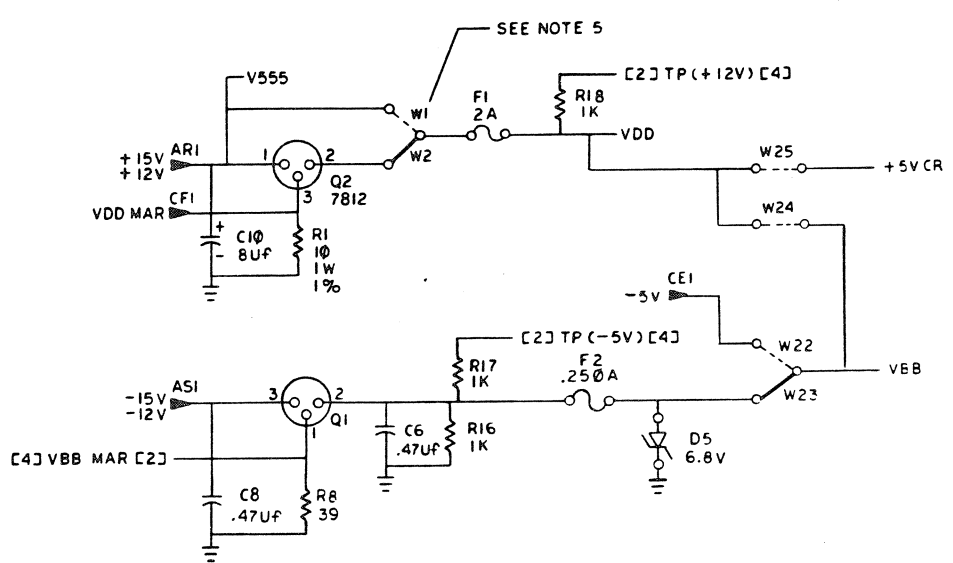
1-0-1682W50d 2

D

C

B

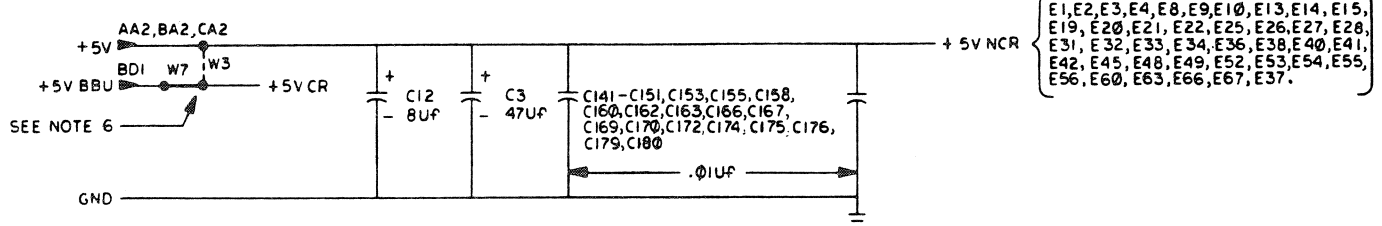
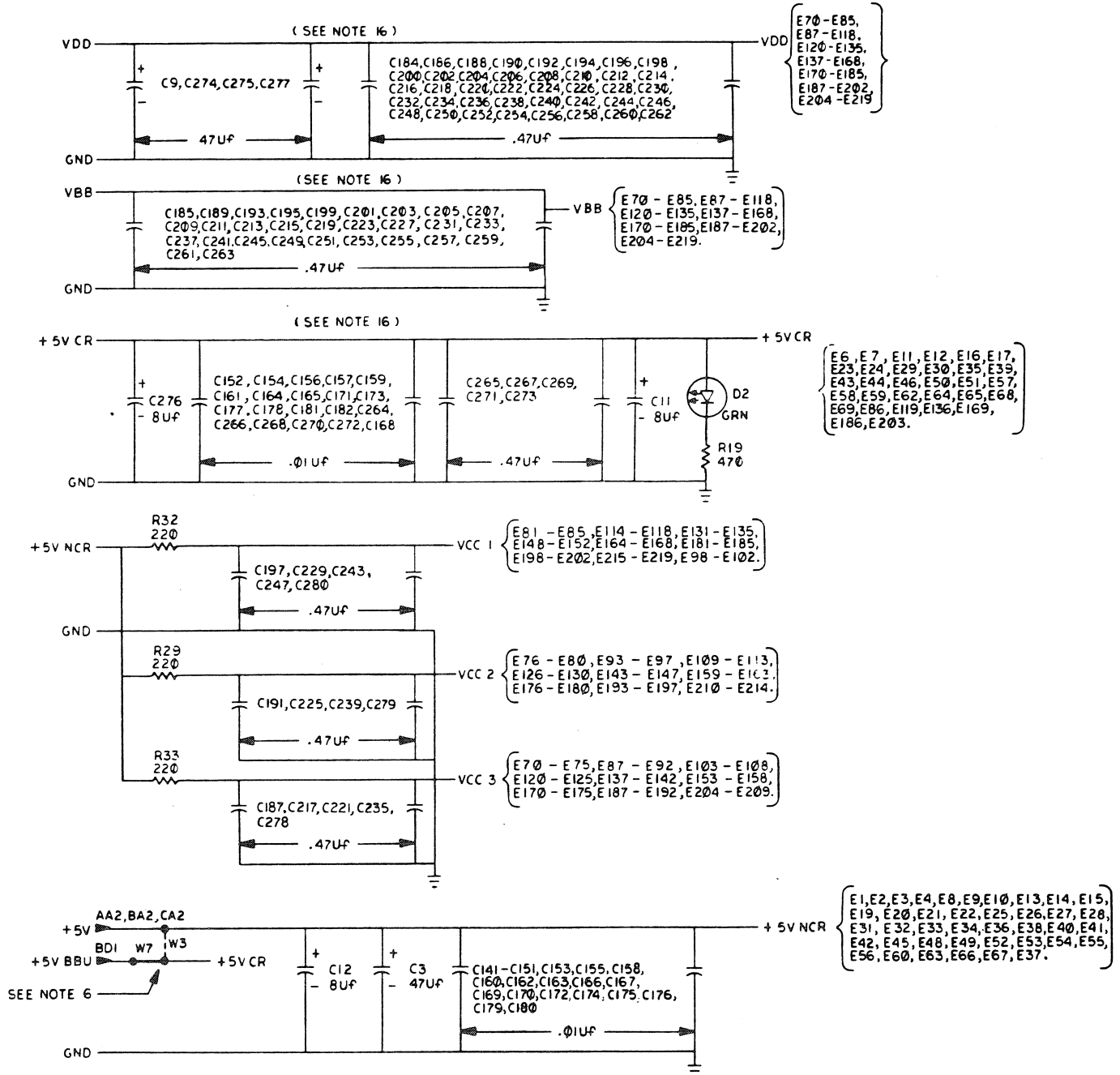
A



AC2, AT1, BC2, BT1,  
CC2, CT1, DC2, DT1,  
EC2, ET1, FC2, FT1

BUS GRANT LINES

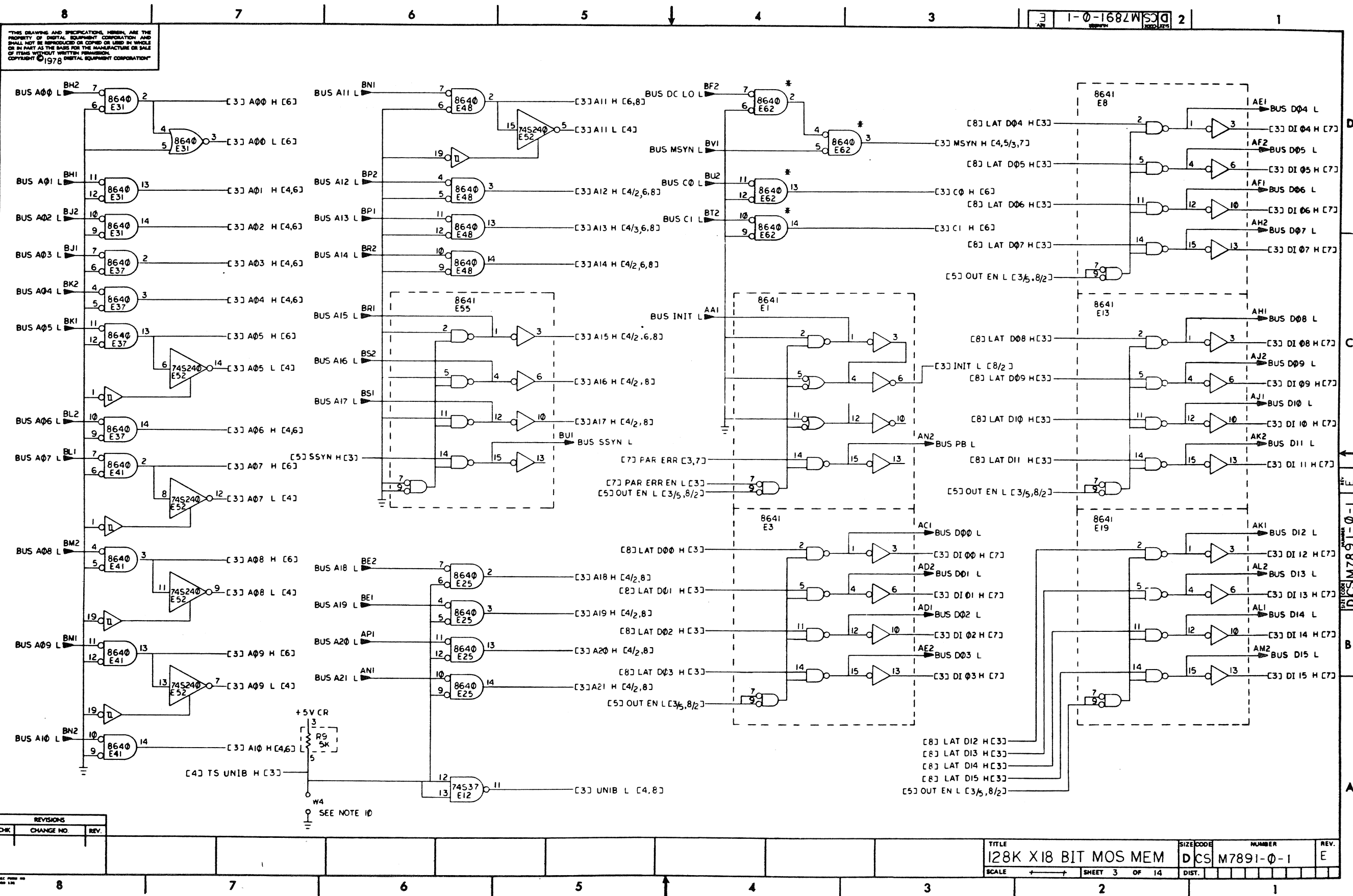
- DK2 DL2
- DP2 DR2
- DM2 DN2
- DS2 DT2
- CA1 CB1

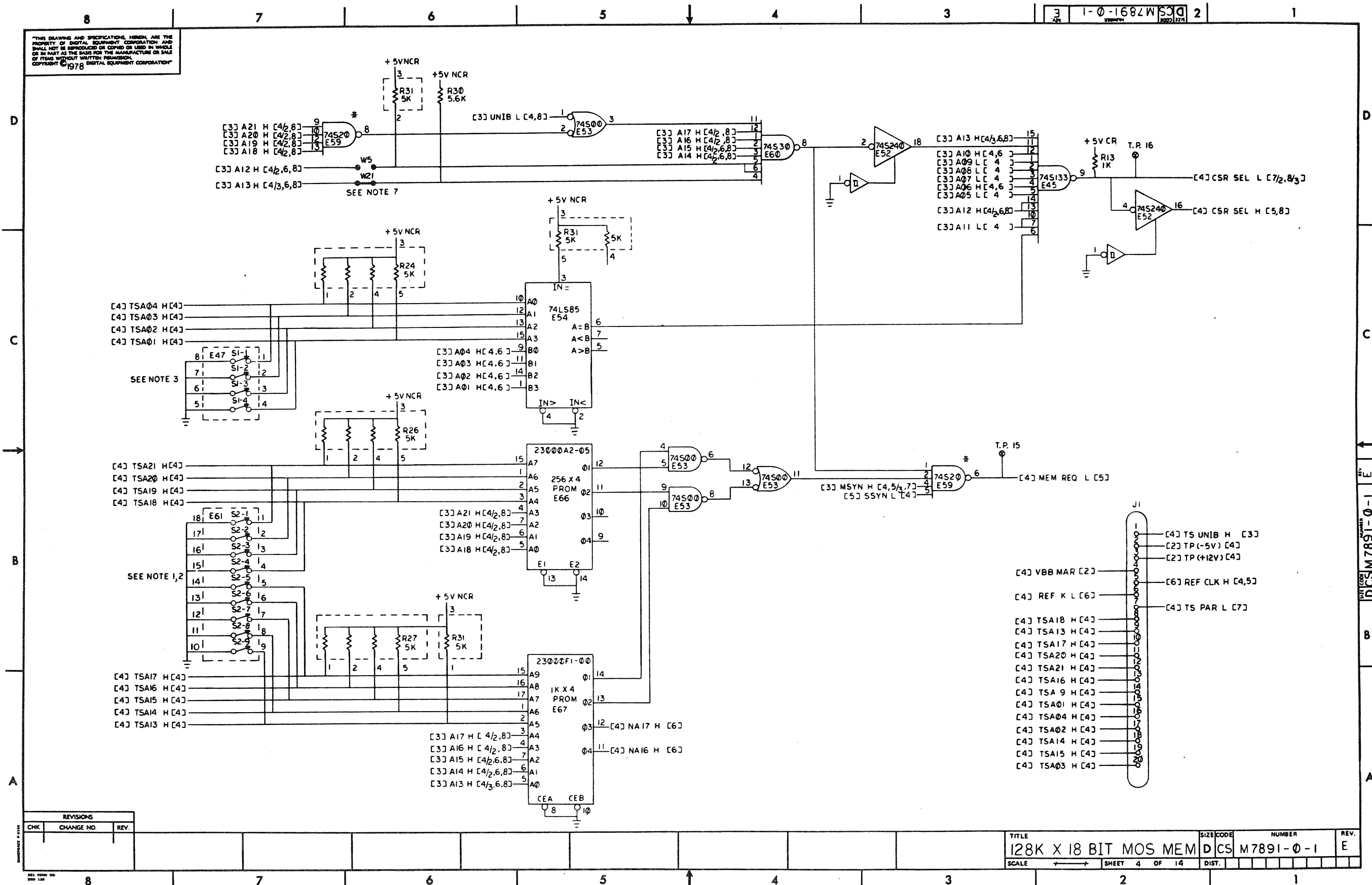


SIZE	CODE	NUMBER	REV.
D	CS	M7891-0-1	E

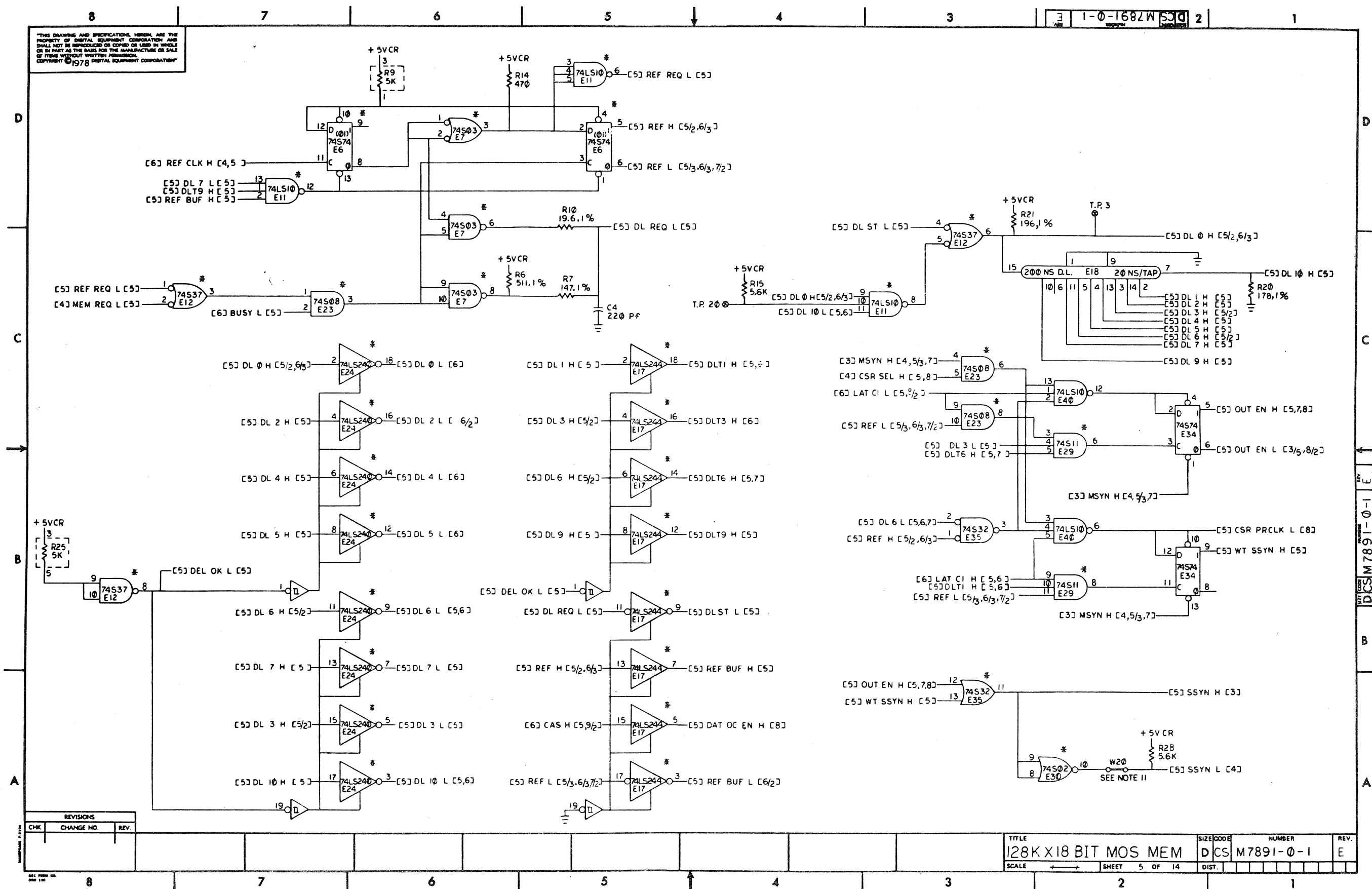
128K X 18 BIT MOS MEM

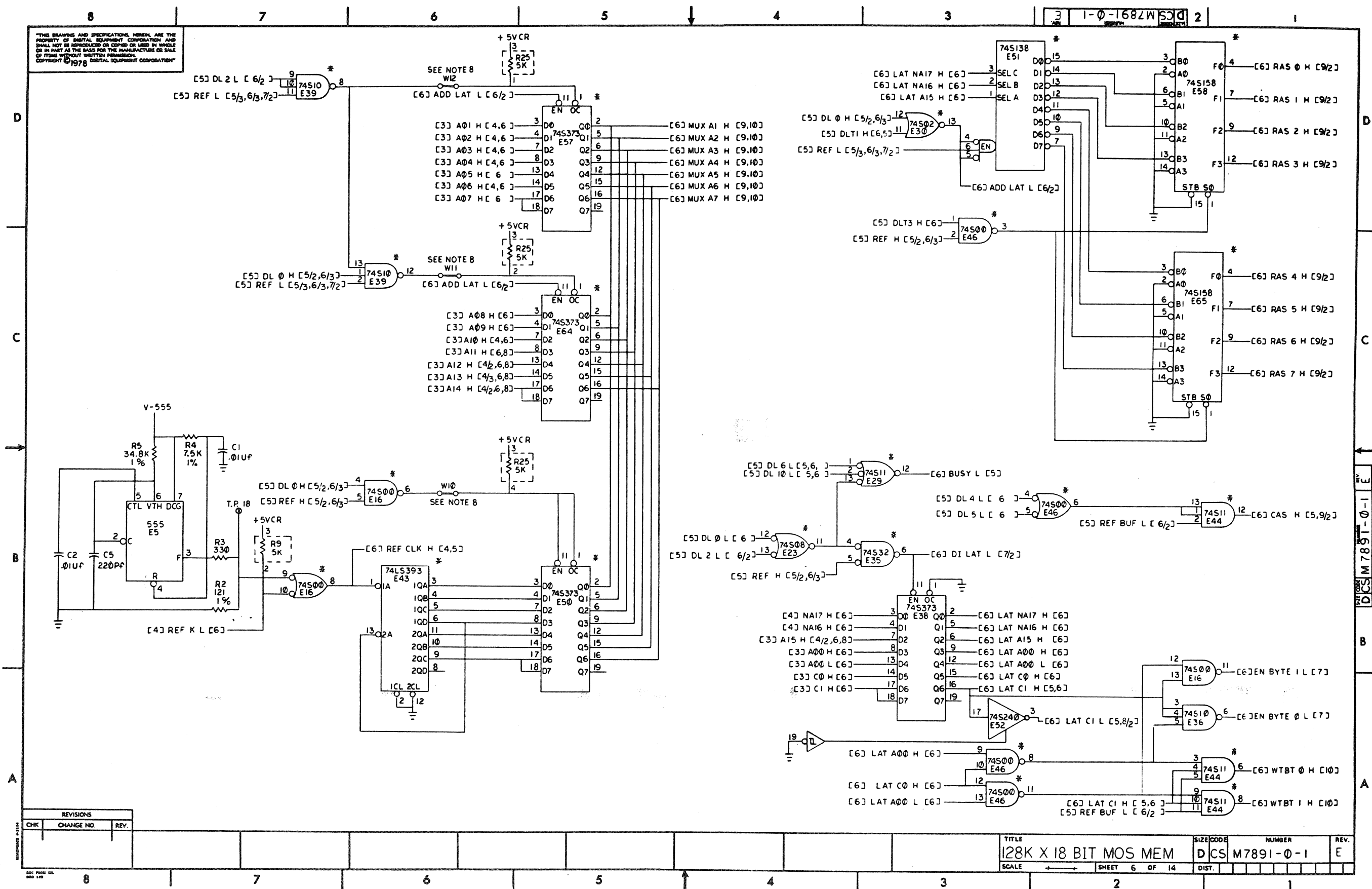
SCALE SHEET 2 OF 14 DIST











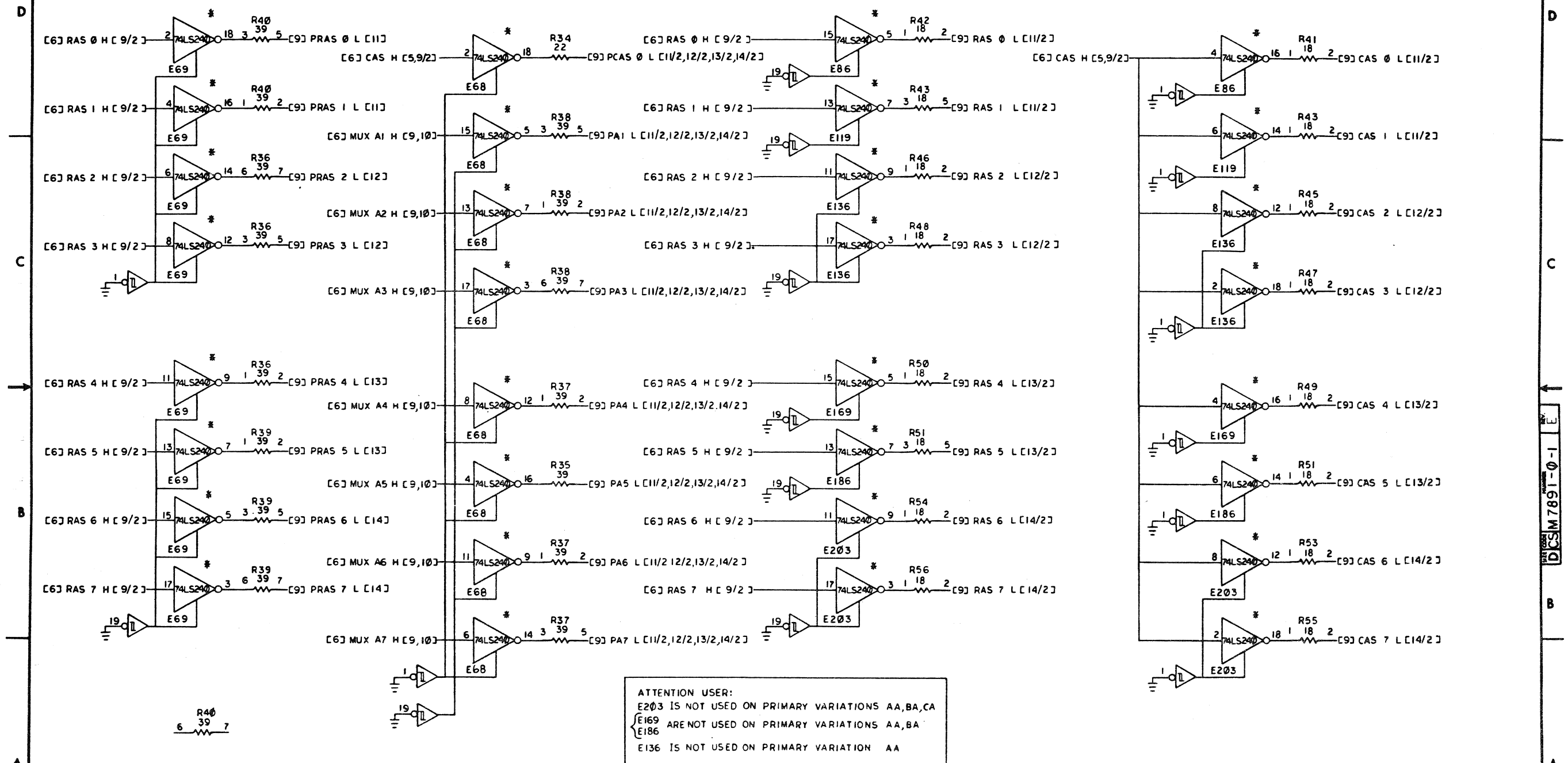






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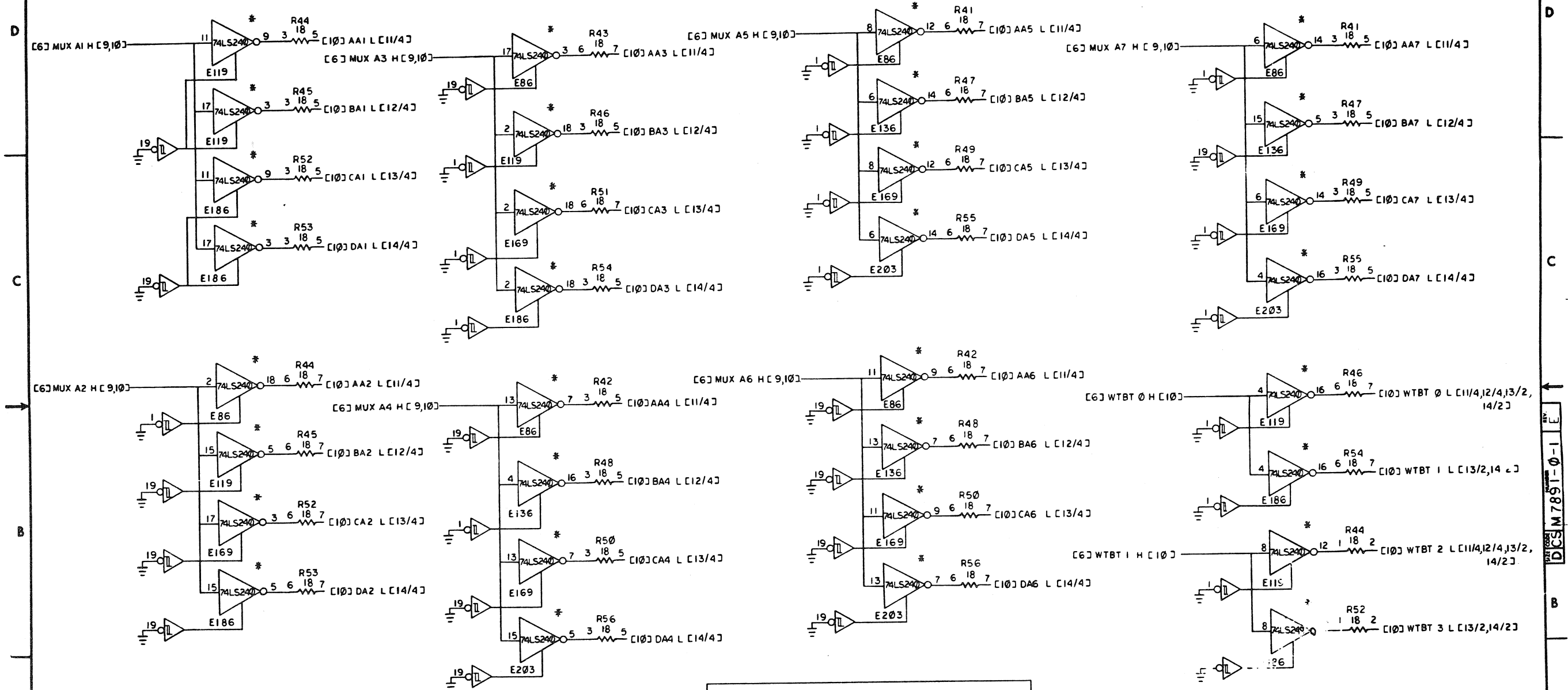
NOTE:  
R36 THRU R56 PIN 4 GOES TO +5V



REVISIONS		
CHK	CHANGE NO.	REV.

TITLE	128K X 18 BIT MOS MEM	SIZE CODE	D CS M7891-0-1	NUMBER		REV.	E
SCALE		SHEET	9 OF 14	DIST.			

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ATTENTION USER:  
E203 IS NOT USED ON PRIMARY VARIATIONS AA,BA,CA  
{E169 ARE NOT USED ON PRIMARY VARIATIONS AA,BA  
E186  
E136 IS NOT USED ON PRIMARY VARIATION AA

REVISIONS		
CHK	CHANGE NO.	REV.

TITLE		SIZE	CODE	NUMBER	REV.
128K X 18 BIT MOS MEM		D	CS	M7891-0-1	E
SCALE	SHEET 10 OF 14	DIST.			

