

THIS IS PRINT SET

SEQUENCE

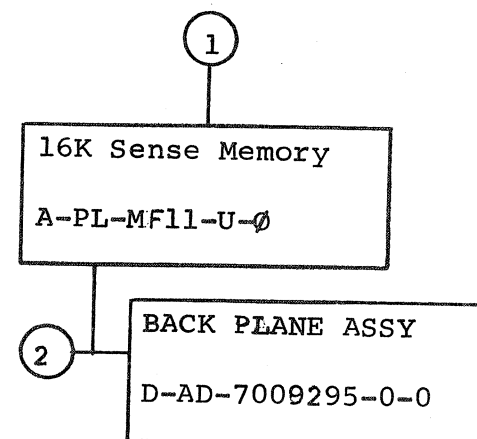
MFG. PRINT SET

B-DD-MF11-U
D-MU-MF11-U-MU
D-TD-MF11-U-1
D-CS-M8293-0-1
D-CS-G235-0-1
D-CS-G114-0-1
D-CS-H217-0-1
D-CS-M7259-0-1
D-CS-5410345-0-1
E-IA-7009535-0-0
A-PL-MF11-U-0
A-SP-MF11-U-3

ON-LINE TEST PROCEDURE A-SP-MF11-U-2
MF11-U ENGINEERING SPECIFICATION A-SP-MF11-U-4

NOTE: TO INSTALL MF11-U OR MF11-UP IN 11/40 CPU WITH SERIAL NUMBER LESS THAN 6000 OR H960-D (OR-E) WITH SERIAL LESS THAN 7000 A FIELD MODIFICATION KIT (FM11-UA, -UB, OR -UC) IS REQUIRED.

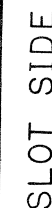
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TITLE	SHEET	SIZE	CODE	NUMBER	REV
16K SENSE MEMORY	2 OF 3	B	DD	MF11-U	A

[illegible]

- * 1. FOR MFI1-UP (MEMORY WITH PARITY), USE H217C MEMORY STACK.
- ** 2. M7259 IS USED WITH MFI1-UP ONLY
- 3. INSTALL "A" MODULES FOR FIRST 16K, "B" MODULES FOR 2ND 16K.



B	SIZE CODE D M	NUMBER MFI-U-MU	REV. A
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BRUNING 40-522 16699

QTY		REF DESIGNATION		DESCRIPTION		PART NO.		ITEM NO.	
FIRST USED ON OPTION MODEL				PARTS LIST					
M F11-U				ETCH BOARD REV					
				DRN. DATE CHK'D. DATE ENG. DATE PROJ. ENG. DATE PROD. DATE NEXT HIGHER ASSY B-DD-MF11-U SCALE SHEET 1 OF 1					
				digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS TITLE MODULE UTILIZATION SIZE CODE D MU NUMBER MF11-U-MU REV. A					
DEC NO.				EIA NO.		DEC NO.		EIA NO.	
SEMICONDUCTOR CONVERSION CHART									

digital EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

MODULE UTILIZATION

SIZE	CODE	NUMBER	REV
D	MU	MFII-U-MU	A



1. * IN THE DATOB MODE CLEAR M_{IRL} ONLY OCCURS IN THE BYTE NOT BEING ADDRESSED.
2. **IN THE DATOB MODE CLOCK M_{IRL} ONLY OCCURS IN THE BYTE BEING ADDRESSED.
3. ***ACTUAL TIME DEPENDS ON BUS AND PROCESSOR DELAYS.
- 4.***IN PARITY SYSTEMS BUS SSYNL WILL BE 20 NS LATER THAN SHOWN FOR DATO- DATOB
 BUSS MODES AND 150NS LATER FOR DATI-DATIP MODES.

A

—S
—A