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[illegible]

ENGINEERING SPECIFICATION		CONTINUATION SHEET			
TITLE AD01-D Acceptance Procedure					
1.0	<p>Purpose: The purpose of this acceptance test is to show that the AD01-D works properly within stated specifications. The test should be run in sequence from beginning to end to prove the operation and specification.</p>				
1.1	<p>Equipment Required - Voltage Standard (EDC or Equivalent) Wave or Pulse Generator (Wavetek, Datapulse, or Equivalent)</p>				
2.0	<p>Visual Inspection (Power Off)</p> <p>All modules should be in the proper slots as referenced to the module utilization drawing. The back plane wiring should be examined for any gross wiring discrepancies. The unibus cable (BC-11) should be in slot AB01 with the terminator card (M930) in slot AB02 if the AD01 is the last device on the bus.</p>				
2.1	<p>Analog Modules (A Series)</p> <p>All adjustment pots on A series modules should be sealed with the exception of those which are not used in certain option combinations. This information is available in the calibration procedure.</p>				
2.2	<p>Multiplexer</p> <p>The AD01 has provision for 32 channels of multiplexing. The MUX module is the A124 and contains four (4) channels. A check should be made to determine the number of channels present is the correct number. The proper location for the</p>				
		SIZE A	CODE SP	NUMBER AD01-D-12	REV

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE AD01-D Acceptance Procedure			
A124 Modules are:			
Slot A17 CH00 - CH03		Slot B17 CH16 - CH19	
A18 CH04 - CH07		B18 CH20 - CH23	
A19 CH08 - CH11		B19 CH24 - CH27	
A20 CH12 - CH15		B20 CH28 - CH31	
2.0 Insure Flip-Chip module handle extenders and hold down bar in place.			
3.0 Logic Testing			
The logic testing is accomplished by loading the AD01D diagnostic Maindec-11-D6AB with the binary loader. The G735 test card should be inserted into slots AB21. Set the voltage standard (EDC) for 0.625V.			
3.1 Running the diagnostic			
3.1.1 Normal (Worst Case) Testing			
The starting address is 200 and the program halts 3 times at the start.			
1st Halt - Load initial channel number (SW0-SW5)			
2nd Halt - Load number of channels to be tested (SW0 - SW5)			
3rd Halt - Set SR option (if AH05 is ordered set SR 8-1 at this halt.			
The continue switch should be hit after each halt. The program should now run testing all logic, control functions, gain, linearity and analog channels. If the AH05 option is installed the program will ask for the proper input voltage to test the sign. The gain linearity test may fail if external noise is excessive. However, the test should be			
SIZE A		CODE SP	NUMBER AD01-D-12
REV			

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE AD01-D Acceptance Procedure			
repeated as continued failures indicate non-linearity.			
4.0 Channel Noise			
This test is used primarily to locate excessively noisy channels. It should be noted here that this test is dependent on many factors, external noise generated at the voltage source, variations in power line voltages and radiated power line noises. Typeouts here do not necessarily indicate failures. When running the was-is test the main thing to watch for is excessively noisy channels. This is indicated by repeated typeouts on the same channel with 3-4 count spreads.			
4.1 Running the Was-Is Test			
1. Set EDC to "0" on polarity switch.			
2. Load and start at SA270; the program will halt 3 times:			
1st Halt - Load initial channel in octal (Bits 05-00) and Gain (Bits 07-06)			
2nd Halt - Load number of channels (Bits 05-00)			
3rd Halt - Set SR option (refer to diagnostic for option settings). For acceptance purposes load SRO at this halt.			
Hit continue after each halt. The test should be run for each gain using the discussion in the previous Section (4.0) as a guide. At the end of each pass (4096 conversions per channel) the teletype bell will ring. Test should run for a minimum of 5 minutes without errors.			
4.2 Channel Continuity			
With the Was-Is test running at a gain of 1 and the EDC set at 9.9900V. Set SW6=1. A table will print out			
SIZE A		CODE SP	NUMBER AD01-D-12
REV			

ENGINEERING SPECIFICATION		CONTINUATION SHEET																															
TITLE AD01-D Acceptance Procedure																																	
<p>giving the channel number, initial value and new value. of the converted data. With the G735 test card in use each channel will be half of the previous channel value until at the tenth(8) channel,the full voltage will then repeat. For a full scale input as set above the printout for 10(8) channel will look like the following:</p> <table><tr><td>Channel</td><td>Initial Value</td><td>Final Value</td></tr><tr><td>CH00</td><td>1777</td><td>1777</td></tr><tr><td>CH01</td><td>1000</td><td>1000</td></tr><tr><td>CH02</td><td>0400</td><td>0400</td></tr><tr><td>CH03</td><td>0200</td><td>0200</td></tr><tr><td>CH04</td><td>0100</td><td>0100</td></tr><tr><td>CH05</td><td>0040</td><td>0040</td></tr><tr><td>CH06</td><td>0020</td><td>0020</td></tr><tr><td>CH07</td><td>0000</td><td>0000</td></tr><tr><td>CH10</td><td>1777</td><td>1777</td></tr></table> <p>If the G735 test card is not available each input should be given a known voltage and the results checked in the display conversion loop SA220.</p> <p>5.0 <u>Repeatability Test (3 Sigma)</u></p> <p>This routine tests repeatability to specifications on the channels desired at gains of 4 and 8. Ten thousand conversions are taken at each gain. The specifications have been interpreted to mean that 35 conversions out of the ten thousand may be outside of 2 states at a gain of 4, and that 35 conversions out of ten thousand may be outside of 3 states at a gain of 8.</p>				Channel	Initial Value	Final Value	CH00	1777	1777	CH01	1000	1000	CH02	0400	0400	CH03	0200	0200	CH04	0100	0100	CH05	0040	0040	CH06	0020	0020	CH07	0000	0000	CH10	1777	1777
Channel	Initial Value	Final Value																															
CH00	1777	1777																															
CH01	1000	1000																															
CH02	0400	0400																															
CH03	0200	0200																															
CH04	0100	0100																															
CH05	0040	0040																															
CH06	0020	0020																															
CH07	0000	0000																															
CH10	1777	1777																															
		SIZE A	CODE SP	NUMBER AD01-D-12	REV																												

ENGINEERING SPECIFICATION		CONTINUATION SHEET			
TITLE AD01-D Acceptance Procedure					
<p>5.1 Running the 3 Sigma Test</p> <p>The Pass- Fail aspect of this test is based on the specification for repeatability of the converter, and requires that the input to each channel be grounded. This can be accomplished by setting the EDC to ZERO (0) at the polarity switch or by connecting the inputs to analog ground. The SA is 300. At the first halt set the initial channel number to be tested in SW05-SW00. At the second halt set the number of consecutive channels to be tested in SW05-SW00. The test will now run, ringing the bell after each successful pass. Again it should be noted here that this test is dependent on outside factors especially radiated power line noise and variations in power line voltages. This test should be run for a minimum of 5 min, without errors.</p> <p>6.0 <u>Accuracy</u></p> <p>The accuracy specification for the AD01-D is 0.1% of F.S. This specification can be proved by checking switching points and comparing the values obtained against the proper calibration chart. (Unipolar chart for basic unit and Bipolar if AH05 is installed). Each switching point should be within the specified tolerance on each gain.</p>					
		SIZE A	CODE SP	NUMBER AD01-D-12	REV

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE AD01-D Acceptance Procedure			
6.1	Running the Accuracy Test		
	This is accomplished by using the display loop section of the diagnostic program. The SA is 220 and the program will immediately run upon starting. Channel and gain can be selected while the program is running. The following are the switch register settings:		
	Channel (Octal) - SW5-SW0		
	Gain (Octal) - SW7-SW6		
	The switching points should be checked at CH00 since the G735 acts as a voltage divider. All the switching points need not be checked for acceptance as production checkout has already been through the calibration sheet. However, six to eight readings on each gain should suffice. It should be noted here that the accuracy of the voltage standard should be considered in making the measurements. For example, if an EDC is used with an accuracy of $\pm 0.01\%$ of F.S. 10V, the EDC reading is good only to ± 1 millivolt.		
	The accuracy test is the final part of the procedure. The G735 card should be removed from slot AB21 and the M908 cards replaced in slot AB22. The hold down bar should now be installed.		
7.0	External Sync Test		
	The wave generator should be connected to the EXT. IN in slot A21. The jumper from Pin A22R2 to A22C2 should be		
	SIZE A	CODE SP	NUMBER AD01-D-12
			REV

ENGINEERING SPECIFICATION		CONTINUATION SHEET	
TITLE AD01-D Acceptance Procedure			
	removed.		
7.1	External Conversion Test		
	This routine counts external conversions and prints out after every tenth conversion. The SA is 230 and the frequency of the input trigger signal should be set at about 1 cycle (1HZ).		
	Switch Register Settings		
	SW5-1. Causes the program not to set external enable thus no printout should occur.		
	SW6-1. Causes it to run internal conversions during external conversions. The proper error message will occur.		
	SW7-1. Prevents it from clearing done after each conversion thus an error printout will occur. It should also be noted that if the external trigger frequency is too fast error printouts will occur.		
8.0	Documentation and Software		
	The following items should be complete and included:		
	1. AD01-D Print Set		
	2. Maintenance Manual or Engineering Specifications.		
	3. Key sheets, checkout log, Q.C. status, accessories as stated on accessory check list.		
	4. AD01-D Maindec-11-D6AB Diagnostic Program and Write-up		
	5. AD01-D Calibration Procedure		
	6. AD01-D Calibration Chart		
	7. ECO Status Sheet		
	SIZE A	CODE SP	NUMBER AD01-D-12
			REV

MASTER DRAWING LIST

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DRA 131

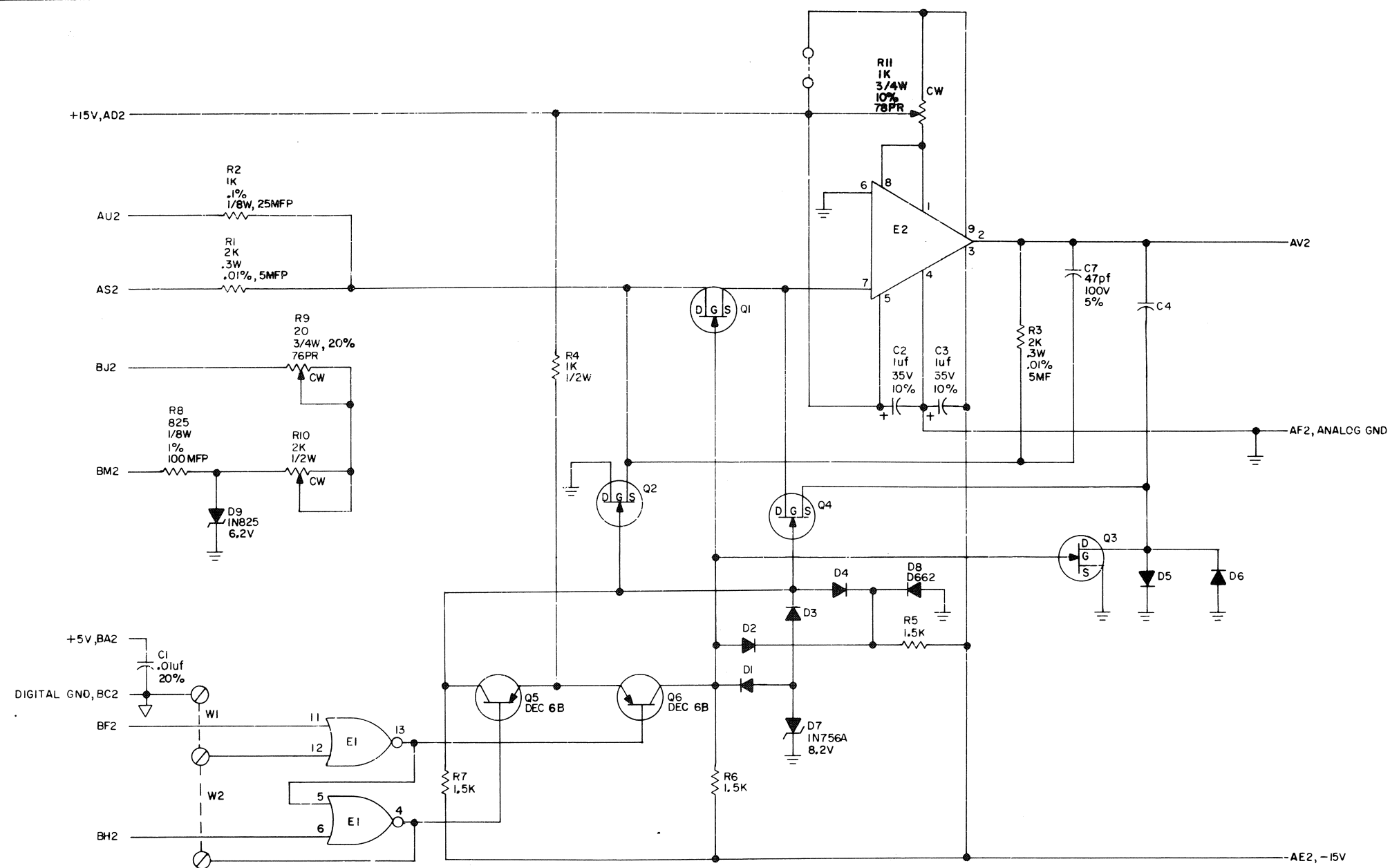
Dec 16 (325)-1048-N471

[illegible]

DRA 132

DEC 16--(325)-1048-1-N471

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UNLESS OTHERWISE INDICATED:
 TRANSISTORS ARE 2N5245
 CAPACITORS ARE .001UF 100V, 10%
 RESISTORS ARE 1/4W, 5%
 DIODES ARE D664
 E1 IS DEC7402
 PIN 7 = GND ON E1
 PIN 14 = +5V
 ○--○ INDICATES JUMPER
 ○--○ SPLIT LUGS WITH JUMPER (CUSTOMER OPTION)
 E2 IS A 1909848-03

DEC 1970 NO. 100

M5921-A

DRN. Nancy Moore
 CHK'D. Ed. Rulucio
 ENG. M. Rulucio
 PROD. M. Rulucio
 DATE 6/2/70
 DATE 7/28/70
 DATE 9/14/70

TRANSISTOR & DIODE CONVERSION CHART			
DEC	EIA	DEC	EIA
D664	IN3806	IN825	SAME
D662	IN645		
DEC68	NONE		
2N5245	NONE		
IN756A	SAME		

EQUIPMENT CORPORATION
 MAYNARD, MASSACHUSETTS

TITLE SAMPLE AND HOLD
 A405
 SIZE C CODE CS NUMBER A405-0-1 REV. B
 PRINTED CIRCUIT REV. 5

DIST. 324,434,435 5 P.N.K.

REV. B
 A405-0-1
 C CS

MASTER DRAWING LIST

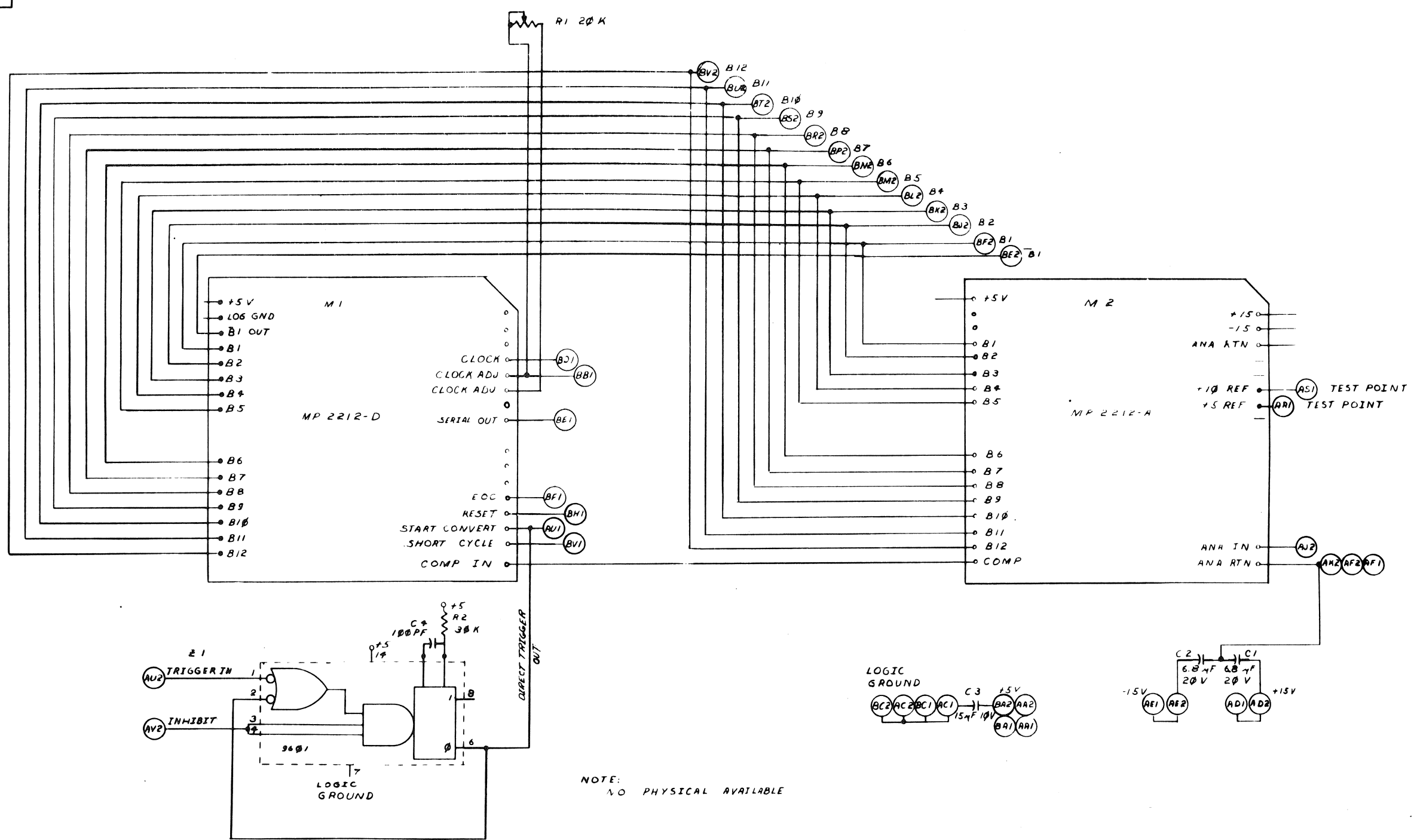
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DRA 131

Dec 16--(325)--1048--N471

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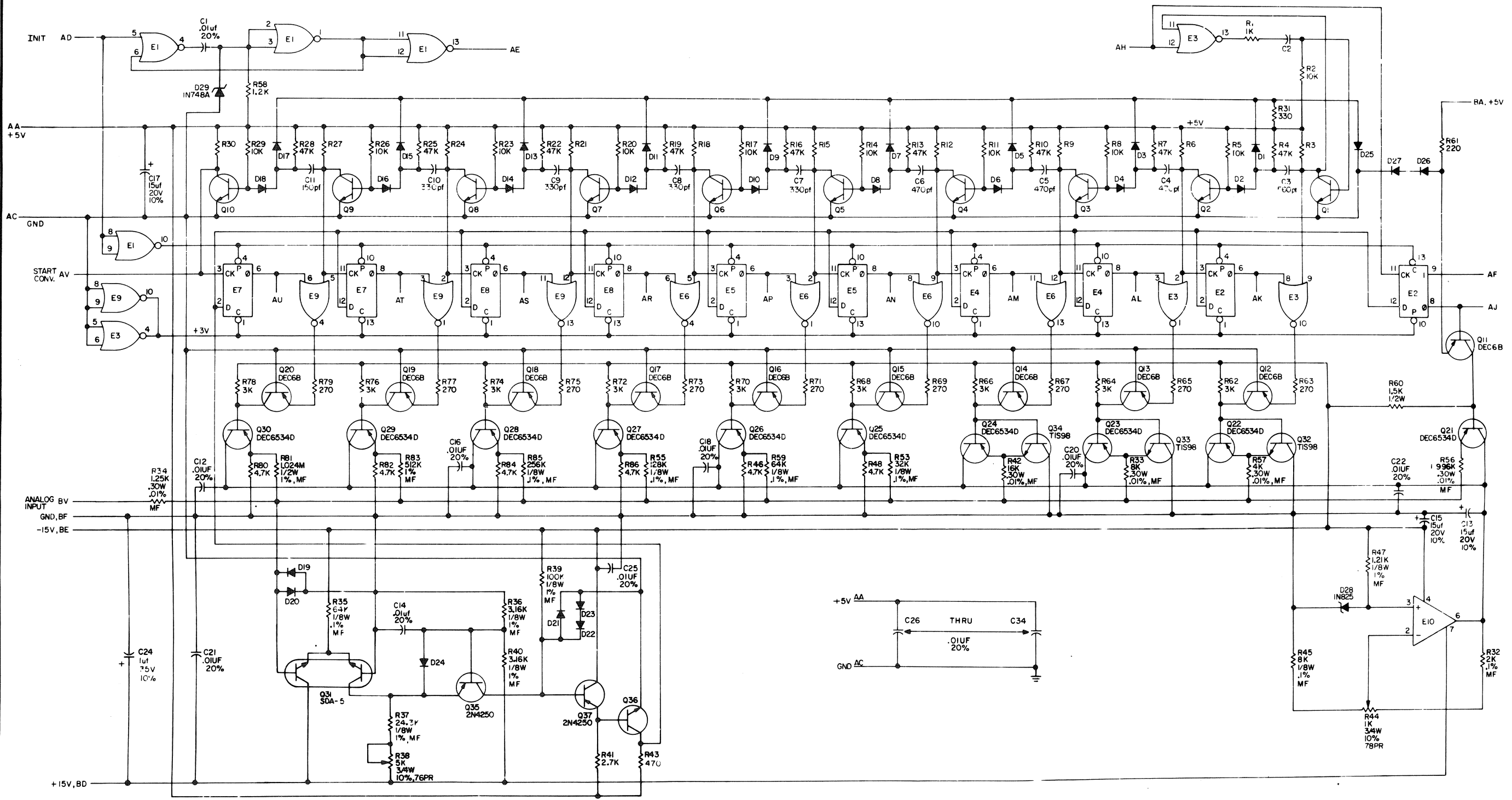
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NOTE:
NO PHYSICAL AVAILABLE

FIRST USED ON OPTION/MODEL + +		UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES		TOLERANCES DECIMALS FRACTIONS ANGLES ± .005 ± 1/64 ± 1/32 FIND SURFACE QUALITY REMOVE BURRS AND BREAK SHARP EDGES		MATERIAL + + +		FINISH + +	
QTY.	DESCRIPTION	PART NO.	PARTS LIST		EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		TITLE		REV.
					HIGH SPEED A/D CONVERTER (A861 & A862)		DCS A862-0-01		
SHEET 1 OF 1		PINK		1					

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UNLESS OTHERWISE INDICATED:
RESISTORS ARE 2.2K, 1/4W, 5%
CAPACITORS ARE 150pF, 100V, 5%
DIODES ARE D664
TRANSISTORS ARE DEC3009B
E2, E4, E5, E7, E8 ARE DEC7474
E1, E3, E6, E9 ARE DEC7402
PIN 7 ON EACH IC = GND
PIN 14 ON EACH IC = +5V
E10 ARE DEC439
--- INDICATES JUMPER (CUSTOMER OPTION)
O INDICATES SPLIT LUGS

REV	DATE	BY	CHKD
1	12-18-70	ALLAN RITCEY	
2	12-23-70		
3	12-23-70		
4	12-23-70		
5	12-23-70		
6	12-23-70		
7	12-23-70		
8	12-23-70		
9	12-23-70		
10	12-23-70		

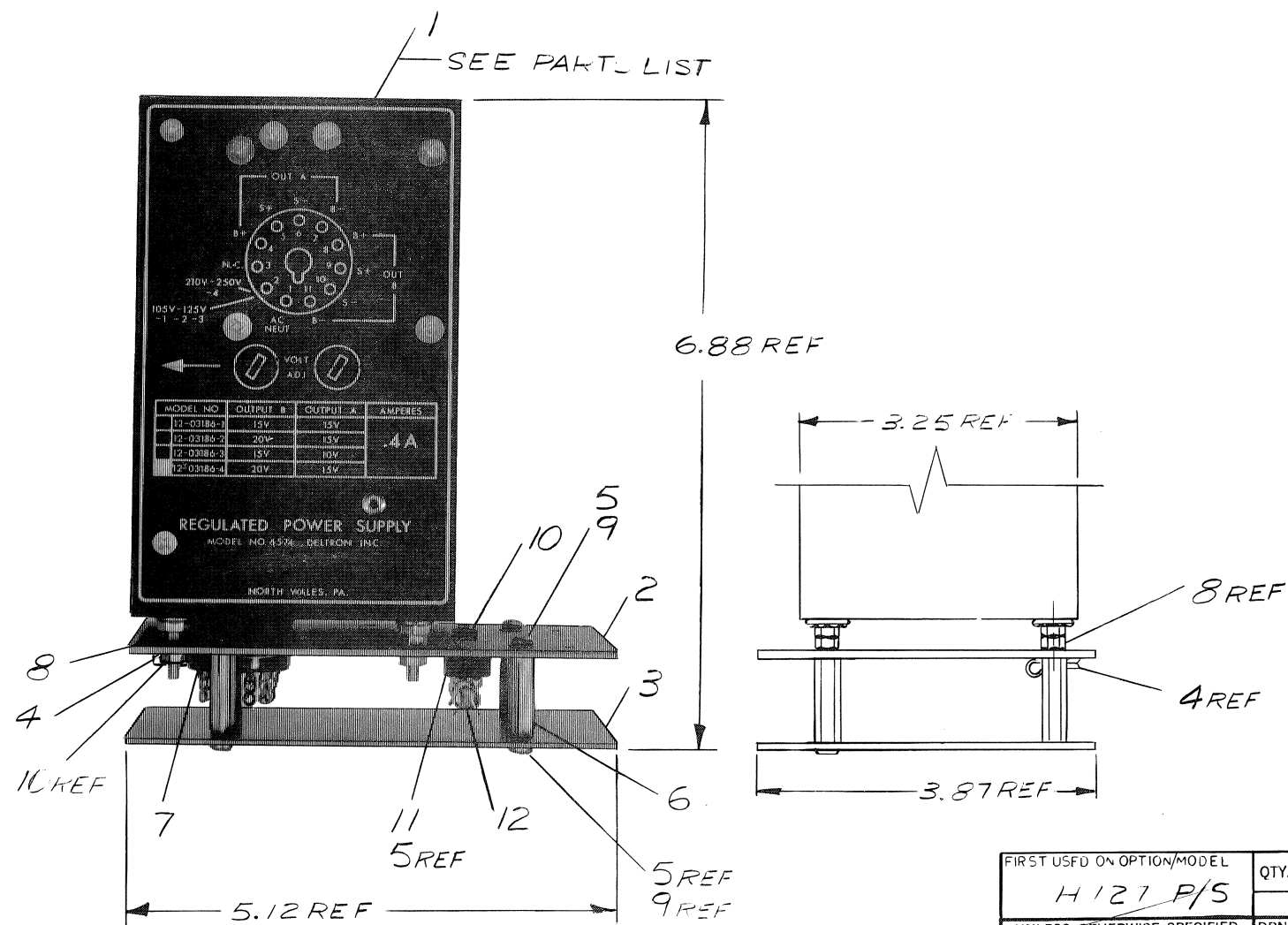
TRANSISTOR & DIODE CONVERSION CHART			
TRANSISTOR	DIODE	TRANSISTOR	DIODE
DEC3009B	IN3608	2N4250	1N4148
DEC7402	2N4250	2N4250	1N4148
DEC7474	2N4250	2N4250	1N4148
DEC439	2N4250	2N4250	1N4148

EQUIPMENT CORPORATION		TITLE: 10 BIT ADC A812	
SALE D	CS	NUMBER	A812-0-1
PRINTED CIRCUIT	REV	C	

PINK 1321

NOTES:

1. FOR CIRCUIT SCHEMATIC
REFER TO DWG. D-CS-H727-Ø-1



TOLERANCES

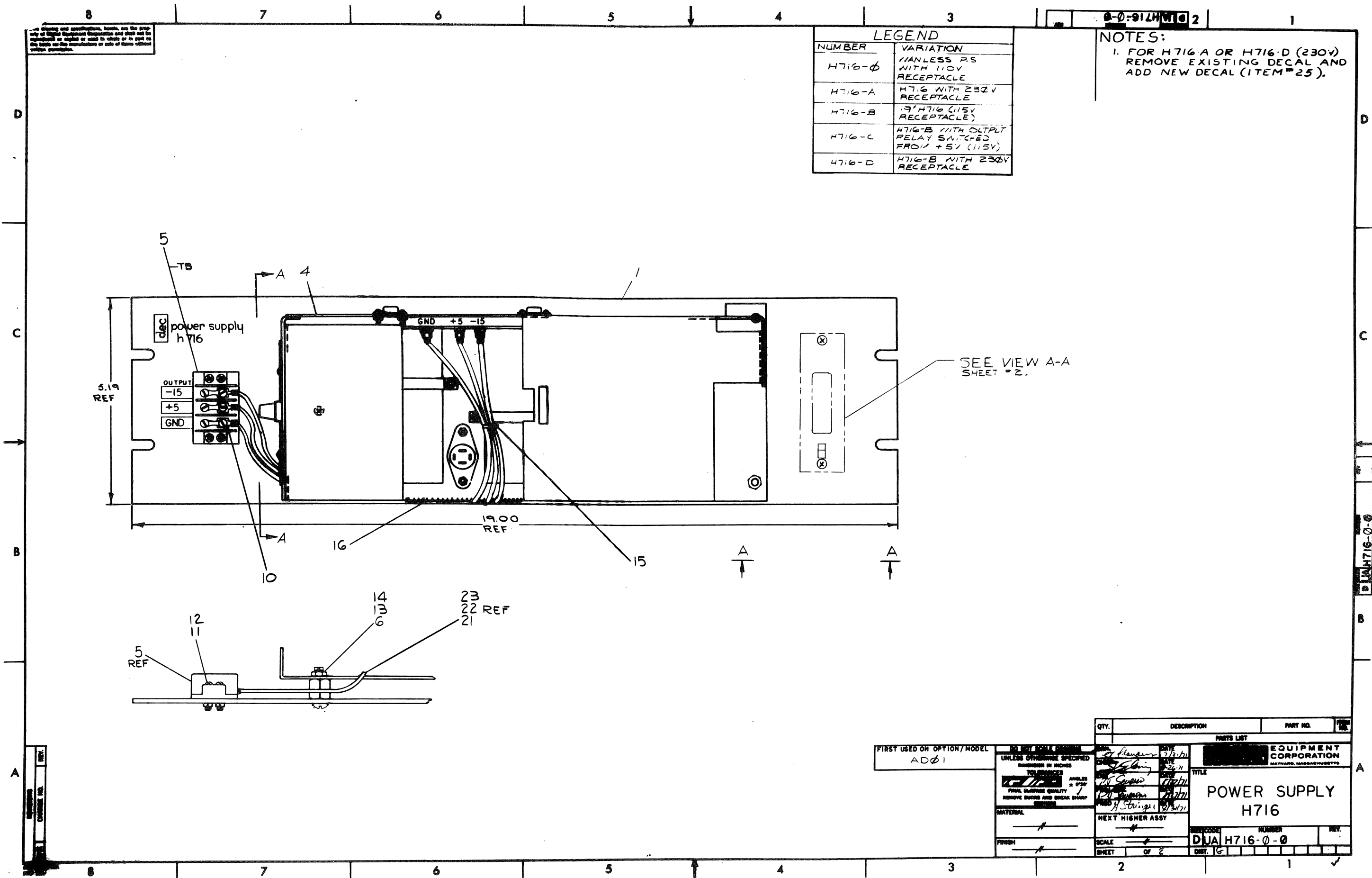
DECIMALS

.XXX = $\pm .005$

.XX = $\pm .02$

.X = $\pm .1$

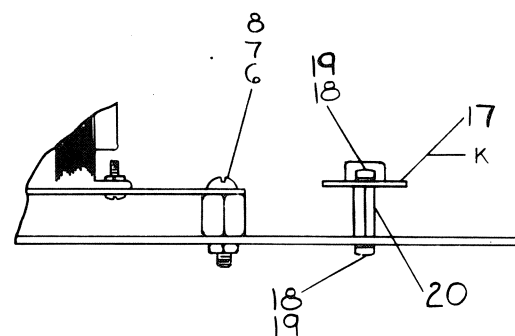
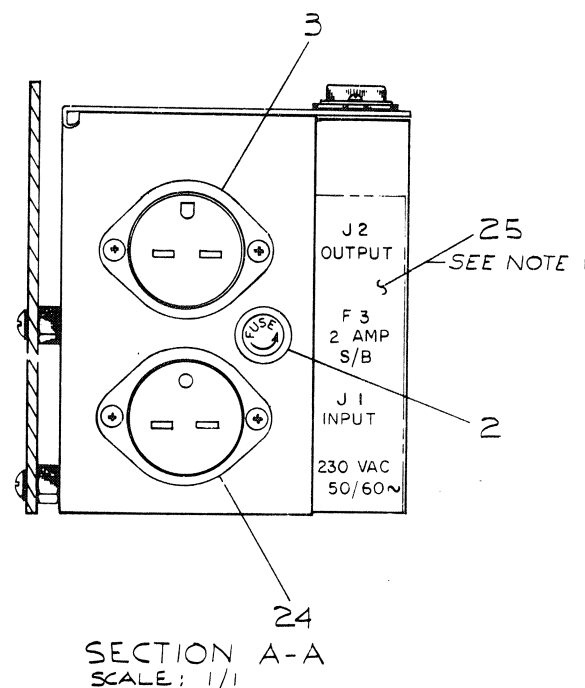
FIRST USED ON OPTION MODEL H127 P/S		QTY.		DESCRIPTION		PART NO.		ITEM NO.	
UNLESS OTHERWISE SPECIFIED		DRN. <i>H. Marin</i>		DATE <i>7/27/70</i>		<div style="display: flex; align-items: center;"> <div style="border: 1px solid black; padding: 2px; margin-right: 5px;">digital</div> <div> EQUIPMENT CORPORATION <small>MAYNARD, MASSACHUSETTS</small> </div> </div>			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		CHK'D <i>J. Russ</i>		DATE <i>8/3/70</i>					
<div style="display: flex; align-items: center;"> <div style="border: 1px solid black; padding: 2px; margin-right: 5px;"> DECIMALS FRACTIONS 0.001 1/64 </div> <div> ANGLES ± 0°30' ± 1/64 </div> </div> <p>FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS</p>		ENG. <i>Paul T. Swann</i>		DATE <i>8/5/70</i>		<div style="border: 1px solid black; padding: 10px;"> <div style="display: flex; justify-content: space-between;"> <div>TITLE</div> <div>DUAL D.C. POWER SUPPLY</div> </div> </div>			
MATERIAL		PROJ. ENG. <i>(Paul T. Swann)</i>		DATE <i>8/5/70</i>					
FINISH		PROD. <i>Alan Smith</i>		DATE <i>8/6/70</i>					
		NEXT HIGHER ASSY		SIZE CODE		NUMBER		REV.	
		SCALE NONE		C UA		H727-0-0			
		SHEET 1 OF 1		DIST.					



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WIRE TABLE H716-B&D					
ITEM NO.	DESCRIPTION	FROM CONNECTION	WITH	TO CONNECTION	WITH
23	18 BLK	PS - GND	9	TB - GND	10
21	18 RED	PS - + 5	9	TB - + 5	10
22	18 BLU	PS - -15	9	TB - -15	10

WIRE TABLE H716-C					
ITEM NO.	DESCRIPTION	FROM CONNECTION	WITH	TO CONNECTION	WITH
23	18 BLK	PS - GND	26	TB - GND	10
21	18 RED	PS - + 5	26	TB - + 5	10
22	18 BLU	PS - -15	26	TB - -15	10



VIEW A-A
FOR H716-C

SECTION A-A
SCALE: 1/1

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES		DRN. <i>[Signature]</i> DATE 8/14/71 CHK'D <i>[Signature]</i> DATE 8/26/71 ENG. <i>[Signature]</i> DATE 8/27/71 MFG. <i>[Signature]</i> DATE 8/31/71 PROD. <i>[Signature]</i> DATE 8/31/71	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DECIMALS ANGLES XXX - .005 ±0° 30' XX - .02 X - .1		TITLE POWER SUPPLY H716		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		NEXT HIGHER ASSY.		
MATERIAL		SIZE CODE NUMBER REV.		
FINISH		DUA H716-0-0		
SHEET 2 OF 2		DIST.		