

[illegible]

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---

PART NUMBER: 23-034K4-00

LOCATION: E50

DEVICE TYPE: PAL16R6

PIN NUMBER = SYMBOL TABLE:

1= CLK	8= Clsn H	15= Pre LCOL L
2= RTRY H	9= Pream2 State L	16= LCOL L
3= Tx Ena H	10= GND	17= Jam Sta L
4= Tx Act H	11= EN	18= Retry Sta L
5= Done Jam L	12= Retry H	19= Retry Init L
6= Slottim En H	13= Pre LCAR L	20= VCC
7= Carr H	14= LCAR L	

NOTES: RETRY.PAL - (LNKK)

This program is the memory contents of the PAL logic used to form the Retry Logic state machine. The Retry Logic State Machine is used to implement the Retry algorithm specified in the Ethernet Specification. This PAL is also used to detect LCOL and LCAR.

PAL PINS: |0|0|0|0|0|0|0|0|1|1|1|1|1|1|1|1|1|1|

|2|3|4|5|6|7|8|9|2|3|4|5|6|7|8|9|

OUTPIN 19 X X X X X X X X X X X X X X X VCC
X H X X X X X X X X X X X X X Tx Ena H
X X X X X H X X X X X H H X Clsn H # /Jam Sta L # /Retry Sta L
X L X L X X X X X X X X X X X Done Jam L # /Tx Ena H

OUTPIN 18 X L X X X X X X X X X X L X X Jam Sta L # /Tx Ena H
L L X X X X X X L X X X X L X X Retry Sta L # /RTRY H # /Retry L # /Tx Ena H

OUTPIN 17 X X H H X X H X X X X X X X X Clsn H # /Done Jam L # Tx Act H # /Retry Sta L
X X H H X X X X X X X X L X X Jam Sta L # /Done Jam L # Tx Act H

OUTPIN 16 X L H X X X H X X X X L X X X X Pre LCOL L # Clsn H # Tx Act H # /Tx Ena H
X L X X X X X X X X X X L X X X LCOL L # /Tx Ena H

OUTPIN 15 X L H X X H X X X X X X X X X Slottim En H # Tx Act H # /Tx Ena H
X L H X X X X X X X X L X X X X Pre LCOL L # Tx Act H # /Tx Ena H

OUTPIN 14 X L H X X L X X X L X X X X H X /Carr H # Pre LCAR L # Tx Act H # /Retry Sta L # /Tx Ena H
X L L X X X X X X H X L X X H X /Pre LCAR L # /Tx Act H # Pre LCOL L # /Retry Sta L # /Tx Ena H
X L H X X X X X X L X X H X LCAR L # /Retry Sta L # Tx Act H # /Tx Ena H
X L L X X X X X X L X X X X LCAR L # /Tx Act H # /Tx Ena H

OUTPIN 13 X L H X X H X X X X X X X X Carr H # /Retry Sta L # Tx Act H # /Tx Ena H
X L H X X X X X X L X X X X H X Pre LCAR L # /Retry Sta L # Tx Act H # /Tx Ena H
X L L X X X X X X L X X X X X Pre LCAR L # /Tx Act H # /Tx Ena H

OUTPIN 12

THIS DRAWING AND SPECIFICATIONS ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED IN WHOLE OR IN PART AS A BASIS FOR THE MANUFACTURE OF ANY ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT © 1982, DIGITAL EQUIPMENT CORPORATION.		REVISIONS		TITLE: DEUNA LINK MODULE PAL LISTINGS
		CHK	CHANGE NO.	

digital	DRN: <i>X. Sargent</i> CHK'D: Ken Sargent	DATE: 00-NOV-82	ENG: Neil Bloomberg	DATE: 00-NOV-82	NEXT HIGHER ASSEMBLY: D-UA-M7793-0-0
	DSK: LNKTDD.T2P(4,27) 108-NOV-82 16:14	SHEET 1 OF 1	BOARD LOCATION:	SIZE CODE K CS M7793-0-TTD	
FIRST USED ON OPTION/MODEL: DEUNA				NUMBER 1	REV. A

Tw 1

	8	7	6	5	V	4	3	2	1																									
D																																		
C	PART NUMBER: 23-036K4-00 LOCATION: E23 DEVICE TYPE: PAL16R6 PIN NUMBER = SYMBOL TABLE: <table border="0"><tr><td>1= CLK</td><td>8= SEL2 H</td><td>15= RND6 L</td></tr><tr><td>2= RAN8 H</td><td>9= SEL3 H</td><td>16= RMD7 L</td></tr><tr><td>3= RAN7 H</td><td>10= GND</td><td>17= RMD8 L</td></tr><tr><td>4= RAN6 H</td><td>11= EN</td><td>18= RMD9 L</td></tr><tr><td>5= RAN5 H</td><td>12= Load L</td><td>19= RANS H</td></tr><tr><td>6= SEL0 H</td><td>13= MORE L</td><td>20= VCC</td></tr><tr><td>7= SEL1 H</td><td>14= RMD5 L</td><td></td></tr></table> NOTES: RHMS2.PAL - (LNKK) This program implements the Random number mask function specified in the Ethernet spec.									1= CLK	8= SEL2 H	15= RND6 L	2= RAN8 H	9= SEL3 H	16= RMD7 L	3= RAN7 H	10= GND	17= RMD8 L	4= RAN6 H	11= EN	18= RMD9 L	5= RAN5 H	12= Load L	19= RANS H	6= SEL0 H	13= MORE L	20= VCC	7= SEL1 H	14= RMD5 L					
1= CLK	8= SEL2 H	15= RND6 L																																
2= RAN8 H	9= SEL3 H	16= RMD7 L																																
3= RAN7 H	10= GND	17= RMD8 L																																
4= RAN6 H	11= EN	18= RMD9 L																																
5= RAN5 H	12= Load L	19= RANS H																																
6= SEL0 H	13= MORE L	20= VCC																																
7= SEL1 H	14= RMD5 L																																	
B	PAL PINS: 0 0 0 0 0 0 0 1 1 1 1 1 1 1 1 1 1 OUTPIN 19 OUTPIN 18 X X X X X X H H L X X X X X H SEL3 H = SEL2 H = RAN5 H = Load L X X X X X H X H L X X X X X H SEL3 H = SEL1 H = RAN5 H = Load L X X X X X H X H L X X X X X H SEL3 H = SEL0 H = RAN5 H = Load L X X X X X X X H X X X X X L X RMD9 L = /Load L OUTPIN 17 H X X X X X H L X X X X X X SEL3 H = RAN8 H = Load L X X X X X X X H X X X X X L X X RMD8 L = /Load L OUTPIN 16 X X X X X X H L X X X X X X SEL3 H = RAN7 H = Load L X X X X H H X X L X X X X X X SEL2 H = SEL1 H = SEL0 H = RAN7 H = Load L X X X X X X X H X X X X X L X X RMD7 L = /Load L OUTPIN 15 X X H X X X X H L X X X X X X SEL3 H = RAN6 H = Load L X X H X X X H X L X X X X X X SEL2 H = SEL1 H = RAN6 H = Load L X X X X X X X H X X X L X X X X RMD6 L = /Load L OUTPIN 14 X X X H X X X H L X X X X X X SEL3 H = RAN5 H = Load L X X X H X X X H L X X X X X X SEL2 H = SEL0 H = RAN5 H = Load L X X X H X X H X L X X X X X X SEL2 H = SEL1 H = RAN5 H = Load L X X X X X X X H X X L X X X X X RMD5 L = /Load L OUTPIN 13 X X X X X X X H X X X X X X SEL3 H X X X X X X X H X X X X X X SEL2 H X X X X X X X H X X X X X X SEL1 H OUTPIN 12																																	
A	<table border="1"><thead><tr><th colspan="3">REVISIONS</th></tr><tr><th>CHK</th><th>CHANGE NO.</th><th>REV</th></tr></thead><tbody><tr><td></td><td></td><td></td></tr></tbody></table> <table border="1"><tr><td>digit@l</td><td>DRN: [Signature]</td><td>DATE ENG: 08-nov-82</td><td>DATE TITLE: DEUNA LINK MODULE</td></tr><tr><td></td><td>Ken Sargent</td><td>DATE BOARD LOCATION:</td><td>PAL LISTINGS</td></tr><tr><td colspan="2">DSK:LNKTTF.TAP(4.27) 08-NOV-82 16:14 NEX</td><td>SHEET 1 OF 1</td><td>SIZE CODE K CS M7793-0-TTF</td></tr><tr><td>FIRST USED ON OPTION/MODEL: DELUNA</td><td>J-A-47793-0-0</td><td>HIGHER ASSEMBLY:</td><td>REV. A</td></tr></table>									REVISIONS			CHK	CHANGE NO.	REV				digit@l	DRN: [Signature]	DATE ENG: 08-nov-82	DATE TITLE: DEUNA LINK MODULE		Ken Sargent	DATE BOARD LOCATION:	PAL LISTINGS	DSK:LNKTTF.TAP(4.27) 08-NOV-82 16:14 NEX		SHEET 1 OF 1	SIZE CODE K CS M7793-0-TTF	FIRST USED ON OPTION/MODEL: DELUNA	J-A-47793-0-0	HIGHER ASSEMBLY:	REV. A
REVISIONS																																		
CHK	CHANGE NO.	REV																																
digit@l	DRN: [Signature]	DATE ENG: 08-nov-82	DATE TITLE: DEUNA LINK MODULE																															
	Ken Sargent	DATE BOARD LOCATION:	PAL LISTINGS																															
DSK:LNKTTF.TAP(4.27) 08-NOV-82 16:14 NEX		SHEET 1 OF 1	SIZE CODE K CS M7793-0-TTF																															
FIRST USED ON OPTION/MODEL: DELUNA	J-A-47793-0-0	HIGHER ASSEMBLY:	REV. A																															
	8	7	6	5	V	4	3	2	1																									

8	7	6	5	V	4	3	2	A REV.	K CS M7793-0-TTK																				
									1																				
PART NUMBER: 23-037K4-00 LOCATION: E102, E103 DEVICE TYPE: PAL16R6 PIN NUMBER = SYMBOL TABLE: <table style="margin-left: auto; margin-right: auto;"> <tbody> <tr> <td>1= CLK</td><td>8= Rx In H</td><td>15= Match3 L</td></tr> <tr> <td>2= STA ADR0 H</td><td>9= Clr L</td><td>16= Match2 L</td></tr> <tr> <td>3= STA ADR1 H</td><td>10= GND</td><td>17= Match1 L</td></tr> <tr> <td>4= STA ADR2 H</td><td>11= EN</td><td>18= Match0 L</td></tr> <tr> <td>5= STA ADR3 H</td><td>12= CRY Out L</td><td>19= CRY In H</td></tr> <tr> <td>6= STA ADR4 H</td><td>13= Match5 L</td><td>20= VCC</td></tr> <tr> <td>7= STA ADR5 H</td><td>14= Match4 L</td><td></td></tr> </tbody> </table> <p>NOTES: STMT1.PAL - <LNKF></p> <p>This program is the memory contents of the PAL logic used to form the Station Address Detection STA ADR Logic.</p>									1= CLK	8= Rx In H	15= Match3 L	2= STA ADR0 H	9= Clr L	16= Match2 L	3= STA ADR1 H	10= GND	17= Match1 L	4= STA ADR2 H	11= EN	18= Match0 L	5= STA ADR3 H	12= CRY Out L	19= CRY In H	6= STA ADR4 H	13= Match5 L	20= VCC	7= STA ADR5 H	14= Match4 L	
1= CLK	8= Rx In H	15= Match3 L																											
2= STA ADR0 H	9= Clr L	16= Match2 L																											
3= STA ADR1 H	10= GND	17= Match1 L																											
4= STA ADR2 H	11= EN	18= Match0 L																											
5= STA ADR3 H	12= CRY Out L	19= CRY In H																											
6= STA ADR4 H	13= Match5 L	20= VCC																											
7= STA ADR5 H	14= Match4 L																												
PAL PINS: 0 0 0 0 0 0 0 0 1 1 1 1 1 1 1 1 1 1 2 3 4 5 6 7 8 9 2 3 4 5 6 7 8 9 OUTPIN 19 OUTPIN 18 L X X X X X L X X X X X X X X L X /Rx In H * /STA ADR0 H * Match0 L H X X X X X H X X X X X X X X L X Rx In H * STA ADR0 H * Match0 L X X X X X X L X X X X X X X X Clr L OUTPIN 17 X L X X X X L X X X X X X X X /Rx In H * /STA ADR1 H * Match1 L X H X X X X H X X X X X X X X L X Rx In H * STA ADR1 H * Match1 L X X X X X X L X X X X X X X X Clr L OUTPIN 16 X X L X X X L X X X X X X X X /Rx In H * /STA ADR2 H * Match2 L X H X X X X H X X X X X X X X L X Rx In H * STA ADR2 H * Match2 L X X X X X X L X X X X X X X X Clr L OUTPIN 15 X X X L X X L X X X X X L X X X X /Rx In H * /STA ADR3 H * Match3 L X X X H X X H X X X X X L X X X X Rx In H * STA ADR3 H * Match3 L X X X X X X L X X X X X X X X Clr L OUTPIN 14 X X X X L X L X X X L X X X X X /Rx In H * /STA ADR4 H * Match4 L X X X H X X H X X X L X X X X X Rx In H * STA ADR4 H * Match4 L X X X X X X L X X X X X X X X Clr L OUTPIN 13 X X X X X L L X X L X X X X X X /Rx In H * /STA ADR5 H * Match5 L X X X X X H H X X L X X X X X X Rx In H * STA ADR5 H * Match5 L X X X X X X L X X X X X X X X Clr L OUTPIN 12 X X X X X X X X X X X X X X X VCC X X X X X X X X X X X X X X L X Match0 L X X X X X X X X X X X X X X L X Match1 L X X X X X X X X X X X X X X L X Match2 L X X X X X X X X X X X X X X L X Match3 L X X X X X X X X X X X X X X L X Match4 L X X X X X X X X X X X X X X L X Match5 L X X X X X X X X X X X X X X H CRY In H																													

[illegible]

