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DEC FORM NO.16-1033
DRA 103



MECHANICAL			DEPT USAGE			ELECTRICAL			DEPT USAGE		
FIND NO.	DESCRIPTION	PART NO.	PROD	CUST	F/C	FIND NO.	DESCRIPTION	PART NO.	PROD	CUST	F/C
1.	BASIC ASSY/CONFIG BASIC ASSY/CONFIG (PL) SHIPPING BRKT	D-UA-11/20-0-0 C-PL-11/20-0-0 B-1A-7407301-0-0				1.	BASIC ASSY/CONFIG	A-ML-11/20-0			
3.	BASIC CHASSIS (COVER) BASIC CHASSIS PL BASIC CHASSIS (SLIDES) BASIC CHASSIS PL DRAWING INDEX LIST PDP11 COMPUTER	E-UA-BA11-CC-0 A-PL-BA11-CC-0 E-UA-BA11-CS-0 A-PL-BA11-CS-0 D-DI-BA11-0-1 A-PI-3700017-0-0				4.	MEMORY MM11-E	A-ML-MM11-E			
4.	H960-CA CAB H960-CA CAB PL DRAWING INDEX LIST	D-UA-H960-CA-0 A-PL-H960-CA-0 D-DI-H960-CA-1				5.	CENTRAL PROCESSOR KA11	A-ML-KA11-0			
5.	MEMORY MM11-E DRAWING INDEX LIST	A-ML-MM11-E C-DI-MM11-E-1				6.	CIRCUIT SCHEMATIC H720-A	D-CS-H720-0-1			
6.	CENTRAL PROCESSOR KA11 DRAWING INDEX LIST	A-ML-KA11-0 C-DI-KA11-0-1				8.	CIRCUIT SCHEMATIC PRINTED CIRCUIT	D-CS-5408475-0-1 PC-5008474-0-1			
7.	POWER SUPPLY H720-A POWER SUPPLY H720-A (PL) POWER SUPPLY H720-B POWER SUPPLY H720-B (PL) PWR SUPPLY CHASSIS PWR SUPPLY COVER BRKT SUPPORT CAPACITOR BRKT PWR SUPPLY DECALS PWR SUPPLY DECALS (120V) PWR SUPPLY DECALS (240V)	D-UA-H720-A-0 A-PL-H720-A-0 D-UA-H720-B-0 A-PL-H720-B-0 E-MD-5308519-0-0 C-MD-5308518-0-0 C-MD-5308521-0-0 D-MD-5308520-0-0 B-DC-5308524-0-0 A-DC-5308771-0-0 A-DC-5308772-0-0				10.	TELETYPE LT33-DC TELETYPE LT33-DD	A-ML-LT33-DC A-ML-LT33-DD			
8.	HEAT SINK ASSY HEAT SINK ASSY (PL) HEAT SINK COIL, TRANSFORMER	D-AD-5408525-0-0 A-PL-5408525-0-0 C-SC-1209685-0-0 C-SC-1609762-0-0				11.	TELETYPE CONTROL KL11	A-ML-KL11-0			
9.	PWR REGULATOR BD ASSY PWR REGULATOR BD ASSY (PL) HEAT SINK ASSY/DRILLING HOLE LAYOUT PWR REGULATOR ETCH BD	D-AD-5408475-0-0 A-PL-5408475-0-0 B-MD-5308733-0-0 D-AH-5408475-0-5 D-1A-5008474-0-0									
10.	CONSOLE ASSY CONSOLE ASSY (PL) DRAWING INDEX LIST	E-UA-KY11-A-0 A-PL-KY11-A-0 C-DI-KY11-A-1									
11.	TELETYPE LT33-DC (PL) TELETYPE LT33-DD (PL)	A-PL-LT33-DC-0 A-PL-LT33-DD-0									
*11.	TABLE ASSY TABLE ASSY (PL) DRAWING INDEX LIST	E-UA-H952-HA-0 A-PL-H952-HA-0 D-DI-H952-0-1									
*12.	EXPANDER CHASSIS ASSY EXPANDER CHASSIS ASSY (PL) EXPANDER CHASSIS ASSY EXPANDER CHASSIS ASSY (PL)	E-UA-BA11-EC-0 A-PL-BA11-EC-0 E-UA-BA11-ES-0 A-PL-BA11-ES-0									

QTY.	DESCRIPTION	PART NO.	ITEM NO.
	PARTS LIST		
FIRST USED ON OPTION / MODEL PDP-11			
DO NOT SCALE DRAWING			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES			
TOLERANCES			
DECIMAL FRACTIONS ANGLES			
± .005 ± .005 ± .005 ± .005			
FINAL SURFACE QUALITY			
REMOVE BURRS AND BREAK SHARP CORNERS			
MATERIAL			
+ + +			
FINISH			
+ + +			
DRN: DATE: 1/20/70			
CHK'D: DATE: 1/20/70			
ENG: DATE: 1/20/70			
PROJ ENG: DATE: 1/20/70			
PROJ: DATE: 1/20/70			
NEXT HIGHER ASSY D-UA-11/20-0-0			
SCALE: NONE			
SHEET: OF: 1			
TITLE: DRAWING INDEX LIST (PDP-11)			
SIZE CODE: NUMBER: DIST. 11/20-0-1			
REV: 1A			

DIGITAL EQUIPMENT CORPORATION

MAYNARD, MASSACHUSETTS

PARTS LIST

MADE BY AL PFYFFER	CHECKED AL PFYFFER	SECTION
DATE 3/11/70	DATE 3/11/70	1
ENG J. J. O'Laughlin	PROD Hugh Donald	ISSUED SECT.
DATE 3-17-70	DATE 3/19/70	1

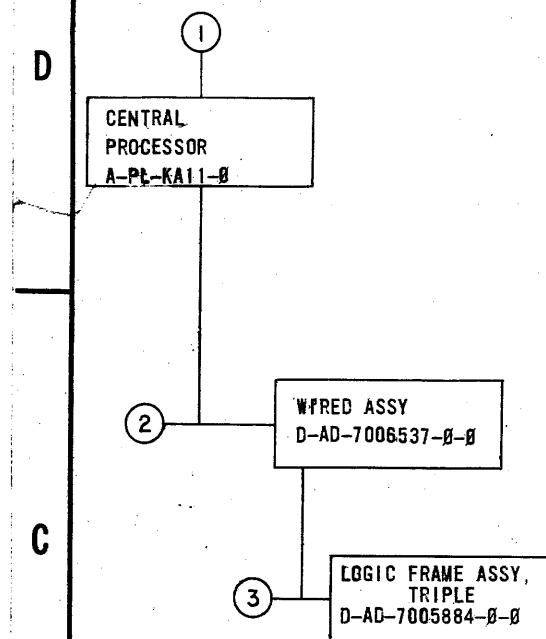
[illegible]

QUANTITY / VARIATION

TITLE CENTRAL PROCESSOR	ASSY NO.	SIZE A	CODE PL	NUMBER K11-0-0				REV.	ECO NO.
	SHEET 1 OF 1	DIST.							

DEC FORM NO. 16-1031
DRA 110

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MECHANICAL					USAGE		
FIND NO	DESCRIPTION	PART NO.	PROD	CUST	FC		
1.	CENTRAL PROCESSOR MODULE UTILIZATION	A-PL-KA11-B-B A-PL-KA11-B-MU					
2.	WIRED ASSY WIRED ASSY (PL) LOGIC SERIAL #DECAL	D-AD-7006537-0-0 A-PL-7006537-0-0 A-SS-7407846-0-0					
3.	LOGIC FRAME ASSY, TRIPLE LOGIC FRAME ASSY, TRIPLE STRAP, TRIPLE FRAME CONN BLOCK MTG FRAME	D-AD-7005884-0-0 A-PL-7005884-0-0 C-MD-7407513-0-0 D-1A-7407507-0-0					

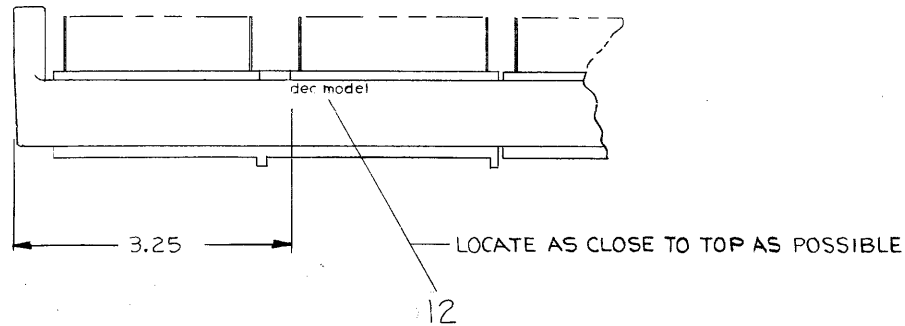
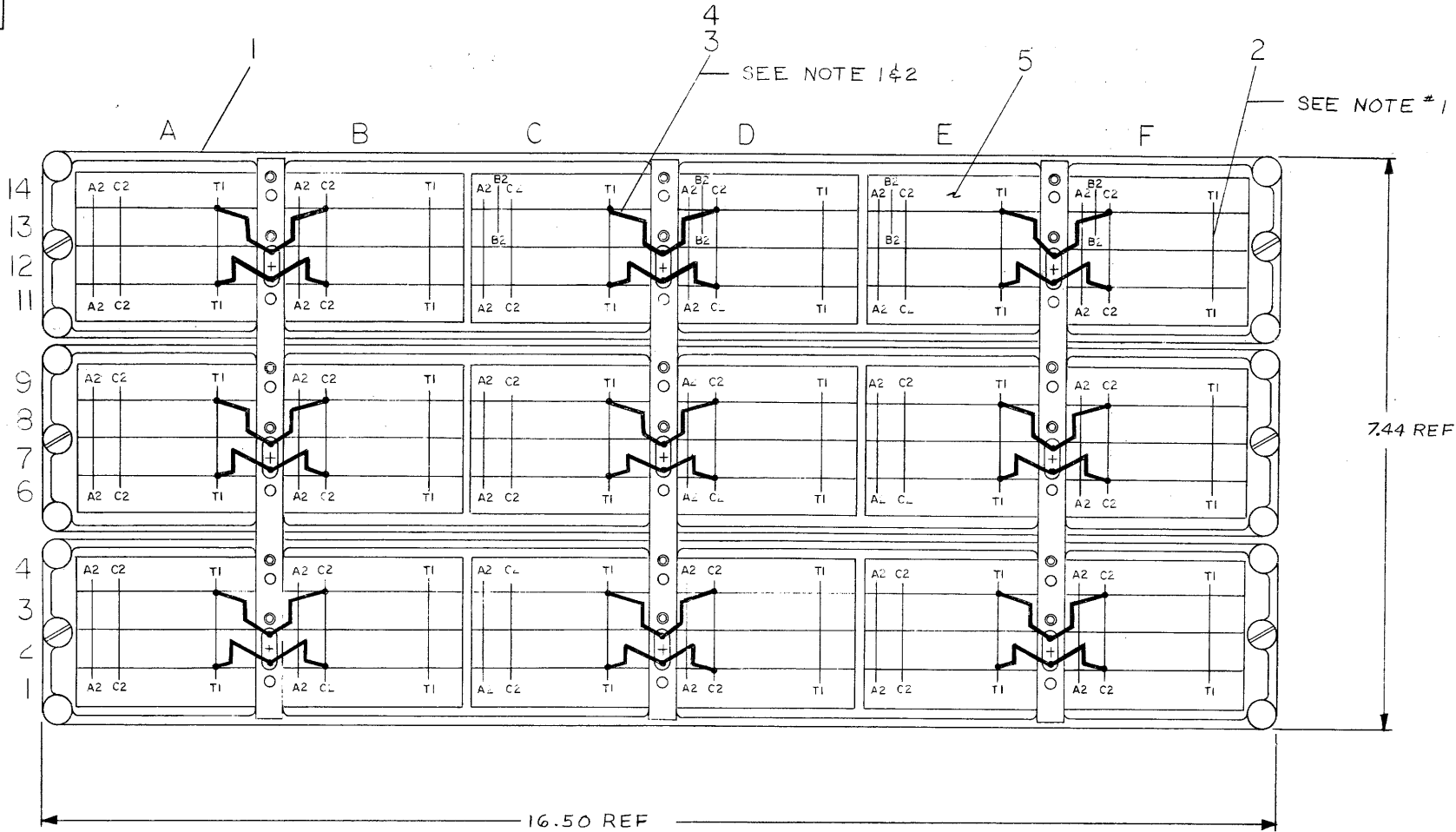
ELECTRICAL					USAGE		
FIND NO	DESCRIPTION	PART NO.	PROD	CUST	FC		
1.	CENTRAL PROCESSOR TIMING & STATES M728 STATE CNTL M727 PRIORITY M824 REGISTER CNTL M821 REGISTER M225 DATA PATH CNTL M820 DATA PATH 1 M224 DATA PATH 2 M224 BUS INTERFACE & IR M725 IR DECODE M726 CODES DATA M823 FLAG CNTL M822 BUS & CONSOLE CNTL M724 KA11 BUS & POWER CONNECTIONS PWR FAIL & CNTL M825 MODULE UTILIZATION BLOCK DIAG. CUT ACROSS 1 BIT INSTRUCTION FLOW DIAGRAM BUS FLOW WAVE FORMS KA11 BLOCK DIAGRAM	A-ML-KA11-B D-CS-M728-B-1 D-CS-M727-B-1 D-CS-M824-B-1 D-CS-M821-B-1 D-BS-KA11-B-REG D-CS-M820-B-1 D-BS-KA11-B-DP1 D-BS-KA11-B-DP2 D-CS-M725-B-1 D-CS-M726-B-1 D-CS-M823-B-1 D-CS-M822-B-1 D-CS-M724-B-1 D-FC-KA11-B-BP D-CS-M825-B-1 D-MU-KA11-B-MU D-BD-KA11-B-DBCA D-FD-KA11-B-IFD D-FD-KA11-B-BF D-TD-KA11-B-WF D-BD-KA11-B-BD					
2.	WIRED ASSY WIRE LIST	D-AD-7006537-0-0 K-WL-KA11-B-WL					

REV.	CHANGE NO.	REV.
A	KA11-00002	A
1	10/15/70	
2	11/10/70	
3	12/10/70	
4	1/10/71	

FIRST USED ON OPTION/MODEL PDP-II	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED	DRN.	PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES ± .005 ± 1/64 ± 0°30' FINAL SURFACE QUALITY REMOVE RIBS AND BREAK CHAMF CORNERS	CHK'D DATE 3/11/70 ENG DATE 3-19-70 PROJ. ENG. DATE 3-19-70 DATE 3-19-70	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		
TITLE DRAWING INDEX (KA11)		SIZE CODE CIDI KA11-Ø-1		REV. A

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- NOTES:
1. CONNECTIONS ON ITEMS 2 & 3 TO BE SOLDERED AND LOCATED AT MINIMUM PRACTICAL HEIGHT ABOVE BLOCKS.
 2. CONNECTOR BLOCKS TO BE GROUNDED TO GROUND LUG AS SHOWN.
 3. ALL VERTICAL HAND WIRING TO RUN OUTSIDE OF CONNECTOR BLOCKS.



FIRST USED ON OPTION/MODEL
KAI1

DO NOT SCALE DRAWING
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS FRACTIONS ANGLES
± .005 ± 1/64 ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS
MATERIAL
NEXT HIGHER ASSY
FINISH

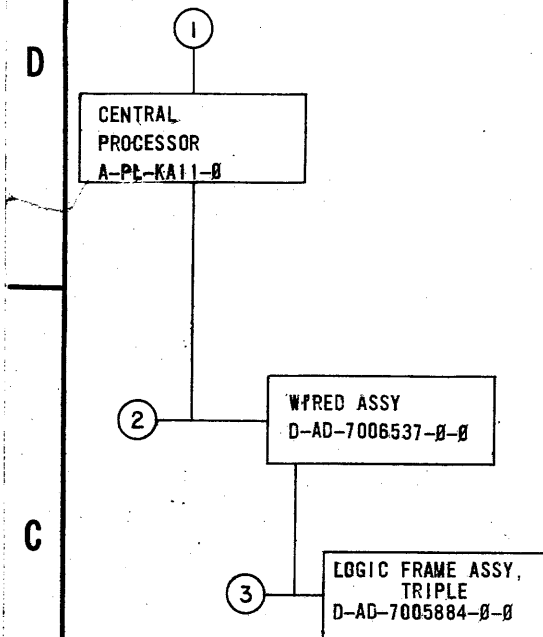
QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST			
digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			
TITLE WIRED ASSY (KAI1)			
SIZE/CODE DAD7006537-0-0		REV. A	
SCALE 1/1		SHEET 1 OF 2	

REV.	CHANGE NO.	DATE
A	0002	5-18-70

CHK. *[Signature]*
O'LOUGHLIN
5/20/70

DEC FORM NO. DRD 100

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MECHANICAL					ELECTRICAL						
FIND NO	DESCRIPTION	PART NO.	PROD	CUST	FC	FIND NO	DESCRIPTION	PART NO.	PROD	CUST	FC
1.	CENTRAL PROCESSOR MODULE UTILIZATION	A-PL-KA11-0-0 A-PL-KA11-0-MU				1.	CENTRAL PROCESSOR TIMING & STATES M728 STATE CNTL M727 PRIORITY M824 REGISTER CNTL M821 REGISTER M225 DATA PATH CNTL M820 DATA PATH 1 M224 DATA PATH 2 M224 BUS INTERFACE & IR M725 IR DECODE M726 CODES DATA M823 FLAG CNTL M822 BUS & CONSOLE CNTL M724 KA11 BUS & POWER CONNECTIONS PWR FAIL & CNTL M825 MODULE UTILIZATION	A-ML-KA11-0 D-CS-M728-0-1 D-CS-M727-0-1 D-CS-M824-0-1 D-CS-M821-0-1 D-BS-KA11-0-REG D-CS-M820-0-1 D-BS-KA11-0-DP1 D-BS-KA11-0-DP2 D-CS-M725-0-1 D-CS-M726-0-1 D-CS-M823-0-1 D-CS-M822-0-1 D-CS-M724-0-1 D-FC-KA11-0-BP D-CS-M825-0-1 D-MU-KA11-0-MU			
2.	WIRED ASSY WIRED ASSY (PL) LOGIC SERIAL #DECAL	D-AD-7006537-0-0 A-PL-7006537-0-0 A-SS-7407846-0-0					BLOCK DIAG. CUT ACROSS 1 BIT INSTRUCTION FLOW DIAGRAM BUS FLOW WAVE FORMS KA11 BLOCK DIAGRAM	D-BD-KA11-0-DBCA D-FD-KA11-0-IFD D-FD-KA11-0-BF D-TD-KA11-0-WF D-BD-KA11-0-BD			
3.	LOGIC FRAME ASSY, TRIPLE LOGIC FRAME ASSY, TRIPLE STRAP, TRIPLE FRAME CONN BLOCK MTG FRAME	D-AD-7005884-0-0 A-PL-7005884-0-0 C-MD-7407513-0-0 D-IA-7407507-0-0				2.	WIRED ASSY WIRE LIST	D-AD-7006537-0-0 K-WL-KA11-0-WL			

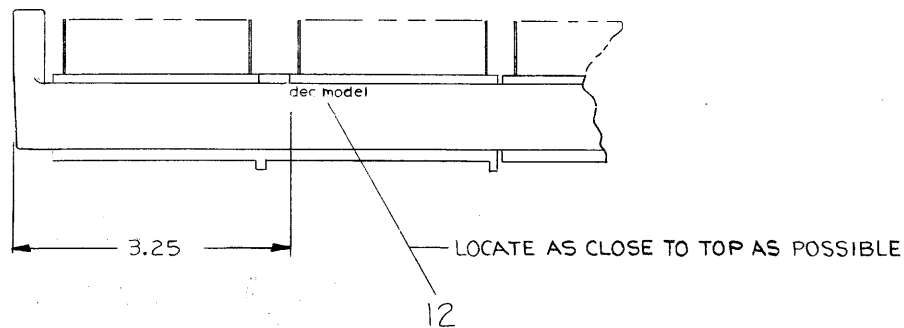
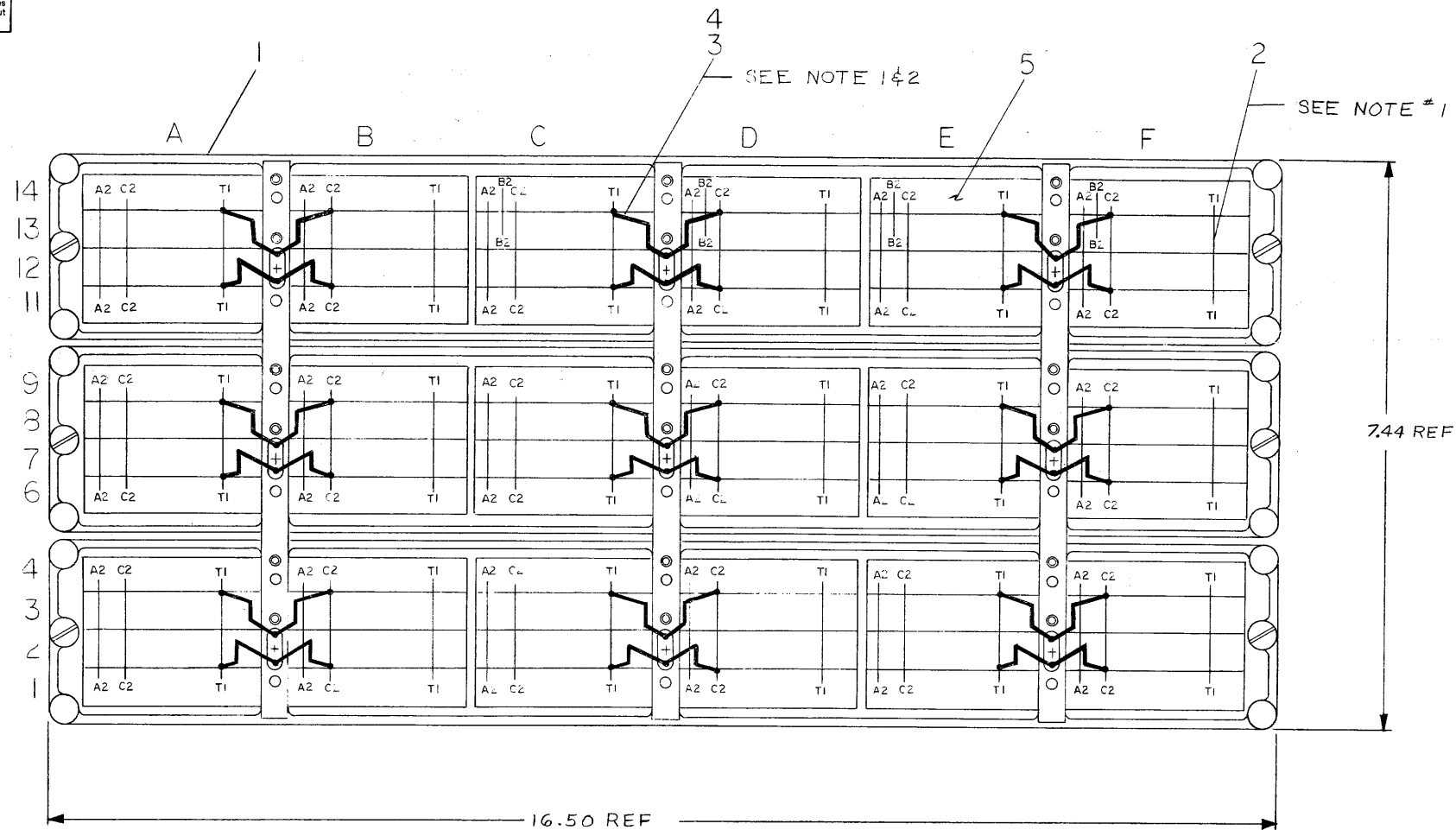
REV.	CHANGE NO.	CHK
A	000002	0000
O'LOUGHLIN		
5/20/70		
5/20/70		

FIRST USED ON OPTION/MODEL PDP-11		QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED		PARTS LIST			
UNLESS OTHERWISE SPECIFIED		DRN.	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DIMENSION IN INCHES		F. MARCOTTE	10-19-69	DRAWING INDEX (KA11)	
TOLERANCES		CHK'D	DATE		
DECIMALS FRACTIONS ANGLES		01/1/70	3/11/70		
± .005 ± 1/64 ± 0°30'		ENG	DATE		
FINAL SURFACE QUALITY		PROJ. ENG.	DATE	SIZE CODE NUMBER REV.	
REMOVE BURRS AND BREAK SHARP CORNERS		1/1/70	3-10-70	C D I KA11-0-1 A	
MATERIAL		DATE	DATE	SHEET OF DIST.	
NEXT HIGHER ASSY		DATE	DATE		
A-ML-KA11-0					

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NOTES:

1. CONNECTIONS ON ITEMS 2 & 3 TO BE SOLDERED AND LOCATED AT MINIMUM PRACTICAL HEIGHT ABOVE BLOCKS.
2. CONNECTOR BLOCKS TO BE GROUNDED TO GROUND LUG AS SHOWN.
3. ALL VERTICAL HAND WIRING TO RUN OUTSIDE OF CONNECTOR BLOCKS.



FIRST USED ON OPTION / MODEL
KAI1

DO NOT SCALE DRAWING
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS FRACTIONS ANGLES
± .005 ± 1/64 ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS
MATERIAL
FINISH

QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST			
DRN.	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
CHK'D.	DATE		
ENG.	DATE	TITLE WIRED ASSY (KAI1)	
PROJ. ENG.	DATE		
PROB.	DATE	NEXT HIGHER ASSY A-ML-KAI1-0	
SCALE	1/1		
SHEET	1 OF 2	SIZE/CODE DAD	NUMBER 7006537-0-0
		DIST.	REV. A

REVISIONS	CHANGE NO.	REV.
CHK	KAI1-00002	A
DES	10-16-51-77	
	O'LOUGHLIN	
	5/26/76	

DEC FORM NO
DRD 100

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MASTER DRAWING LIST									
DWG. NO.		REV. LET.	NO. OF SHEETS	TITLE					
A-PL-KA11-Ø-Ø			1	CENTRAL PROCESSOR					
C-DI-KA11-Ø-1		A	1	DRAWING INDEX					
D-AD-7006537-0-0		A	2	WIRED ASSY					
A-PL-7006537-0-0		A	1	WIRED ASSY					
D-BD-KA11-Ø-BD			1	KA11 BLOCK DIAGRAM					
D-FD-KA11-Ø-IFD			1	INSTRUCTION FLOW DIAGRAM					
D-FD-KA11-Ø-BF			1	BUS FLOW					
D-TD-KA11-Ø-WF			1	WAVEFORMS					
D-CS-M728-Ø-1		C	4	TIMING & STATES M728 K1					
D-CS-M727-Ø-1		E	3	STATE CNTL M727 K2					
D-CS-M824-Ø-1		C	3	PRIORITY M824 K3					
D-CS-M821-Ø-1		B	3	REGISTER CNTL M821 K4					
D-CS-M820-Ø-1		A	2	REGISTER M225 K5					
D-CS-M820-Ø-1		B	5	DATA PATH CNTL M820 K6					
D-CS-M820-Ø-1		A	2	DATA PATHS 1 M224 K7					
D-CS-M820-Ø-1		A	2	DATA PATHS 2 M224 K8					
D-CS-M725-Ø-1		B	5	BUS INTERFACE & IR M725 K9					
D-CS-M726-Ø-1		A	4	IR DECODE M726 K10					
D-CS-M823-Ø-1			2	CODES DATA M823 K11					
D-CS-M822-Ø-1		B	3	FLAG CNTL M822 K12					
D-CS-M724-Ø-1		C	4	BUS & CONSOLE CNTL M724 K13					
D-IC-KA11-Ø-BP		A	1	KA11 BUS & POWER CONNECTIONS K14					
D-CS-M825-Ø-1		B	2	PWR FAIL & CNTL M825 K15					
A-ML-KY11-A		REF	1	KA11 CONSOLE					
REVISIONS				DRN. AL. PFYFFER	DATE 3/9/70	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			
REV.	DATE	CHG. NO.	APP'D.	CHK'D.	DATE				
A	4/70	00001	J.O.L.	ENG.	3/9/70	TITLE CENTRAL PROCESSOR			
B	4/70	00002	J.O.L.	ENG.	3/9/70				
C	5/70	00003	J.O.L.	ENG.	3/9/70				
D	6/70	00005	J.O.L.	PROJ. ENG.	3/9/70				
E	8/70	00006	J.L.	PROD.	3/9/70				
F	8/70	00007	J.L.	PROD.	3/9/70				
				FIRST USED ON PDP 11		SIZE	CODE	NUMBER	REV.
				SCALE		A	ML	KA11-Ø	F
				SHEET 1 OF 2		DIST.			

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WIRE TABLE

ITEM NO.	SIGNAL NAME	FROM PIN	TO PIN	COLOR	REMARKS	ITEM NO.	SIGNAL NAME	FROM PIN	TO PIN	COLOR	REMARKS
6.	GND	A03D2	A73E2	BLACK		11	-15V	C13B2	D13B2	BLUE ↓ BLUE	WIRES MUST RUN PHYSICALLY TO RIGHT OF THE THIRD BLOCKS AND PINS
		A03C2	A03D2			E13B2	F13B2				
		A03E2	A03F2			D13B2	E13B2				
		A03N1	A03P1			C13B2	A13L1				
		A03R1	A03S1								
		A03P1	A03R1								
		A03U1	A03V1								
		A03S1	A03U1								
		A03F2	A03N1								
		A08D2	A08E2								
A08C2	A08D2										
A08E2	A08F2										
A08N1	A08P1										
A08R1	A08S1										
A08P1	A08R1										
A08U1	A08V1										
A08S1	A08U1										
A08F2	A08N1										
A13D2	A13E2										
A13C2	A13D2										
A13E2	A13F2										
A13N1	A13P1										
A13R1	A13S1										
A13P1	A13R1										
A13U1	A13V1										
A13S1	A13U1										
A13F2	A13N1										
7	+5V	A03A2	B02A2	RED	WIRES MUST RUN PHYSICALLY TO LEFT OF THE FIRST BLOCKS AND PINS						
		B01A2	C01A2								
		A03H2	D01A2								
		A03J2	E01A2								
		E01A2	F01A2								
		A03K2	B03A2								
		B04A2	C04A2								
		A03L2	D04A2								
		A03M2	E04A2								
		E04A2	F04A2								
		A08A2	B07A2								
		B06A2	C06A2								
		A08H2	D06A2								
		A08J2	E06A2								
		E06A2	F06A2								
		A08K2	B08A2								
		B09A2	C09A2								
		A08L2	D09A2								
		A08M2	E09A2								
		E09A2	F09A2								
		A13A2	B12A2								
		B11A2	C11A2								
		A13H2	D11A2								
		A13J2	E11A2								
		E11A2	F11A2								
		A13K2	B13A2								
		B14A2	C14A2								
		A13L2	D14A2								
A13M2	E14A2										
E14A2	F14A2										
B03A2	B07A2										
C03A2	C07A2										
D03A2	D07A2										
E03A2	E07A2										
F03A2	F07A2										
B08A2	B11A2										
C08A2	C11A2										
D08A2	D11A2										
E08A2	E11A2										
F08A2	F11A2										

FIRST USED ON OPTION/MODEL
KAI

DO NOT SCALE DRAWING
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS FRACTIONS ANGLES
± 1/64 ± 0°30'
FACE QUALITY
REWORK SURFACES AND BREAK SHARP CORNERS
MATERIAL
FINISH

QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST			
DRN	DATE	digital EQUIPMENT CORPORATION	
CHK'D	DATE	TITLE	
ENG	DATE	WIRED ASSY (KAI)	
PROJ ENG	DATE	SIZE CODE NUMBER	
PROD	DATE	DAD 7006537-0-0	
NEXT HIGHER ASSY		REV. A	
A-ML-KAI-0		DIST.	
SCALE NONE			
SHEET 2 OF 2			

REVISIONS
CHANGE NO.
CHK

DEC FORM NO.
DRD 100

DIGITAL EQUIPMENT CORPORATION

MAYNARD, MASSACHUSETTS

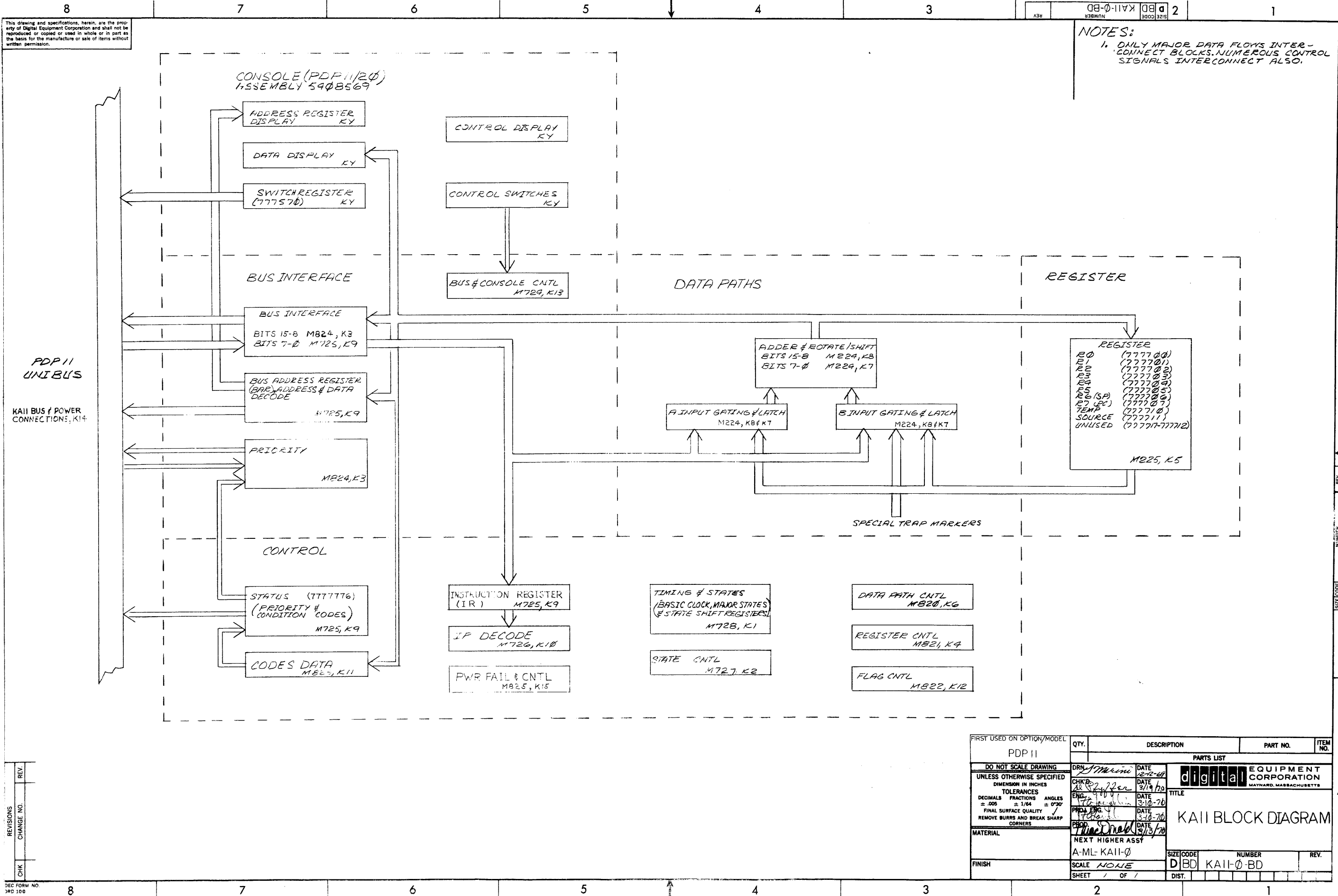
PARTS LIST

MADE BY P. MARCOTTE	CHECKED K. RUSS	SECTION
DATE 10/6/69	DATE 10/10/69	1
ENG J. J. O'Laughlin	PROD Thurgood	ISSUED SECT.
DATE 3-17-70	DATE 3/19/70	1

ITEM NO.	DWG NO. / PART NO.	DESCRIPTION
1	D-AD-7005884-0-0	LOGIC FRAME ASSY TRIPLE
2	1205541	BUS STRIP
3	9107560-01	#22 AWG BUS WIRE
4	9107265-09	#22 TUBING TEFLON, WHITE
5	9105740-44	#30 AWG SOLID KYNAR INS WIRE, YEL
6	9107470-00	#24 AWG SOLID KYNAR INS WIRE, BLK
7	9107470-22	#24 AWG SOLID KYNAR INS WIRE, RED
8	9107470-33	#24 AWG SOLID KYNAR INS WIRE, ORN
9	9107470-55	#24 AWG SOLID KYNAR INS WIRE, GRN
10	9107470-44	#24 AWG SOLID KYNAR INS WIRE, YEL
11	9107470-66	#24 AWG SOLID KYNAR INS WIRE, BLUE
12	A-SS-7407846-0-0	LOGIC SERIAL # <u>DECAL</u>
REF	K-WL-KA11-Ø-WL	WIRE LIST

TITLE WIRED ASSY (KA11)	ASSY NO. D-AD-700653700-0	SIZE A	CODE PL	NUMBER 7006537-0-0	REV. A	ECO NO. KA11-00002
	SHEET 1 OF 1	DIST.				

DEC FORM NO.
DRA 110



FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11					
DO NOT SCALE DRAWING		DRN	DATE	PARTS LIST	
UNLESS OTHERWISE SPECIFIED		CHK'D	DATE	digital EQUIPMENT CORPORATION	
DIMENSION IN INCHES		ENG	DATE	MAYNARD, MASSACHUSETTS	
TOLERANCES		PROJ	DATE	TITLE	
DECIMALS FRACTIONS ANGLES		PROJ	DATE	KAI BLOCK DIAGRAM	
± .005 ± 1/64 ± 0°30'		PROJ	DATE	SIZE CODE NUMBER REV.	
FINAL SURFACE QUALITY		PROJ	DATE	D BD KAI-0-BD	
REMOVE BURRS AND BREAK SHARP CORNERS		PROJ	DATE	DIST.	
MATERIAL		NEXT HIGHER ASSY			
FINISH		A-ML: KAI-0			
SCALE NONE		SHEET 1 OF 1			

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PARTS REFERENCE

ITEM NO.	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E23	DEC 74H50N	1909060	2
2	E5, E10, E28E37	DEC 7402N	1909004	5
3	E3	DEC 74H74N	1909667	2
4	E25, E26, E29, E38, E43, E50, E54	DEC 74H40N	1905586	8
5	E7, E11, E19, E20, E31, E49	DEC 7400N	1905575	6
6	E8	DEC 8881	1909705	1
7	E12, E14, E18, E39, E40, E41, E42, E44, E53	DEC 74H00N	1909056	9
8	E13, E15E24, E34	DEC 74H10N	1909057	4
9	E16	DEC 74H20N	1905635	1
10	E17, E47, E48	DEC 74H30N	1909059	3
11	E21E51	DEC 7410N	1905576	2
12	E22E46	DEC 8815A	1909713	2
13	E27E30, E45	DEC 8271	1909615	2
14	E32E33, E35, E36	8251B	1909594	4
15	E52	DEC 7420N	1905577	1
16	C1-C10, C13, C15-C27, C29-C55, C59	.01 MFD, 100V, 20% CAP	1001610	52
17	C11, C60	330 MMFD, 100V, 5% CAP	1000023	2
18	C12	18 MMFD, 100V, 5% CAP	1002608	1
19	C14	150 MMFD, 100V, 5% CAP	1000019	1
20	C56-C58, C28	6.8 MFD, 35V, 20% CAP	1000067	4
21	R1	300, 1/4W, 5% RES	1301425	1
22	R2, R4, R7, R10	1000, 1/4W, 5% RES	1300365	4
23	R3	47, 1/4W, 5% RES	1300202	1
24	R5	470, 1/4W, 5% RES	1300316	1
25	R6, R9	220, 1/4W, 5% RES	1300271	2
26	R8	5000 OHM POTENTIOMETER	1305451	1
27	D1	D664 DIODE	1100114	1
28	Q1	DEC 3009B TRANSISTOR	1503100	1

NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.

2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED: MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPERATE PINS ARE USED: MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.

3. PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.

4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.

5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

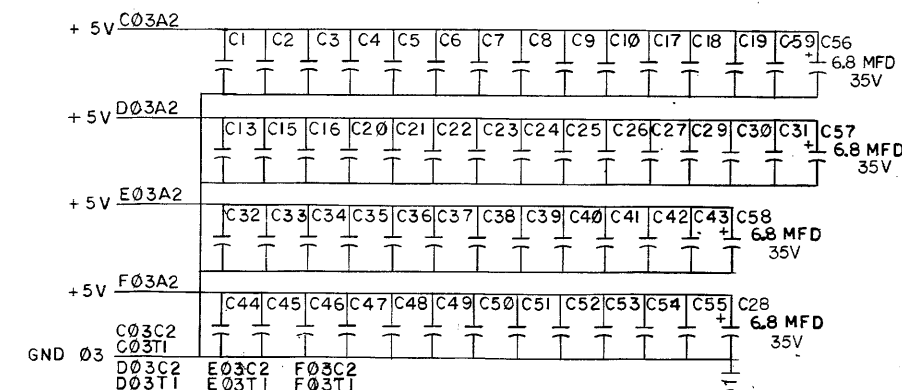
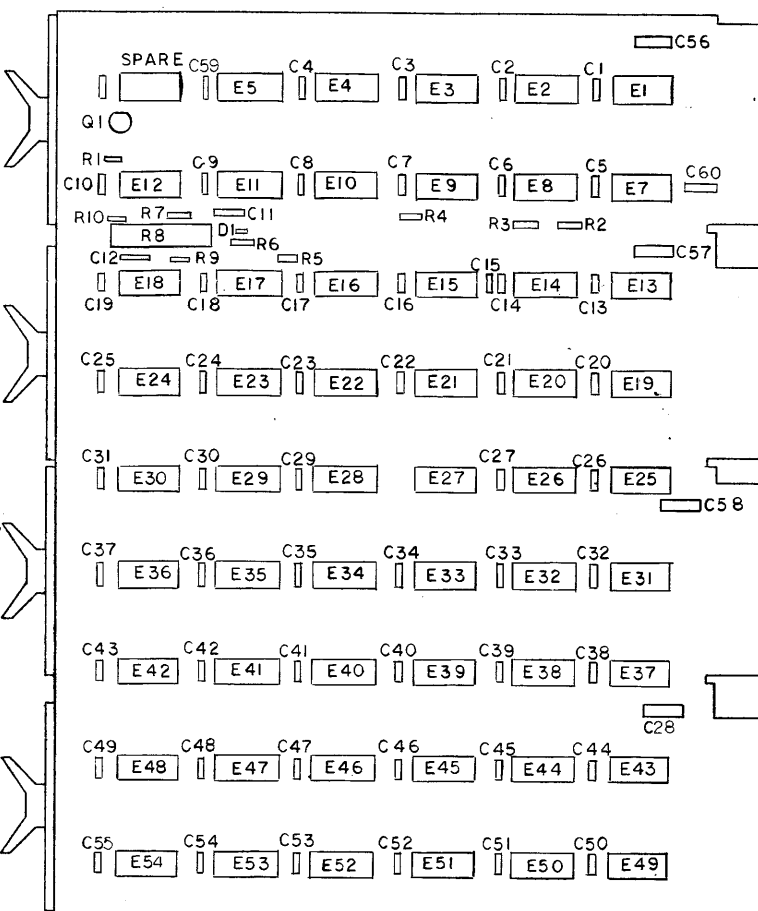
IC TYPE	GND	+5V
DEC 7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

6. UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICO FARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01 MFD.

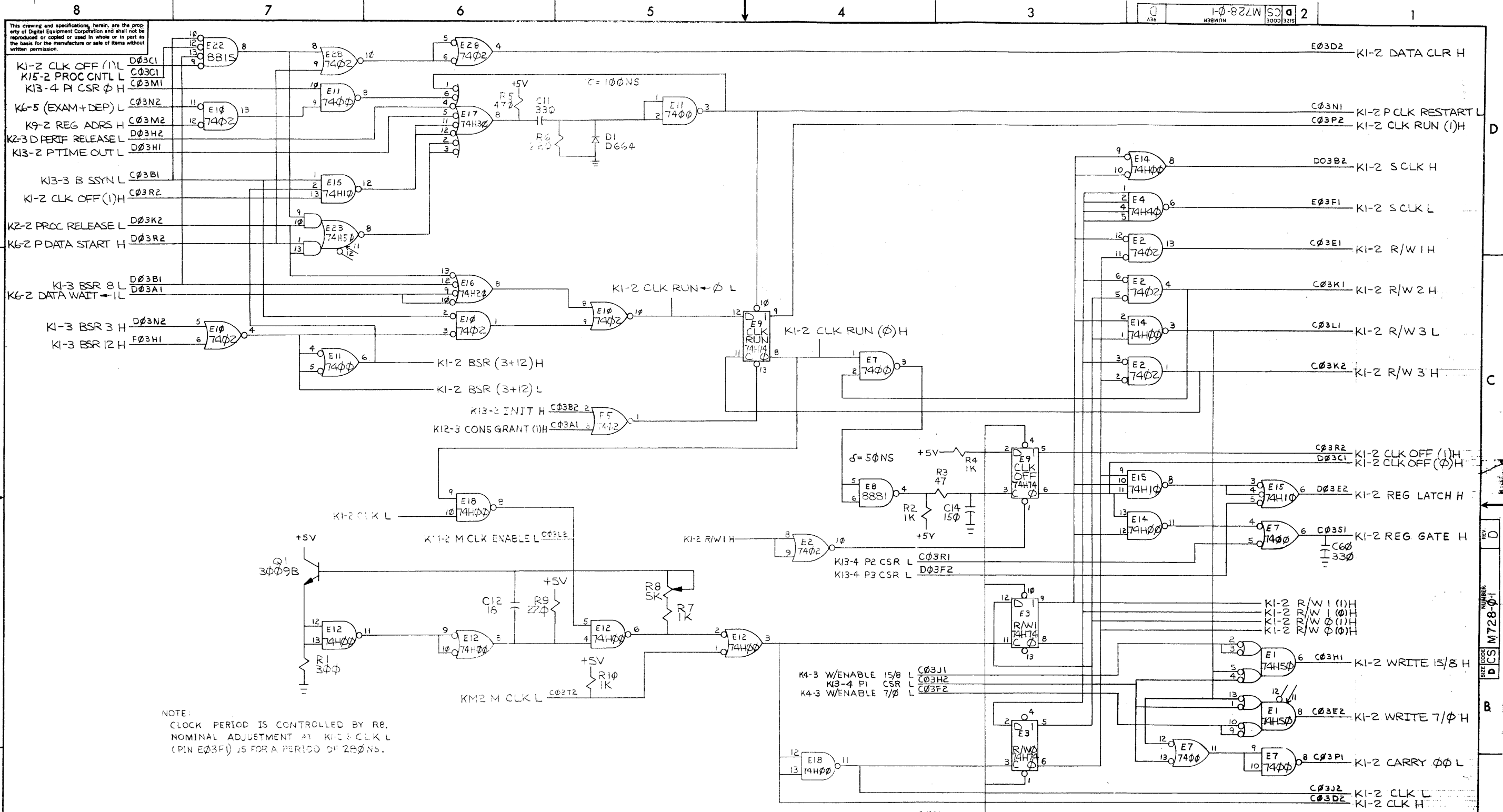
COMPONENT PLACEMENT

PIN NOMENCLATURE

MODULE PROCESSOR



FIRST USED ON OPTION MODEL PDP 11		QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED		PARTS LIST			
UNLESS OTHERWISE SPECIFIED		digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			
UNLESS OTHERWISE SPECIFIED		TITLE TIMING & STATES			
UNLESS OTHERWISE SPECIFIED		M728 KI-1			
UNLESS OTHERWISE SPECIFIED		SIZE CODE DCS M728-0-1			
UNLESS OTHERWISE SPECIFIED		REV. D			
UNLESS OTHERWISE SPECIFIED		SHEET 1 OF 4			
UNLESS OTHERWISE SPECIFIED		ETCH REV B C D			
UNLESS OTHERWISE SPECIFIED		DIST. 1 Pin			

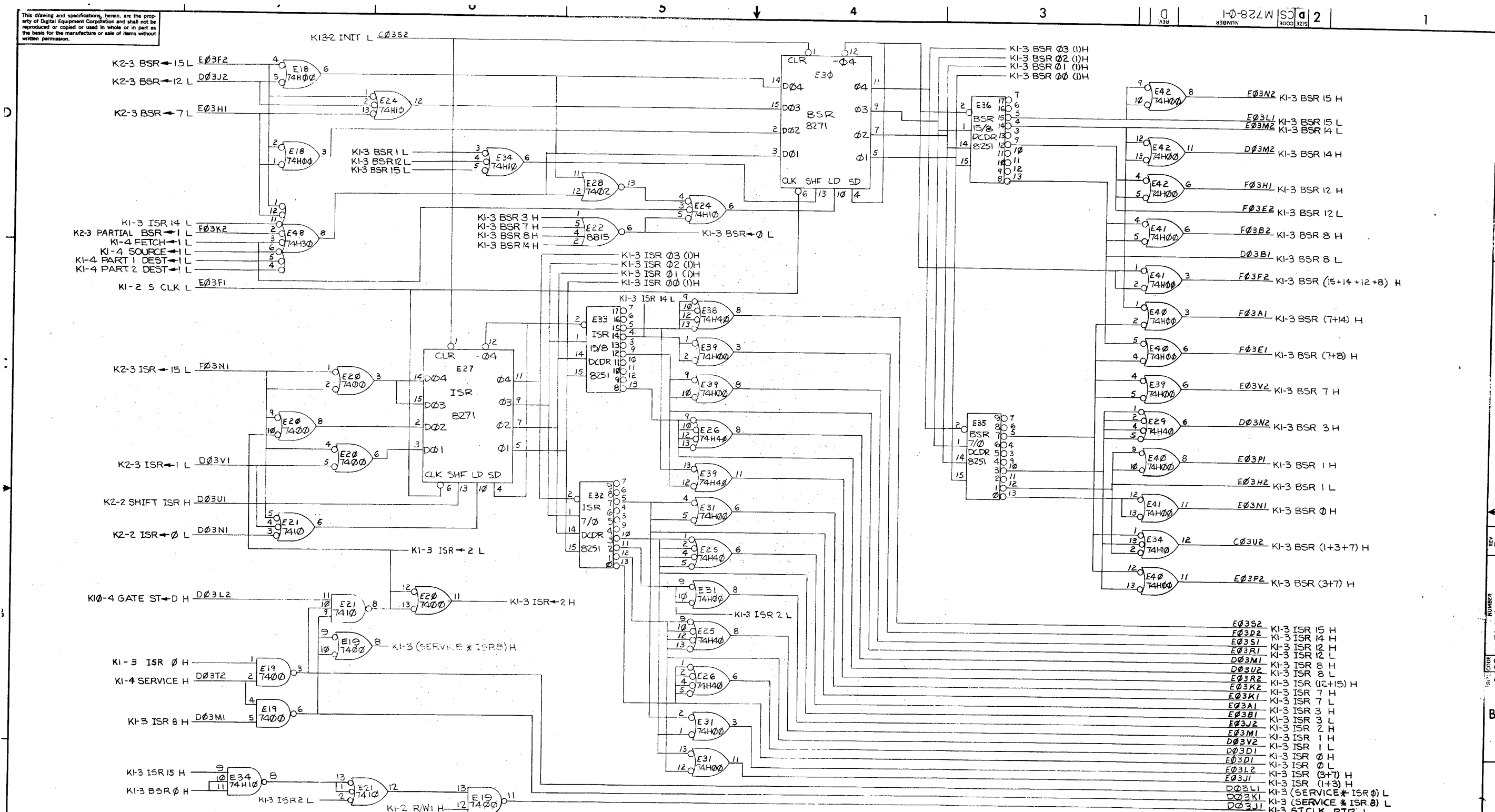


NOTE:
CLOCK PERIOD IS CONTROLLED BY R8.
NOMINAL ADJUSTMENT AT KI-2 CLK L
(PIN E03F1) IS FOR A PERIOD OF 280 NS.

FIRST USED ON OPTION MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP11					
UNLESS OTHERWISE SPECIFIED		DRN.	DATE	PARTS LIST	
UNLESS OTHERWISE SPECIFIED		CHK'D	DATE	digital EQUIPMENT CORPORATION	
DIMENSION IN INCHES		ENG.	DATE	MAYNARD, MASSACHUSETTS	
TOLERANCES		PROB.	DATE	TITLE	
DECIMALS FRACTIONS ANGLES		PROD.	DATE	TIMING & STATES	
± .005 ± .005 ± 1/16 ± 0.005				M728 KI-2	
MATERIAL		NEXT HIGHER ASSY		SIZE CODE NUMBER	
FINISH		A-ML-KAII-0		DCS M728-0-1	
		SCALE NONE		REV. DI	
		SHEET 2 OF 4		DIST.	

REV.	CHANGE NO.
1	1

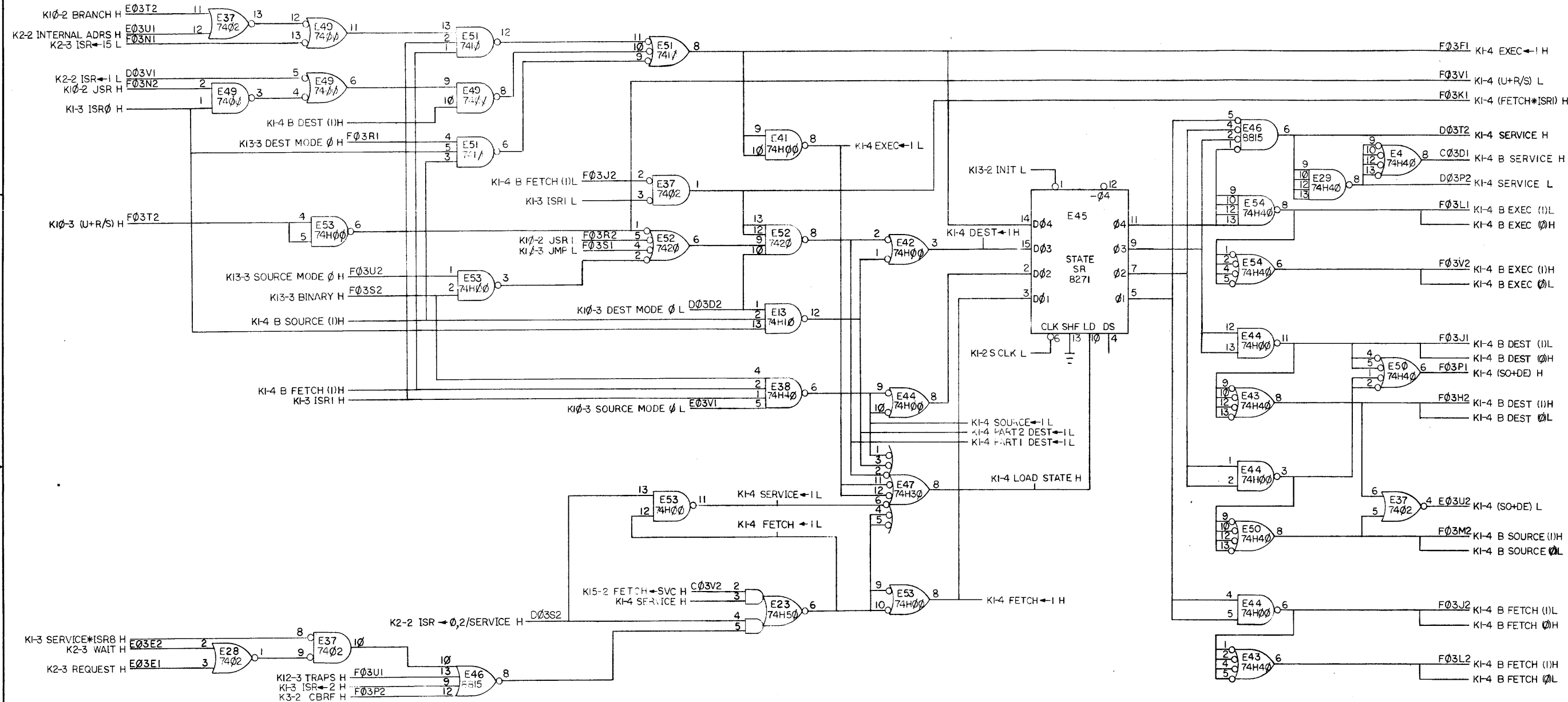
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DEC FORM NO.	
CHK	REV.
REVISIONS	
CHANGE NO.	

FIRST USED ON OPTION MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDPII					
UNLESS OTHERWISE SPECIFIED		PARTS LIST			
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES		DRN 1 PDHumb	DATE 12/2/69	digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS	
TOLERANCES		CHWD 1 PDHumb	DATE 12/2/70	TITLE	
DECIMALS	FRACTIONS	ENG 1 PDHumb	DATE 3-5-70	TIMING & STATES	
± .005	± 1/64 ± 0°30'	PROL ENG 1 PDHumb	DATE 3-5-70		
FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS		PROD.	DATE		
MATERIAL		NEXT HIGHER ASSY		M728 KI-3	
FINISH		A-ML-KAII-Ø		SIZE CODE NUMBER REV. D C S M728-Ø1 D.	
		SCALE NONE			
		SHEET 3 OF 4			

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REV.	CHANGE NO.
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
14	14
15	15
16	16
17	17
18	18
19	19
20	20

DEC FORM NO. 102A

FIRST USED ON OPTION/MODEL PDPII	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED: DIMENSION IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES ± .005 ± 1/64 ± 0°30' FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS	DRN CHKD ENG PROL ENG PROD.	DATE 12-11-69 3-18-70 3-5-70 3-5-70	PARTS LIST digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS TITLE TIMING & STATES M728 NEXT HIGHER ASSEMBLY A-ML-KAI-0 SCALE SHEET 4 OF 4	
MATERIAL			REV D	
FINISH			REV D	

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PARTS REFERENCE

ITEM NO.	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E1, E9, E18	DEC 74H53N	IC 1909062	3
2	E2, E5, E19, E21, E25, E27, E31, E36, E39, E45, E47	DEC 7402N	IC 1909004	11
3	E3, E28, E34, E40, E42	DEC 8815A	IC 1905713	5
4	E4, E10, E11, E16, E22, E24, E26, E37, E44, E49	DEC 7400N	IC 1905575	10
5	E6, E32	DEC 7400N	IC 1905577	2
6	E7, E17	DEC 74H60N	IC 1909064	2
7	E8, E29	DEC 7450N	IC 1905560	2
8	E12, E13, E14, E30, E35	DEC 7450N	IC 1905561	5
9	E15	DEC 7453N	IC 1905582	1
10	E33, E48	DEC 7410N	IC 1905576	2
11	E35	DEC 7474N	IC 1905547	1
12	E41	DEC 380	IC 1909485	1
13	E43	DEC 9601	IC 1909487	1
14	E46	DEC 8861	IC 1909705	1
15	C1-C39, C41, C43, C45-C47, C49-C51, C56	.01 MFD, 100V, 20%	CAP 1001610	48
16	C40	1000 MMF, 100V, 5%	CAP 1002344	1
17	C42	68 MMF, 100V, 5%	CAP 1000014	1
18	C44, C48	330 MMF, 100V, 5%	CAP 1000023	2
19	C52-C55	6.8 MFD 35V, 20%	CAP 1000067	4
20	R1, R6	47, 1/4W, 5%	RES 1300202	2
21	R2	5.6 K, 1/4W, 5%	RES 1301674	1
22	R3, R7	470, 1/4W, 5%	RES 1300316	2
23	R4	220, 1/4W, 5%	RES 1300271	1
24	R5	1000, 1/4W, 5%	RES 1300365	1
25	D1	D664 DIODE	1100114	1

NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.

2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPARATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.

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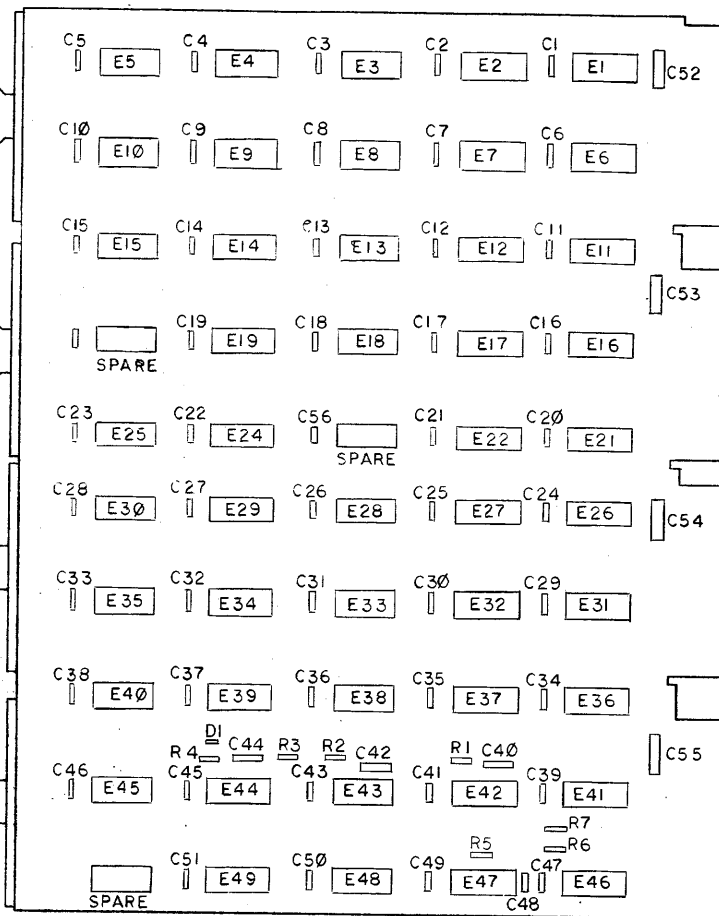
4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.

5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

6. UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICOFARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01 MFD.

COMPONENT PLACEMENT



PIN NOMENCLATURE

MODULE PROCESSOR

A

C

B

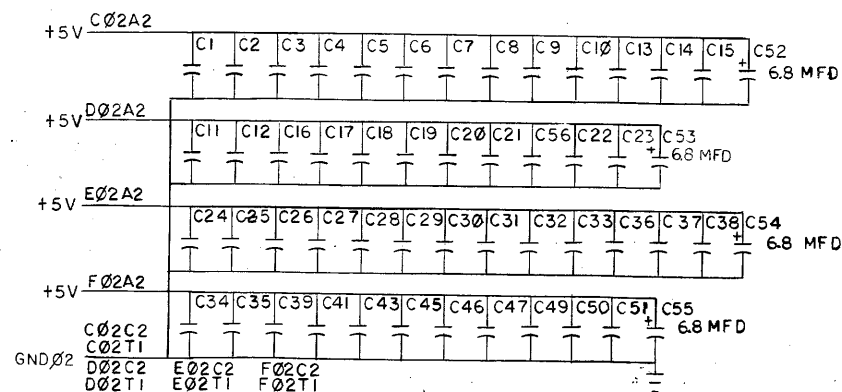
D

C

E

D

F

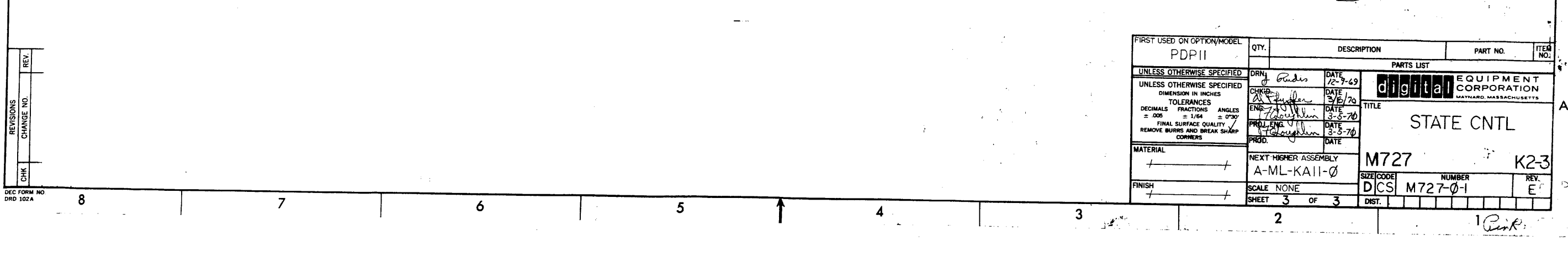


FIRST USED ON OPTION MODEL		QTY.	DESCRIPTION		PART NO.	ITEM NO.
PDP 11						
UNLESS OTHERWISE SPECIFIED		DATE	PARTS LIST			
UNLESS OTHERWISE SPECIFIED		DATE	digital EQUIPMENT CORPORATION			
DIMENSION IN INCHES		DATE	MAYNARD, MASSACHUSETTS			
TOLERANCES		DATE	TITLE			
DECIMALS FRACTIONS ANGLES		DATE	STATE CNTL			
± .005 ± 1/64 ± .030		DATE	M727 K2-1			
FINAL SURFACE QUALITY		DATE	SIZE CODE NUMBER			
REMOVE BURRS AND BREAK SHARP CORNERS		DATE	DCS M727-0-1			
MATERIAL		DATE	ETCH REV B C D			
NEXT HIGHER ASSY		DATE	DIST.			
SCALE		DATE	1			
SHEET		DATE	OF 3			

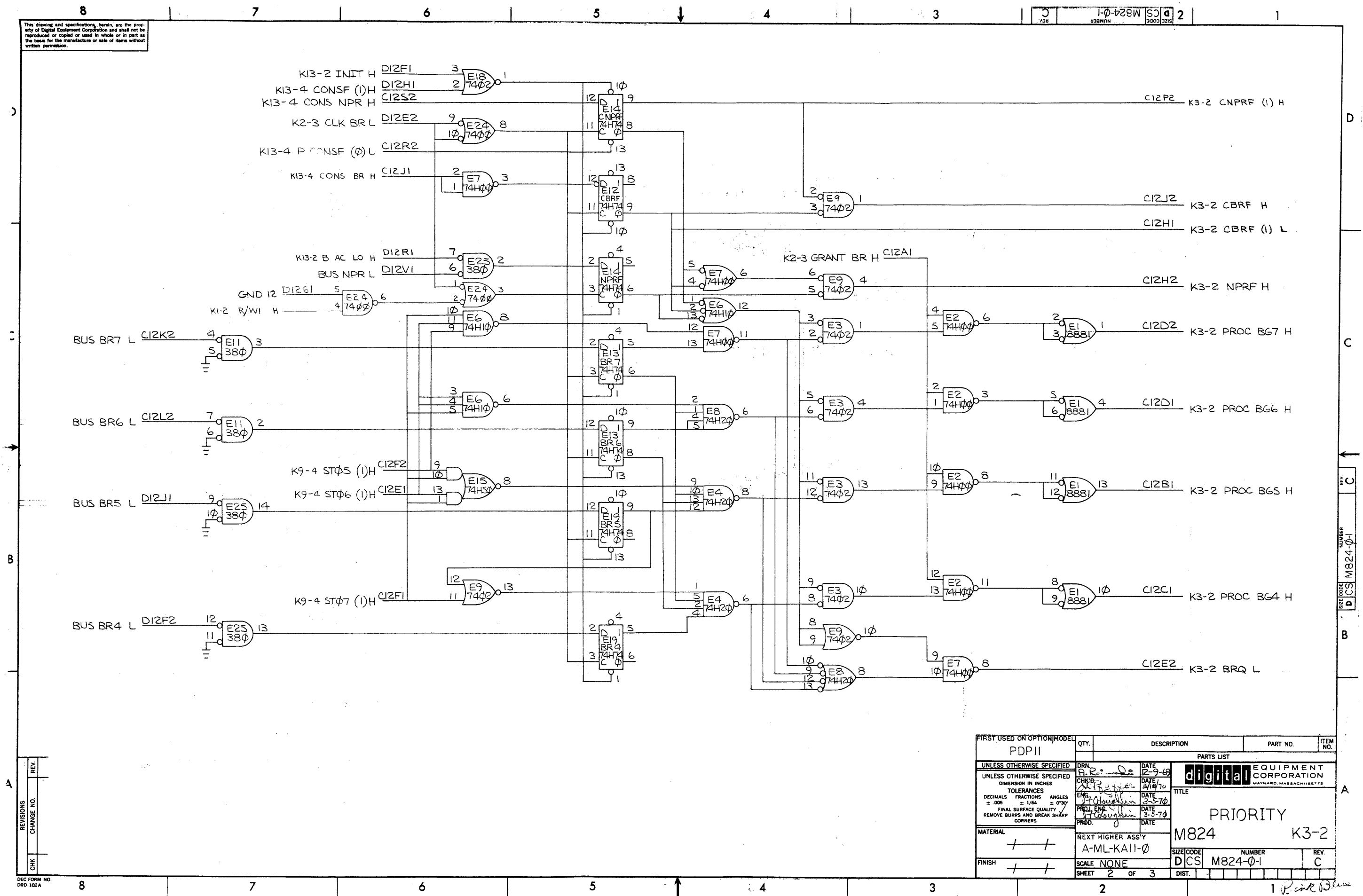
REV.	CHANGE NO.	DATE	BY	CHKD.
B	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
A	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
C	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
D	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
E	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
F	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
G	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
H	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
I	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
J	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
K	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
L	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
M	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
N	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
O	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
P	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
Q	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
R	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
S	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
T	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
U	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
V	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
W	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
X	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
Y	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN
Z	1	10/10/70	J. O'LOUGHLIN	J. O'LOUGHLIN

[illegible]

FIRST USED ON OPTION/MODEL PDP11		QTY.	DESCRIPTION		PART NO.		ITEM NO.
UNLESS OTHERWISE SPECIFIED		DRN	PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES		CHK'D	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			
TOLERANCES		ENG	DATE	TITLE			
DECIMALS FRACTIONS ANGLES ± .005 ± 1/64 ± 93°		PROL ENG	DATE	STATE CNTL			
FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS		PROD.	DATE				
MATERIAL		NEXT HIGHER ASSEMBLY		M727		K2-2	
+ +		A-ML-KAII-Ø		SIZE/CODE		NUMBER	REV.
FINISH		SCALE NONE		D/C		M 727-Ø-1	E
+ +		SHEET 2 OF 3		DIST.			



FIRST USED ON OPTION/MODEL PDP11		QTY.	DESCRIPTION		PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED		DRN. <i>J. Gades</i>	DATE <i>12-7-69</i>	PARTS LIST		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES = .000 = 1/64 = 0°00'		CHKD. <i>J. Gades</i>	DATE <i>3/6/70</i>	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		
FINAL SURFACE QUALITY / REMOVE BURRS AND BREAK SHARP CORNERS		ENG. <i>J. Gades</i>	DATE <i>6-6-70</i>	TITLE STATE CNTL		
MATERIAL		PROL. ENG. <i>J. Gades</i>	DATE <i>3-3-70</i>	M727 K2-3		
+ + +		PROD. <i>J. Gades</i>	DATE	NEXT HIGHER ASSEMBLY A-ML-KA11-0		
FINISH + + +		SCALE NONE		SIZE CODE D10C		NUMBER M727-0-1
SHEET 3 OF 3		REV. E		DIS.		



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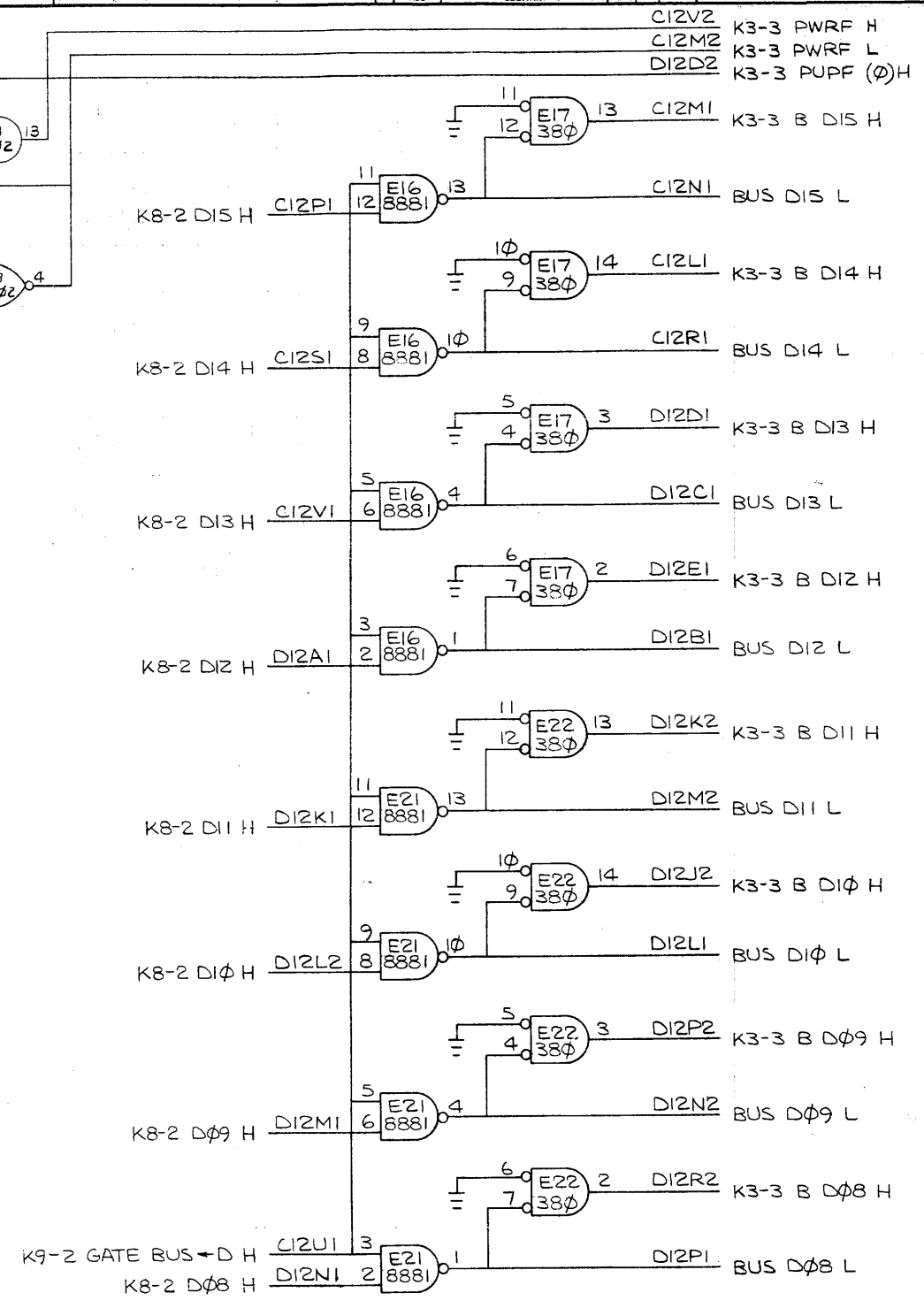
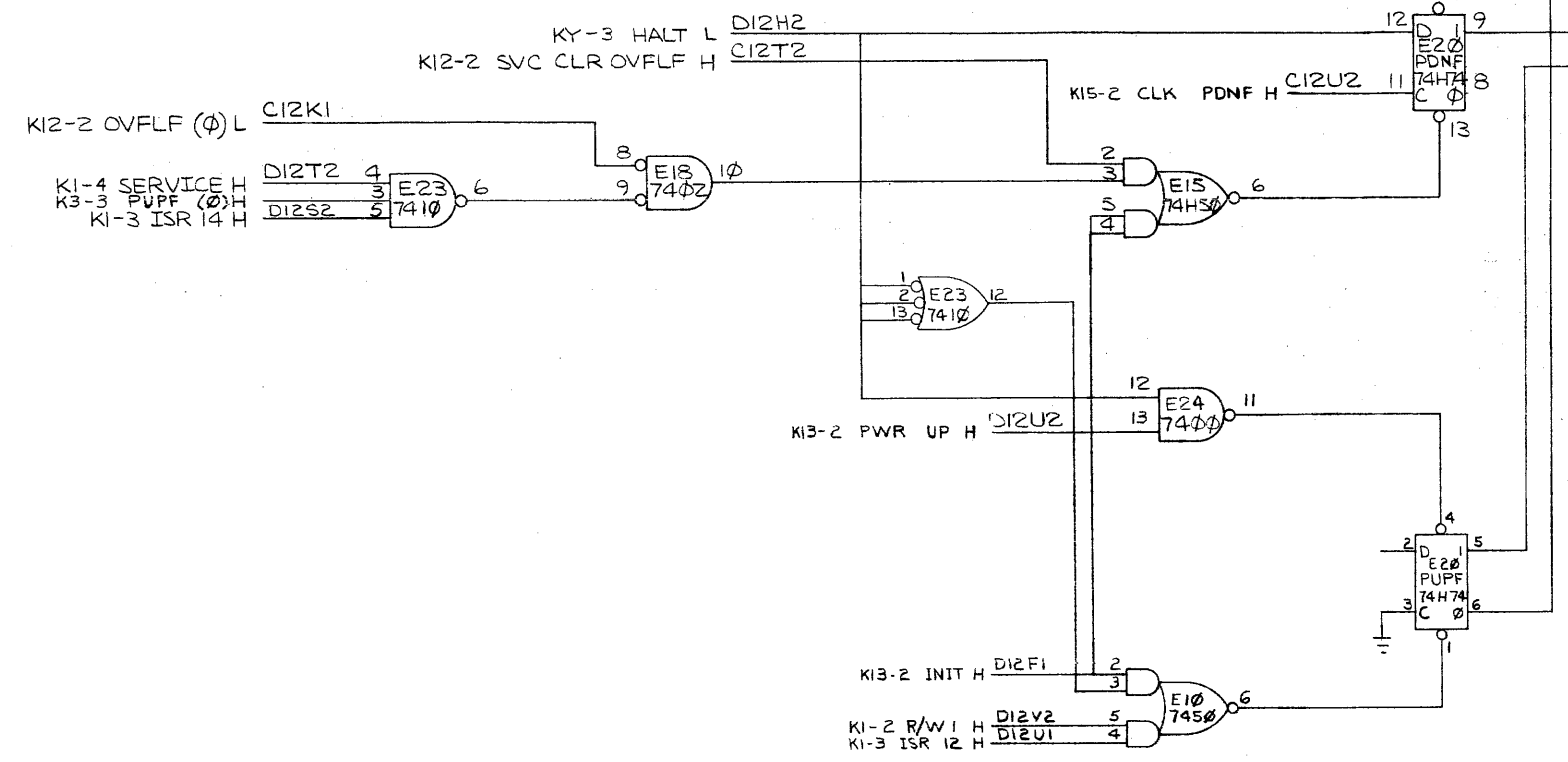
D

3

A

REV	CHANGE NO

DEC FORM NO
DRD 102A



FIRST USED ON OPTION/MODEL PDP11	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED				
DRN. A. Raimondo DATE 12-7-69				
CHK'D. DATE 3/18/70				
TOLERANCES				
DECIMALS FRACTIONS ANGLES				
± .005 ± 1/64 ± 0°30'				
FINAL SURFACE QUALITY				
REMOVE BURRS AND BREAK SHARP CORNERS				
MATERIAL				
FINISH				
NEXT HIGHER ASSY				
A-ML-KA11-0				
SCALE NONE				
SHEET 3 OF 3				
PARTS LIST				
digital EQUIPMENT CORPORATION				
MAYNARD, MASSACHUSETTS				
TITLE				
PRIORITY				
M824 K3-3				
SIZE CODE				
D/CS M824-0-1				
NUMBER				
REV				
C				

1 Pink Blue

PARTS REFERENCE

[illegible]

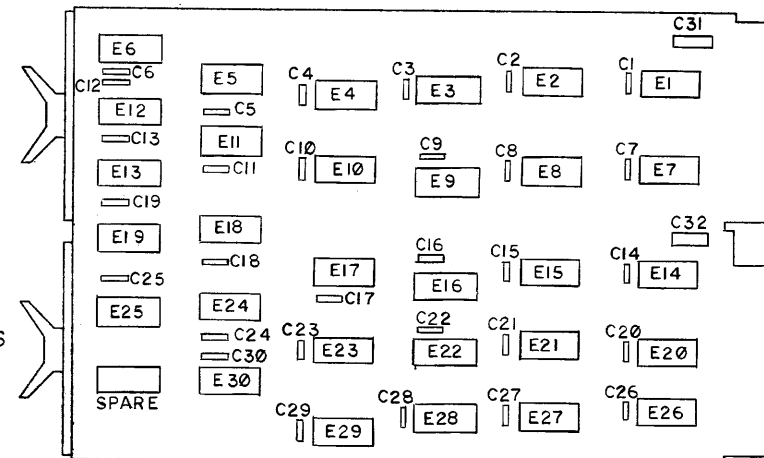
NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.
2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPARATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.
3. PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.
4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.
5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

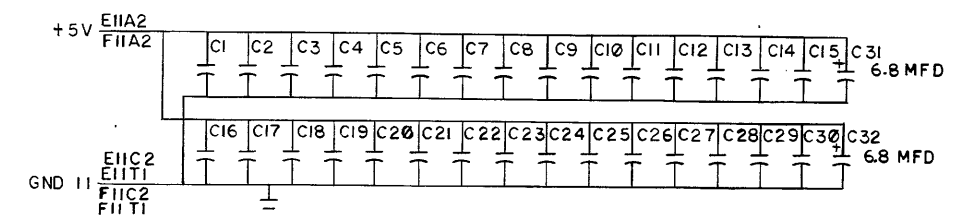
6. UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS;
CAPACITANCE IS IN PICO FARADS. CAPACITORS WITHOUT
ANY NOTED VALUES ARE .01 MFD.

COMPONENT PLACEMENT.



PIN NOMENCLATURE

MODULE	PROCESSOR
A	E
B	F



FIRST USED ON OPTION/MODEL PDP 11		QTY.	DESCRIPTION		PART NO.		ITEM NO.
UNLESS OTHERWISE SPECIFIED		DRN	PARTS LIST				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES = .005 ± 1/64 = 0°30' = FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS		DRN <i>1. Co Blume</i>	DATE <i>5/28/70</i>	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			
MATERIAL		CHND	DATE	TITLE REGISTER CNTL M821 K4-1			
FINISH		ENG	DATE				
		PROJ ENG	DATE				
		PROD	DATE				
NEXT HIGHER ASSY		A-ML-KAII-Ø SIZE CODE NUMBER REV.					
SCALE		D CS M821-Ø-1 B					
SHEET		E C REV B C					
		OF 3					

DIST.						1 Pink
-------	--	--	--	--	--	--------

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D

C

B

D

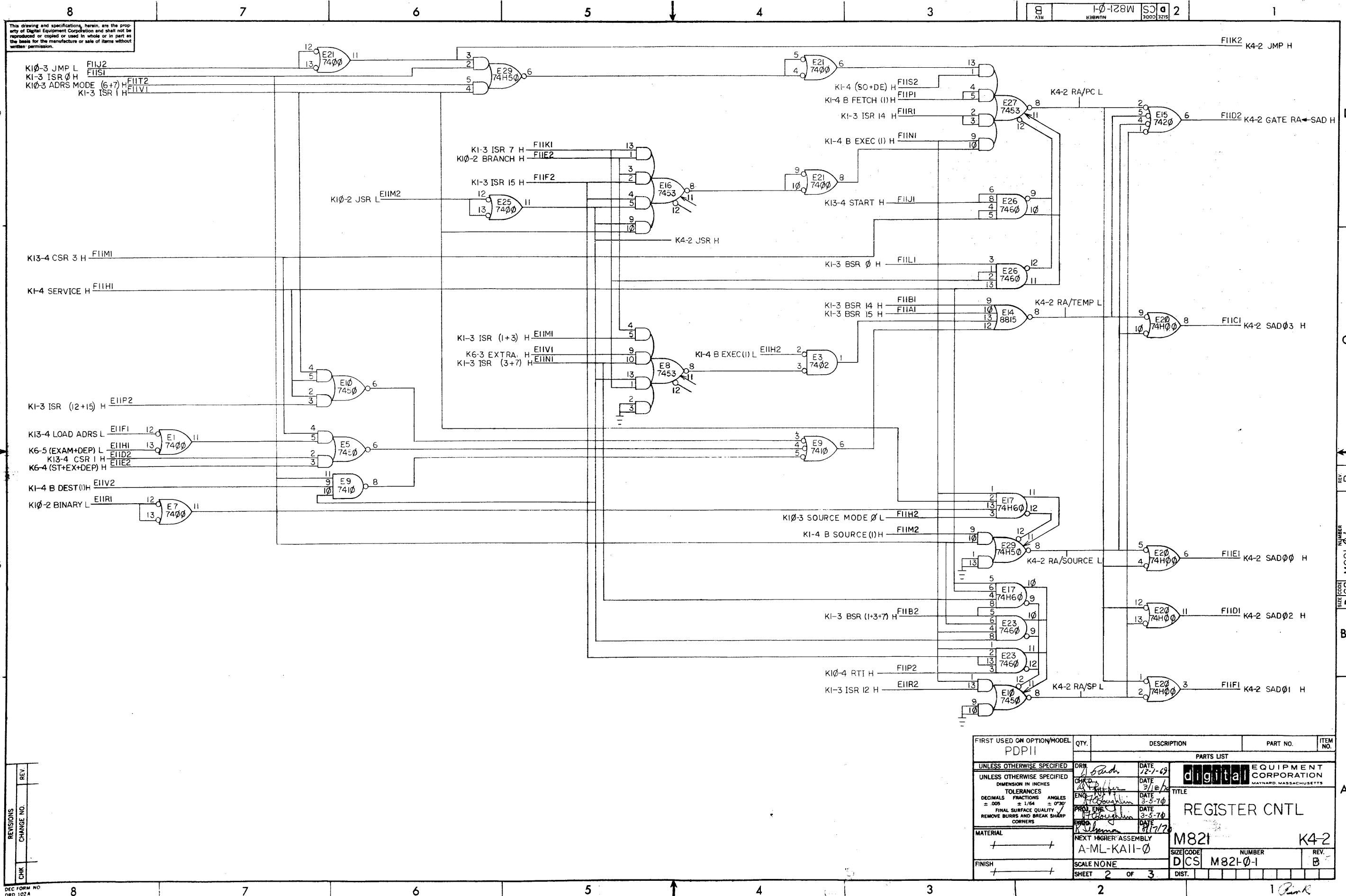
C

B

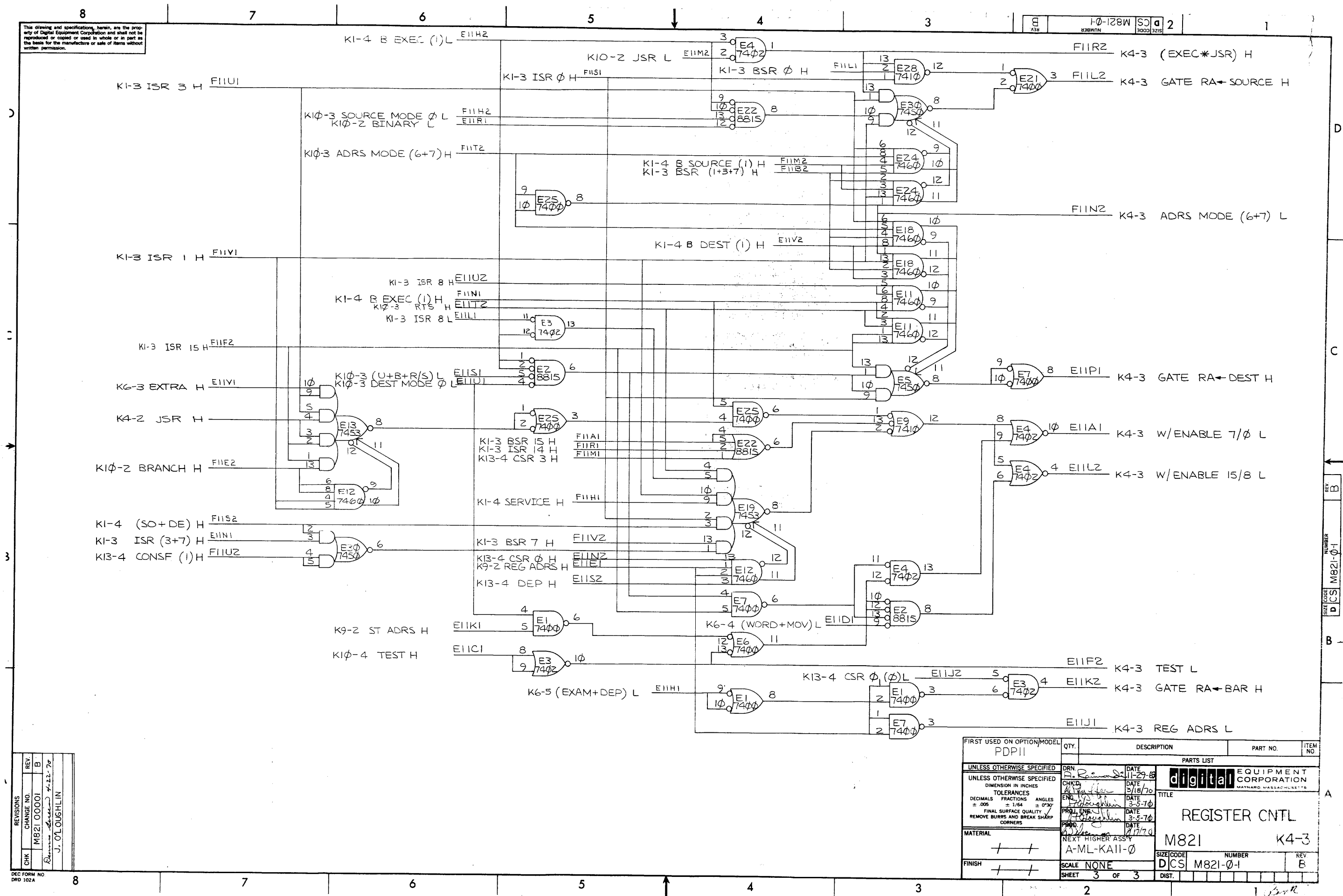
A

REV	CHANGE NO.
1	1

DEC FORM NO
ORD 1024



FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP11				
UNLESS OTHERWISE SPECIFIED	OR	DATE	PARTS LIST	
UNLESS OTHERWISE SPECIFIED	CHKD	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
TOLERANCES	ENG	DATE	TITLE	
DECIMALS FRACTIONS ANGLES	PROJ ENG	DATE	REGISTER CNTL	
± .005 ± 1/64 ± 0°30'	PROJ	DATE	M821 K4-2	
FINAL SURFACE QUALITY	DATE	SIZE CODE NUMBER REV.		
REMOVE BURRS AND BREAK SHARP CORNERS	DATE	DICS M821-0-1 B		
MATERIAL	NEXT HIGHER ASSEMBLY	SCALE NONE		
FINISH	A-ML-KAII-0	SHEET 2 OF 3		



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PARTS REFERENCE

ITEM NO	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E1,E2,E8,E9,E19,E20,E27,E28	DEC 7402N	I.C. 1905004	8
2	E3,E4,E5,E6,E12,E13,E14,E15,E23,E24,E25,E26,E29,E30,E31,E32	7481N	I.C. 1909714	16
3	E7,E11,E18,E22	DEC 74103N	I.C. 1909862	4
4	E10,E21	DEC 8851	I.C. 1909725	2
5	E16	DEC 741001	I.C. 1908255	1
6	E17	DEC 7440N	I.C. 1905579	1
7	Q1,Q2,Q3,Q4,Q5,Q6,Q7,Q8	DEC 3009B TRANSISTOR	1503100	8
8	R1,R4,R7,R10,R13,R16,R19,R22	330,1/4W 5%	RES. 1300295	8
9	R3,R6,R9,R12,R15,R18,R21,R24	1.5 K,1/4W 5%	RES. 1300391	8
10	R25-R40	1 K,1/4W 5%	RES. 1300365	16
11	R41-R56	3 K,1/4W 5%	RES. 1300432	16
12	C1-C40	.01 MFD,100V,20%	CAP. 1001610	40
13	C41,C42	6.8 MFD,35V,20%	CAP. 1000067	2
14	R2,R5,R8,R11,R14,R17,R20,R23	100,1/4W,5%	RES. 1300229	8

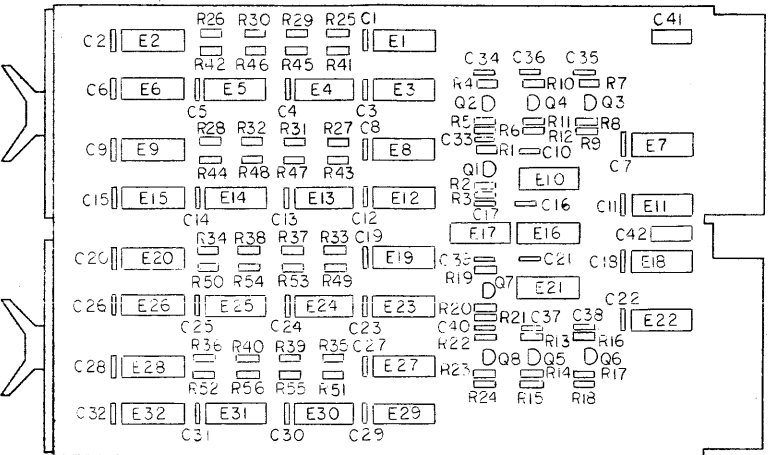
NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.
2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPERATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.
3. PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.
4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE, PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.
5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

6. UNLESS OTHERWISE NOTED RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICOFARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01 MFD.

COMPONENT PLACEMENT



PIN NOMENCLATURE

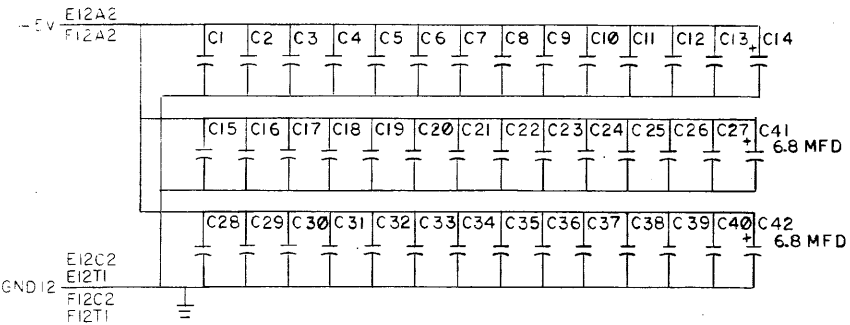
MODULE PROCESSOR

A

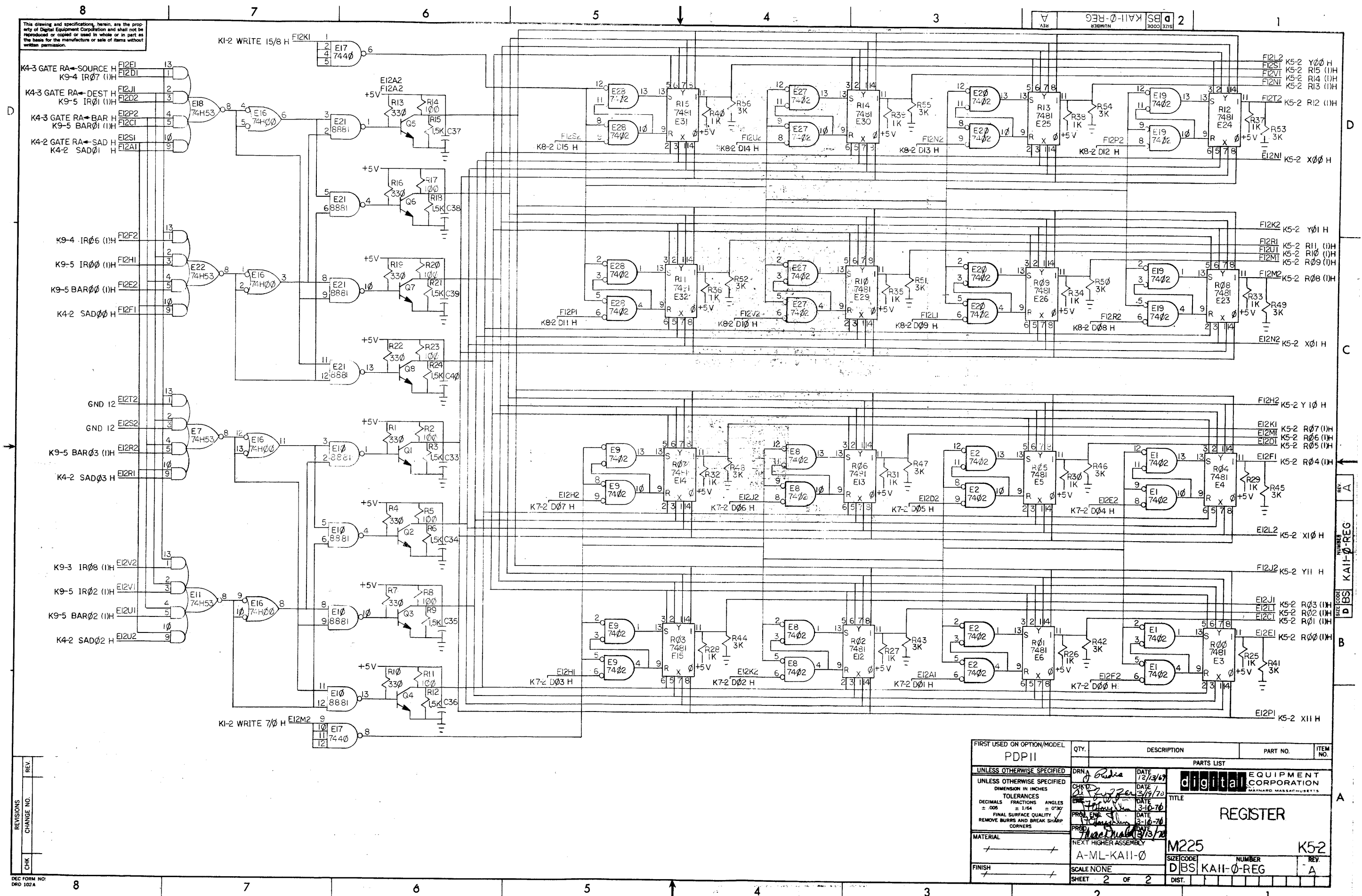
E

B

F



FIRST USED	USED	OPTION	MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
			PDP 11				
PARTS LIST							
UNLESS OTHERWISE SPECIFIED							
DRAWN BY: <i>W. J. Blane</i> DATE: <i>3-6-70</i>							
CHECKED BY: <i>W. J. Blane</i> DATE: <i>3-6-70</i>							
ENGINEER: <i>W. J. Blane</i> DATE: <i>3-6-70</i>							
PROJ. ENG: <i>W. J. Blane</i> DATE: <i>3-6-70</i>							
PROJ. MGR: <i>W. J. Blane</i> DATE: <i>3-6-70</i>							
MATERIAL: <i>W. J. Blane</i> DATE: <i>3-6-70</i>							
FINISH: <i>W. J. Blane</i> DATE: <i>3-6-70</i>							
SCALE: <i>W. J. Blane</i> DATE: <i>3-6-70</i>							
SHEET: <i>W. J. Blane</i> DATE: <i>3-6-70</i>							
NEXT HIGHER ASSY: <i>W. J. Blane</i> DATE: <i>3-6-70</i>							
A-ML-KAI-Ø							
REGISTER							
M225 K5-1							
SIZE CODE: D BS							
NUMBER: KAI-Ø-REG							
REV: A							



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PARTS REFERENCE				
ITEM NO.	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	F1, F2, F3, F4, F5, F6, F7, F8, F9, F10, F11, F12, F13, F14, F15, F16, F17, F18, F19, F20, F21, F22, F23, F24, F25, F26, F27, F28, F29, F30, F31, F32, F33, F34, F35, F36, F37, F38, F39, F40, F41, F42, F43, F44, F45, F46, F47, F48, F49, F50, F51, F52, F53, F54, F55, F56, F57, F58, F59, F60	DEC 7400N	IC 1905575	14
2	F2, F12, F24, F25, E52, E54	DEC 7410N	IC 1905576	6
3	F3, F10, F37, F40, F42, F47, F49, F50	DEC 7450N	IC 1905580	8
4	F4, F6, F21, E61	DEC 8815A	IC 1905713	4
5	F5, F13, F17, F32, F33, F45, F53, E56	DEC 7402N	IC 1905004	8
6	F7, F23, E26, E57	DEC 7440N	IC 1905579	4
7	F9	DEC 7453N	IC 1905582	1
8	F11	DEC 7420N	IC 1905577	1
9	F14, F15, F38, F41, F43, F46, F48, F51	DEC 7460N	IC 1905581	8
10	F19	DEC 7453N	IC 1905062	1
11	F20	DEC 74H00N	IC 1905056	1
12	F28, F29	DEC 7474N	IC 1905547	2
13	F31	DEC 7401N	IC 1905590	1
14	F35	DEC 74H30N	IC 1905059	1
15	F60	DEC 74H40N	IC 1905586	1
16	C1-C54	.01 MFD 100V 20%	CAP 1001610	54
17	C55-C58	6.8 MFD 35V 20%	CAP 1000067	4
18	C59	270 MMF 100V 5%	CAP 1000022	1
19	C60, C61, C62	330 MMF 100V 5%	CAP 1000023	3
20	R1	1000 OHMS 1/4W 5%	RES 1300365	1
21	R2	47 OHMS 1/4W 5%	RES 1300202	1
22	R3, R5	470 OHMS 1/4W 5%	RES 1300316	2
23	R4, R6, R8	220 OHMS 1/4W 5%	RES 1300271	3
24	R7	560 OHMS 1/4W 5%	RES 1301890	1
25	D1, D2, D3	D664 DIODE	1100114	3

NOTES:

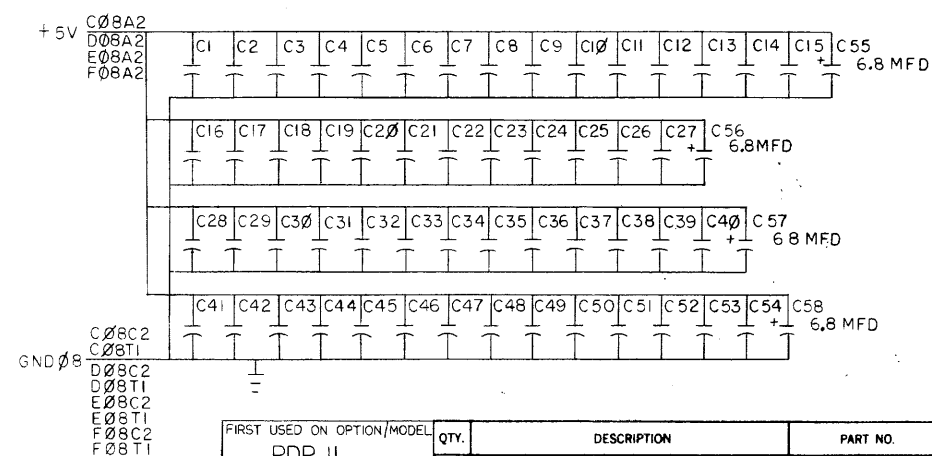
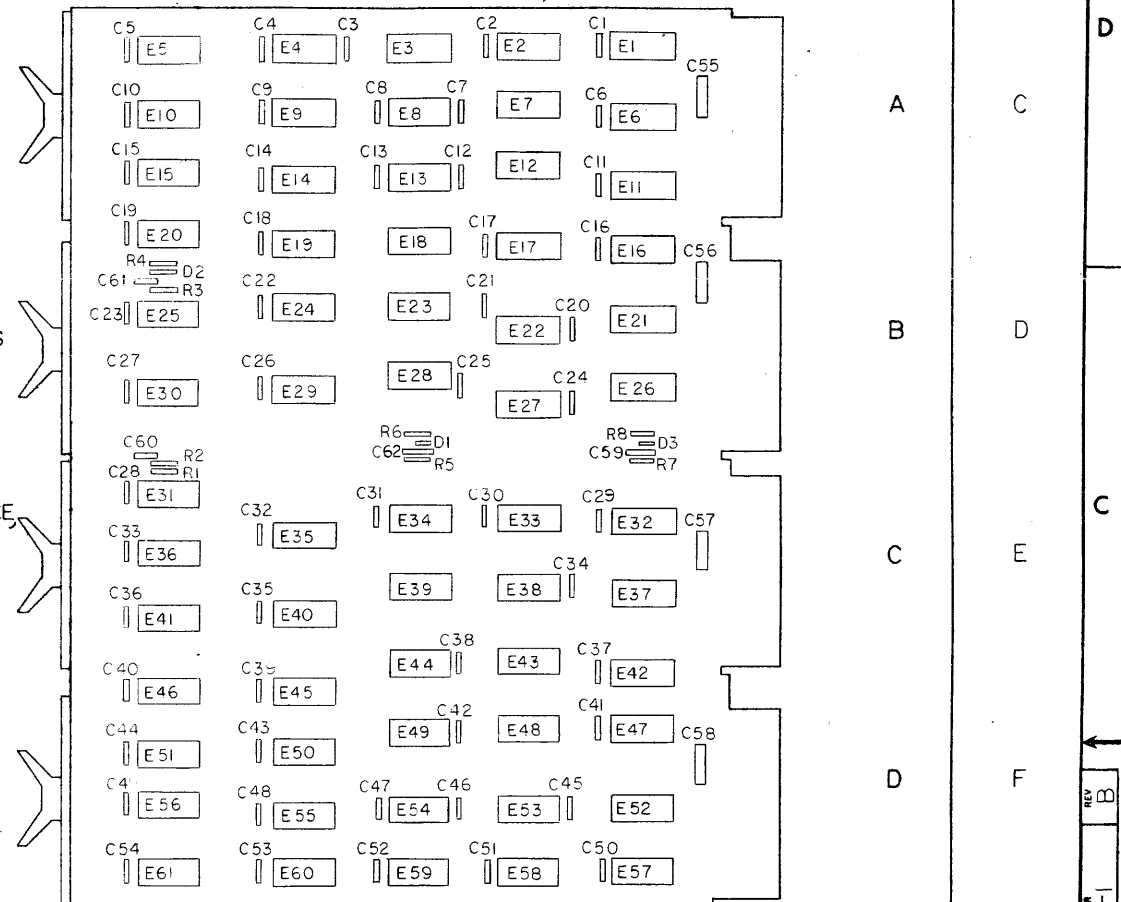
- PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.
- ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPERATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.
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- DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.
- GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7401	PIN 10	PIN 4
DEC 7402	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

- UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICO FARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01 MFD.

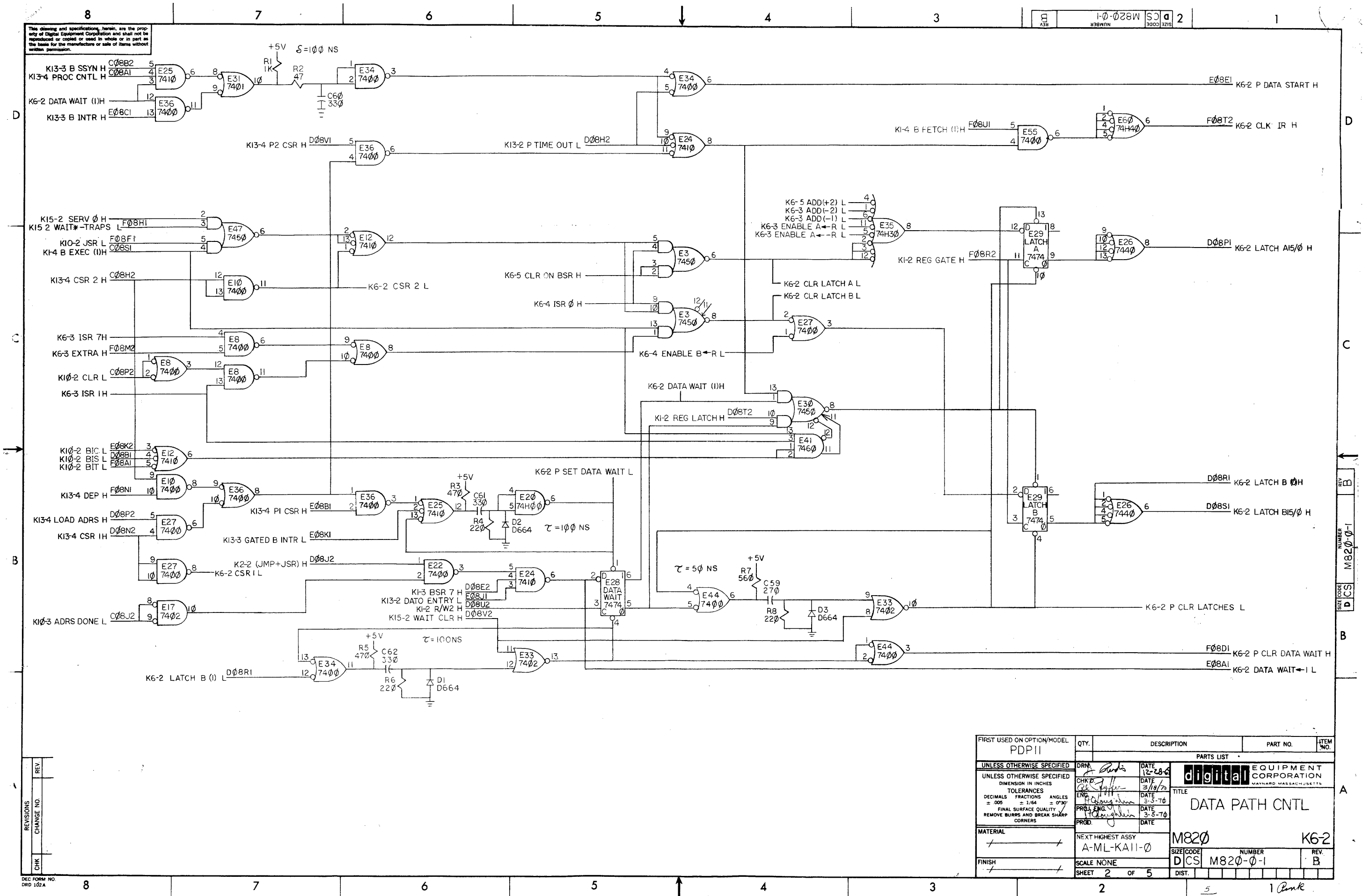
COMPONENT PLACEMENT

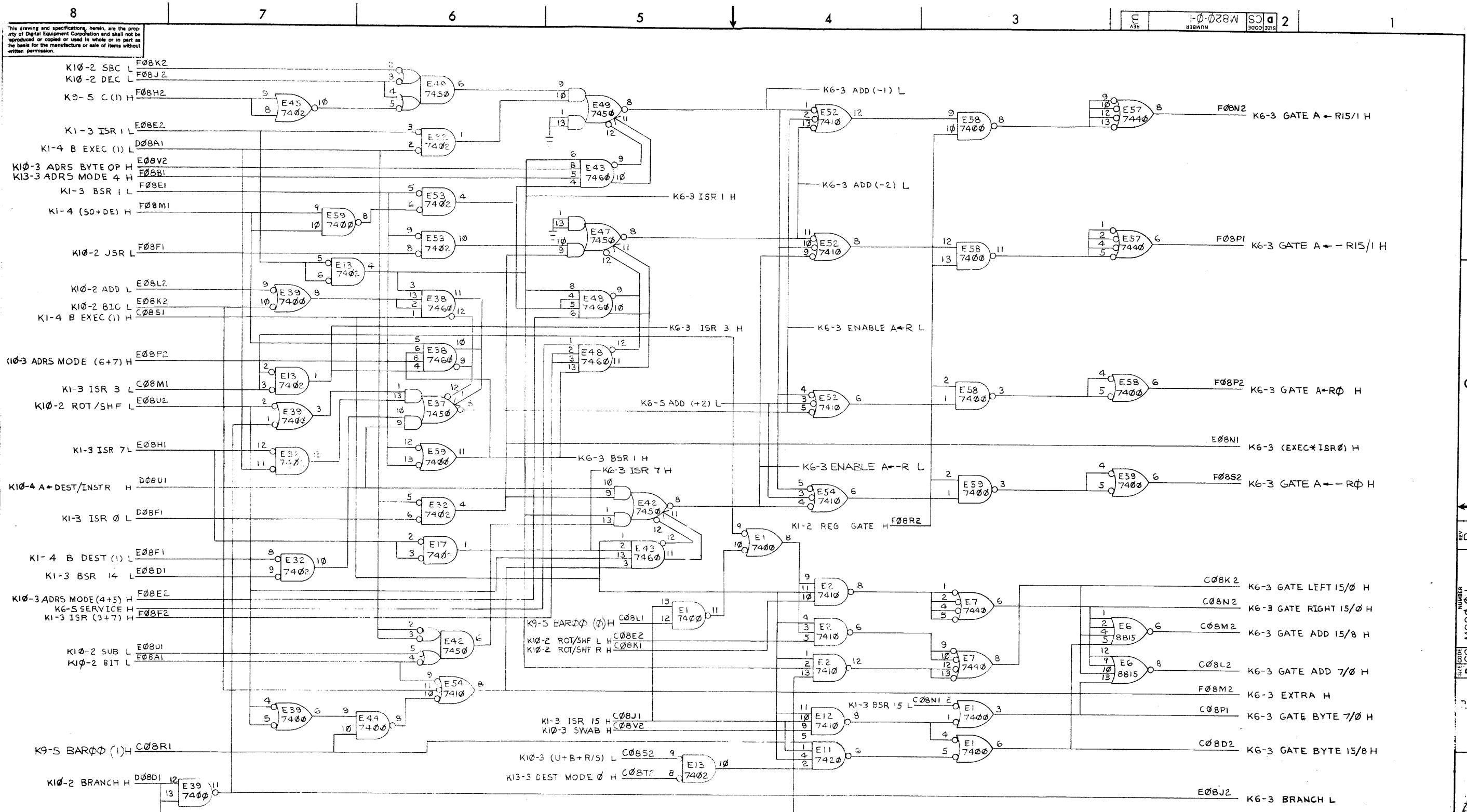
PIN NOMENCLATURE



FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11					
UNLESS OTHERWISE SPECIFIED		DRN	PARTS LIST		
UNLESS OTHERWISE SPECIFIED		CHK'D	digital EQUIPMENT CORPORATION		
DIMENSION IN INCHES		DATE	MAYNARD, MASSACHUSETTS		
TOLERANCES		DATE	TITLE		
DECIMALS FRACTIONS ANGLES		DATE	DATA PATH CNTL		
± .005 ± 1/64 ± 0°30'		DATE	M820 K6-1		
FINAL SURFACE QUALITY		DATE	SIZE CODE NUMBER		
REMOVE BURRS AND BREAK SHARP CORNERS		DATE	DCS M820-0-1		
MATERIAL		DATE	REV. B		
NEXT HIGHER ASSY		DATE	SCALE		
A-ML-KAI1-0		DATE	SHEET 1 OF 5		
FINISH		DATE	ETCH REV. B C		

DIST. 344 454 435 1 Pin

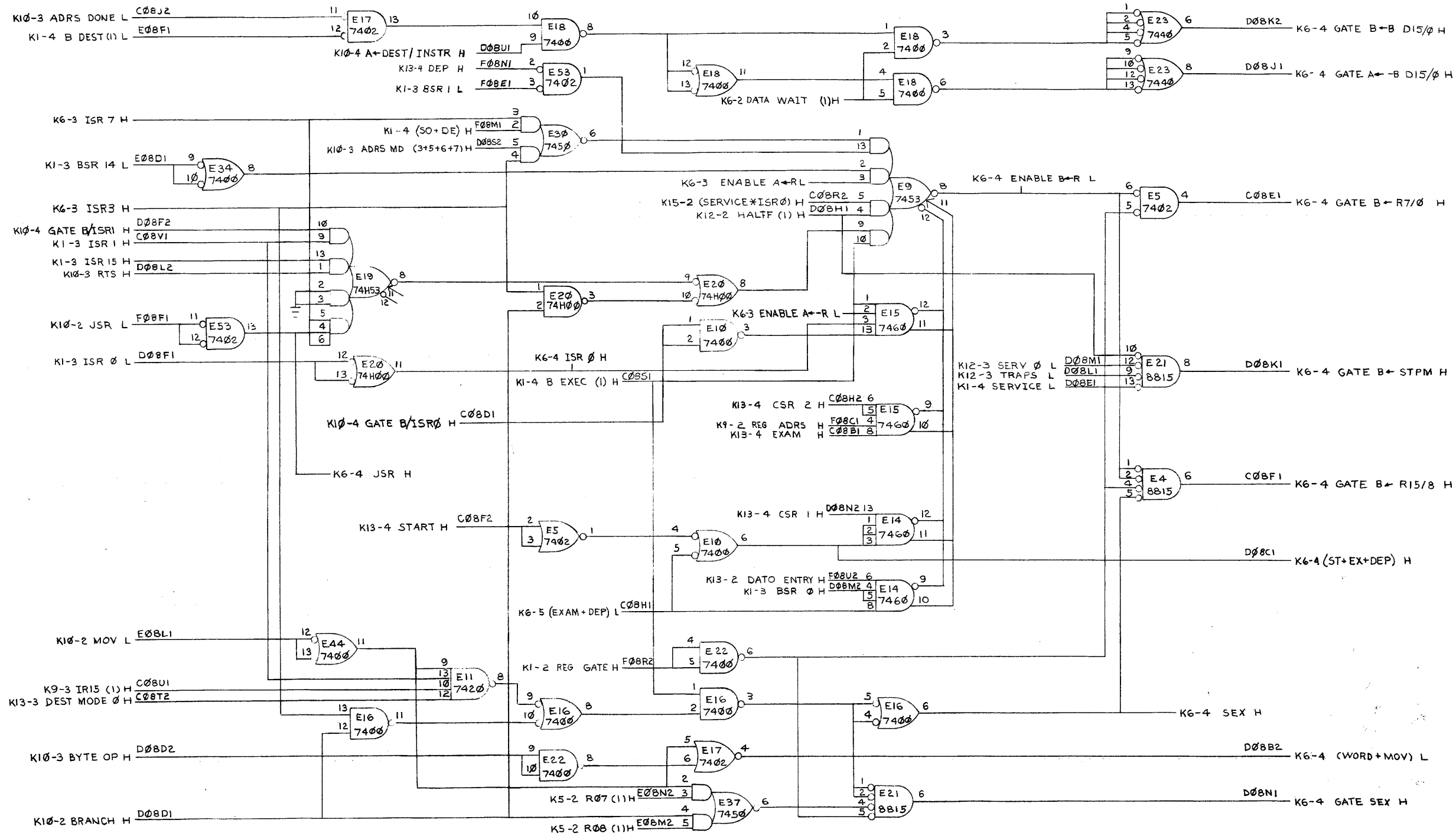




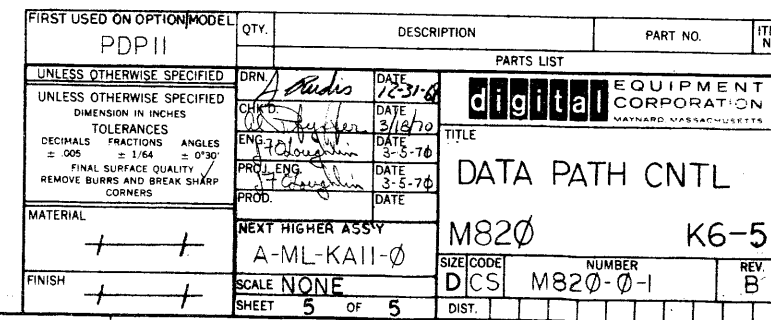
REV.	CHANGE NO.	CHK.

FIRST USED ON OPTION/MODEL PDPII		QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED		PARTS LIST			
UNLESS OTHERWISE SPECIFIED		digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			
DIMENSION IN INCHES		TITLE DATA PATH CNTL			
TOLERANCES		M820 K6-3			
DECIMALS FRACTIONS ANGLES		SIZE/CODE NUMBER REV			
± .005 ± 1/64 ± 0°30'		DICS M820-0-1 B			
FINAL SURFACE QUALITY		SHEET 3 OF 5			
REMOVE BURRS AND BREAK SHARP CORNERS		DIST.			
MATERIAL ++		NEXT HIGHER ASSY A-ML-KA11-0			
FINISH ++		SCALE NONE			

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FIRST USED ON OPTION/MODEL		QTY.		DESCRIPTION		PART NO.		ITEM NO.	
PDPII						PARTS LIST			
UNLESS OTHERWISE SPECIFIED		DRN.		DATE		digital EQUIPMENT CORPORATION MAYNARD MASSACHUSETTS			
UNLESS OTHERWISE SPECIFIED		CHKD.		DATE					
DIMENSION IN INCHES		DATE		DATE		TITLE			
TOLERANCES		DATE		DATE					
DECIMALS FRACTIONS ANGLES		ENR.		DATE		DATA PATH CNTL			
± .005 ± 1/64 ± 0°30'		DATE		DATE					
FINAL SURFACE QUALITY		PROJ. ENR.		DATE					
REMOVE BURRS AND BREAK SHARP CORNERS		DATE		DATE					
MATERIAL		PROD.		DATE		M820 K6-4			
++									
		NEXT HIGHER ASSY				SIZE CODE NUMBER REV			
		A-ML-KAII-0							
FINISH		SCALE		D/C'S		M820-0-1		B-1	
++		NONE		SHEET		4		OF 5	
				DIST.					



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PARTS REFERENCE

[illegible]

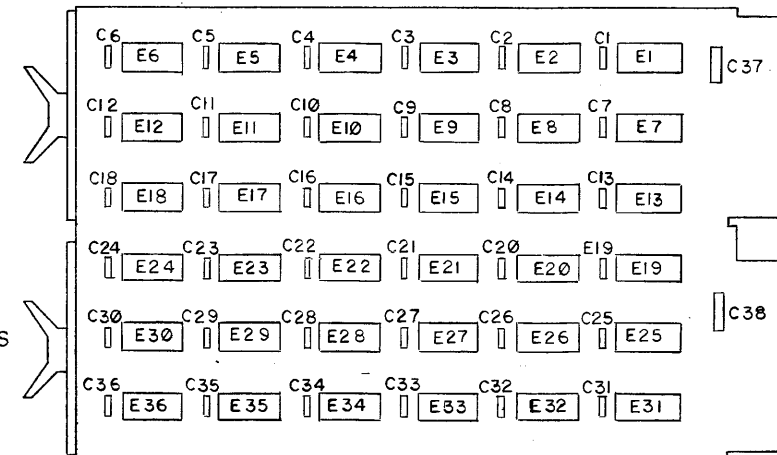
NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.
2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPERATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. DATA FLOW IS FROM THE BOTTOM TO THE TOP.
3. PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.
4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE, PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.
5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

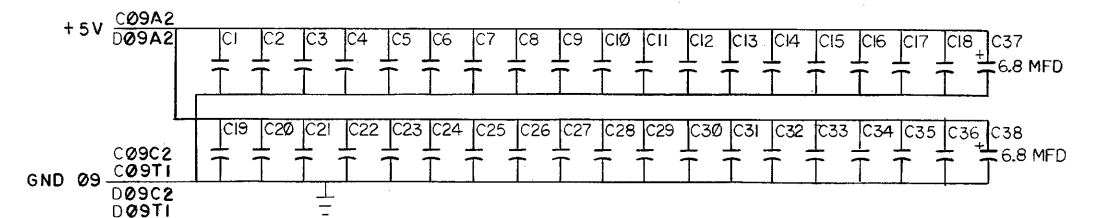
- 6 UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS;
CAPACITANCE IS PICOFARADS. CAPACITORS WITHOUT
ANY NOTED VALUES ARE .01 MFD.

COMPONENT PLACEMENT

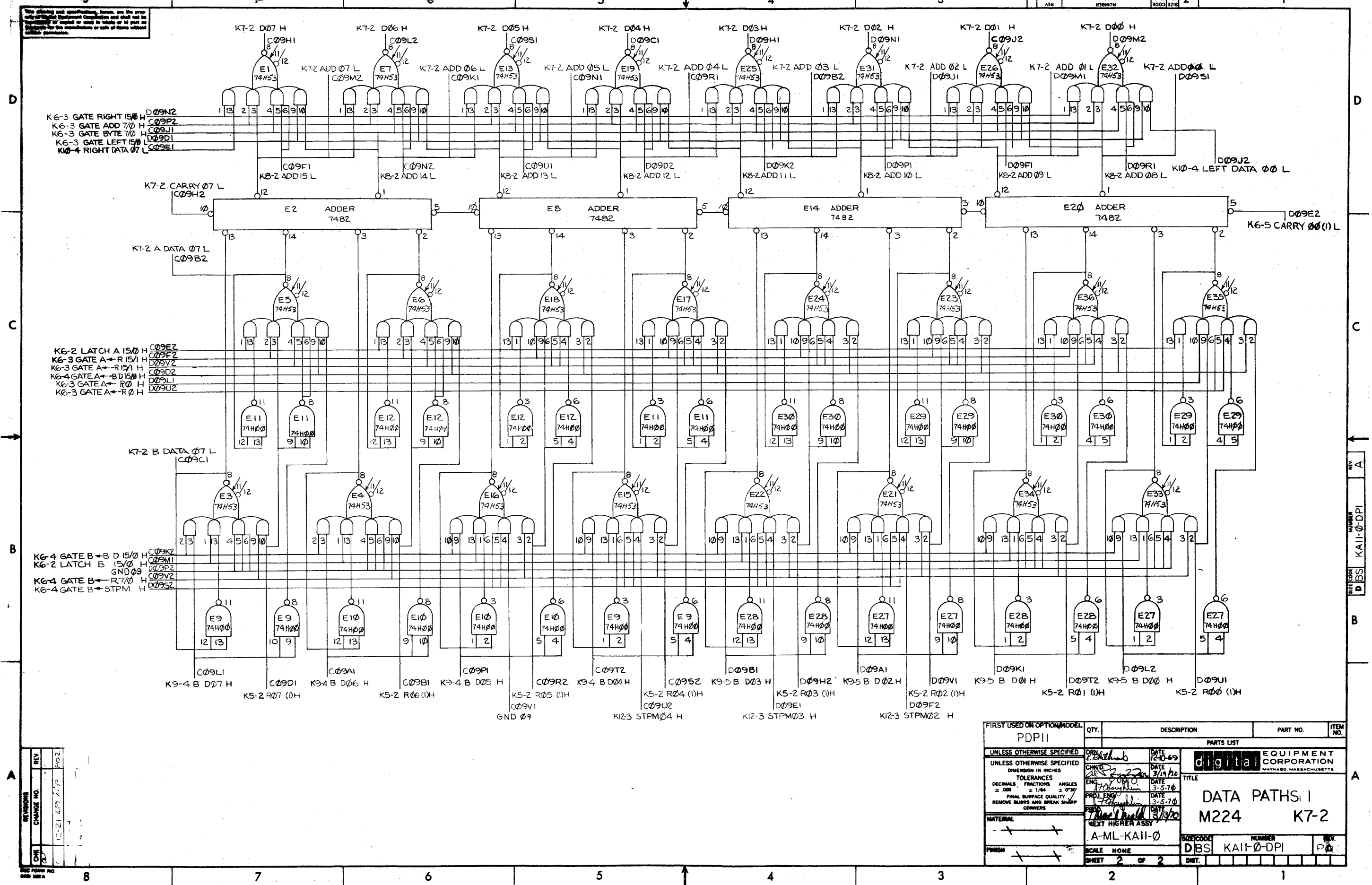


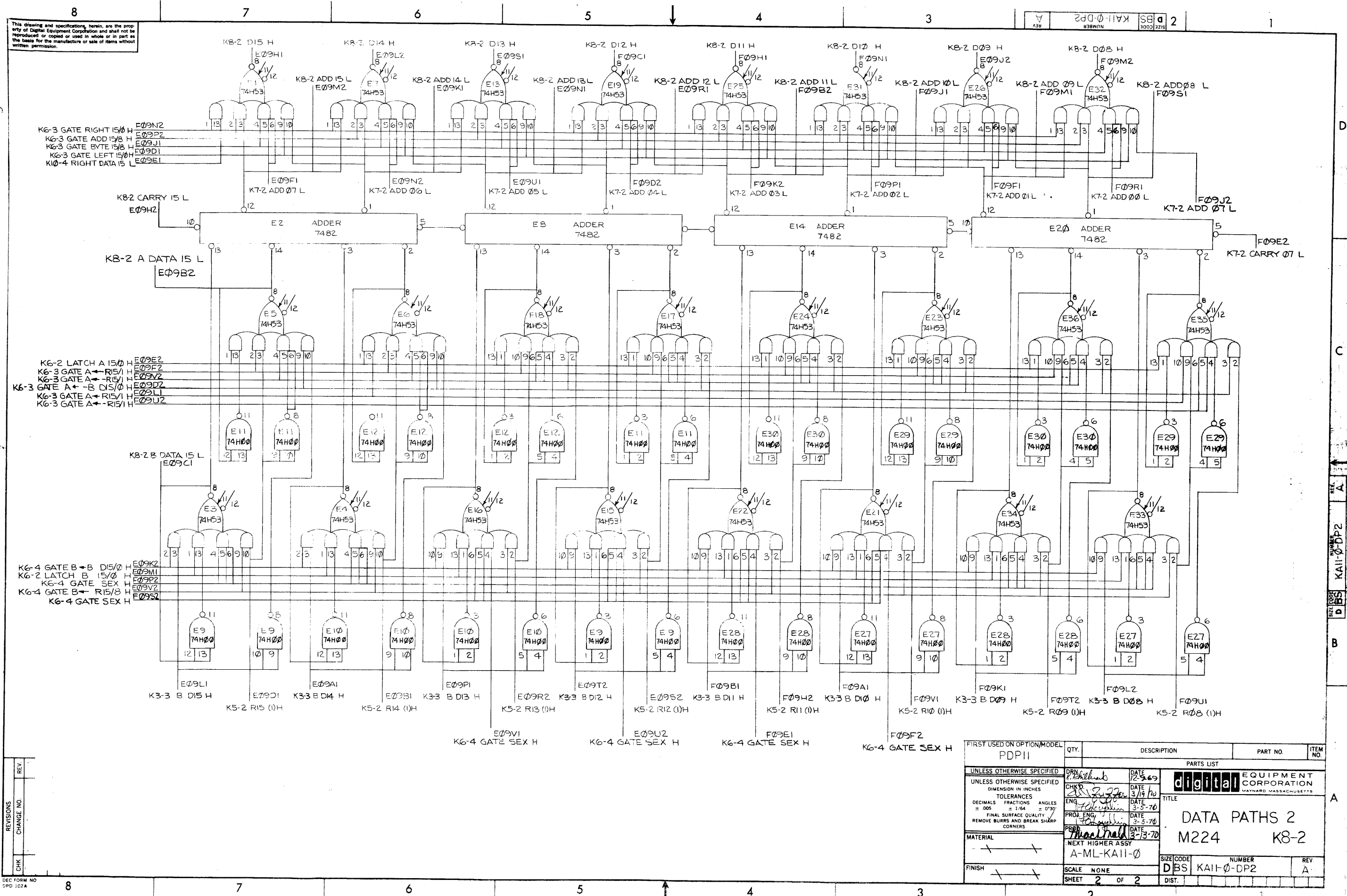
PIN NOMENCLATURE

MODULE	PROCESSOR
A	C
B	D



FIRST USED ON OPTION / MODEL PDP 11		QTY.	DESCRIPTION	PART NO.	ITEM NO.
		PARTS LIST			
UNLESS OTHERWISE SPECIFIED	DRN. <i>23168 base</i>	DATE <i>30 Jan 70</i>	 DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		
UNLESS OTHERWISE SPECIFIED	CHK'D <i>012</i>	DATE <i>3-2-70</i>			
DIMENSION IN INCHES	ENG. <i>7/10/70</i>	DATE <i>3-6-70</i>			
TOLERANCES	PROL. ENG. <i>7/10/70</i>	DATE <i>3-6-70</i>			
DECIMALS FRACTIONS ANGLES ± .005 ± 1/64 ± 0°30'	DRN. <i>23168 base</i>	DATE <i>3/3/70</i>			
FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS	TITLE DATA PATHS I		M224 K7-1 SIZE CODE NUMBER REV. D B S Kall-Ø-DPI A		
MATERIAL _____ <i>A</i> _____	NEXT HIGHER ASSY A-ML- Kall-Ø				
FINISH _____ <i>H</i> _____	SCALE _____ <i>H</i> _____	SHEET <i>1</i> OF <i>2</i>	DIST.		





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REV.	CHANGE NO.

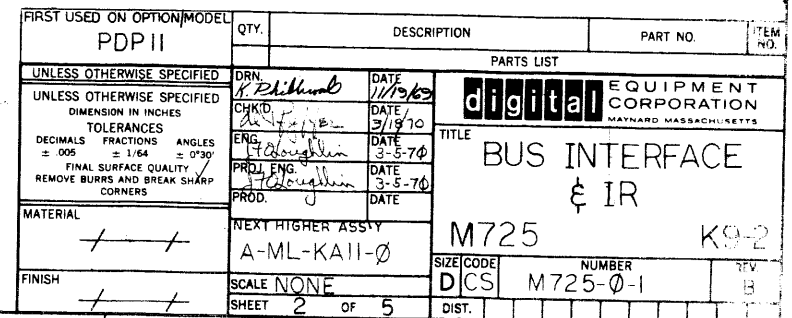
DEC FORM NO 500 102A

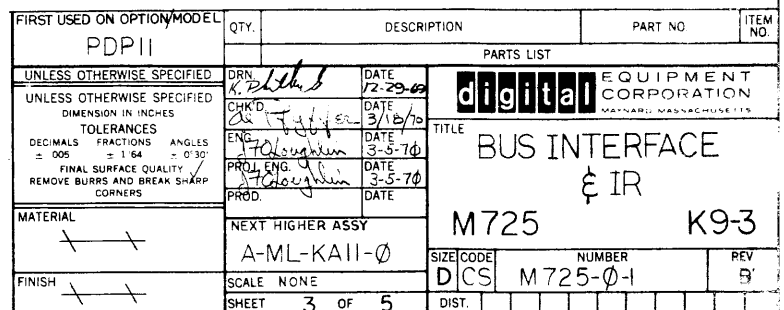
FIRST USED ON OPTION MODEL PDP11		QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED		PARTS LIST			
UNLESS OTHERWISE SPECIFIED		digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS			
DIMENSION IN INCHES		TITLE			
TOLERANCES		DATA PATHS 2 M224 K8-2			
DECIMALS FRACTIONS ANGLES		SIZE CODE NUMBER			
± .005 ± 1/64 ± 0°30'		D B S K A I I - 0 - D P 2			
FINAL SURFACE QUALITY		REV			
REMOVE BURRS AND BREAK SHARP CORNERS		A			
MATERIAL		SCALE NONE			
NEXT HIGHER ASSY		SHEET 2 OF 2			
FINISH		DIST.			

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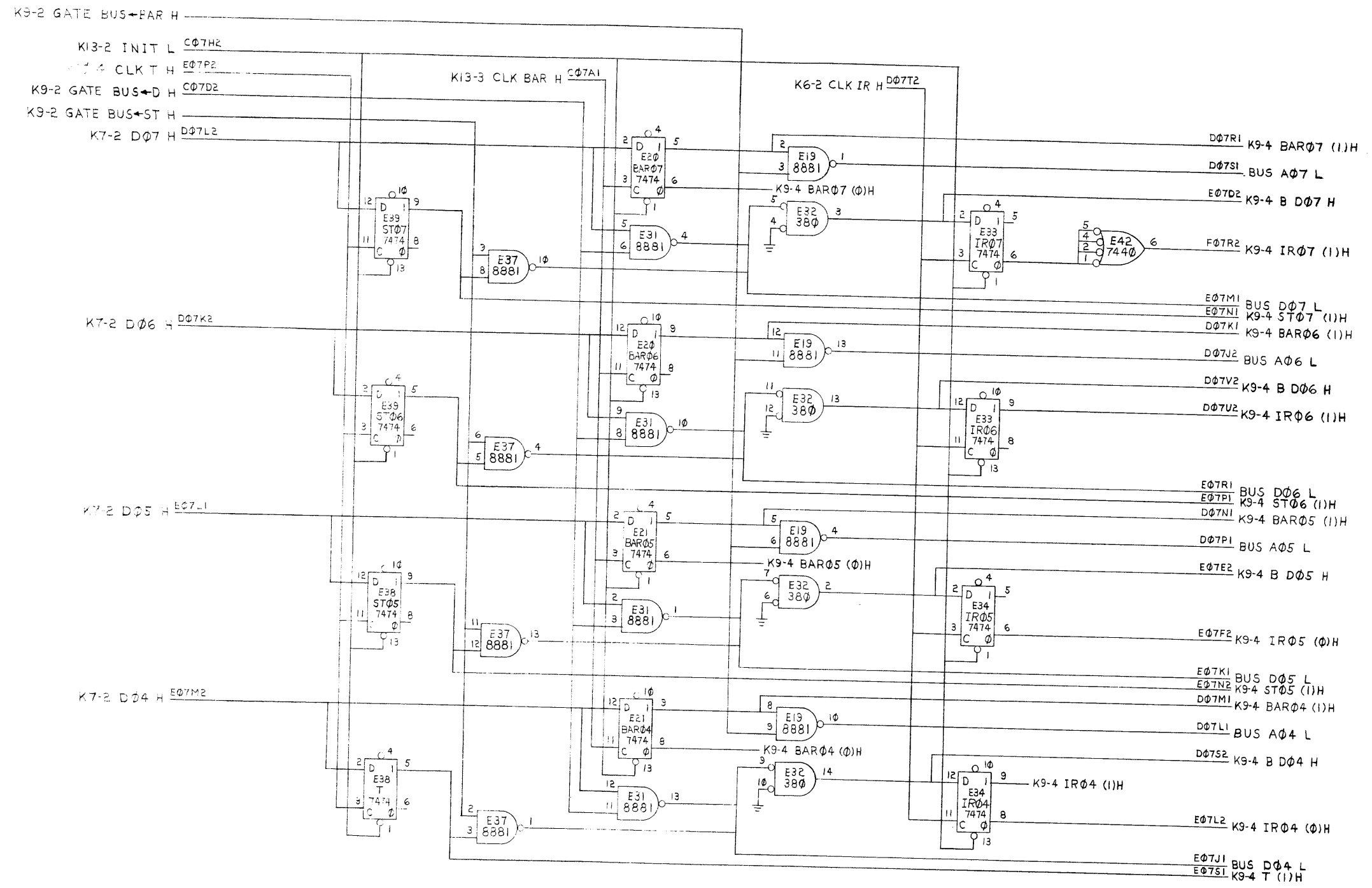
PARTS REFERENCE

ITEM NO.	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E1, E7, E13, E19, E25, E31, E37, E43, E49	DEC 8881	IC 1909705	9
2	E2, E8, E14, E20, E26, E32, E38, E44, E50, E56, E62, E68, E74, E80, E86, E92, E98, E104, E110, E116, E122, E128, E134, E140, E146, E152, E158, E164, E170, E176, E182, E188, E194, E200, E206, E212, E218, E224, E230, E236, E242, E248, E254, E260, E266, E272, E278, E284, E290, E296, E302, E308, E314, E320, E326, E332, E338, E344, E350, E356, E362, E368, E374, E380, E386, E392, E398, E404, E410, E416, E422, E428, E434, E440, E446, E452, E458, E464, E470, E476, E482, E488, E494, E500, E506, E512, E518, E524, E530, E536, E542, E548, E554, E560, E566, E572, E578, E584, E590, E596, E602, E608, E614, E620, E626, E632, E638, E644, E650, E656, E662, E668, E674, E680, E686, E692, E698, E704, E710, E716, E722, E728, E734, E740, E746, E752, E758, E764, E770, E776, E782, E788, E794, E800, E806, E812, E818, E824, E830, E836, E842, E848, E854, E860, E866, E872, E878, E884, E890, E896, E902, E908, E914, E920, E926, E932, E938, E944, E950, E956, E962, E968, E974, E980, E986, E992, E998, E1004, E1010, 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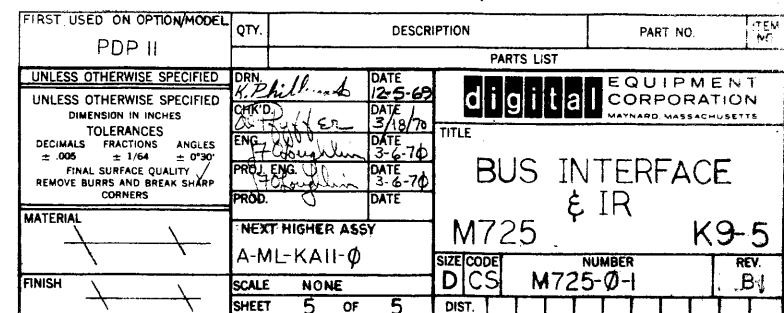


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DEC FORM NO. DRD 102A

FIRST USED ON OPTION/MODEL		QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11					
UNLESS OTHERWISE SPECIFIED		PARTS LIST			
DIMENSION IN INCHES		DRN. 1/28/70	DATE 2-27-70	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
TOLERANCES		CHK'D 2/18/70	DATE 3-18-70	TITLE	
DECIMALS FRACTIONS ANGLES		ENG. 1/28/70	DATE 3-6-70	BUS INTERFACE & IR	
± .005 ± 1/64 ± 0°30'		PROG. ENG. 1/28/70	DATE 3-6-70	M725 K9-4	
FINAL SURFACE QUALITY / REMOVE BURRS AND BREAK SHARP CORNERS		PROD. 1/28/70	DATE 3-6-70	SIZE CODE NUMBER REV.	
MATERIAL		NEXT HIGHER ASSY		D CS M725-0-1 B	
FINISH		SCALE NONE		DIST.	
		SHEET 4 OF 5			

1 (Pink B. line)



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PARTS REFERENCE

ITEM NO.	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E1,E7,E18,E27,E45,E54	8251B	IC 19 05594	6
2	E2,E5,E6,E8,E16,E17,E20,E21,E24,E37,E38	DEC 7400N	IC 19 05575	16
3	E40,E44,E49,E52,E59	DEC 7402N	IC 19 09004	14
4	E9,E10,E26,E57	DEC 7400N	IC 19 05579	4
5	E12,E29,E30,E36,E53,E58	DEC 7400N	IC 19 05580	4
6	E13,E32,E55,E60	DEC 7400N	IC 19 05577	4
7	E19,E31,E33,E48,E56	DEC 7410N	IC 19 05576	5
8	E23,E39	DEC 7453N	IC 19 05582	2
9	E28,E42,E61	DEC 8815A	IC 19 09713	3
10	E35	DEC 7460N	IC 19 05581	1
11	C1-C33,C35-C61	.01 MFD, 100V, 20%	CAP. 10 01610	60
12	C62-C64,C34	6.8 MFD, 35V, 20%	CAP. 10 00067	4

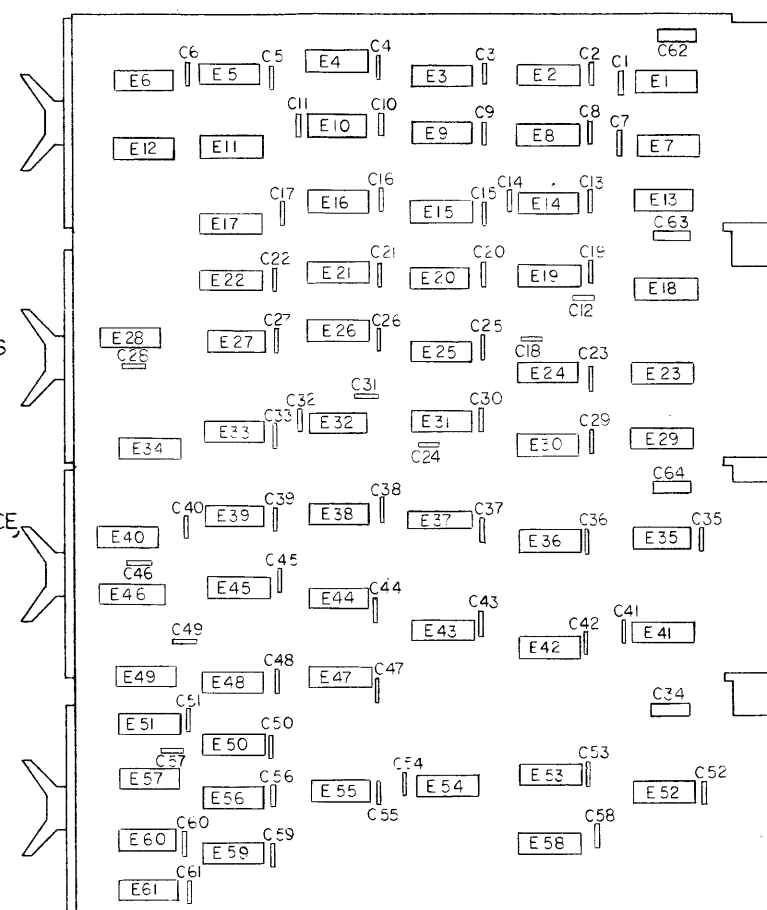
NOTES:

- PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.
- ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED: MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPARATE PINS ARE USED: MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.
- PROCESSOR SIGNAL SOURCE NOTATION (KIO-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.
- DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.
- GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

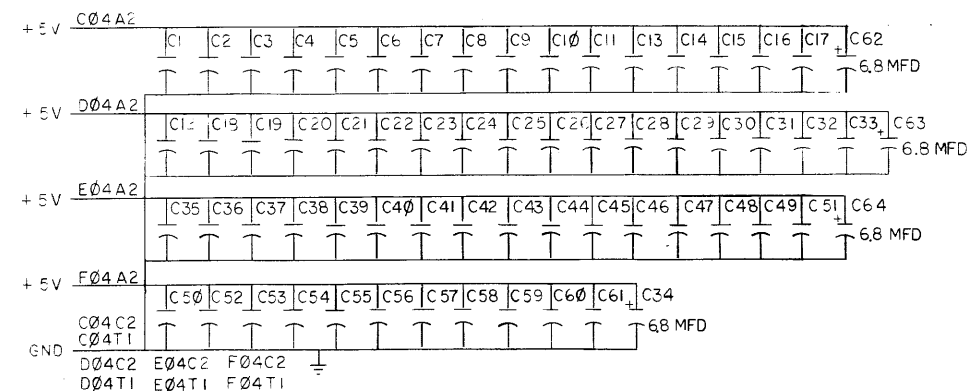
6. UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICOFARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01MFD

COMPONENT PLACEMENT



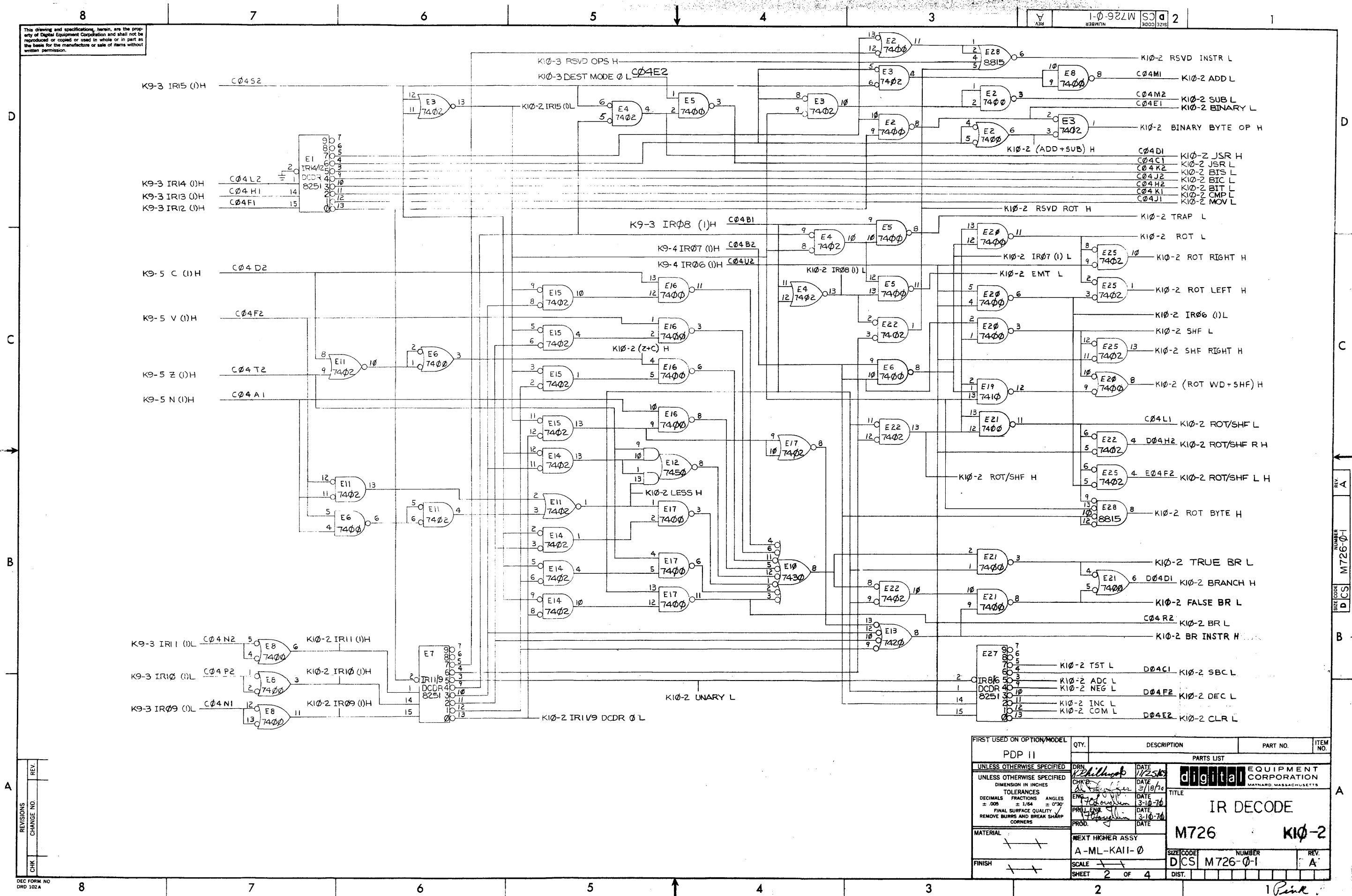
PIN NOMENCLATURE

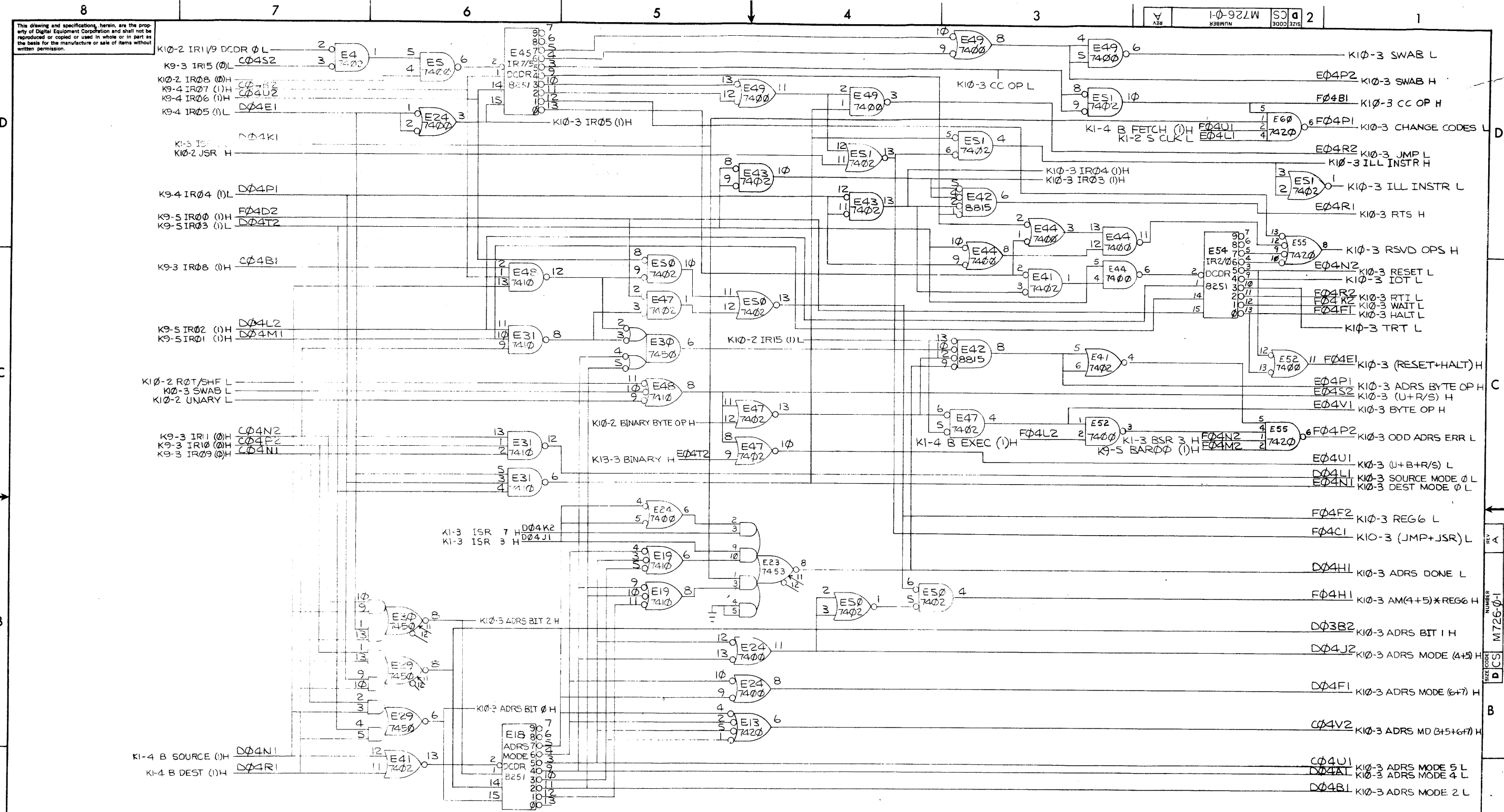
MODULE PROCESSOR



FIRST USED ON OPTION / MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11				
UNLESS OTHERWISE SPECIFIED				
DRN	DATE	2-4-70	digital EQUIPMENT CORPORATION	
CHK'D	DATE	3-6-70	MAYNARD, MASSACHUSETTS	
ENG	DATE	3-6-70	TITLE	
PROJ. ENG.	DATE	3-6-70	IR DECODE	
PROD.	DATE		M726 KI0-1	
NEXT HIGHER ASSY				
A-ML-KAI1-0				
SCALE				
SHEET 1 OF 4				
ETCH REV. B				
DIST.				

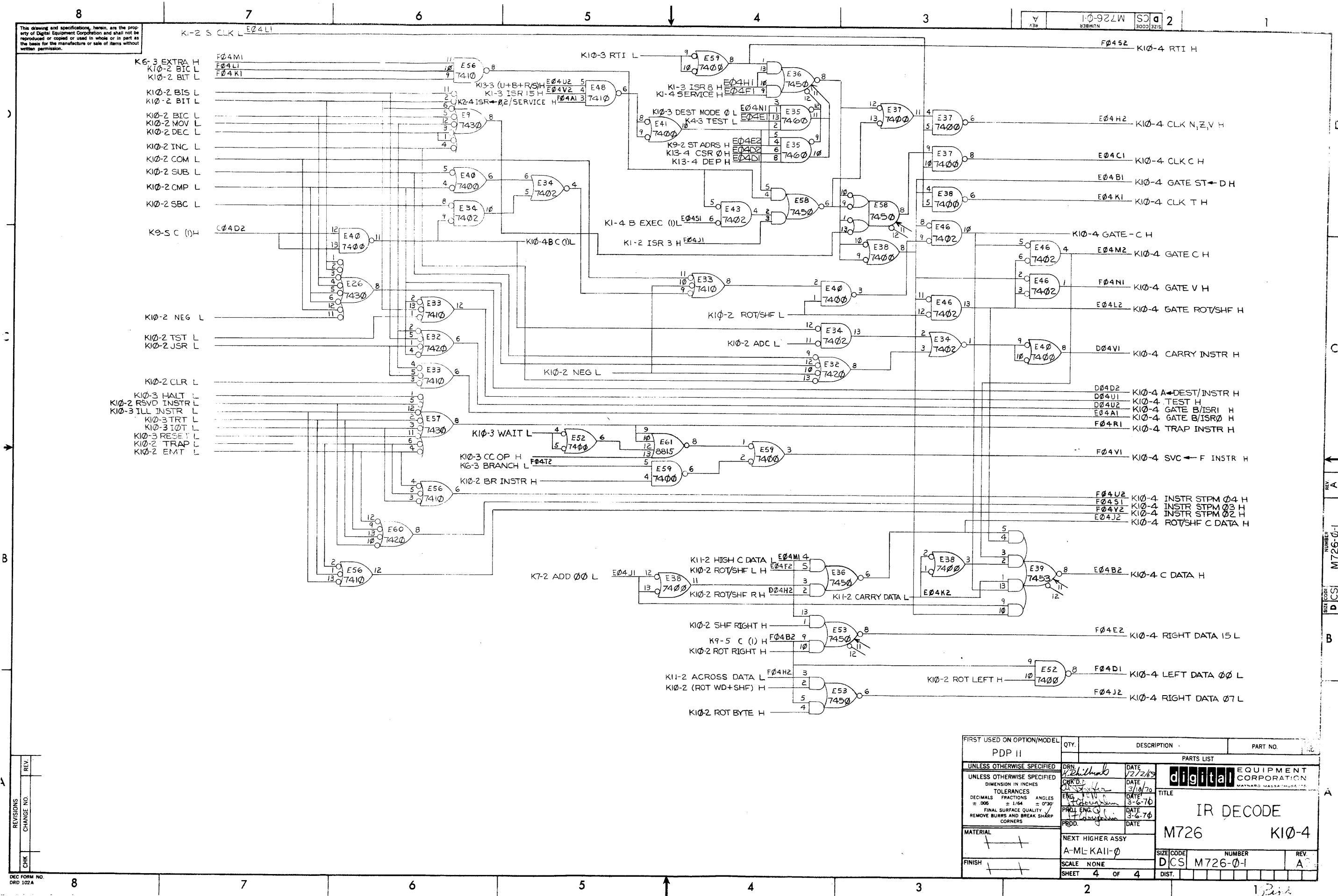
REVISIONS	REV
CHANGE NO.	
CHK	

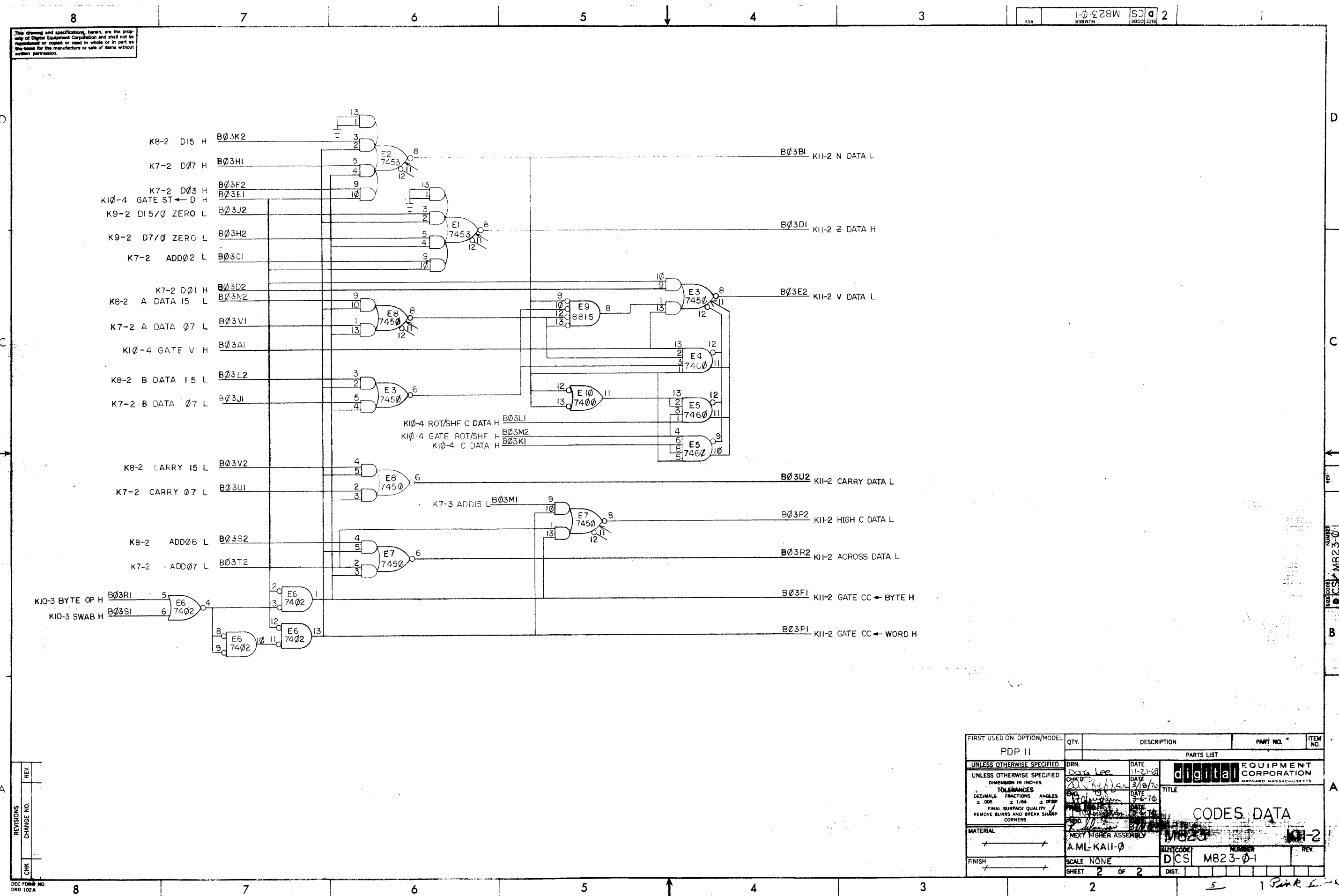




REV	CHANGE NO.	CHK
1		

FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP 11				
UNLESS OTHERWISE SPECIFIED	DRN	DATE	PARTS LIST	
UNLESS OTHERWISE SPECIFIED	CHK'D	DATE	digital EQUIPMENT CORPORATION	
DIMENSION IN INCHES	ENG	DATE	MAYNARD, MASSACHUSETTS	
TOLERANCES	PROL. ENG.	DATE	TITLE	
DECIMALS FRACTIONS ANGLES	PROD.	DATE	IR DECODE	
± .005 ± 1/64 ± .030			M726 K10-3	
FINAL SURFACE QUALITY			SIZE CODE NUMBER REV.	
REMOVE BURRS AND BREAK SHARP CORNERS			DCS M726-01 A	
MATERIAL	NEXT HIGHER ASSY		SHEET 3 OF 4	
FINISH	SCALE NONE		DIST.	





PIN NOMENCLATURE

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.
2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED; MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPERATE PINS ARE USED; MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.
3. PROCESSOR SIGNAL SOURCE NOTATION (KIO-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.
4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.
5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

C4	E6	C3	E5	C2	E4	C1	E3				C27
								SPARE	SPARE		
		C9	E11	C8	E10	C7	E9	C6	E8	C5	E7
	SPARE										
C14	E18			C13	E16	C12	E15	C11	E14	C10	E13
			SPARE								
C20	E24	C19	E23	C18	E22	C17	E21	C16	E20	C15	E19
											C28
C26	E30	C25	E29	C24	E28	C23	E27	C22	E26	C21	E25

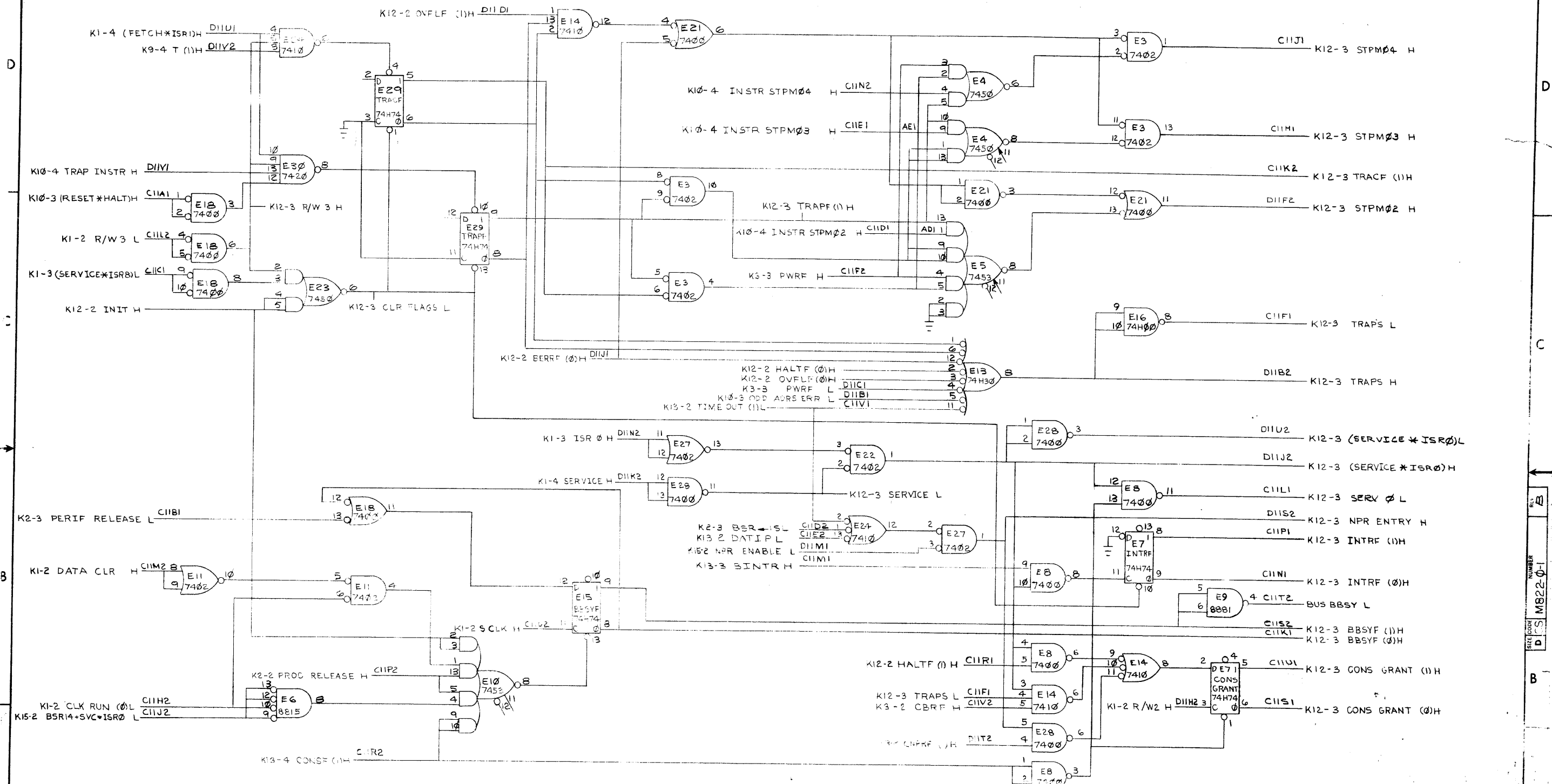
MODULE	PROCESSOR
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MODULE	PROCESSOR
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Diagram of the input filter for the 100W 1000V power supply. The input is +5V, which is connected to the input of the filter. The filter consists of a series of capacitors (C1 through C29) and two 6.8 MFD capacitors connected to ground. The output of the filter is connected to the GND line.

FIRST USED ON OPTION MODEL PDP II		QTY.	DESCRIPTION		PART NO.		ITEM NO.
UNLESS OTHERWISE SPECIFIED		DRN	DATE		 DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES		CHK'D	DATE				
TOLERANCES		ENG	DATE				
DECIMALS FRACTIONS ANGLES ± .005 ± 1/64 ± 0°30'		PROJ. ENG.	DATE				
FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS		PROD.	DATE				
MATERIAL		NEXT HIGHER ASSY		TITLE FLAG CNTL			
— # —		A-ML-KAII-Ø					
FINISH		SCALE					
— # —		SHEET 1 OF 3		SIZE CODE NUMBER DCS M822-Ø-1		REV B	
				ETCH REV B			

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REV.	CHANGE NO.	CHK

DEC FORM NO. DRD 102A

FIRST USED ON OPTION/MODEL PDP 11		QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED		DRN.	PARTS LIST		
UNLESS OTHERWISE SPECIFIED		CHK'D	digital EQUIPMENT CORPORATION		
DIMENSION IN INCHES		DATE	TITLE		
TOLERANCES		12-11-68	FLAG CNTL		
DECIMALS FRACTIONS ANGLES		3/18/70			
± .005 ± 1/64 ± 0°30'		DATE			
FINAL SURFACE QUALITY		3-6-70			
REMOVE BURRS AND BREAK SHARP CORNERS		DATE			
MATERIAL		PROD.			
FINISH		DATE			
NEXT HIGHER ASSY		SCALE			
A-ML-KA11-0		NONE			
SHEET		3 OF 3			
DIST.					

M822 K12-3
SIZE CODE DCS NUMBER M822-01 REV. 8

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PARTS REFERENCE

ITEM NO.	DRAWING REFERENCE	DESCRIPTION	PART NUMBER	QUANTITY
1	E1,E3,E5,E7,E9,E13,E15,E20,F23,F26,F27,E37,E39,E42	DEC 7400N	I.C. 1905575	14
2	E2,E38,E52	DEC 7410N	I.C. 1905576	3
3	E4,E33	DEC 380	I.C. 1909485	2
4	E6,E41	DEC 7420N	I.C. 1905577	2
5	E8,E45	DEC 8815A	I.C. 1309713	2
6	E10,E11,E14,E21,E24,E28	DEC 7402N	I.C. 1909004	6
7	E12,E18,F22,E29,E40,E46,E48	DEC 7474N	I.C. 1905547	7
8	E16,E44,E49	DEC 8881	I.C. 1909705	3
9	E17,E19,E32	DEC 7450N	I.C. 1905580	3
10	E25,E34,E35,E43,E47,E50,E51	DEC 9601	I.C. 1909487	7
11	E30	DEC 74H40N	I.C. 1905586	1
12	E31	DEC 7440N	I.C. 1905579	1
13	E36	DEC 7453N	I.C. 1905582	1
14	C1-C4,C7,C9,C14,C20	330 MMF. 100V. 5%	CAP. 1000023	8
15	C5,C11,C13,C67	1000 MMF. 100V. 5%	CAP. 1000042	4
16	C6	120 MMF. 100V. 5%	CAP. 1000018	1
17	C8,C10,C12,C68	470 MMF. 100V. 5%	CAP. 1000024	4
18	C15,C16, C19	1.0 MFD. 35V. 10%	CAP. 1001715	3
19	C17	2.2 MFD. 20V. 10%	CAP. 1002627	1
20	C21-C62,C69,C70,C71	.01 MFD. 100V. 20%	CAP. 1001610	45
21	C63-C65,C18	6.8 MFD. 35V.	CAP. 1000067	5
22	R1,R4,R6,R8,R10,R15,R30,R33	220, 1/4 W. 5%	RES. 1300271	8
23	R2,R3,R5,R7,R9,R14,R22,R29	470, 1/4 W. 5%	RES. 1300316	8
24	R11	5600, 1/4 W. 5%	RES. 1301874	1
25	R12,R13,R16,R18,R32	1000, 1/4 W. 5%	RES. 1300365	5
26	R17,R19,R25,R26	30K, 1/4 W. 5%	RES. 1302394	4
27	R20,R21,R34,R35	47, 1/4 W. 5%	RES. 1300202	4
28	R23,R24,R27	15K, 1/4 W. 5%	RES. 1300496	3
29	R28	10K, 1/4 W. 5%	RES. 1300479	1
30	R31	3.9K, 1/4 W. 5%	RES. 1300444	1
31	D1-D12	D664 DIODE	1100114	12
32	Q1	DEC 3009B TRANSISTOR	1503100	1

NOTES:

1. PIN NOTATION THROUGHOUT IS ORDERED UPON MODULE PLACEMENT IN THE KAI1 PROCESSOR. MODULE REFERENCE ALONE IS OBTAINED BY DELETING THE NUMBER (SLOT LOCATION) AFTER THE FIRST LETTER, AND CONVERTING THE FIRST LETTER ACCORDING TO THE PIN NOMENCLATURE CHART AT RIGHT.

2. ALL SIGNALS THAT HAVE MODULE PINS ARE SO NOTED: MULTIPLE NOTATIONS OF THE SAME SIGNALS WITHIN A MODULE HAVE THE PIN NOTED ON EACH. AN INPUT SIGNAL IS NOTED ONLY ONCE PER SHEET UNLESS SEPARATE PINS ARE USED: MULTIPLE INPUTS ARE CONNECTED. MODULE OUTPUT SIGNALS ARE BROUGHT TO THE EXTREME RIGHT OF EACH SHEET.

3. PROCESSOR SIGNAL SOURCE NOTATION (K10-2, FOR EXAMPLE) IDENTIFIES THE SIGNAL SOURCE (PRINT AND MODULE). THE FIRST NUMBER AFTER THE K INDICATES THE MODULE PRINT SET, WHILE THE SECOND INDICATES THE SHEET WITHIN THE SET. IF ON A PRINT, THE FIRST NUMBER OF THE K PREFIXES COINCIDE FOR A SIGNAL NAME AND THE PRINT (SEE TITLE BLOCK), THE SIGNAL IS GENERATED ON THE MODULE. A DIFFERENCE IN THE FIRST NUMBER OF THE K PREFIXES INDICATES A SIGNAL GENERATED OFF THE MODULE. SIGNALS WITH A "BUS" PREFIX REPRESENT A "WIRED OR" SITUATION, AND MULTIPLE SOURCES FOR THE SIGNAL CAN EXIST.

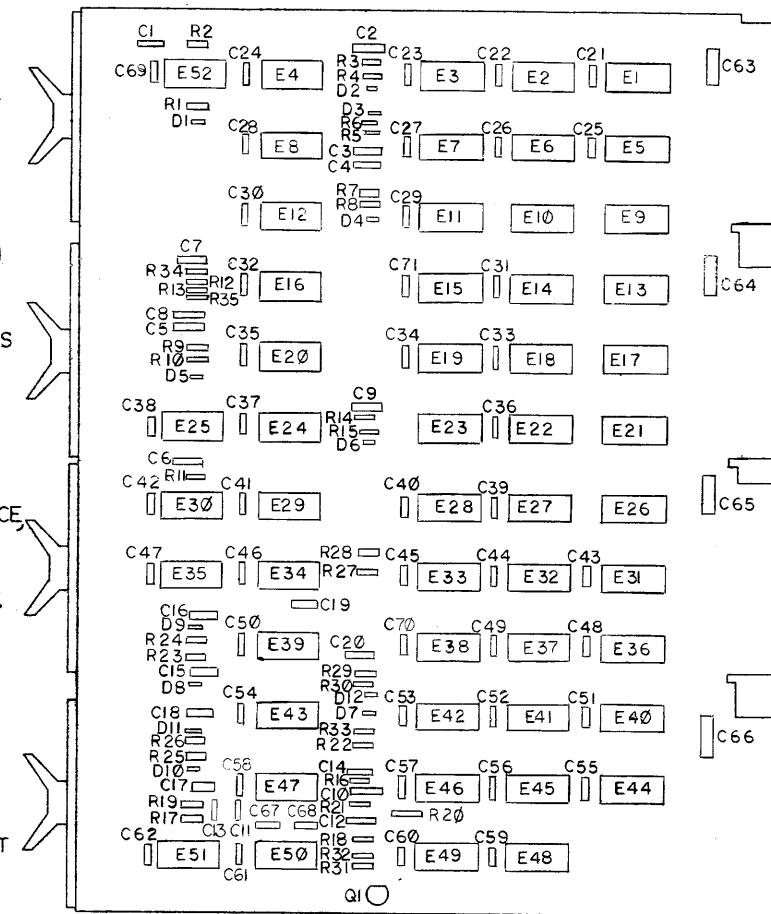
4. DETAILS ON COMPONENTS ARE NOTED IN THE PARTS REFERENCE. PLACEMENT IS NOTED IN THE COMPONENT PLACEMENT DIAGRAM.

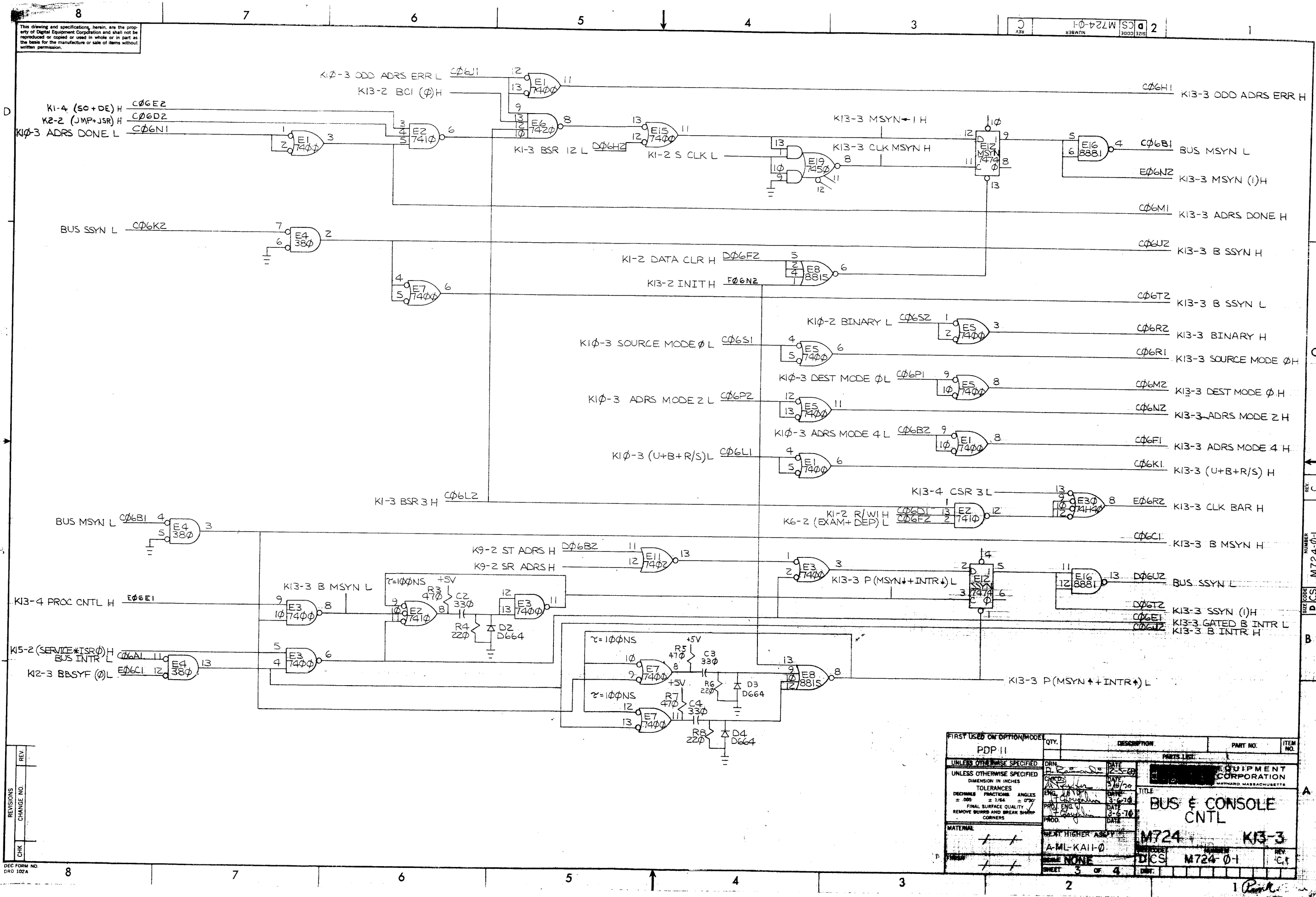
5. GND AND +5V ARE USUALLY PIN 7 AND PIN 14, RESPECTIVELY. EXCEPTIONS ARE:

IC TYPE	GND	+5V
DEC 7481	PIN 10	PIN 4
DEC 7482	PIN 11	PIN 4
DEC 8251	PIN 8	PIN 16
DEC 8271	PIN 8	PIN 16
DEC 380	PIN 1	PIN 8
DEC 384	PIN 1	PIN 8

6. UNLESS OTHERWISE NOTED: RESISTANCE IS IN OHMS; CAPACITANCE IS IN PICO FARADS. CAPACITORS WITHOUT ANY NOTED VALUES ARE .01 MFD.

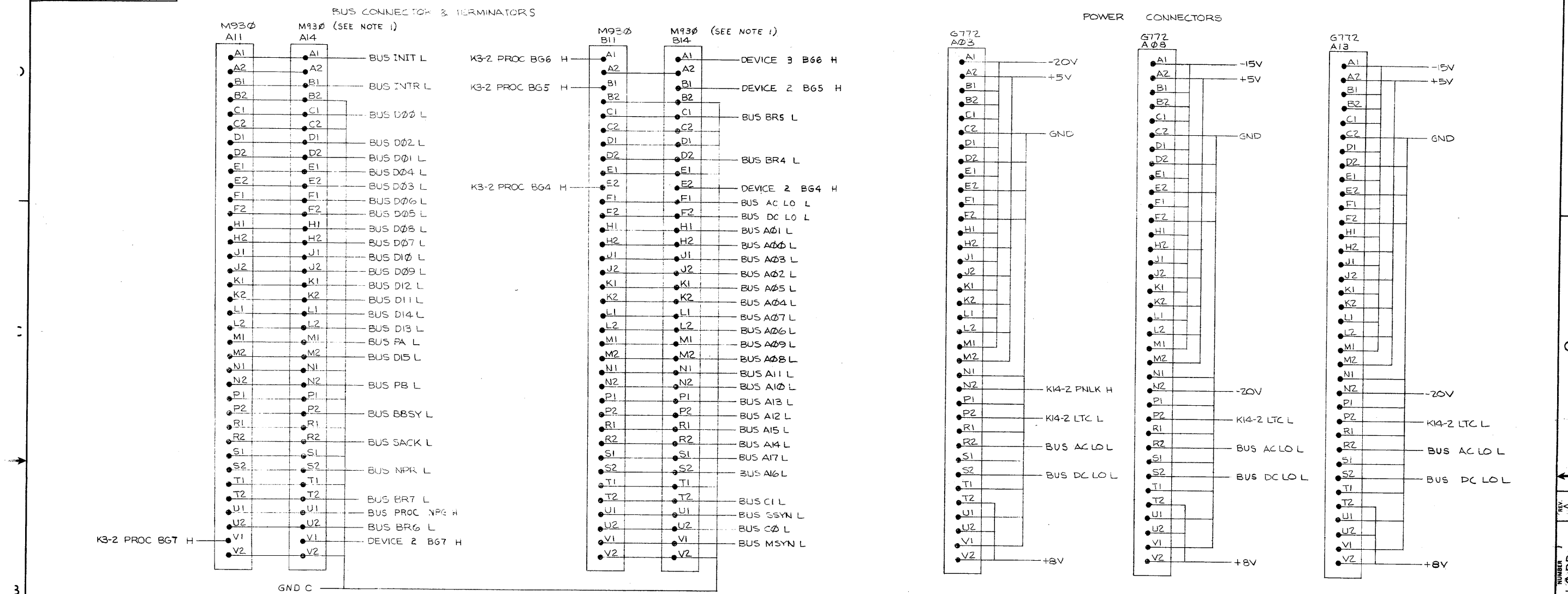
COMPONENT PLACEMENT





FIRST USED ON OPTION MODE		QTY.	DESCRIPTION	PART NO.	ITEM
PDP II					
UNLESS OTHERWISE SPECIFIED		DRN	PRICES LIST		
UNLESS OTHERWISE SPECIFIED		DATE	EQUIPMENT CORPORATION		
DIMENSION IN INCHES		DATE	WAYMART, MASSACHUSETTS		
TOLERANCES		ENG	TITLE		
DECIMALS FRACTIONS ANGLES		DATE	BUS & CONSOLE		
= .005 1/16 1/32		DATE	CNTL		
FINAL SURFACE QUALITY		DATE	M724		
REMOVE BURRS AND BREAK SHARP CORNERS		DATE	K13-3		
MATERIAL		PROD.	REV.		
+ +		NEAR HIGHER ASSY	M724-0-1		
+ +		A-M-L-KAI-0	DICS		
+ +		WHEEL NONE	M724-0-1		
+ +		SHIRT 3 OF 4	REV. C		

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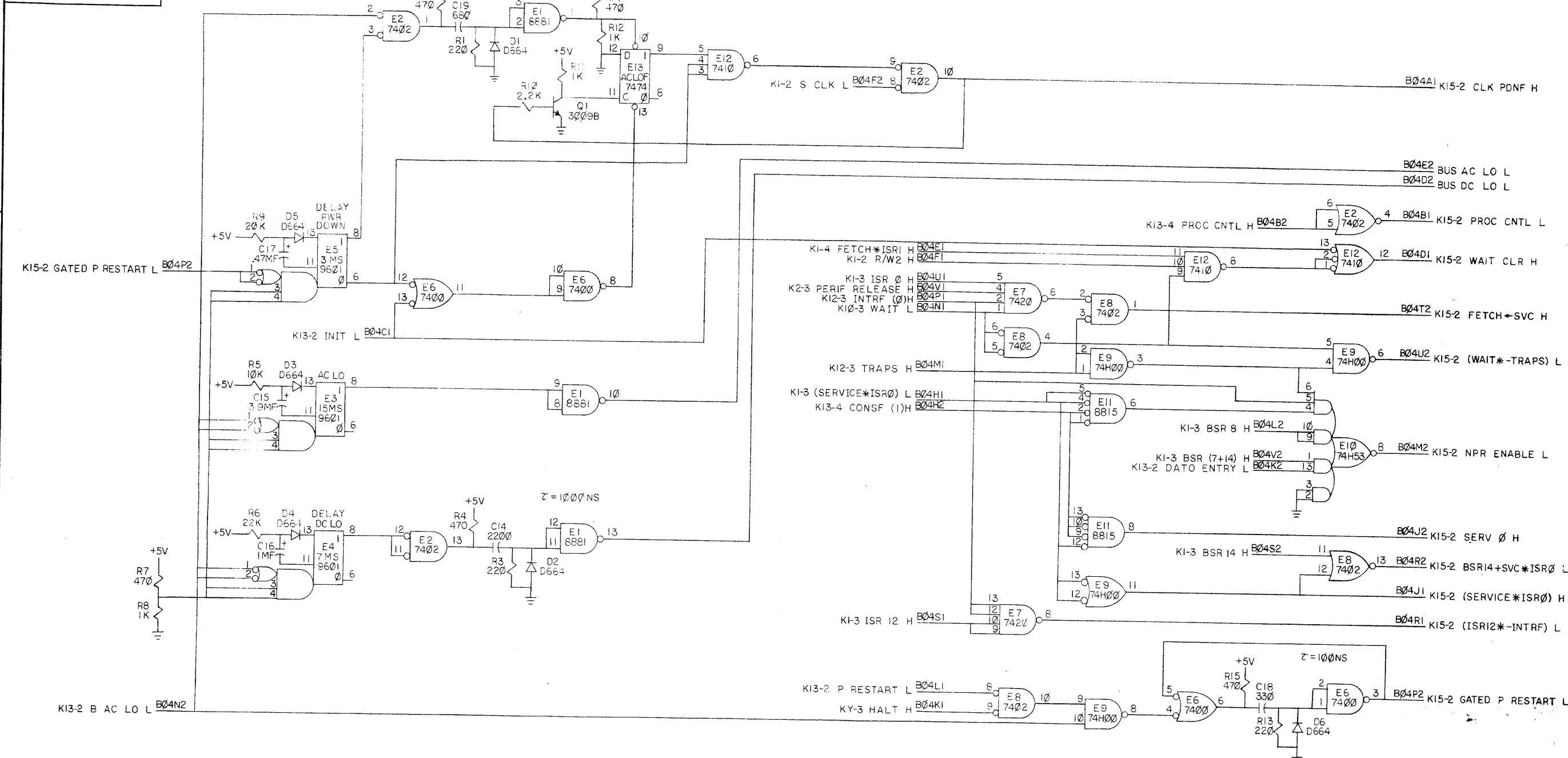


NOTES:
1. BEFORE THE KAI-1 IS INTERCONNECTED TO OTHER SYSTEM UNITS, A M930 BUS TERMINATOR IS IN LOCATIONS AB14. THIS TERMINATOR IS MOVED TO THE END SYSTEM UNIT UPON INTERCONNECTION; AND A M920 INTERNAL BUS CONNECTOR IS IN LOCATIONS AB14.

REV	CHG	NO	DATE
A		0007	3-3-70
		0008	3-6-70
		0009	3-6-70
		0010	3-6-70

FIRST USED ON OPTION/MODEL PDP 11	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED	DRN	DATE	PARTS LIST	
UNLESS OTHERWISE SPECIFIED	CHKD	DATE	digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DIMENSION IN INCHES	ENG	DATE	TITLE KAI-1 BUS & POWER CONNECTIONS K14-2	
TOLERANCES	PRD	DATE	SIZE CODE D1C KAI-0-BP	
DECIMALS FRACTIONS ANGLES	PROB	DATE	NUMBER A	
= .005 ± .005 ± .005			REV. A	
FINAL SURFACE QUALITY			DIST.	
REMOVE BURRS AND BREAK SHARP CORNERS				
MATERIAL	NEXT HIGHER ASSY			
FINISH	A-M-L-KAI-0			
SCALE	SHEET 1 OF 1			

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REV	CHANGE NO.
1	1

DEC FORM NO. DRD 102A

FIRST USED ON OPTION/MODEL PDP 11		QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED		DRN	DATE 2-27-70		
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES		CHK'D	DATE 3-4-70		
TOLERANCES		ENG	DATE 3-5-70		
DECIMALS FRACTIONS ANGLES		PROJ. ENG.	DATE 3-5-70		
± .005 ± .010 ± .030		PRD.	DATE 3-5-70		
FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS		NEXT HIGHER ASSY.			
MATERIAL		A-M-L-KAII-Ø			
FINISH		SCALE NONE			
SHEET 2 OF 2		DIST.			

PARTS LIST		TITLE	
digital EQUIPMENT CORPORATION NATURAL MASSACHUSETTS		PWR FAIL & CNTL	
M825		KI5-2	
SIZE CODE	NUMBER	REV.	
DIC	M825-Ø-1	B	

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NOTES:

1. SLOTS 5 AND 10 ARE CASTING SPACES. NO MODULES CAN BE INSERTED, BUT THE SLOTS ARE NUMBERED.
2. A CONSOLE PANEL IS REQUIRED IN THE KAI1. THE PDP 11/20 SYSTEM UTILIZES THE KY11-A CONSOLE (5408560) IN LOCATIONS ABCDEF1. THE PDP11/10 SYSTEM UTILIZES THE KY11-B CONSOLE () IN LOCATIONS EFI.
3. PREWIRED MODULE SLOTS FOR NOTED OPTIONS.
4. PREWIRED MODULE SLOTS FOR SMALL PERIPHERAL OPTIONS. WITH NO OPTIONS INSTALLED, BUS GRANT CONTINUITY IS PROVIDED BY THE G727 MODULE IN LOCATIONS D13 AND D14. THE G727 MODULE IS REMOVED WHEN A SMALL PERIPHERAL OPTION IS INSTALLED. SHOWN ARE THE USUAL OPTION CONFIGURATIONS OF DEVICE CONTROL IN LOCATIONS C AND D. ADDRESS SELECTOR IN LOCATION E, AND INTERRUPT CONTROL IN LOCATION F. IT IS POSSIBLE FOR OTHER MODULE CONFIGURATIONS TO UTILIZE THE STANDARD WIRING PROVIDED.
5. BEFORE THE KAI1 IS INTERCONNECTED TO OTHER SYSTEM UNITS, A M930 BUS TERMINATOR IS IN LOCATIONS AB14. THIS TERMINATOR IS MOVED TO THE END SYSTEM UNIT UPON INTERCONNECTION; AND A M920 INTERNAL BUS CONNECTOR IS IN LOCATIONS AB14

REV.	CHANGE NO.	DATE
1	KAI1-00002	5-18-70
2	010001	5-24-70
3	010002	5-24-70

DEC FORM NO. DRD 100

FIRST USED ON OPTION/MODEL
PDP 11

DO NOT SCALE DRAWING
UNLESS OTHERWISE SPECIFIED
DIMENSION IN INCHES
TOLERANCES
DECIMALS FRACTIONS ANGLES
± .005 ± 1/64 ± 0°30'
FINAL SURFACE QUALITY
REMOVE BURRS AND BREAK SHARP CORNERS

MATERIAL
A-ML-KAI1-0

FINISH
SCALE NONE
SHEET 1 OF 1

QTY.	DESCRIPTION	PART NO.	ITEM NO.
PARTS LIST			
1	MODULE UTILIZATION		
TITLE		digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	
DATE		3/11/70	
CHK'D.		3/13/70	
ENG.		3-17-70	
PROL. ENG.		3-17-70	
P.D.		3/15/70	
NEXT HIGHER ASSY		A-ML-KAI1-0	
SCALE		NONE	
SHEET		1 OF 1	
SIZE CODE		NUMBER	
DMU KAI1-0-MU		REV. A	