

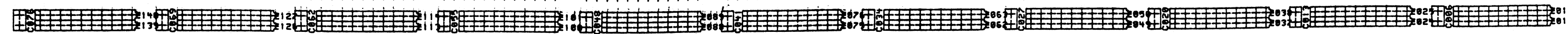
STOP!
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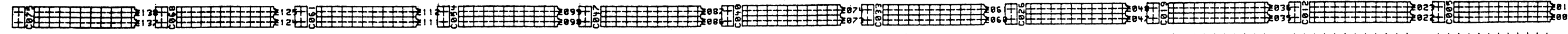
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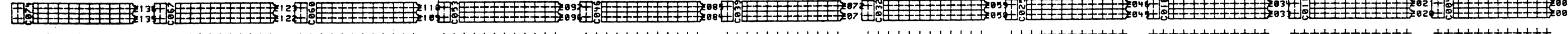
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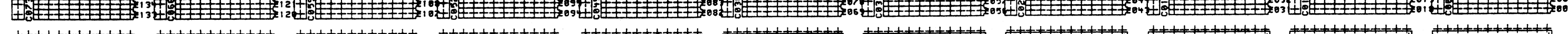
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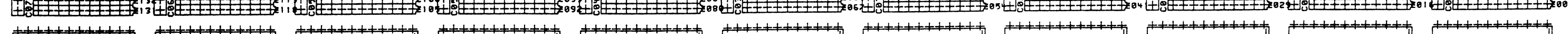
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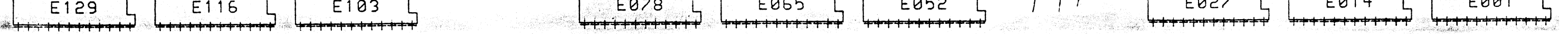


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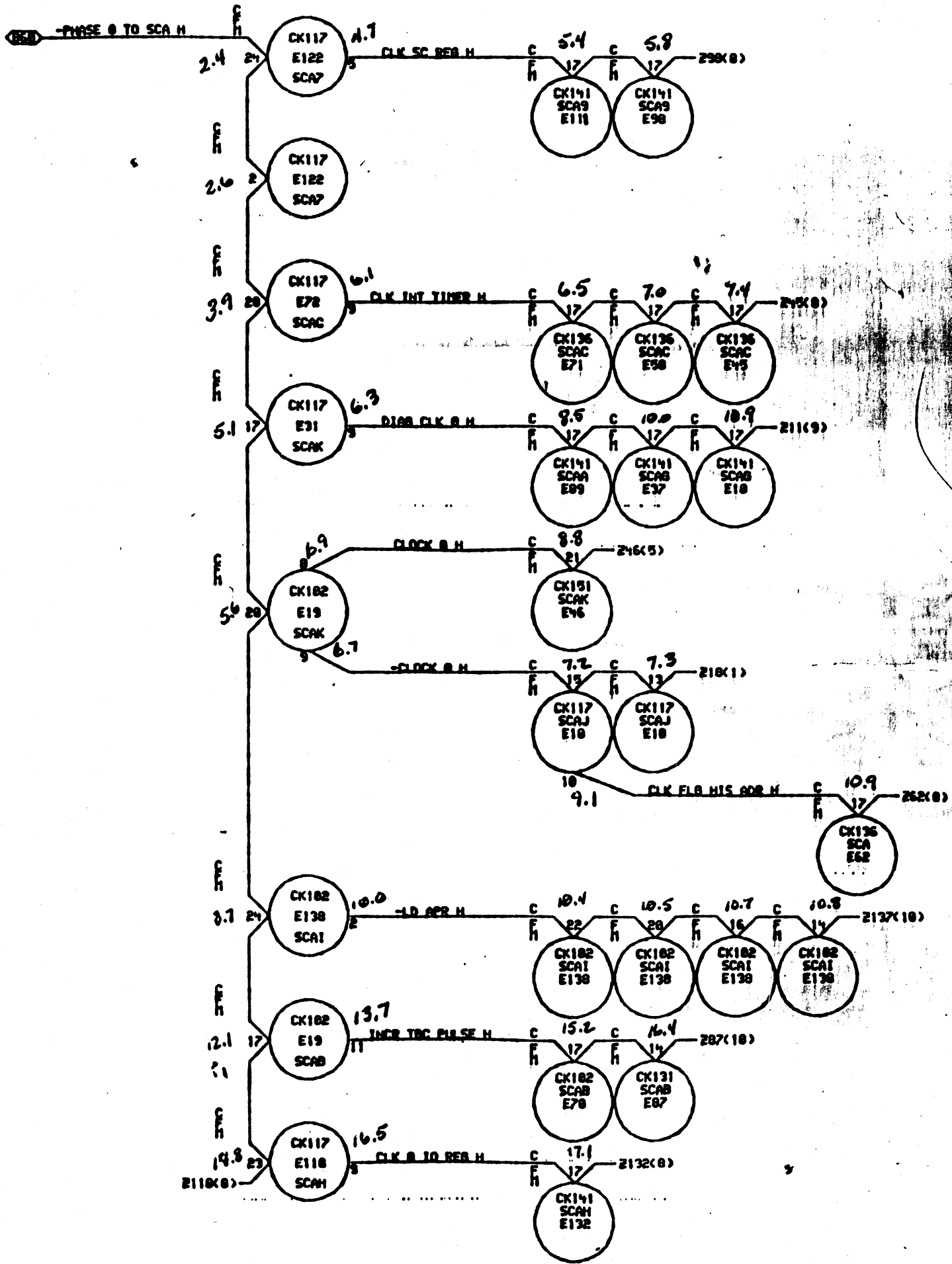


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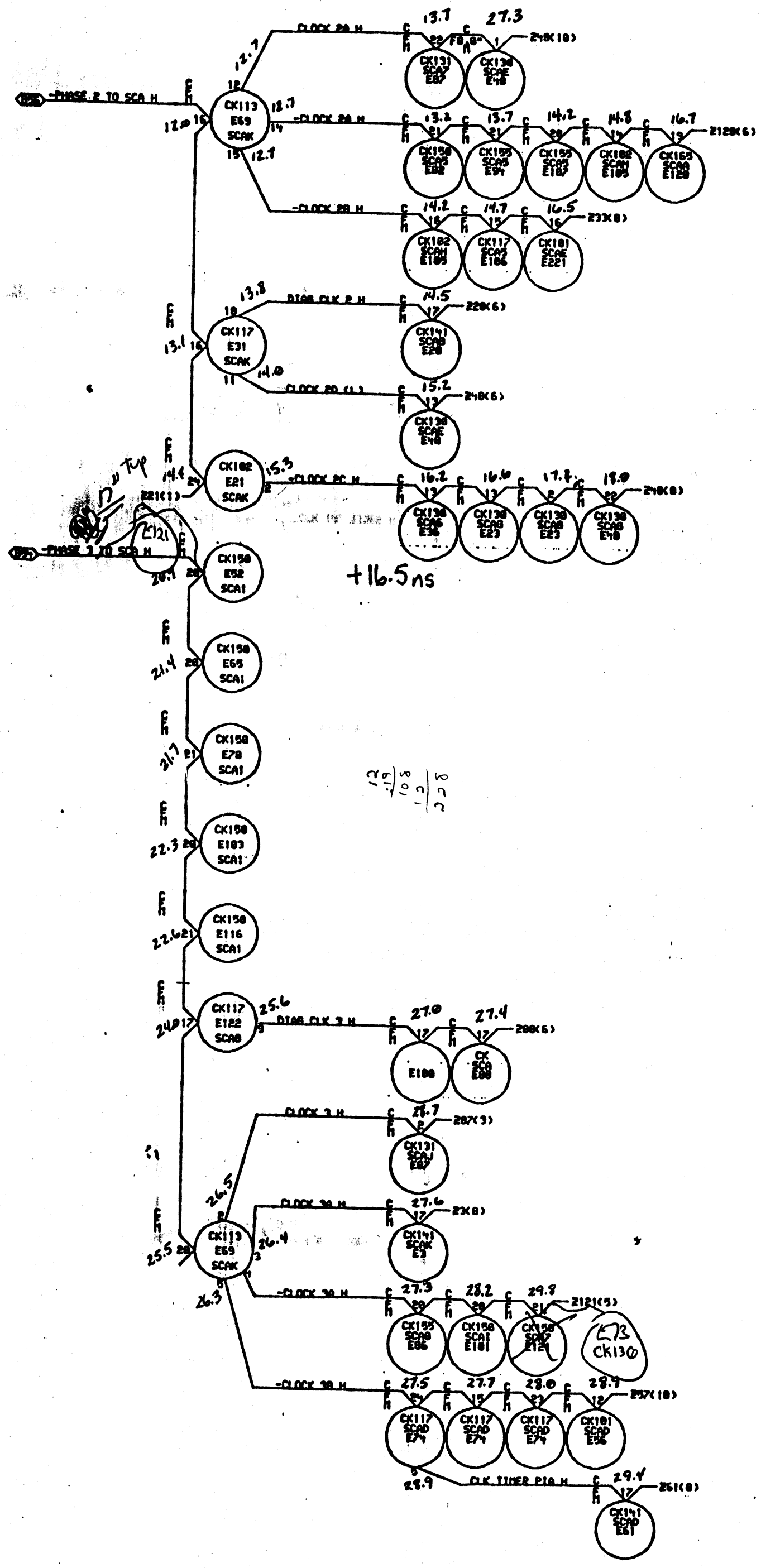
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SCA
BB VERSION
MEASURED

OK by Dave Hartwell 10/18/82

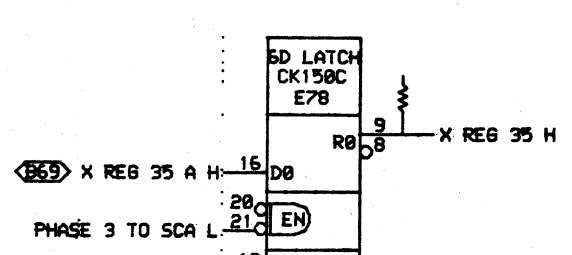
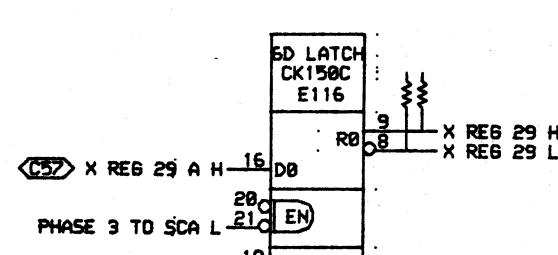
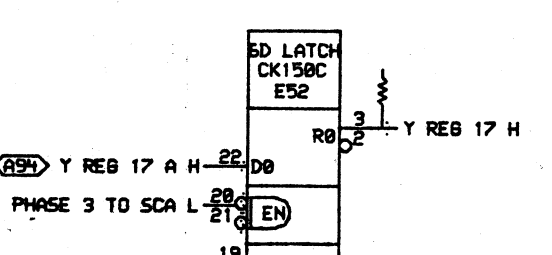
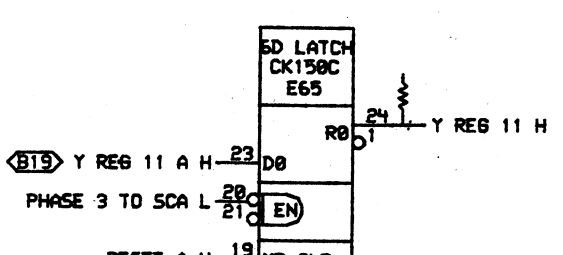
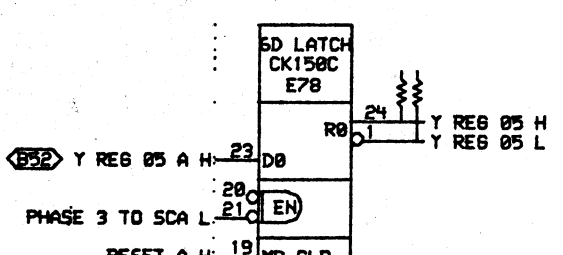
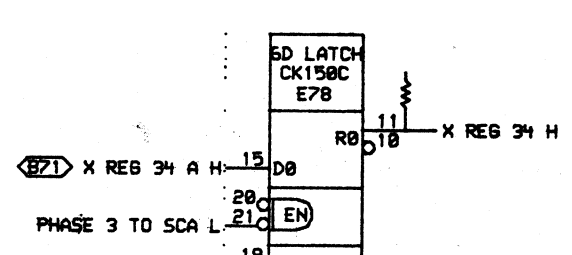
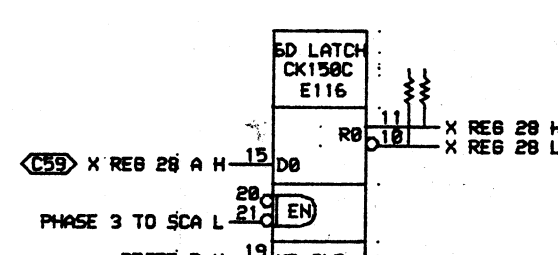
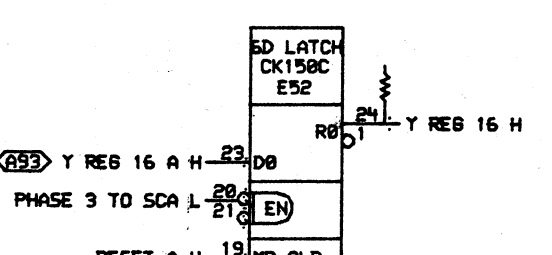
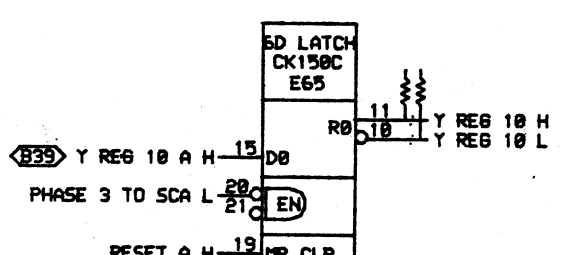
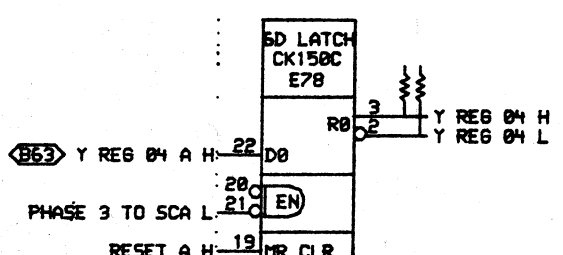
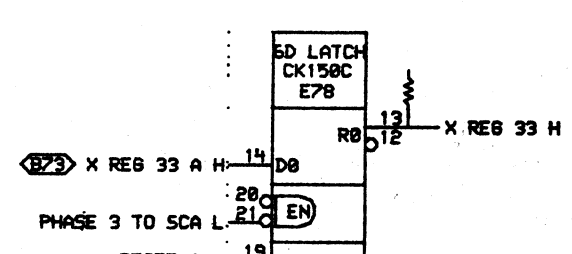
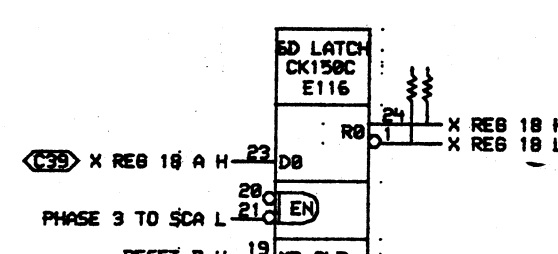
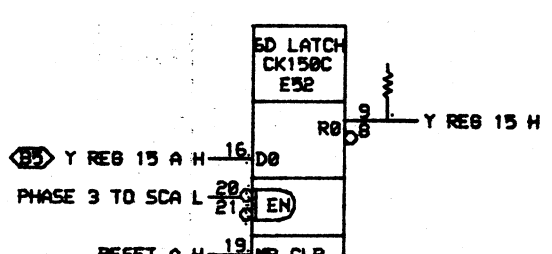
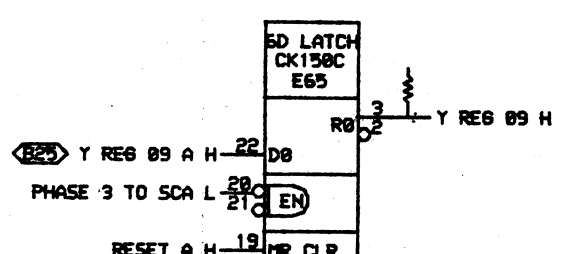
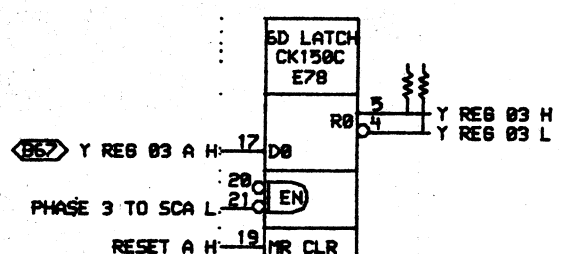
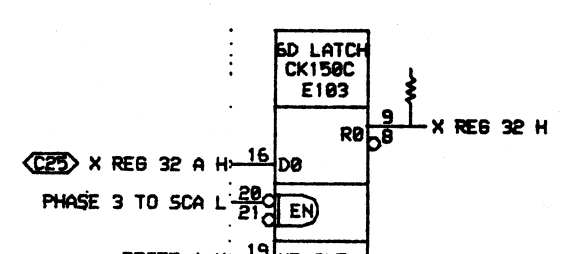
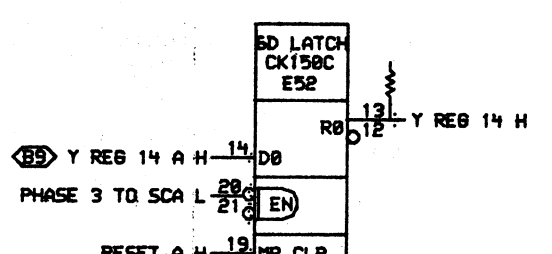
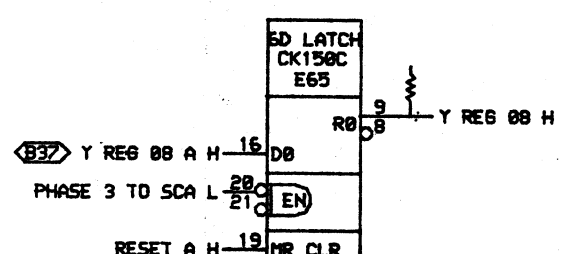
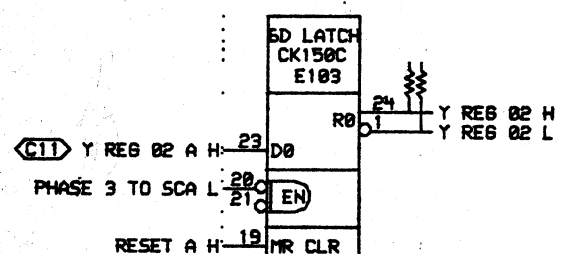
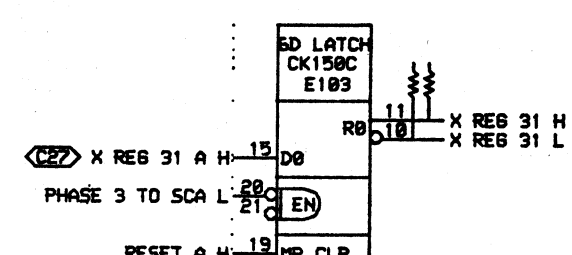
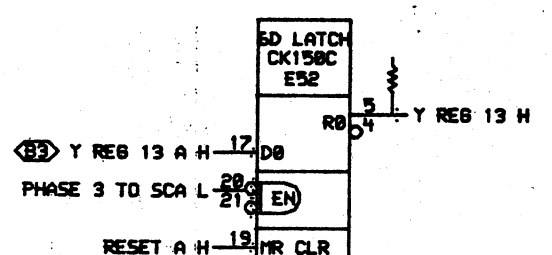
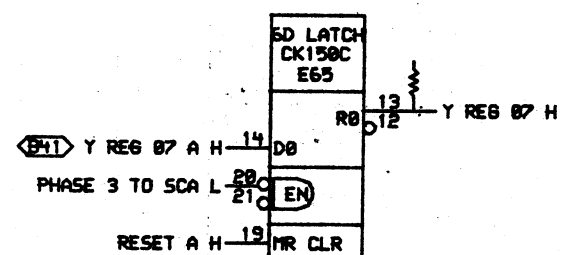
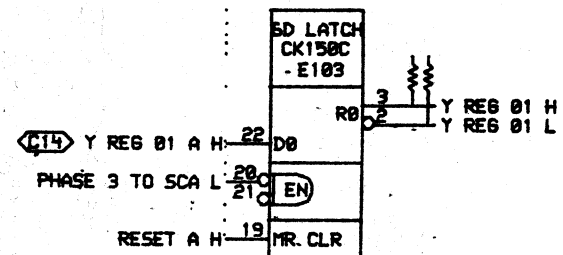
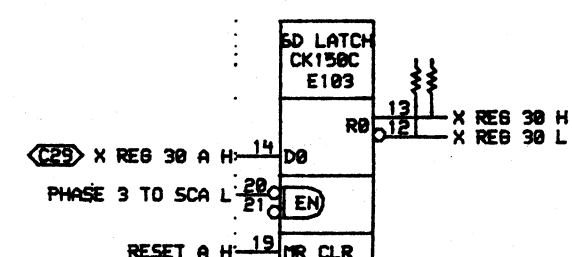
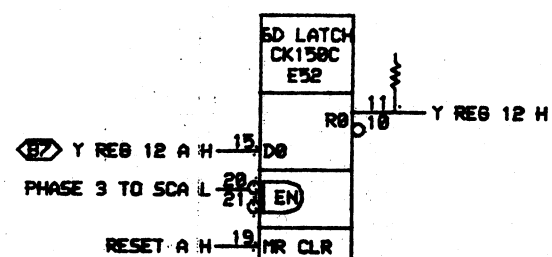
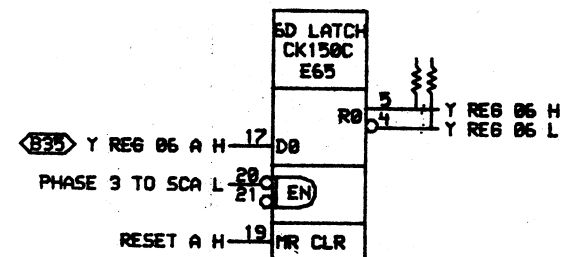
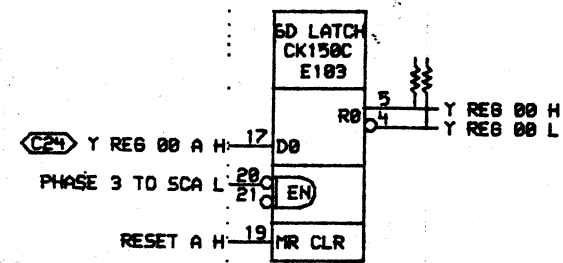
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CK'D.	DATE	BOARD LOCATION		
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FIRST USED ON OPTION MODEL 1				D CS

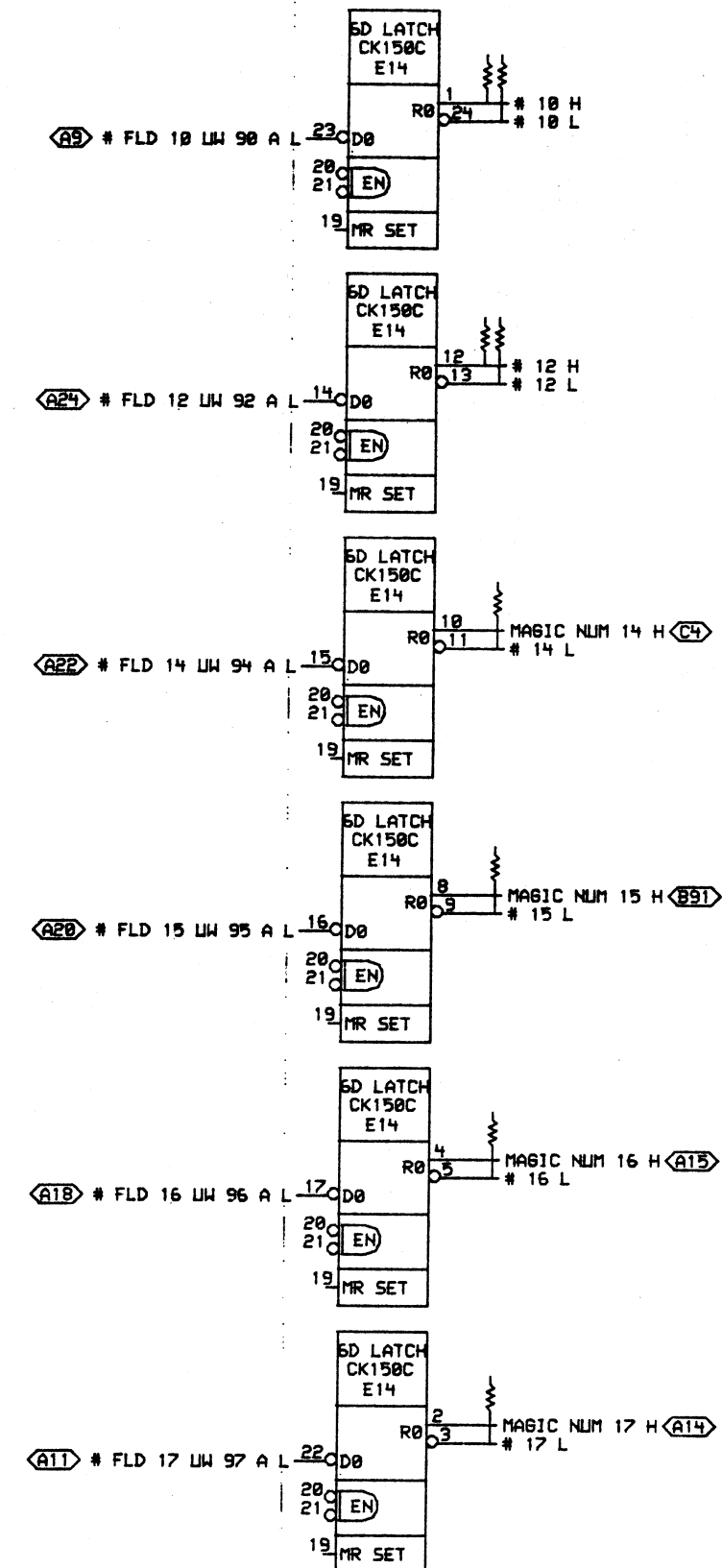
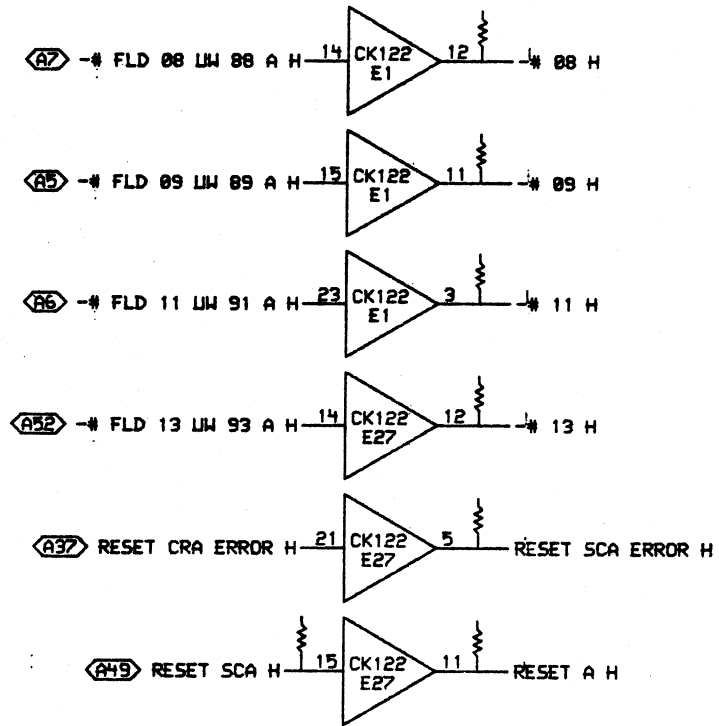
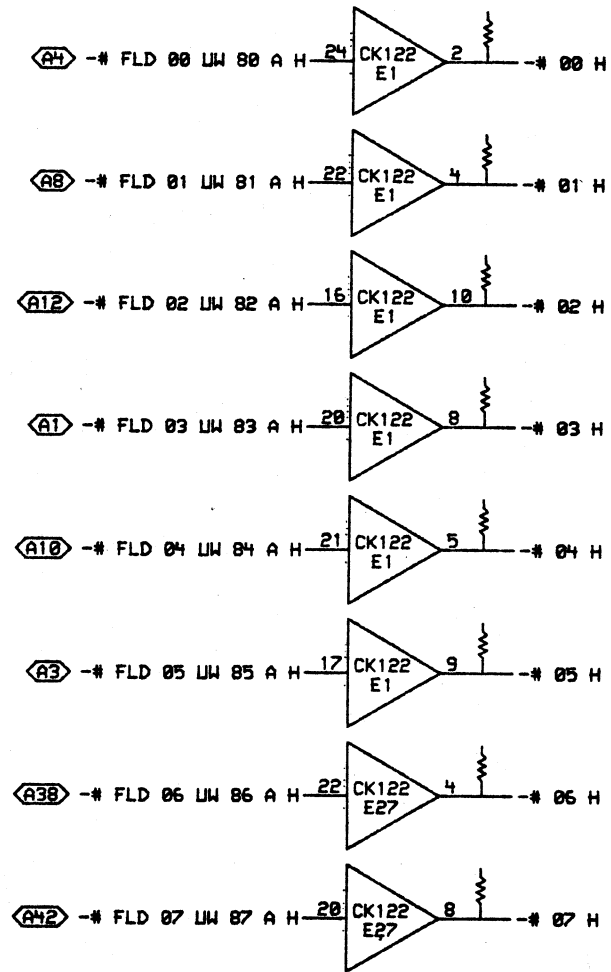


+16.5ns

2/21
8/01
8/22

DRN.	DATE	ENG.	DATE	TITLE
CHK'D.	DATE	BOARD LOCATION	REV.	
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FIRST USED ON OPTION MODEL				D C S
8	7	6	5	4
				3
				2
				1





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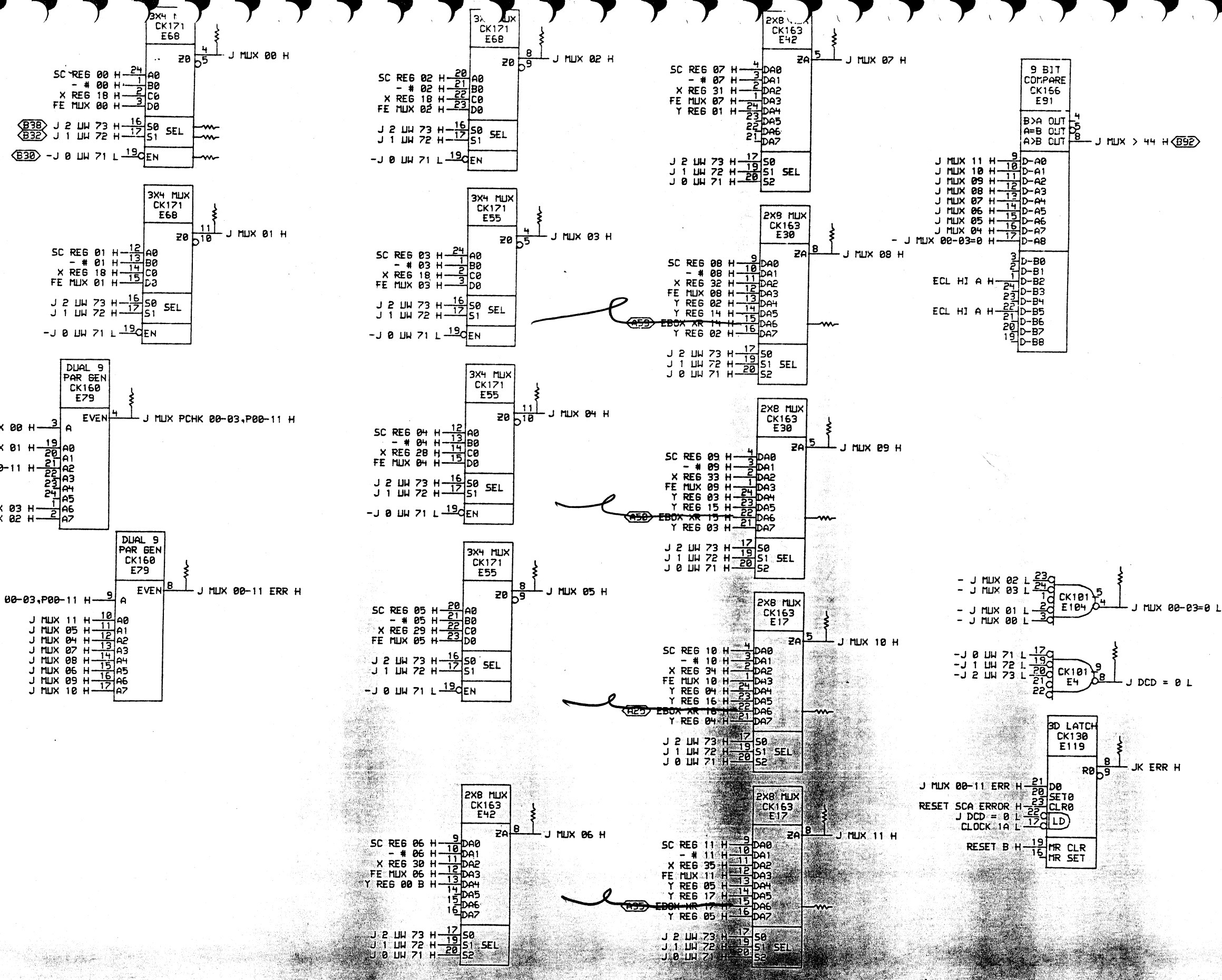
REVISIONS	
CHK	CHANGE NO. REV

digital	DRW. <i>J. Lamy</i>	DATE 15-OCT-82	ENG.	DATE	TITLE: # FLD INPUT
	CHK'D.	DATE	BOARD LOCATION:	SHEET OF	
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		SIZE D	CODE CS
				NUMBER L0046-0-SCA2	REV.

D C B A

REV. NUMBER L0046-0-SCA2

SIZE CODE CS

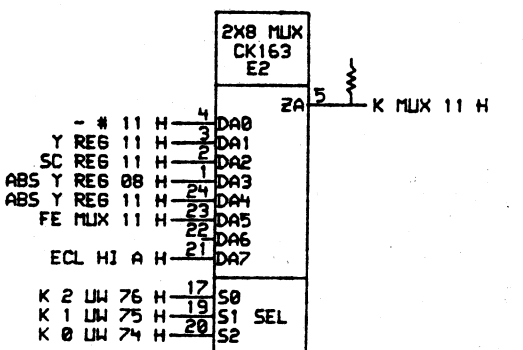
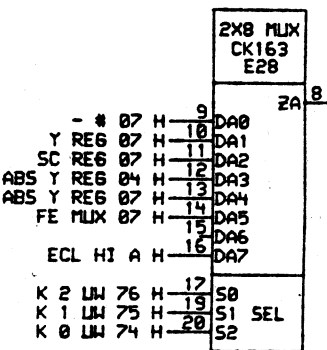
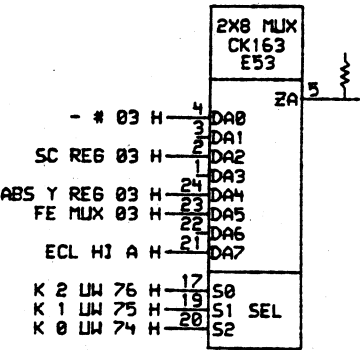
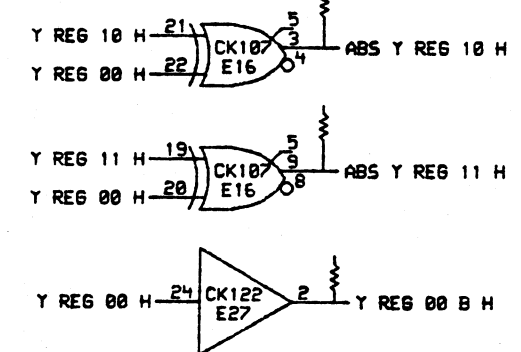
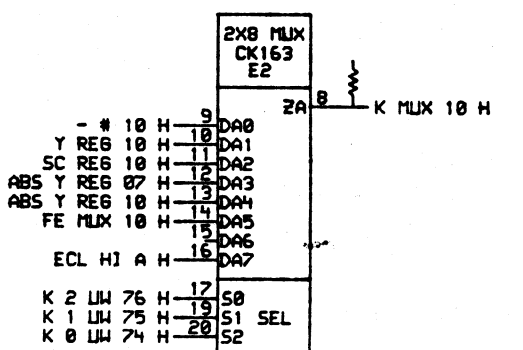
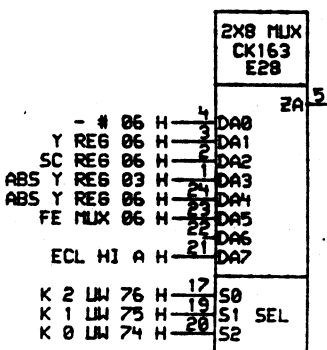
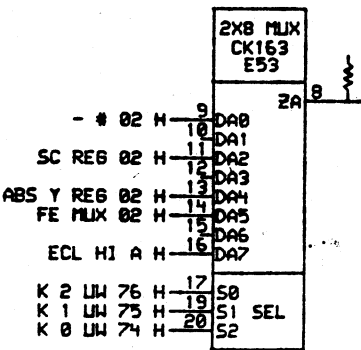
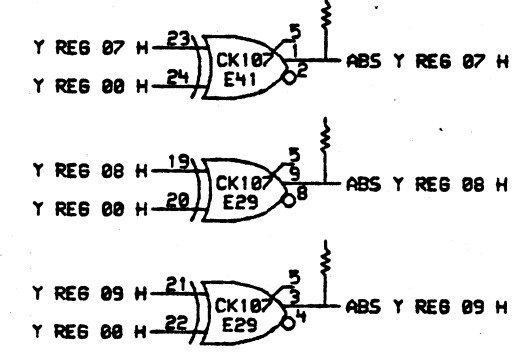
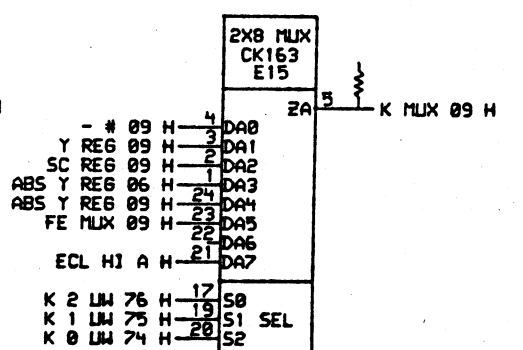
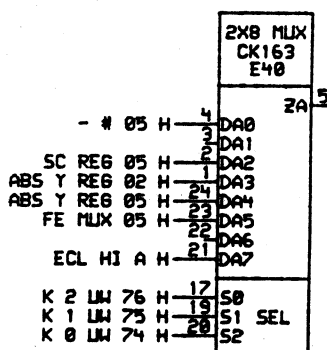
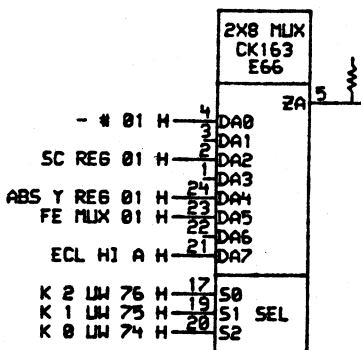
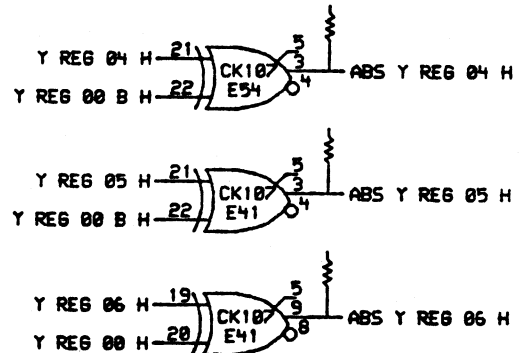
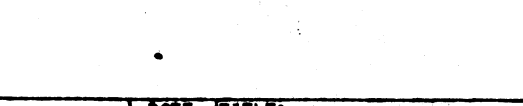
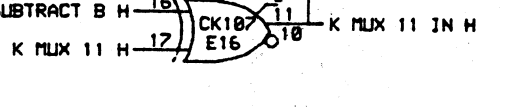
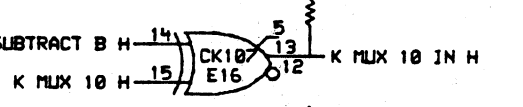
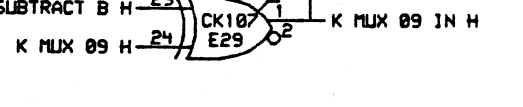
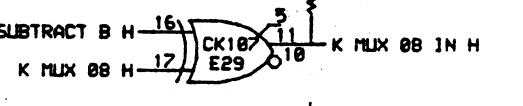
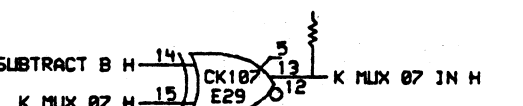
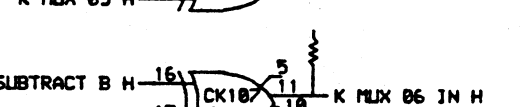
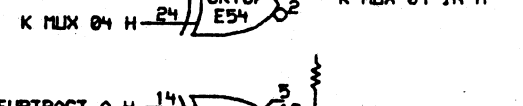
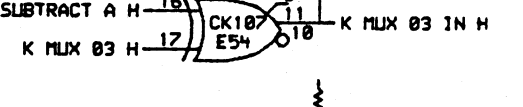
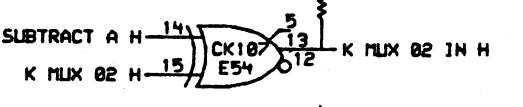
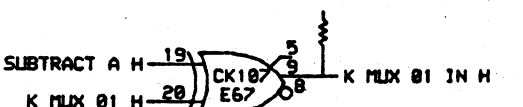
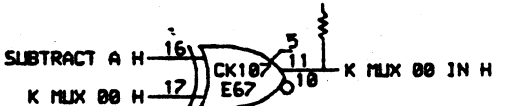
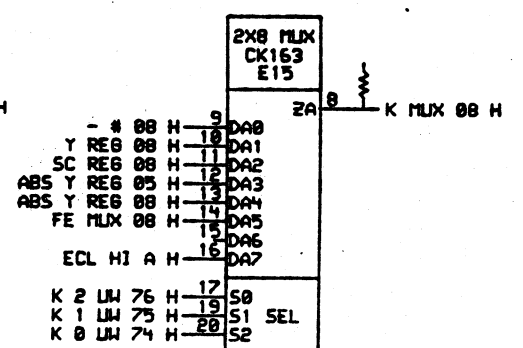
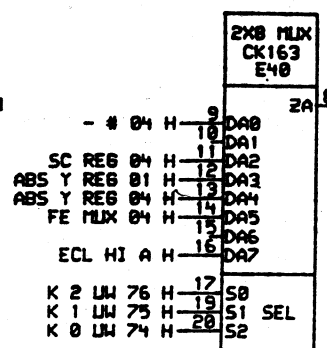
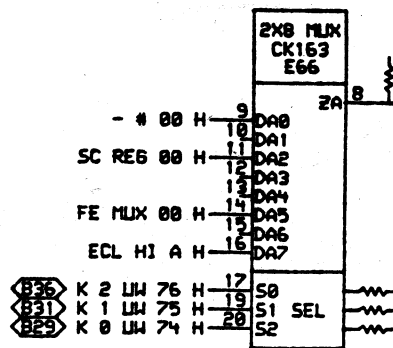
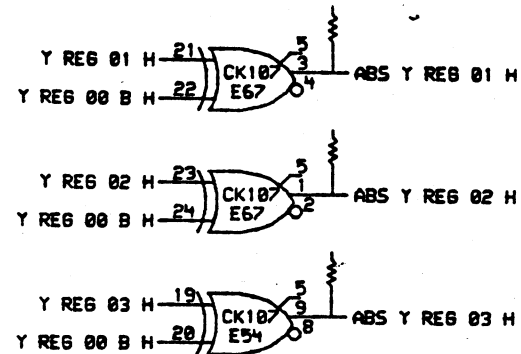


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REVISIONS		
CHK	CHANGE NO.	REV

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DATE: 04-FEB-82	BOARD LOCATION:	SHEET:	
DATE: 104-FEB-82 13:34	NEXT HIGHER ASSEMBLY:	SIZE:	CODE: D

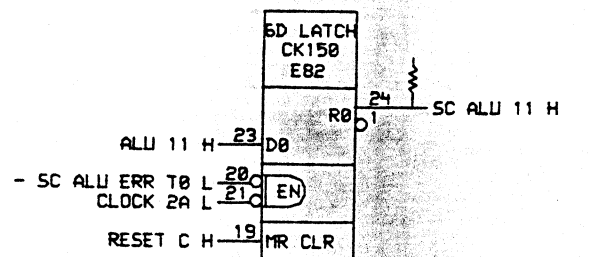
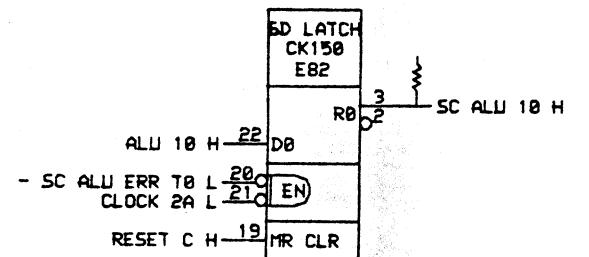
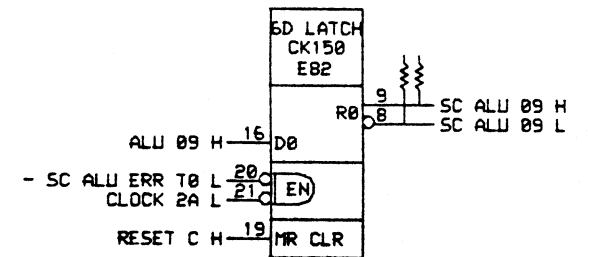
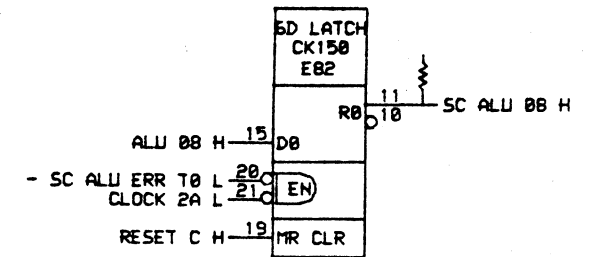
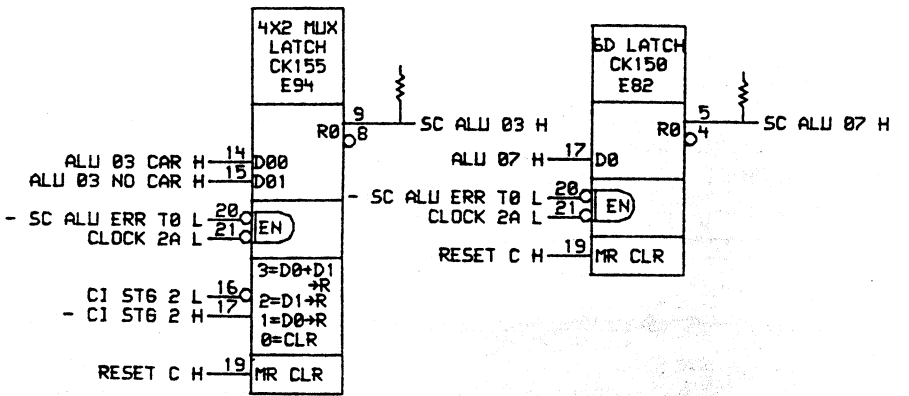
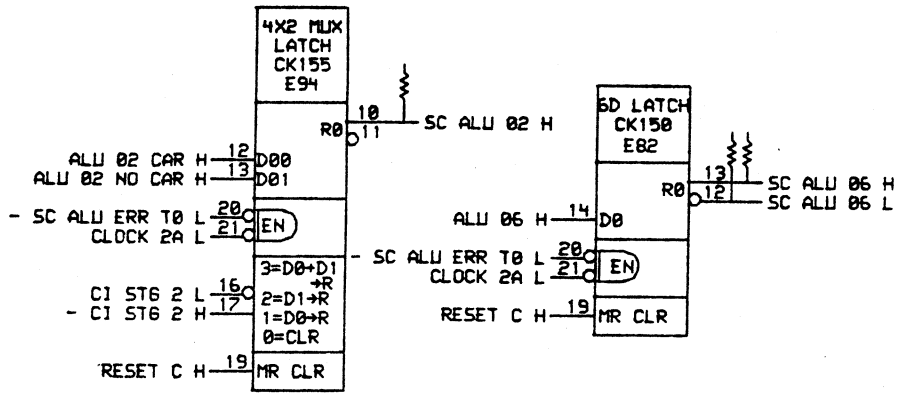
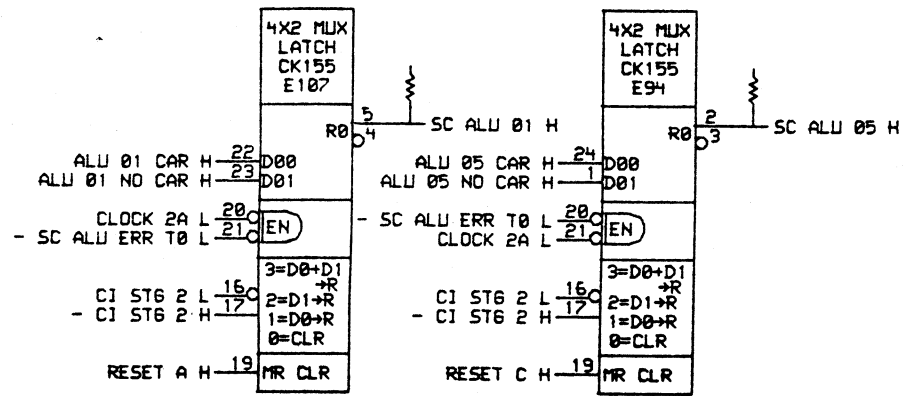
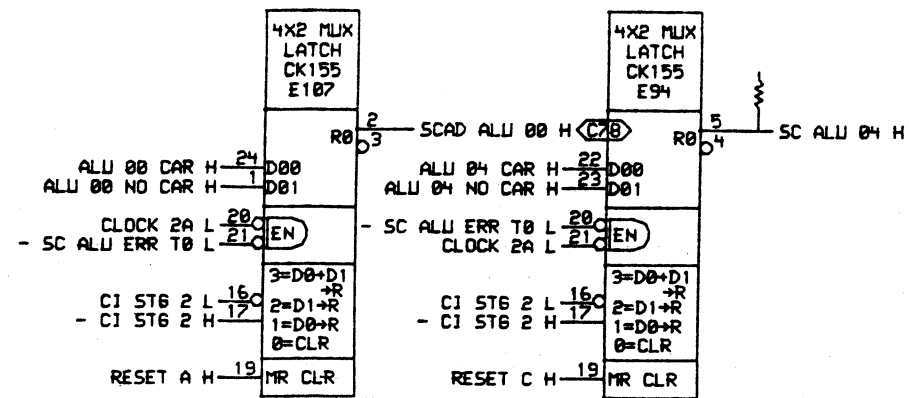
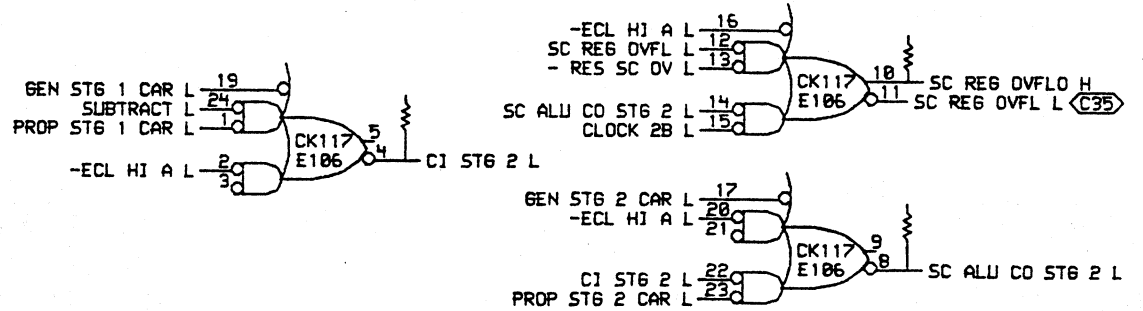
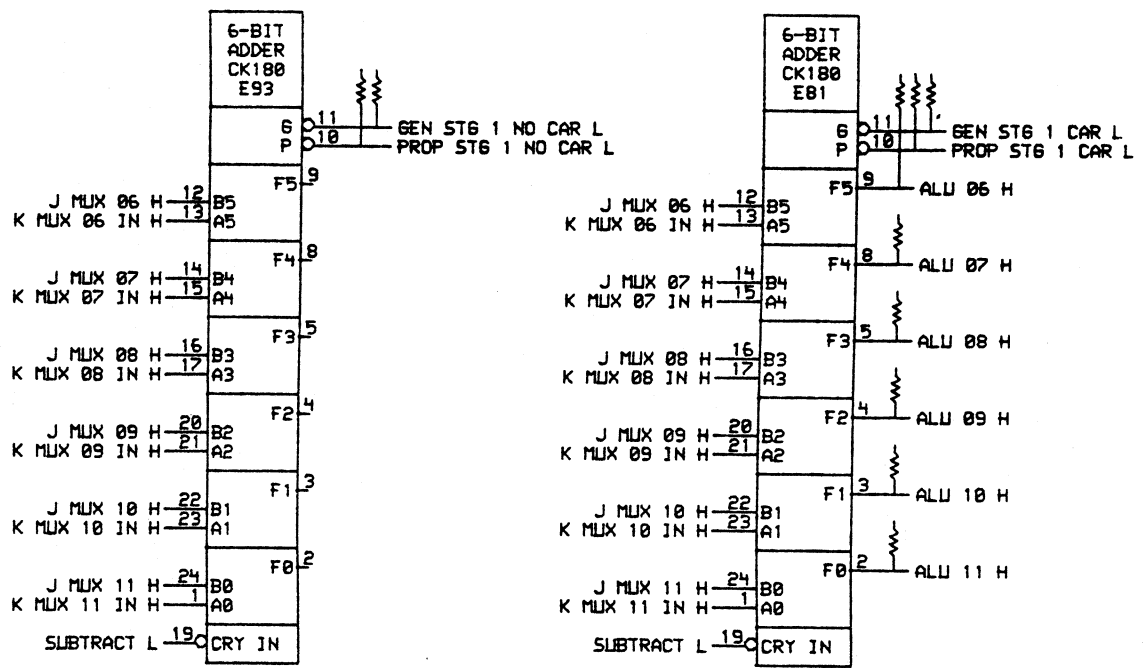
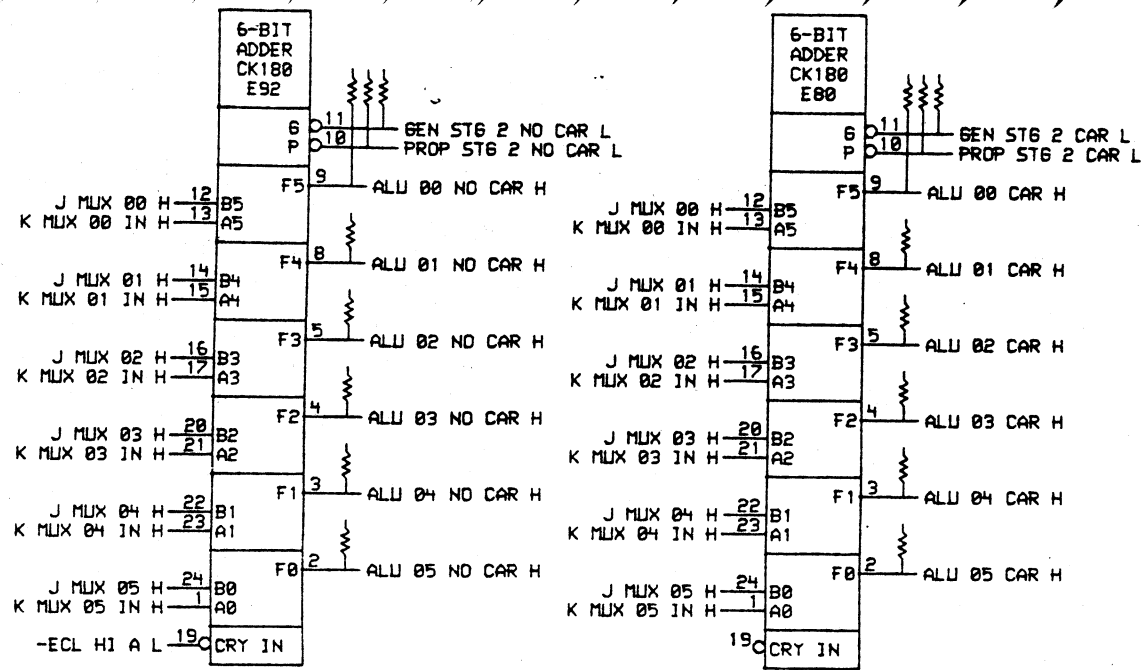
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REVISIONS	
CHK	CHANGE NO. REV

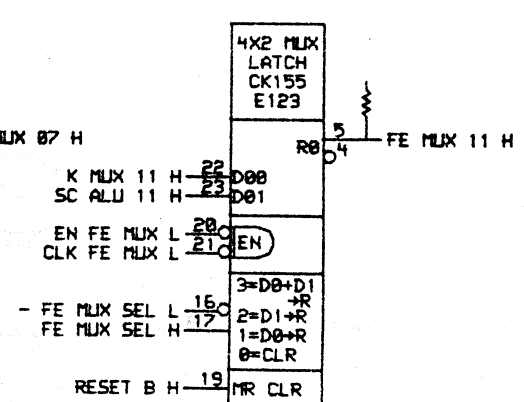
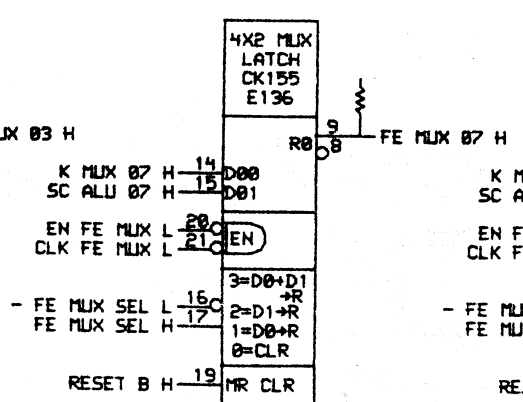
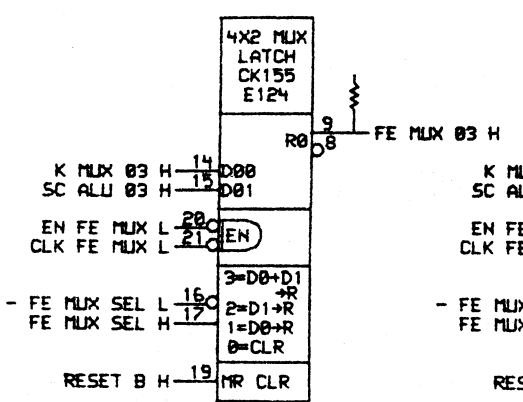
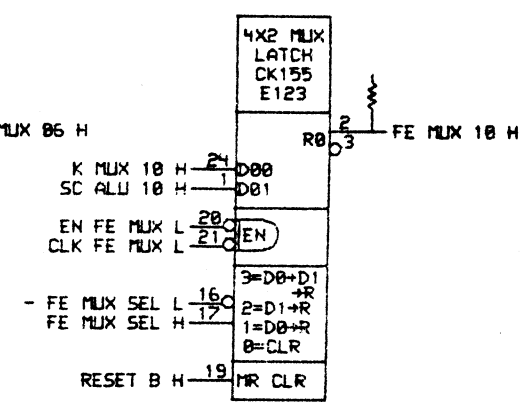
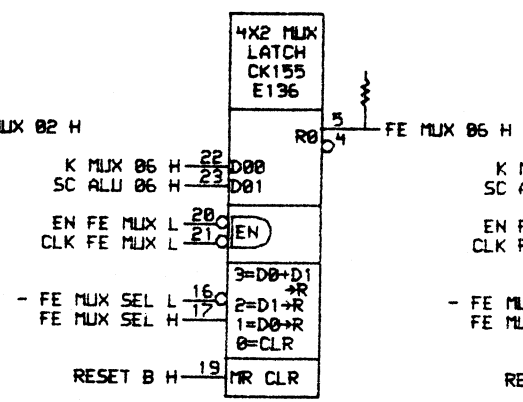
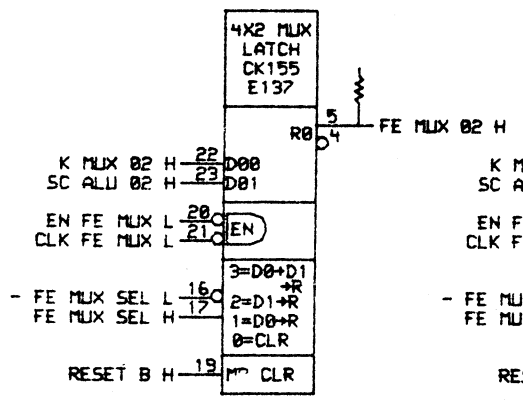
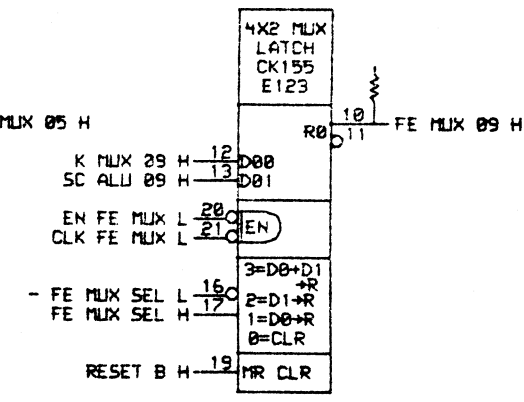
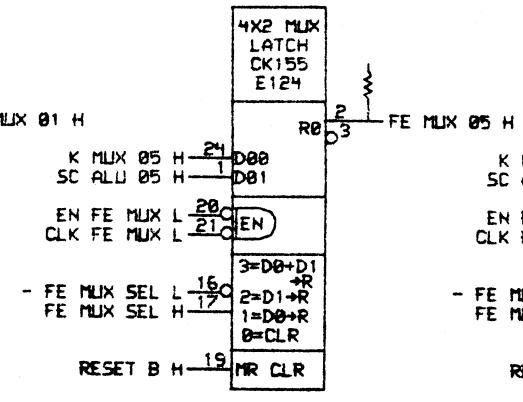
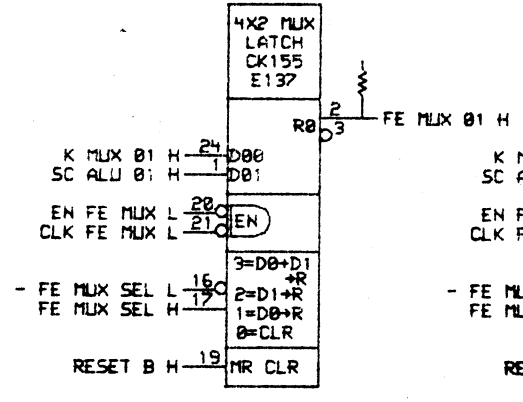
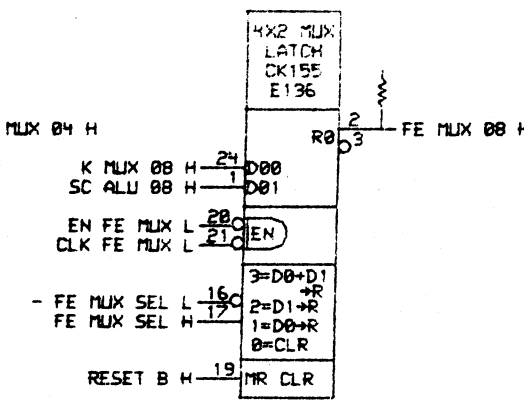
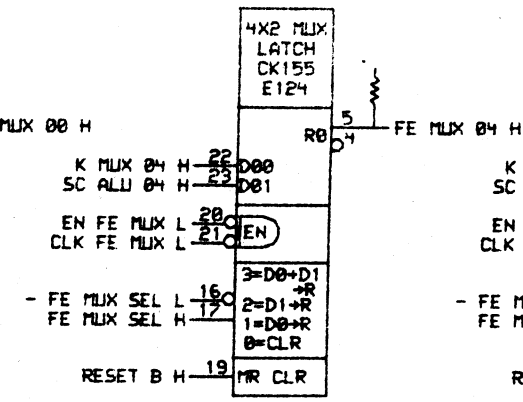
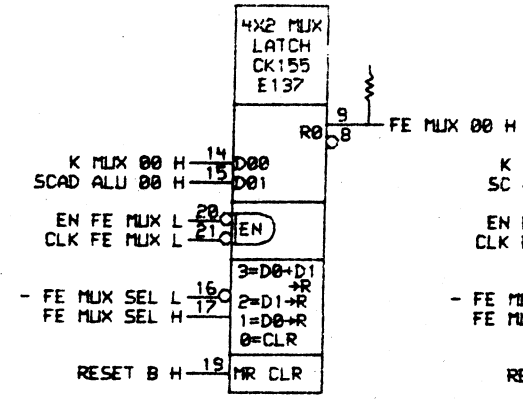
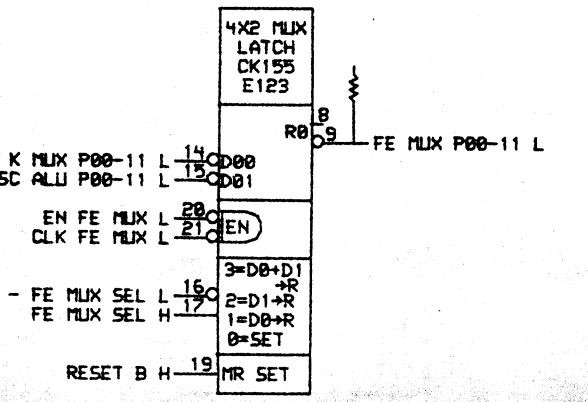
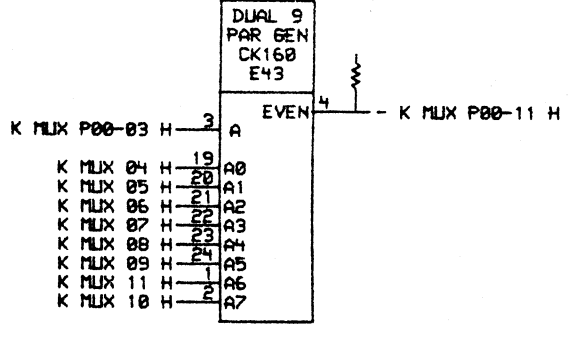
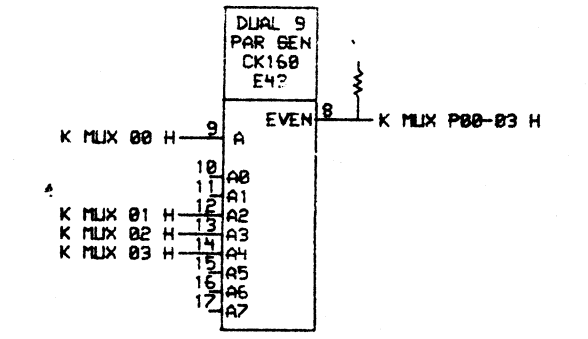
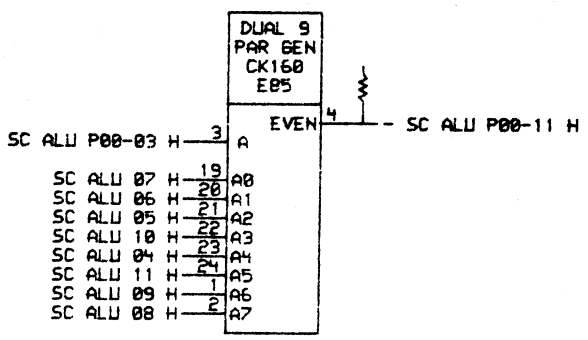
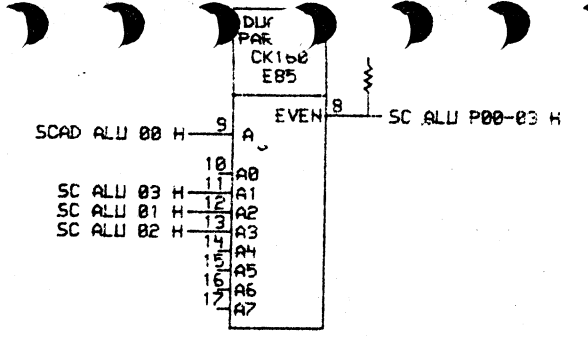
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	DATE	BOARD LOCATION	
FIRST USED ON OPTION/MODEL:	SIZE CODE D CS	NUMBER L0046-0-SCA4	REV.



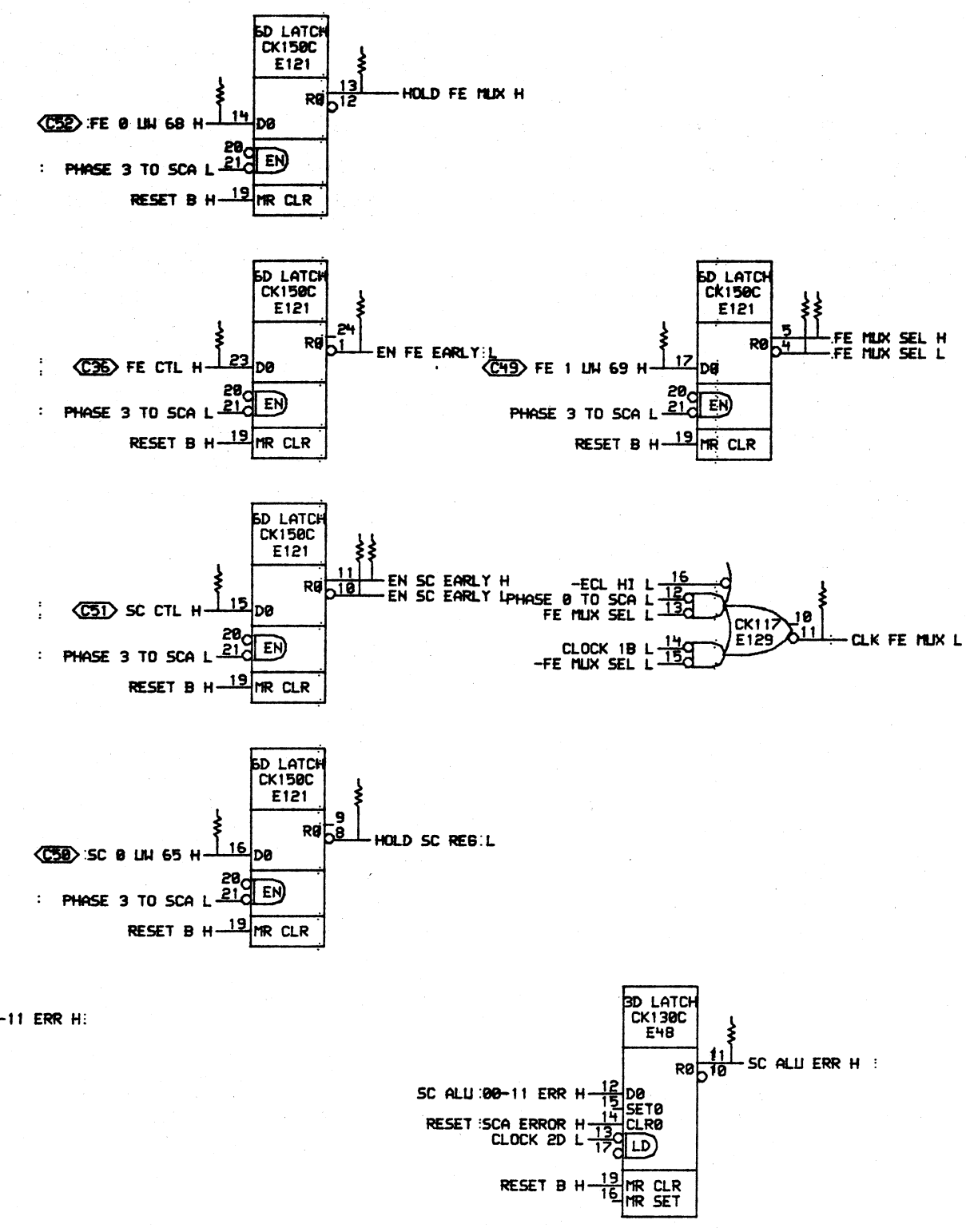
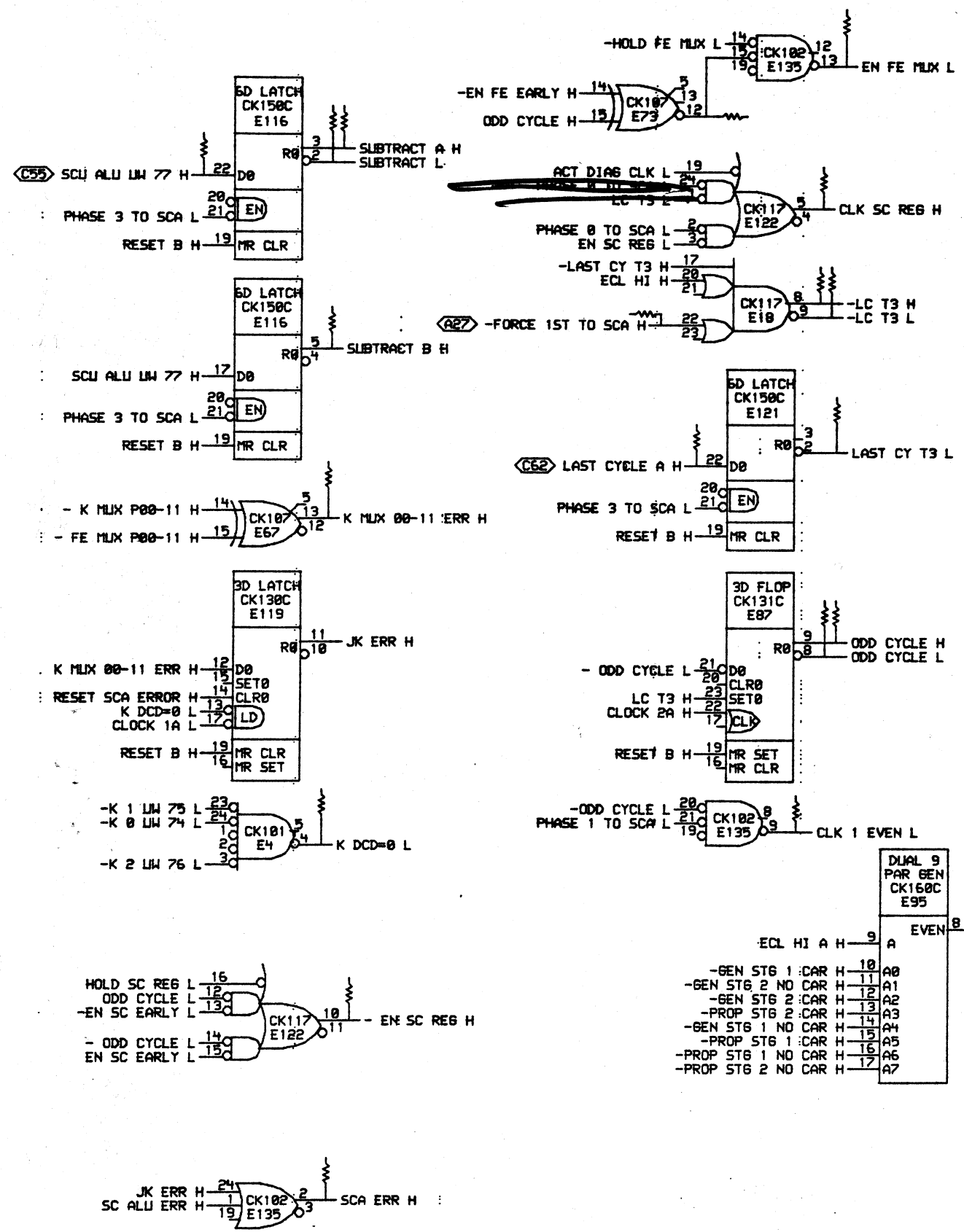
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REVISIONS
CHK CHANGE NO. REV

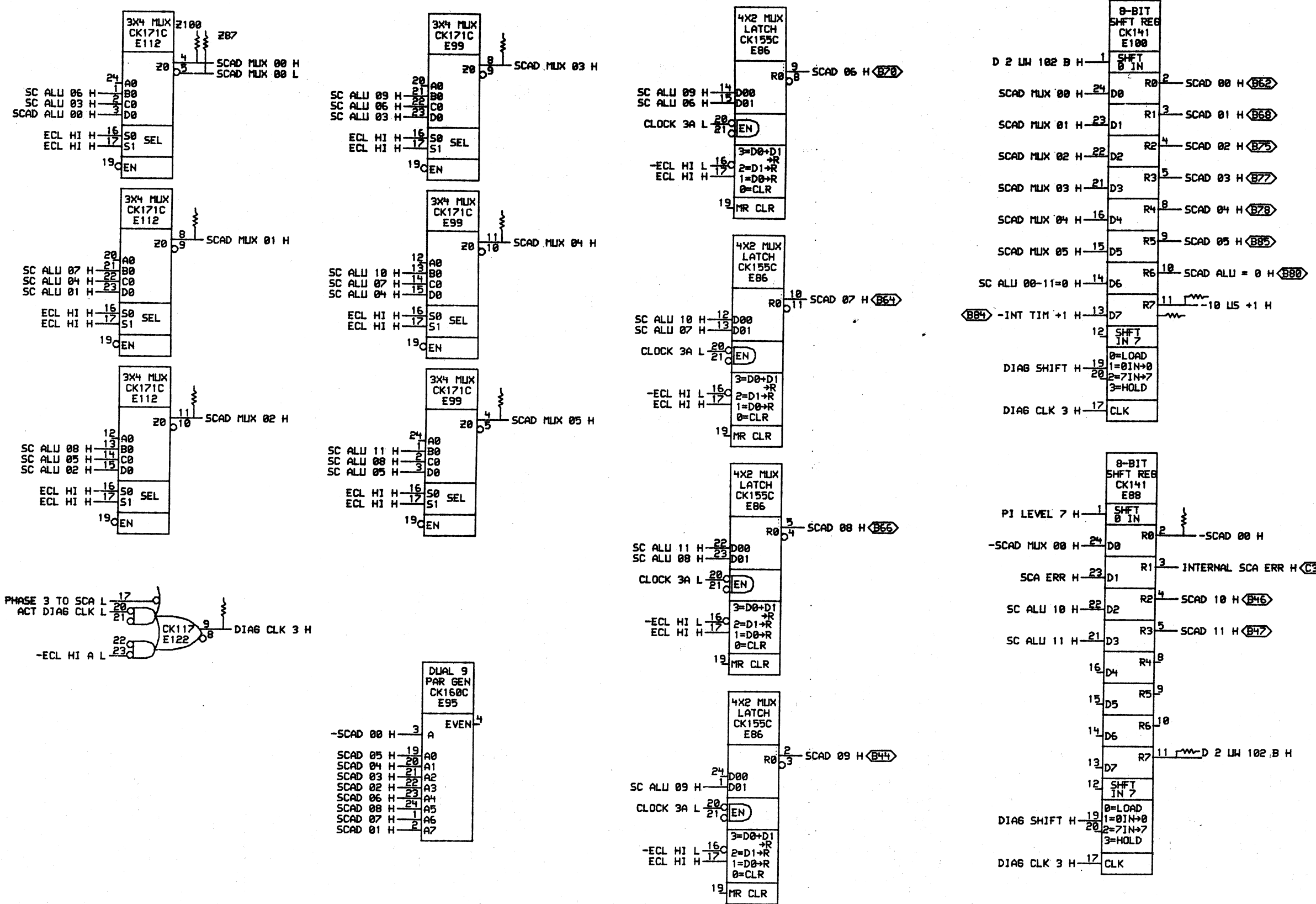
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DRN.	DATE	ENG.	DATE	TITLE:
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SIZE CODE				D C5



DUAL 9 PAR GEN CK150C E95	
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10	A0 -GEN STG 1 CAR H
11	A1 -GEN STG 2 NO CAR H
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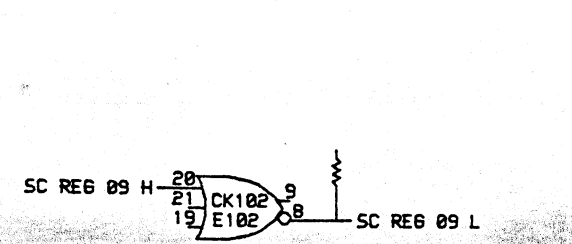
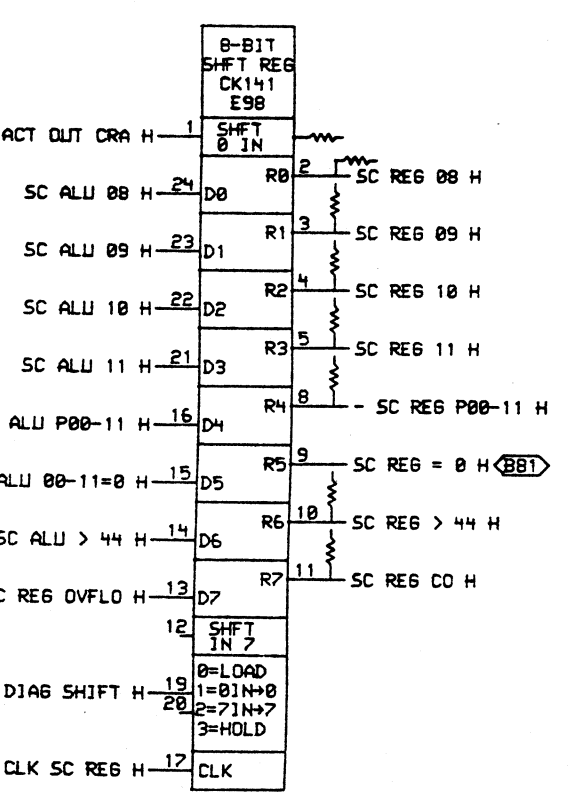
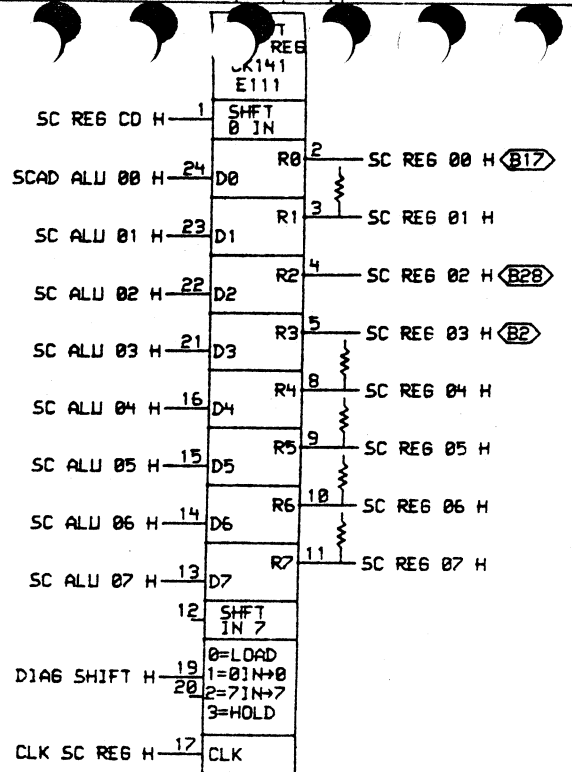
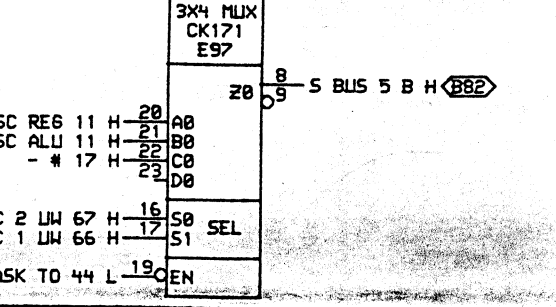
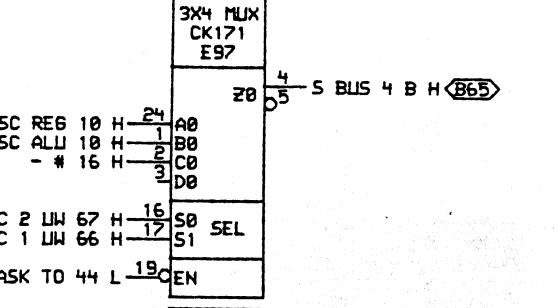
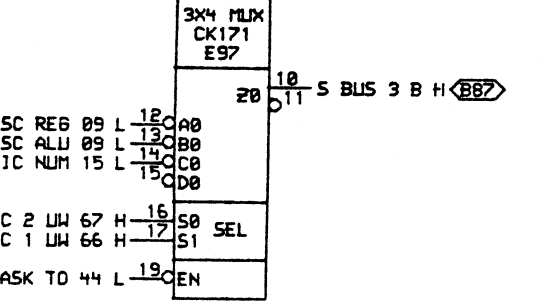
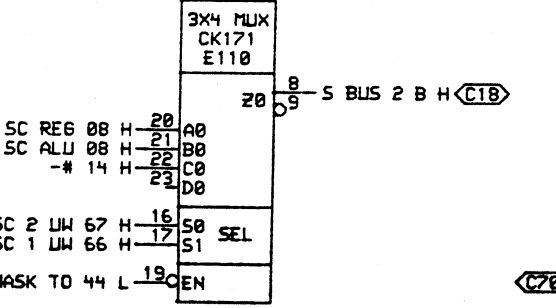
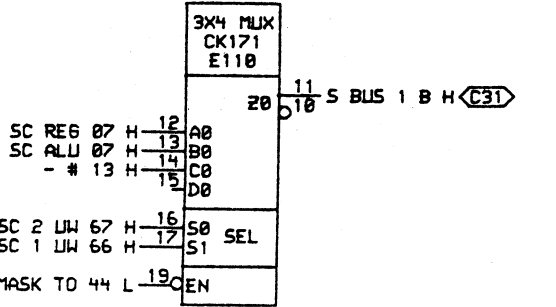
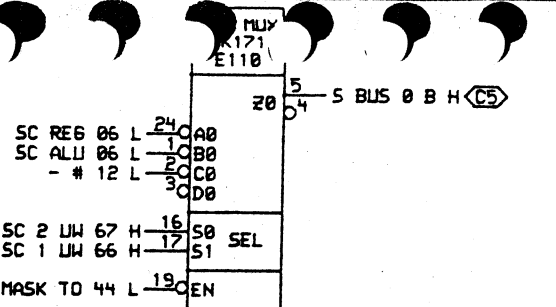
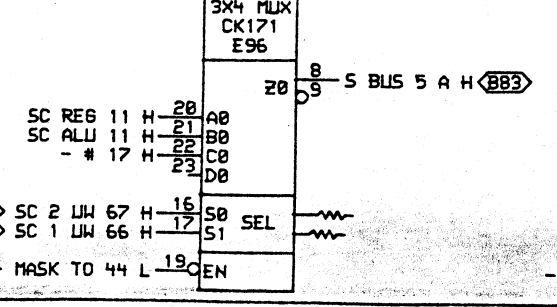
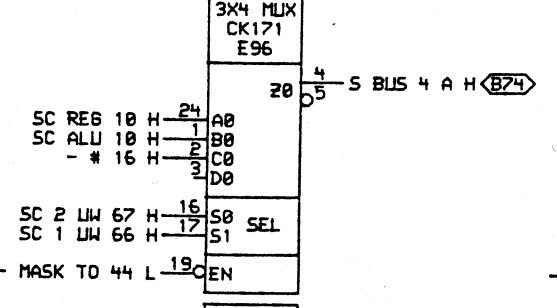
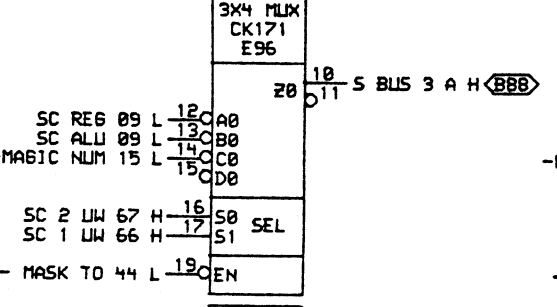
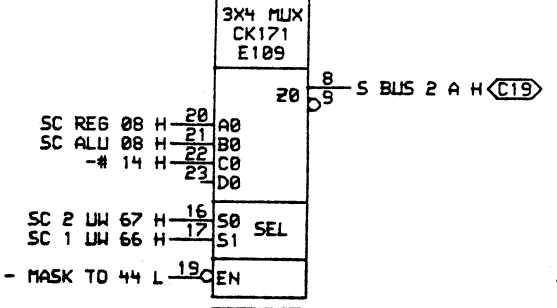
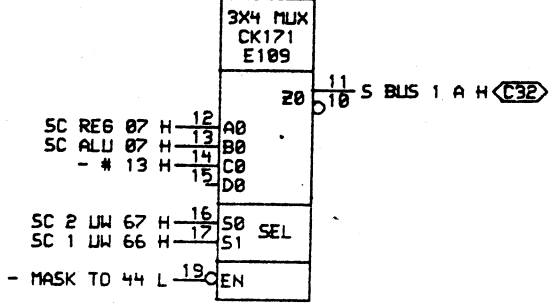
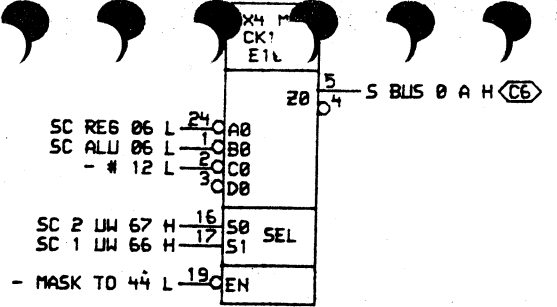
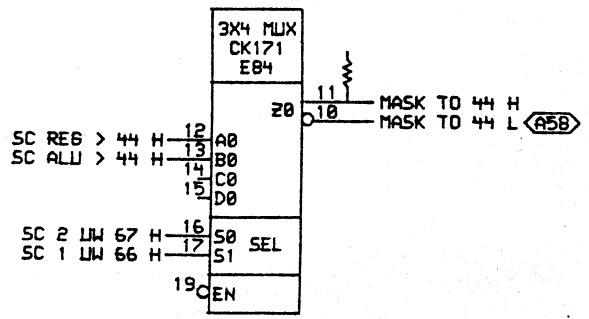
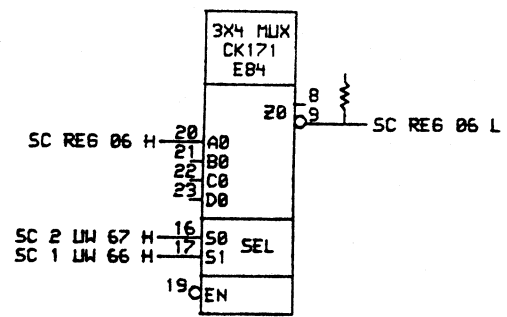
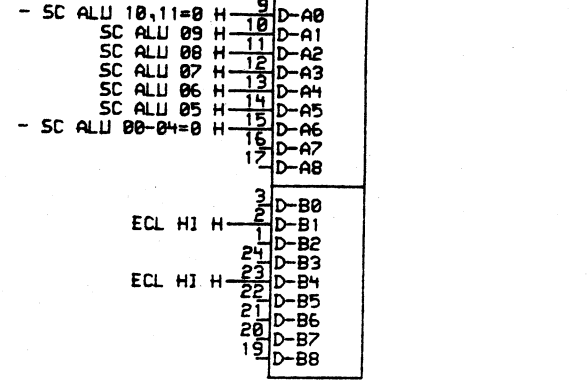
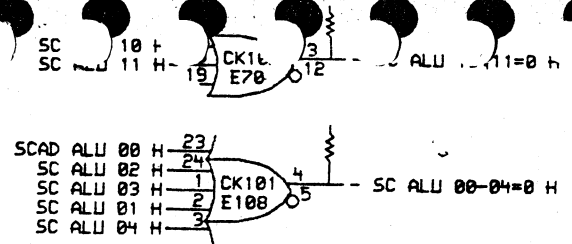


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REVISIONS	
CHK	CHANGE NO. REV

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FIRST USED ON OPTION/MODEL:			D CS	L0046-0-SCAB
			REV.	

D
C
REV.
NUMBER
L0046-0-SCAB
SIZE
D
B
A



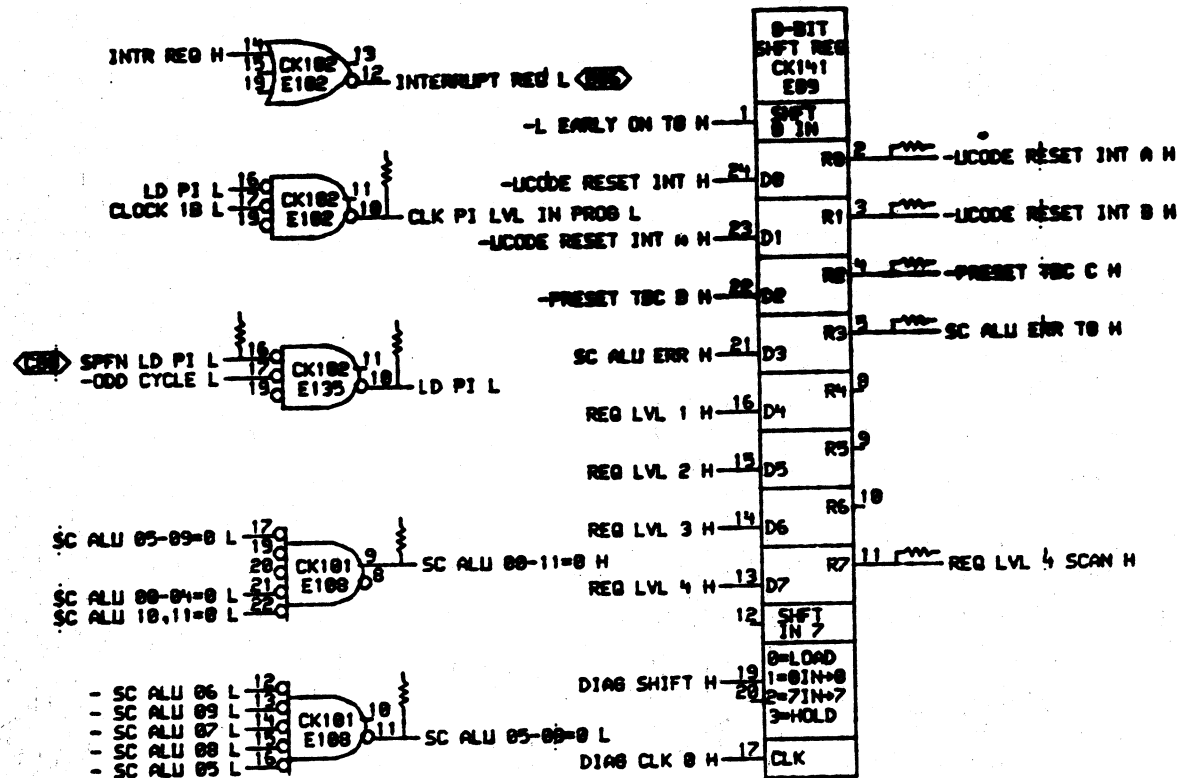
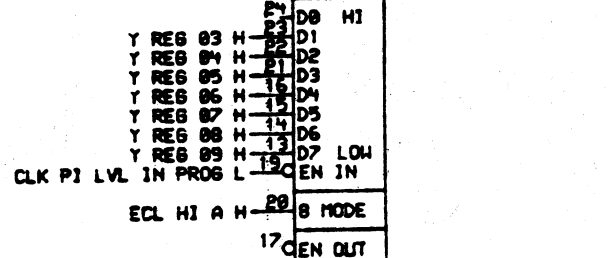
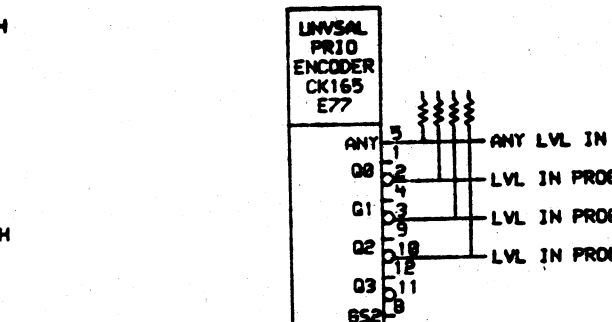
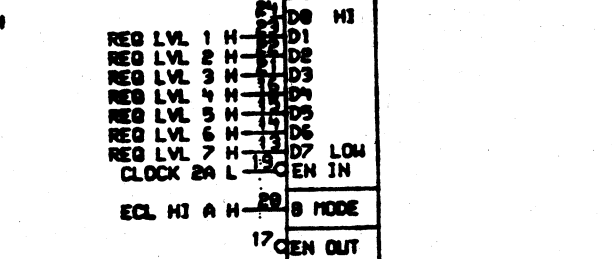
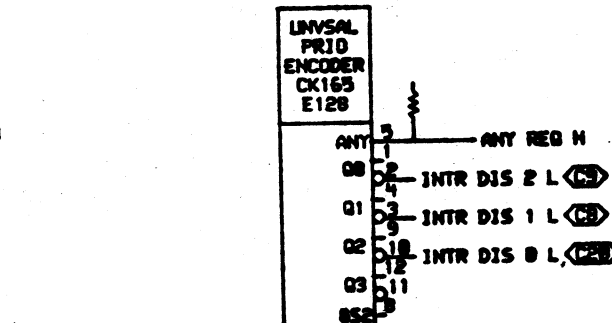
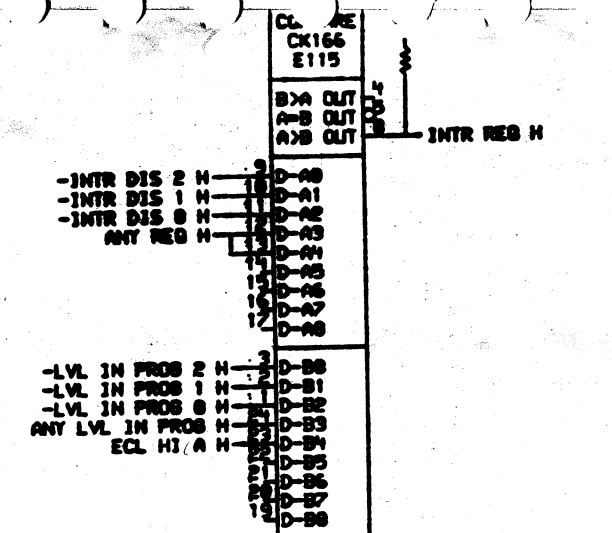
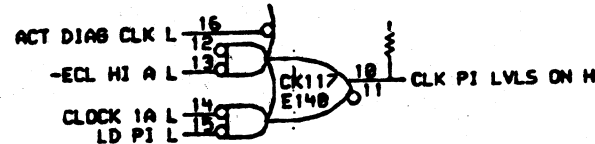
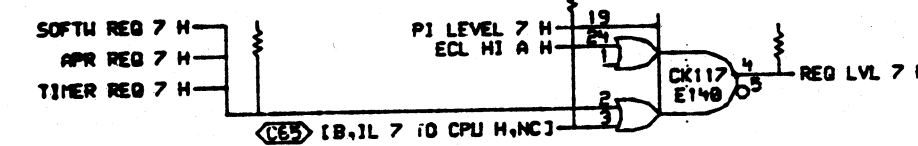
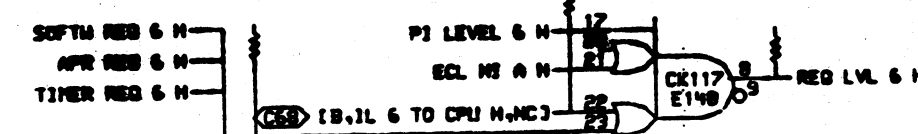
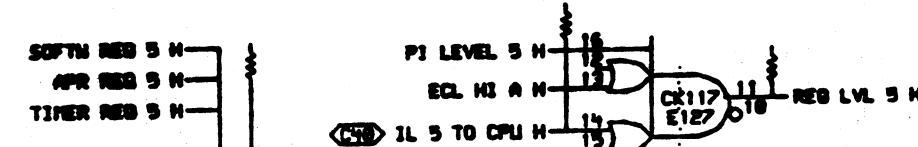
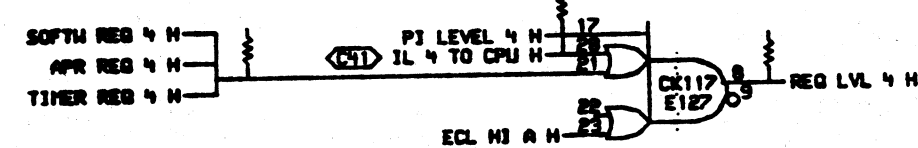
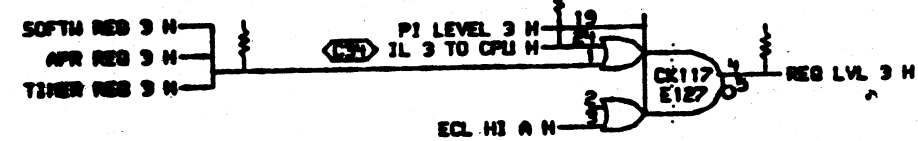
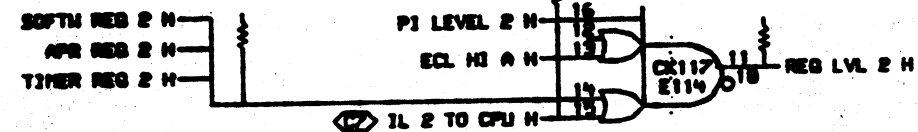
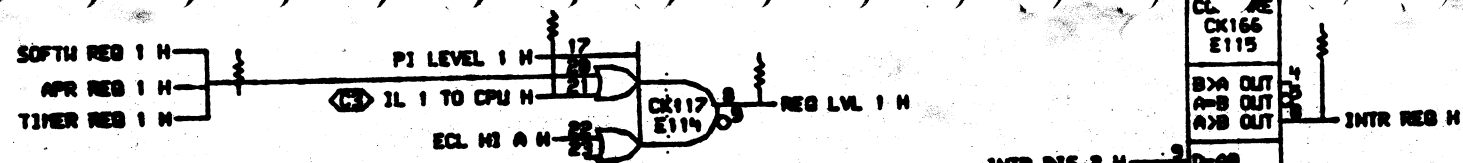
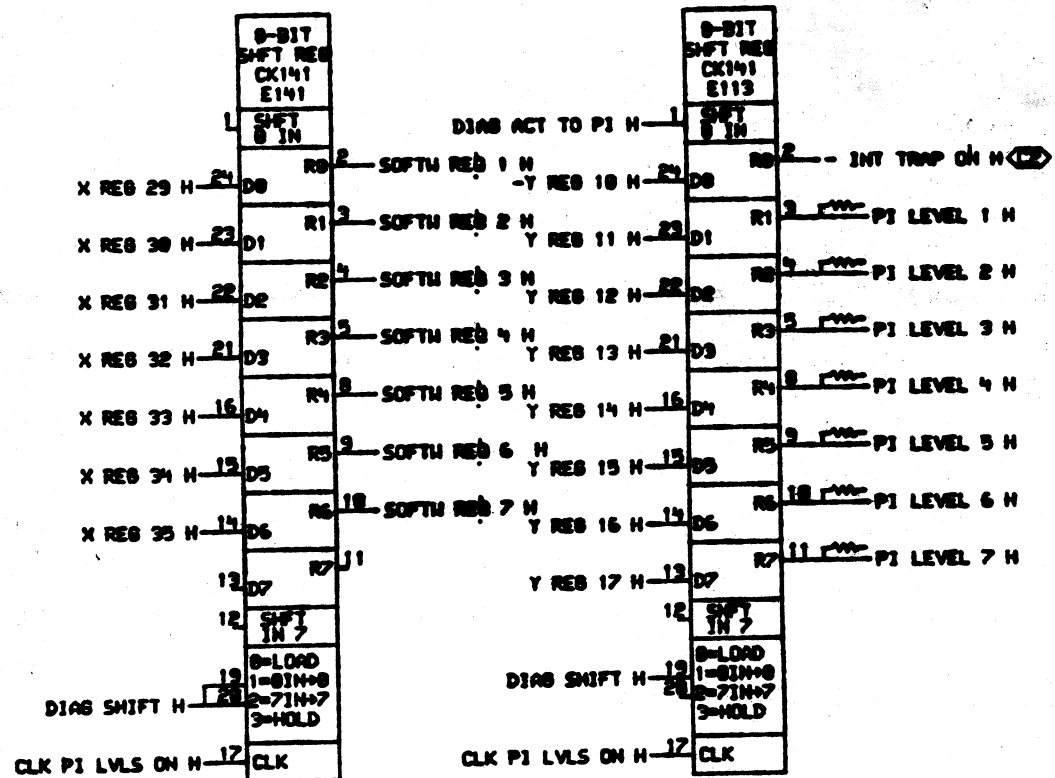
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REVISIONS		
CHK	CHANGE NO.	REV

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 NEXT HIGHER ASSEMBLY:

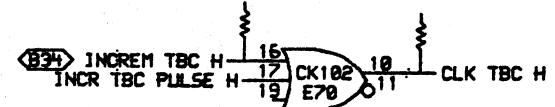
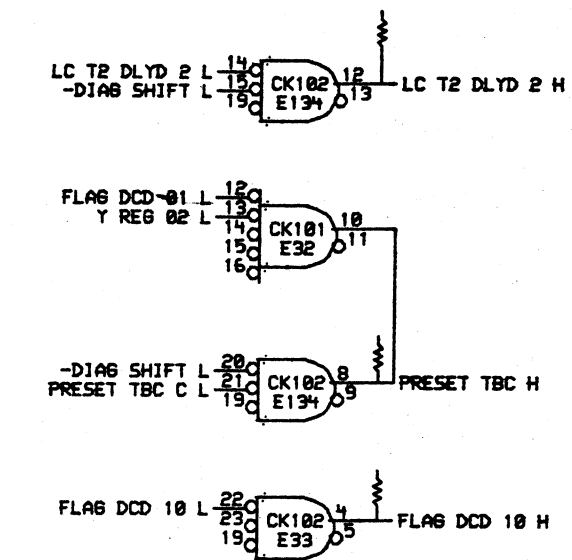
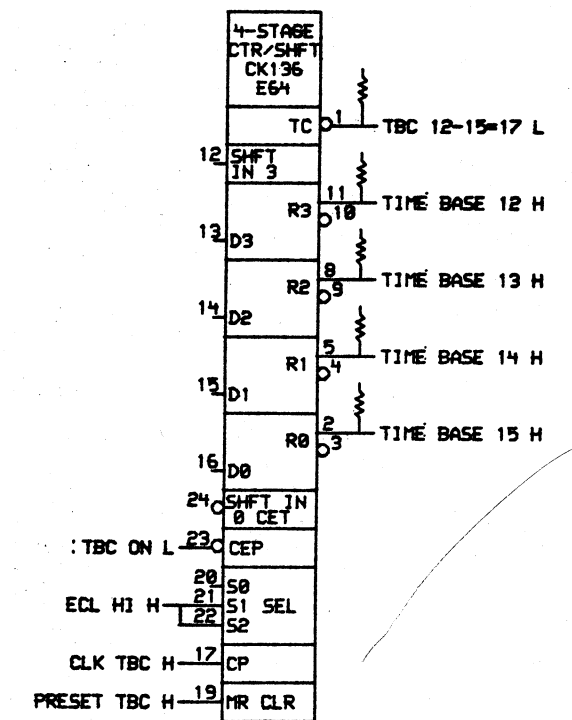
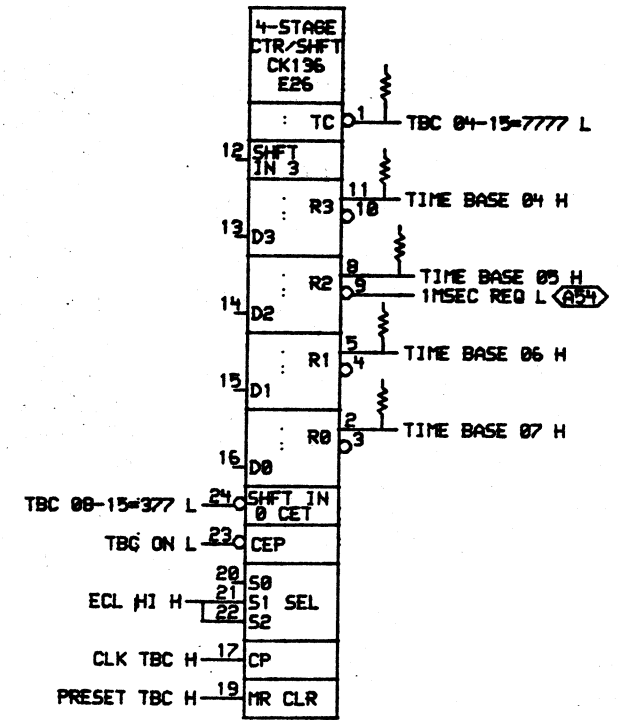
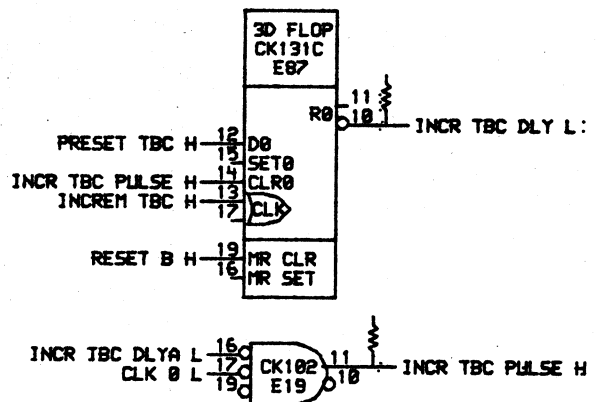
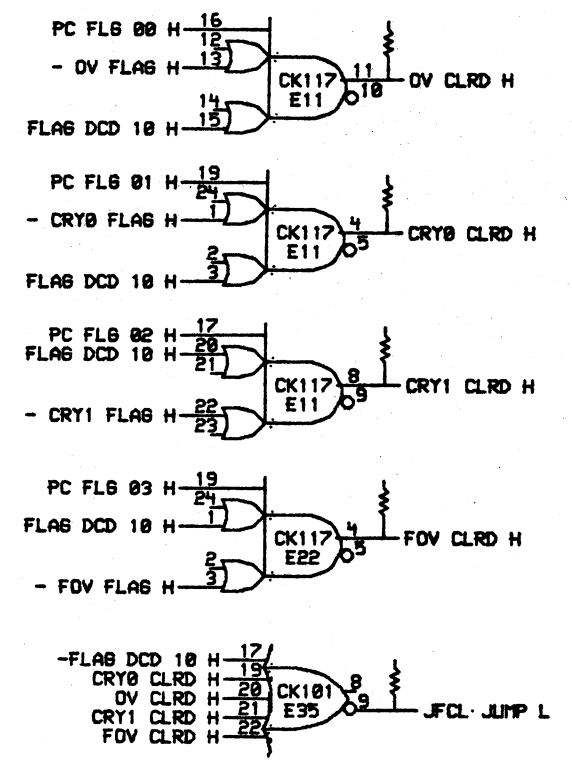
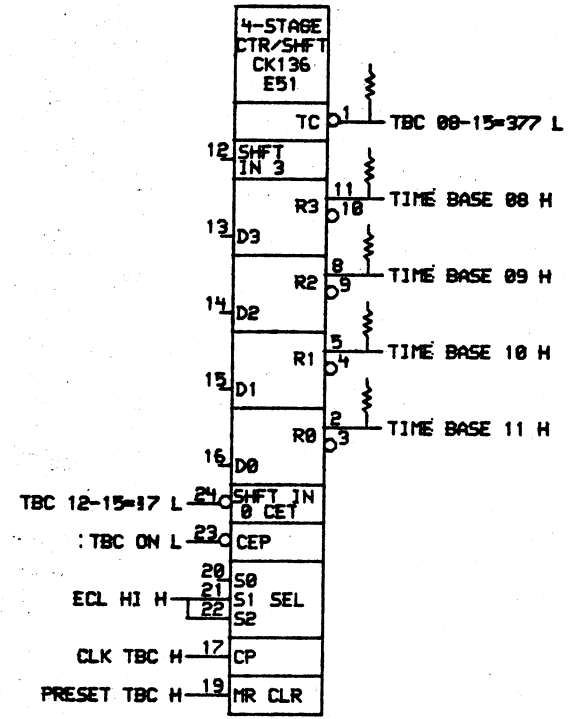
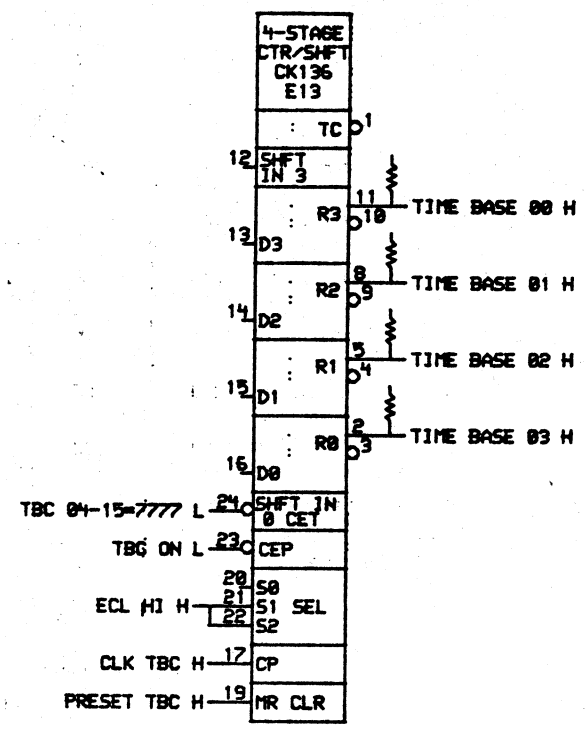
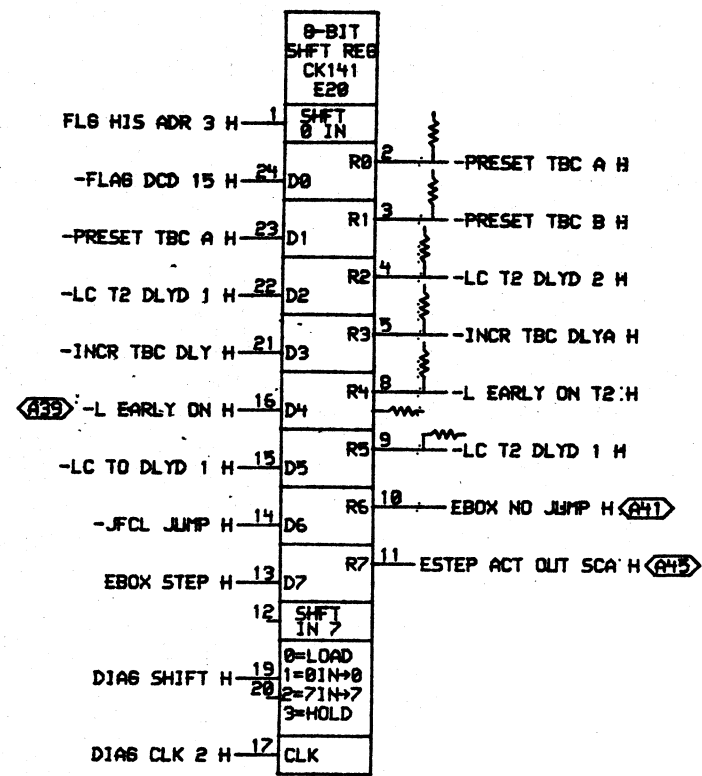
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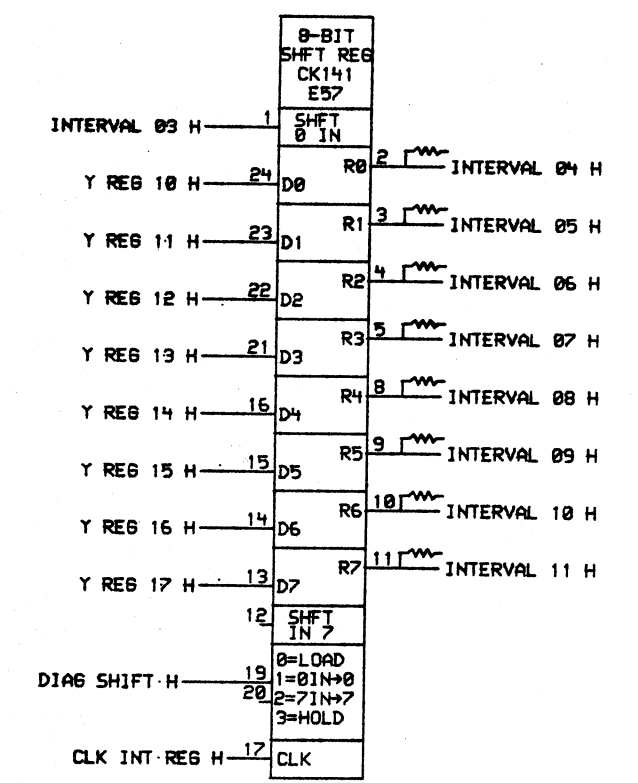
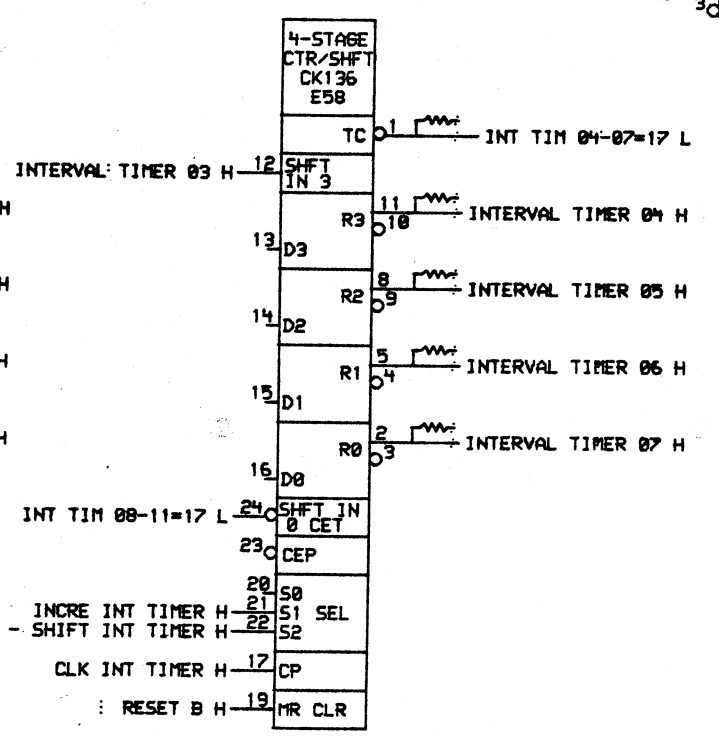
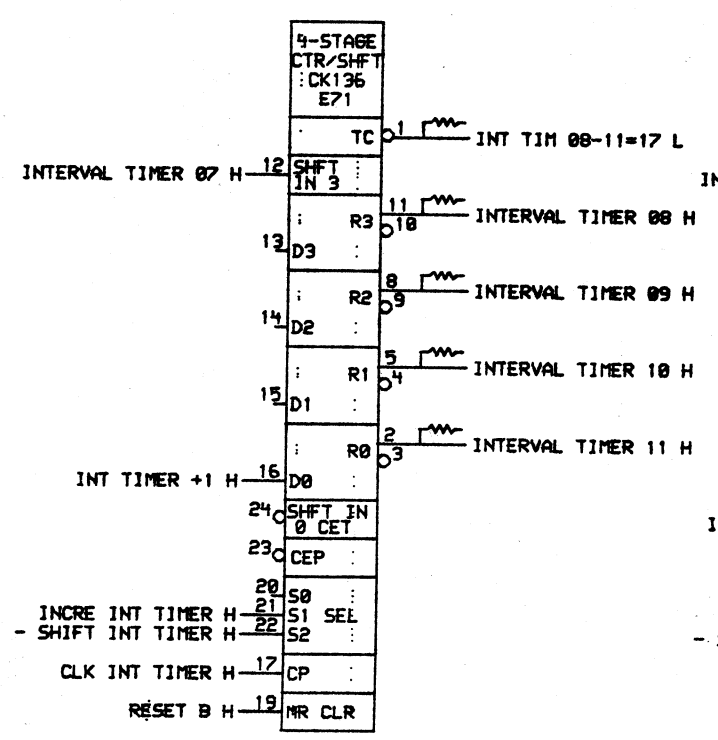
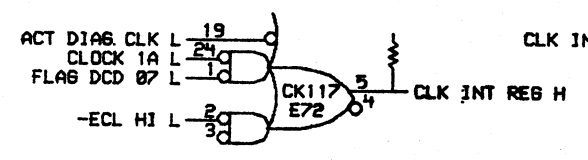
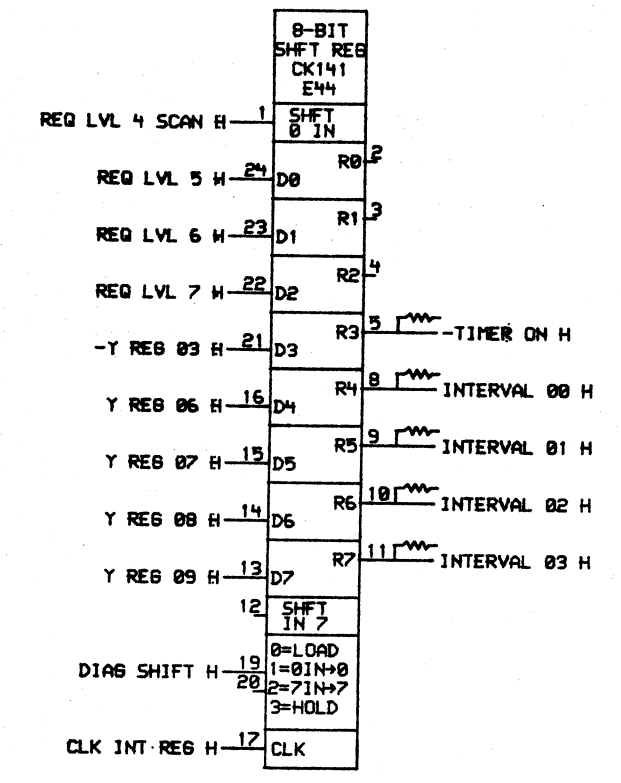
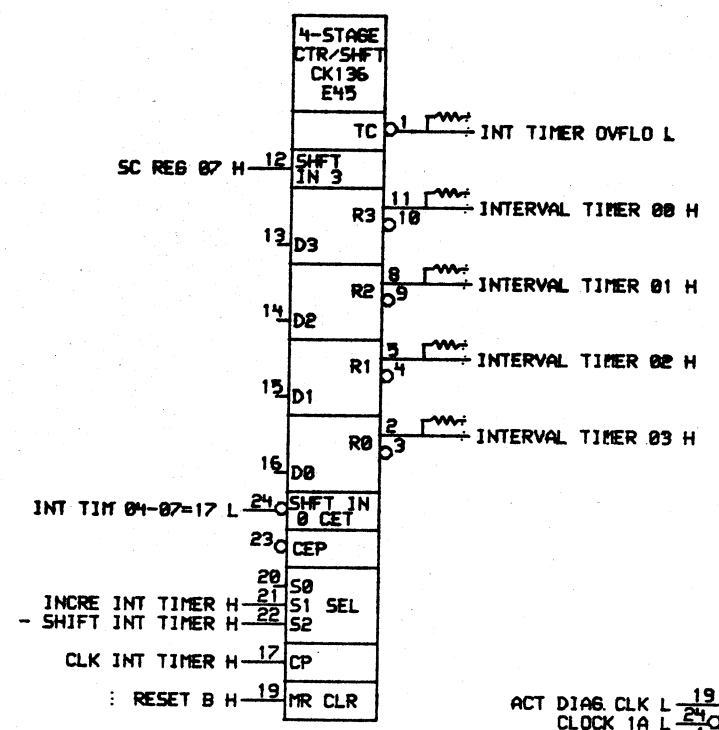
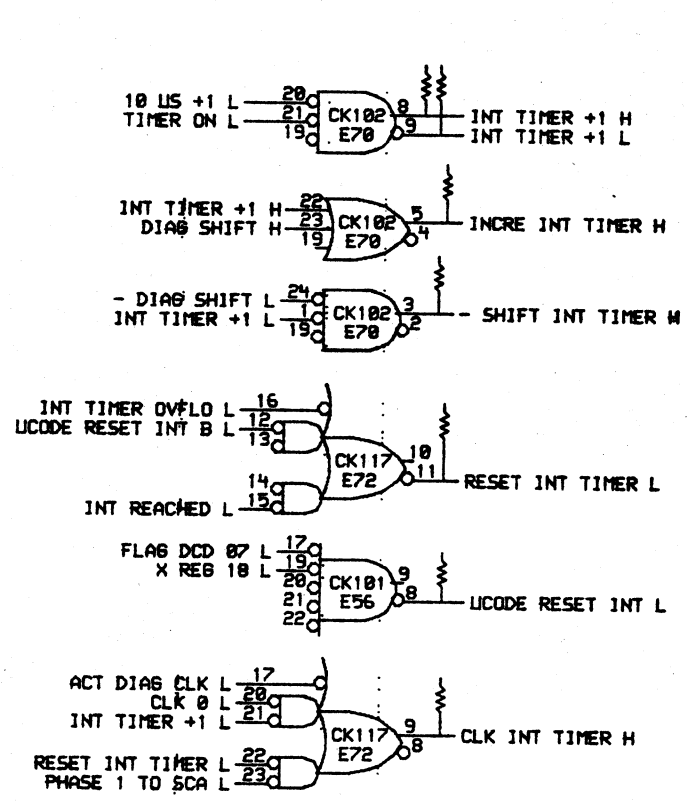


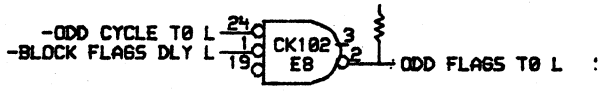
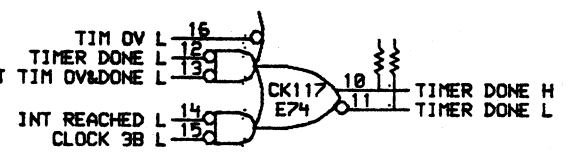
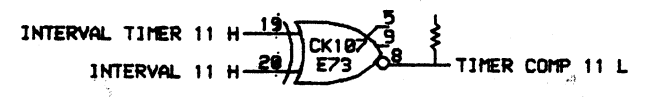
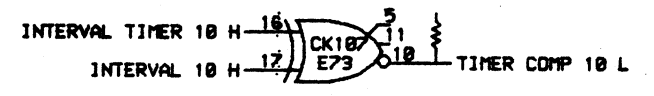
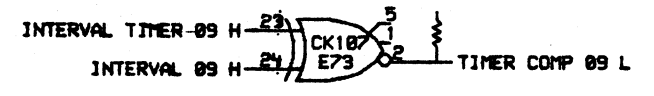
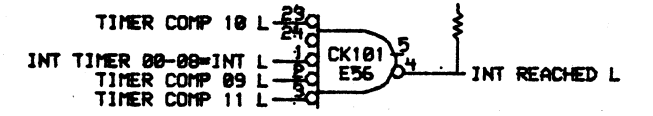
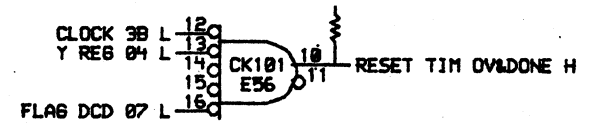
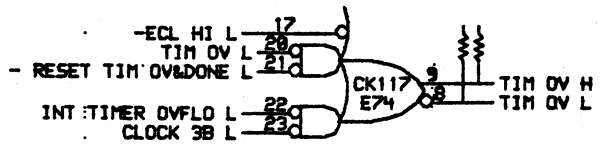
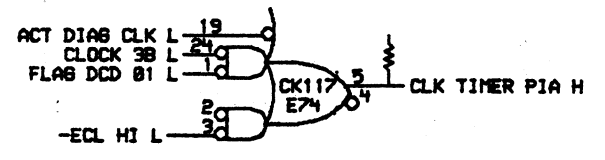
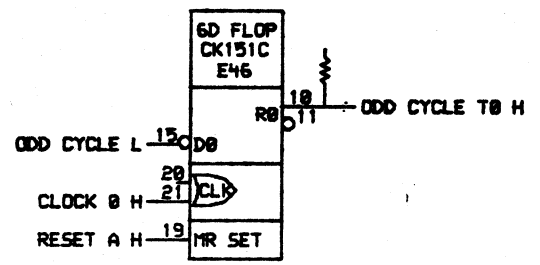
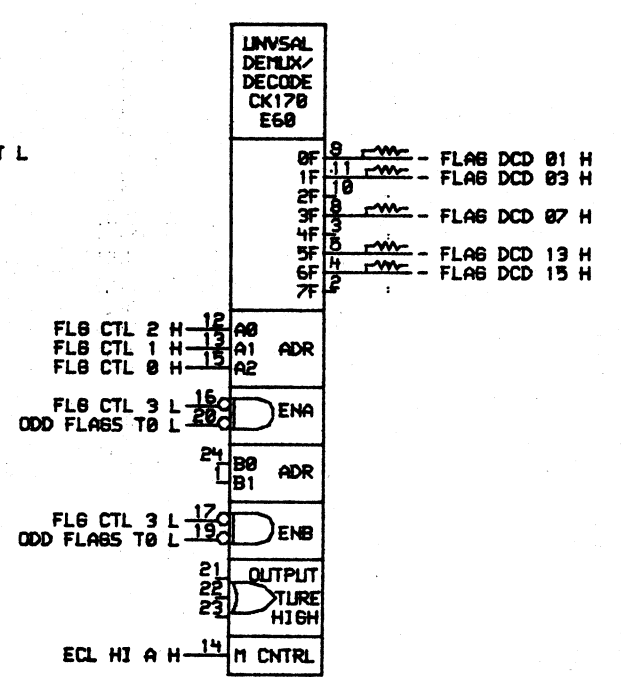
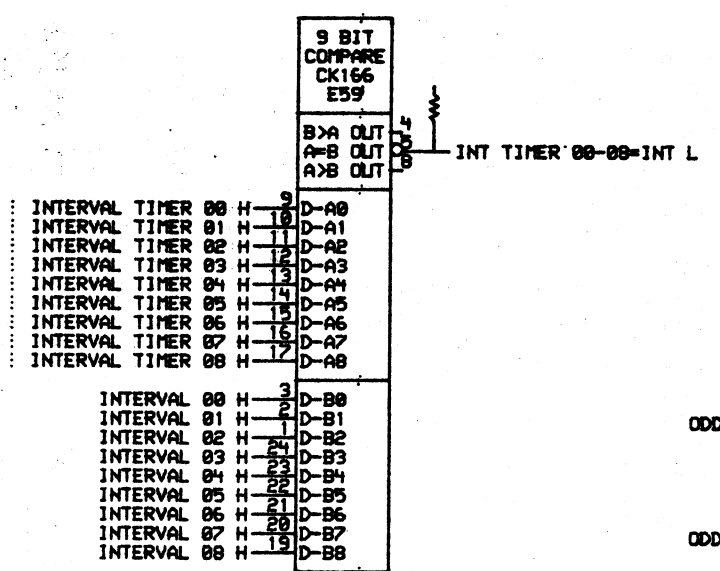
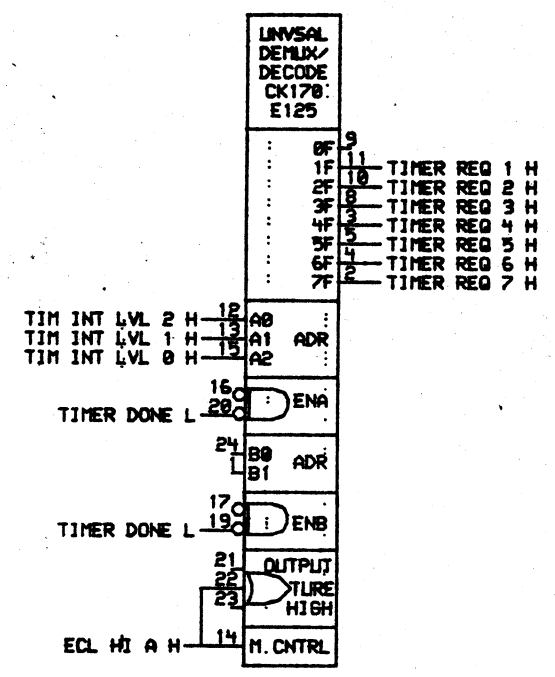
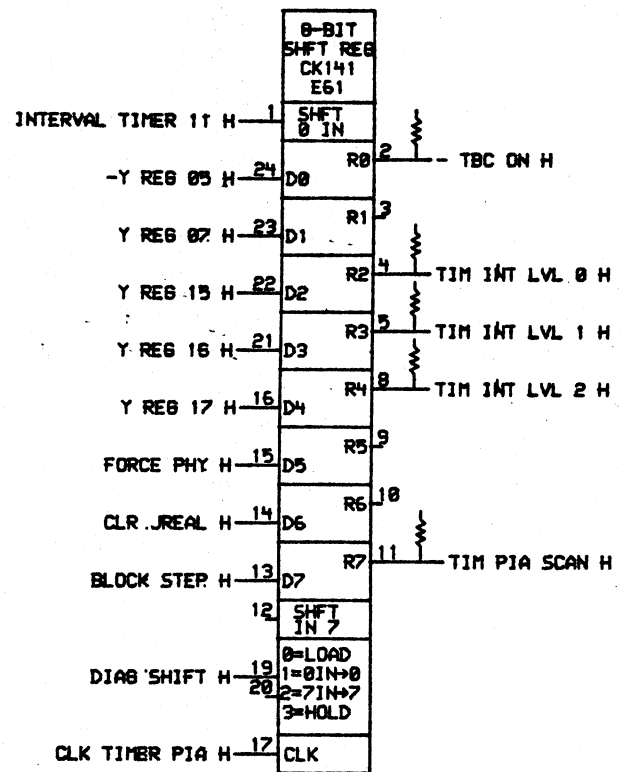
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REVISIONS
CHK CHANGE NO. REV

	DRG. <i>J. J. J.</i>	DATE 17-11-82	ENG.	DATE	TITLE: PI SYSTEM
	CHK'D.	DATE	BOARD LOCATION	SHEET	REV.
FIRST USED ON OPTION MODEL: 116-11-82 2219 NEXT HIGHER ASSEMBLY:					SIZE CODE NUMBER D CS L0046-0-SCAA



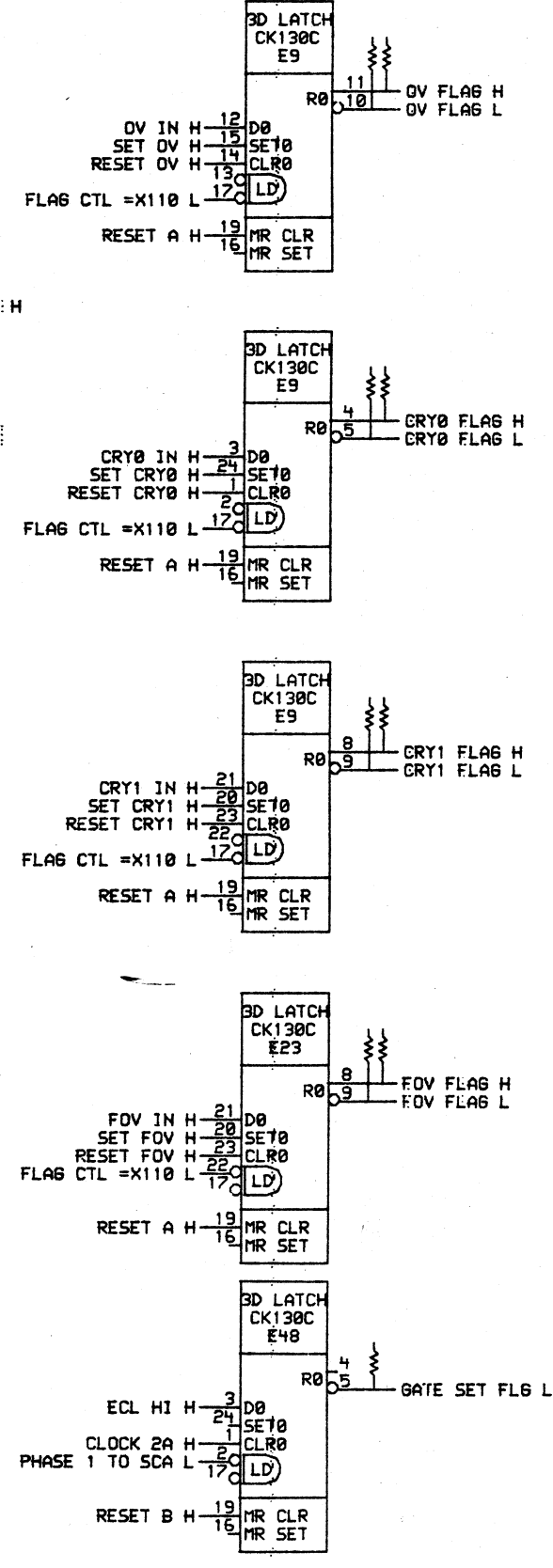
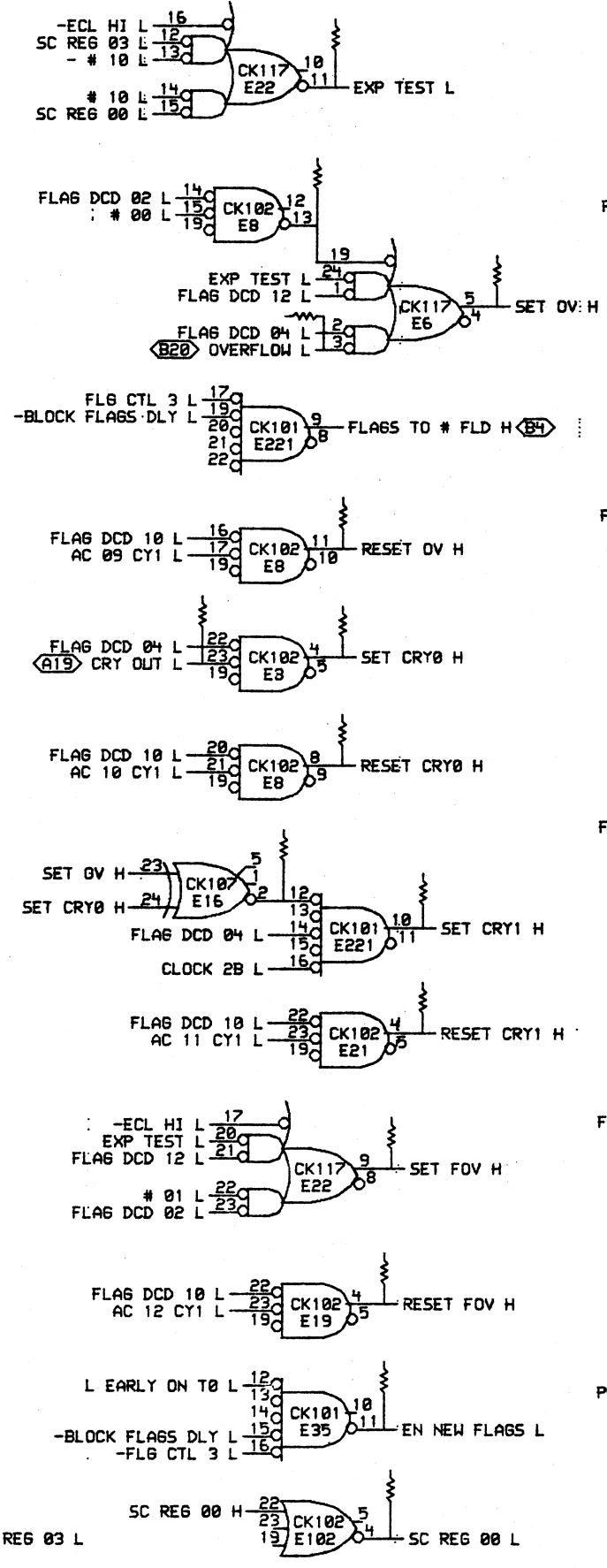
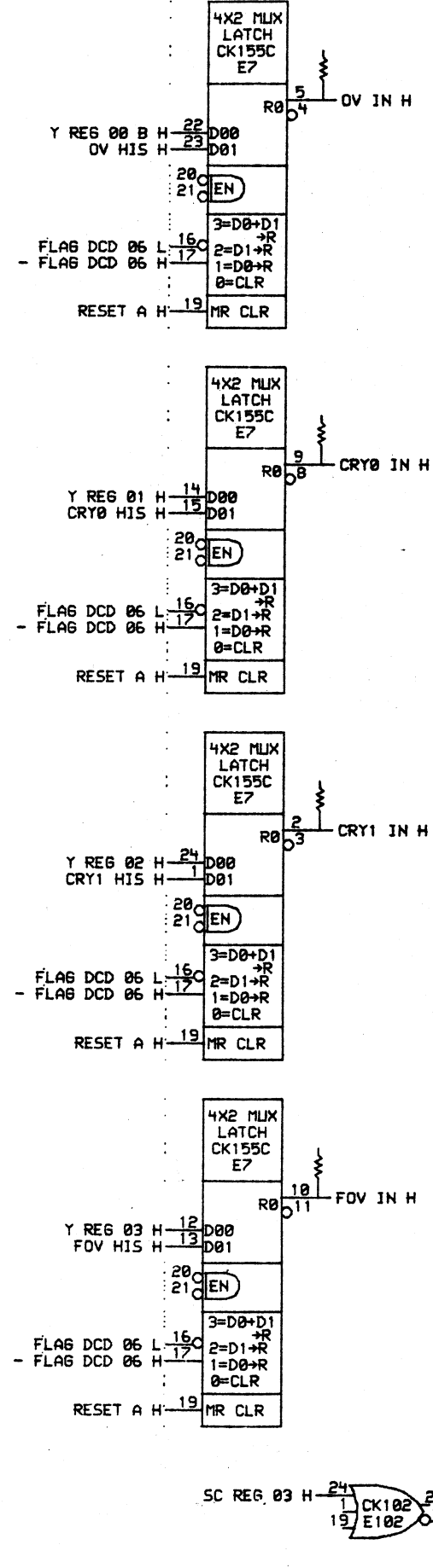
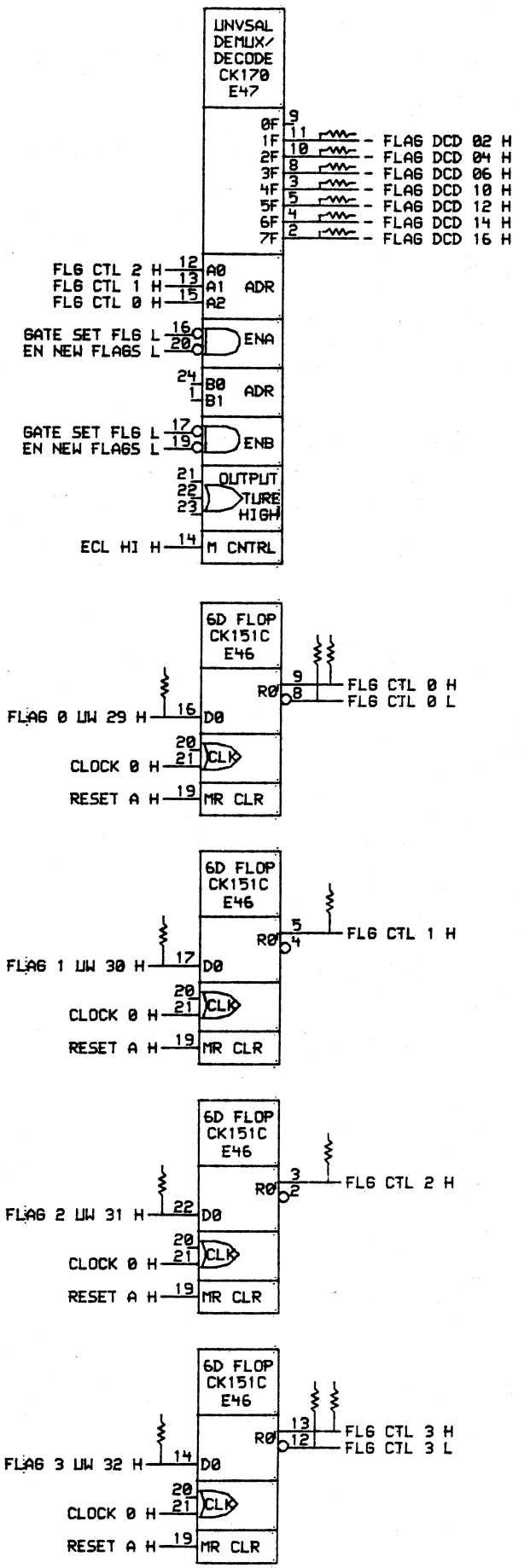




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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE
	12-NOV-82	J. J. J.		TIMER INTERRUPT SYSTEM
	DATE	BOARD LOCATION	SHEET	
	12-NOV-82 10:56			
	FIRST USED ON OPTION/MODEL:	NEXT HIGHER ASSEMBLY:	SIZE	CODE
			D	CS
			NUMBER	REV.
			L0046-0-SCAD	

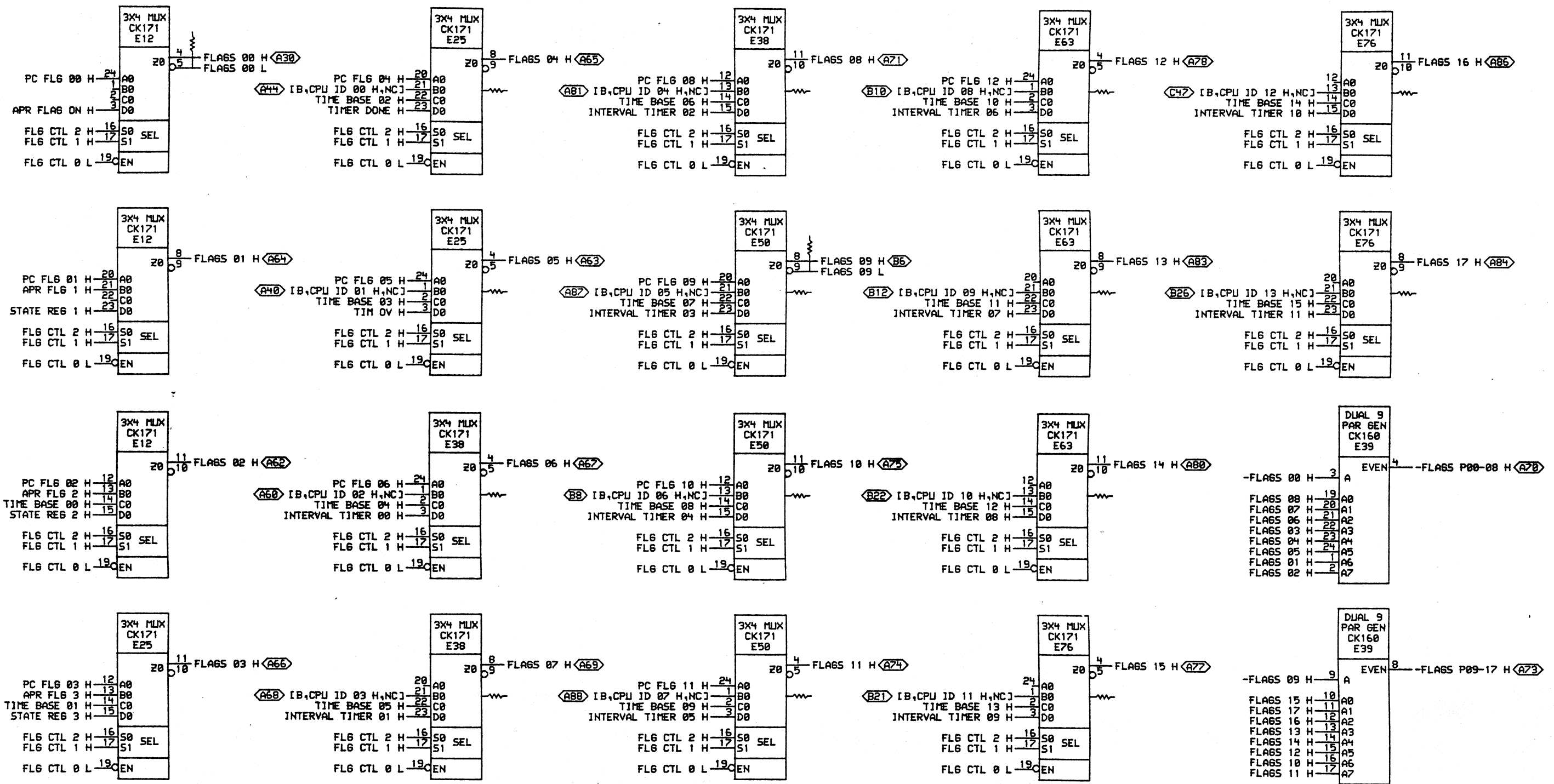


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REVISIONS	
CHK	CHANGE NO. REV

8	7	6	5	4	3	2	1
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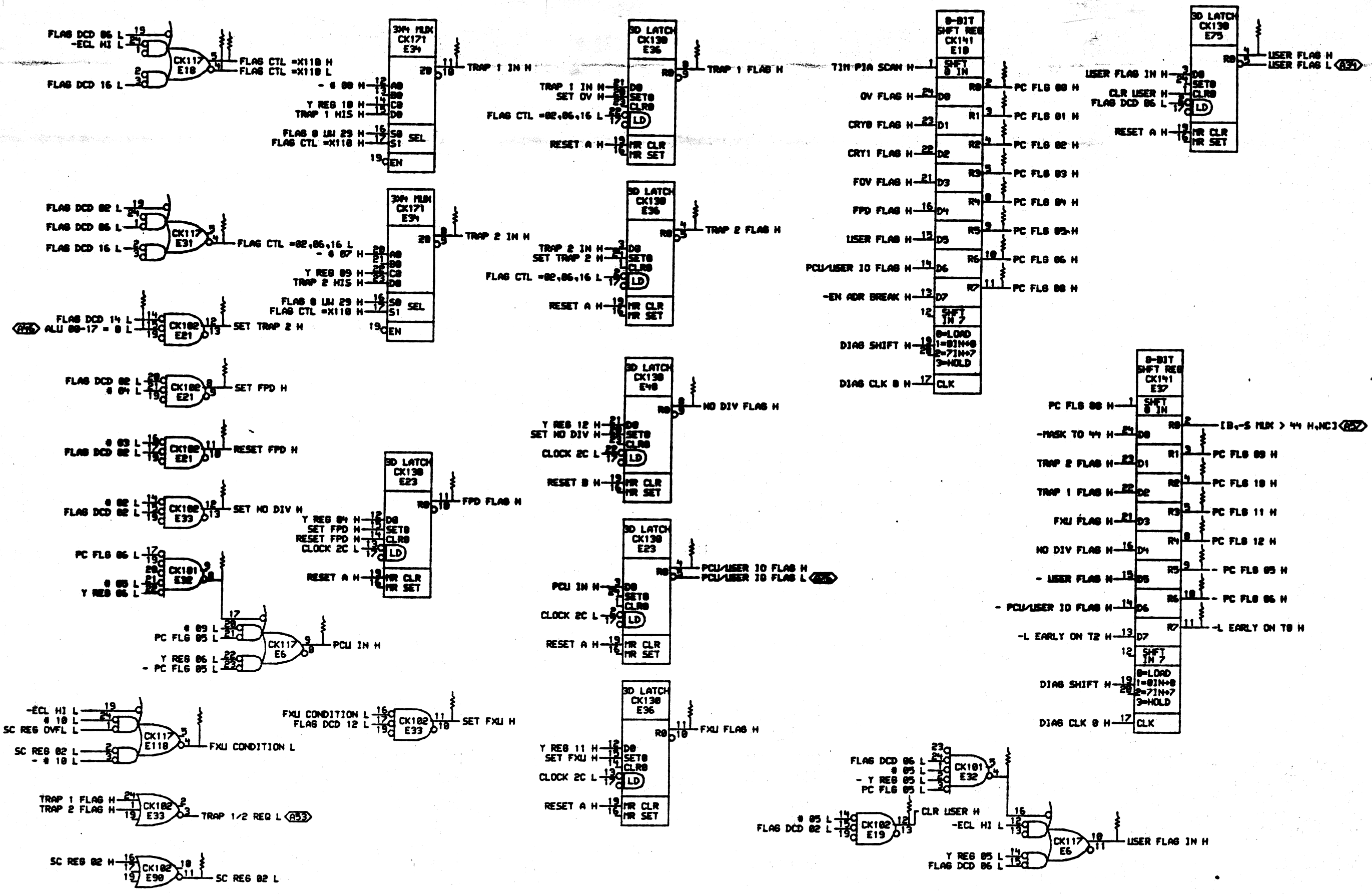
digital	DRN. J. L. L...	DATE 12-NOV-82	ENG.	DATE	TITLE: OV, CRY0-1, FOV FLAGS
	CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
VULCAN (2000-1004) SCALE DRAW		11-NOV-82 18:30	NEXT HIGHER ASSEMBLY:		SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL:				D CS L0046-0-SCAE	



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE: 09-1982	ENG.	DATE:	TITLE: FLAGS MUX 00-17
	CHK'D: J. J. J.	BOARD LOCATION:	SHEET OF:	
FIRST USED ON OPTION/MODEL: 100-MAR-82 1758		NEXT HIGHER ASSEMBLY:		SIZE CODE: D CS
				NUMBER: L0046-0-SCAF
				REV.:

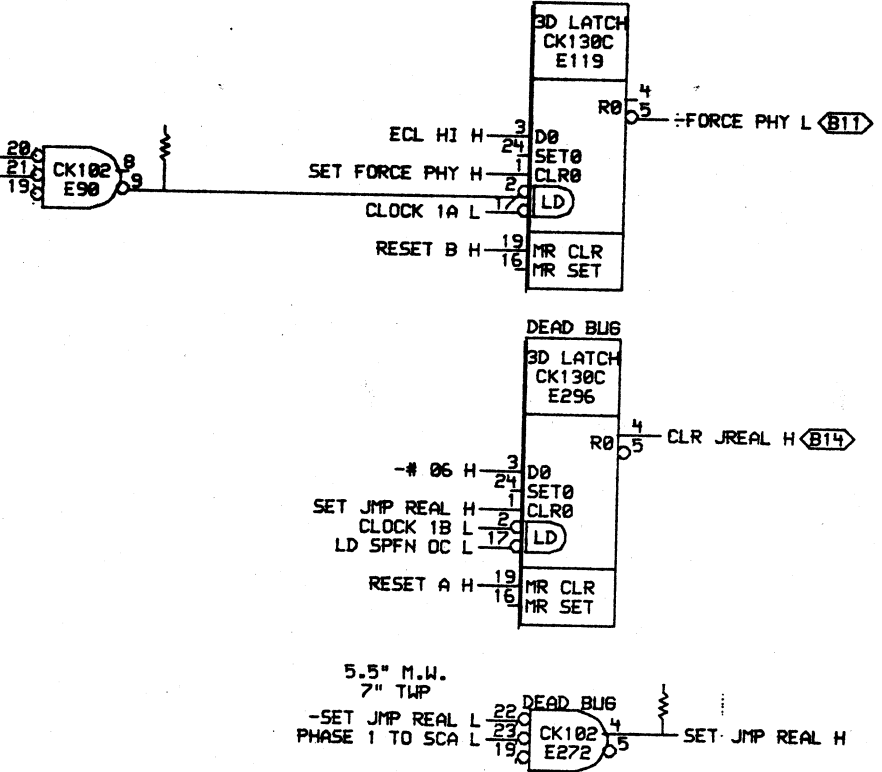
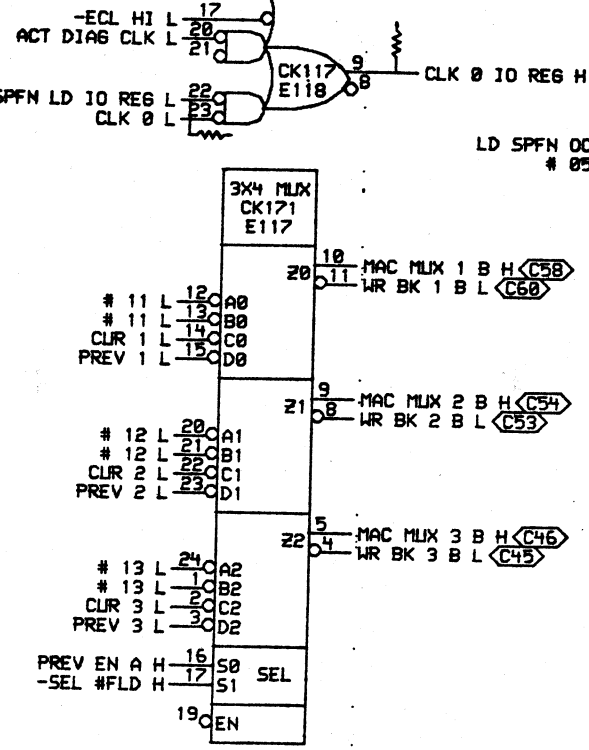
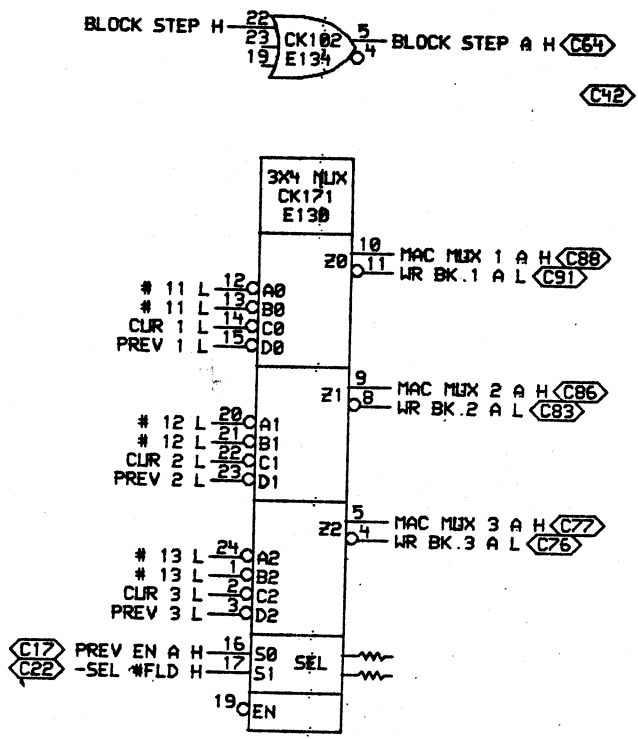
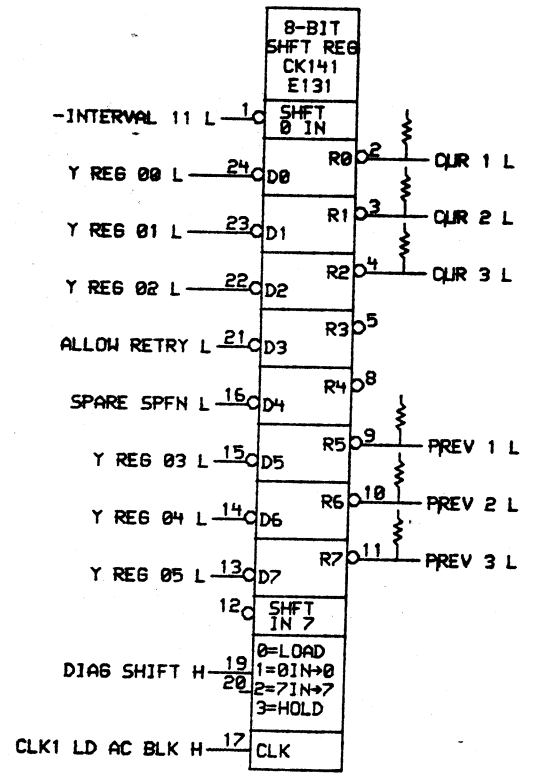
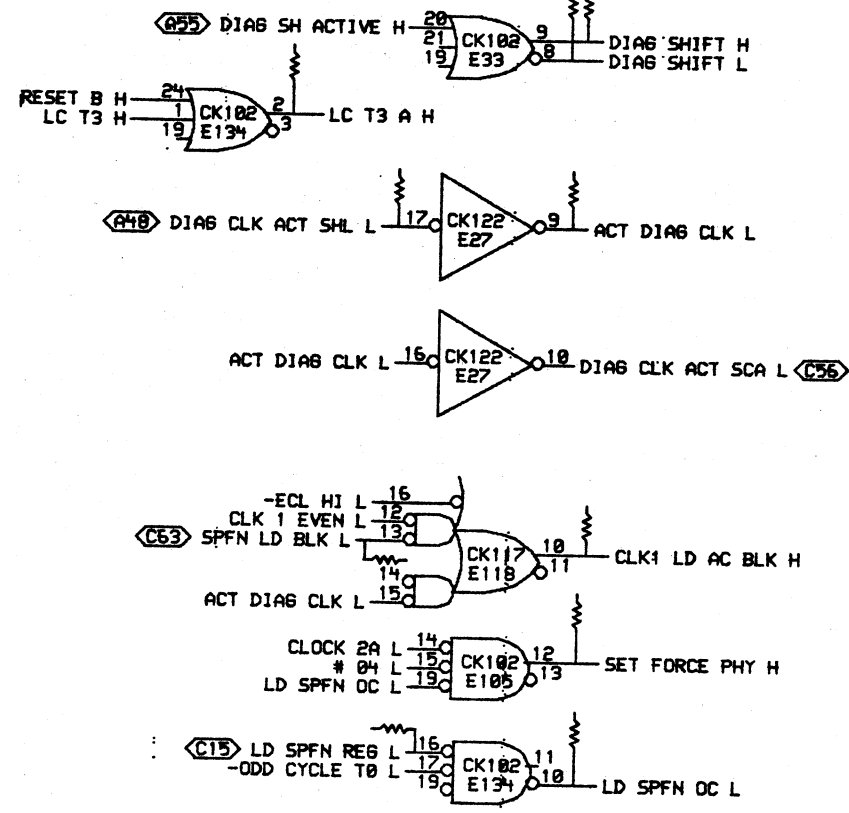
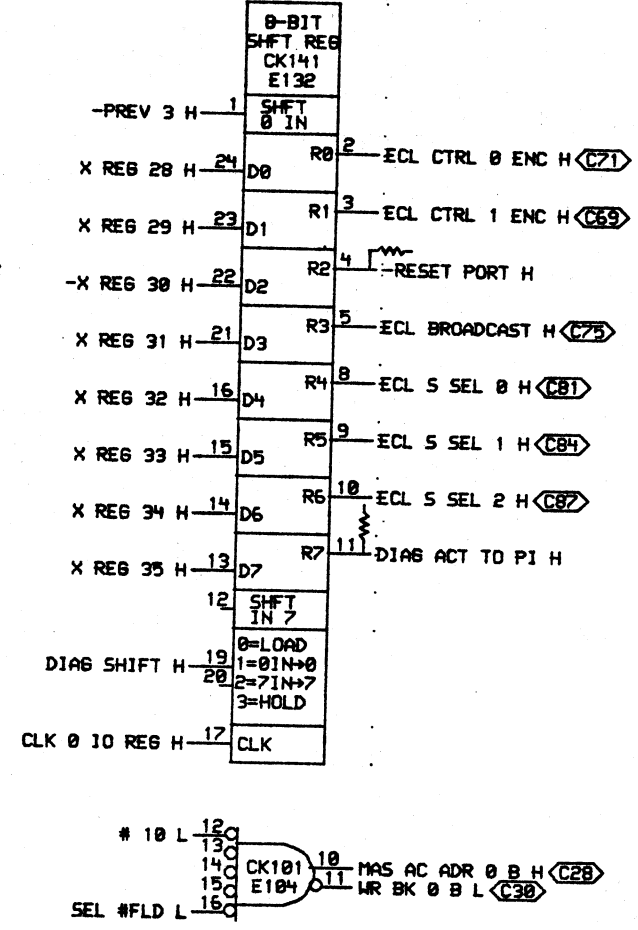
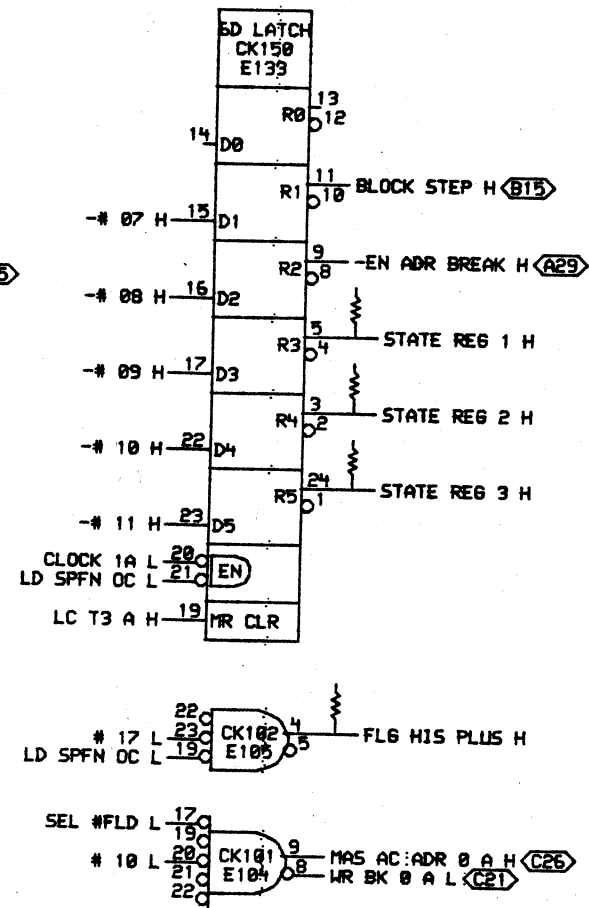
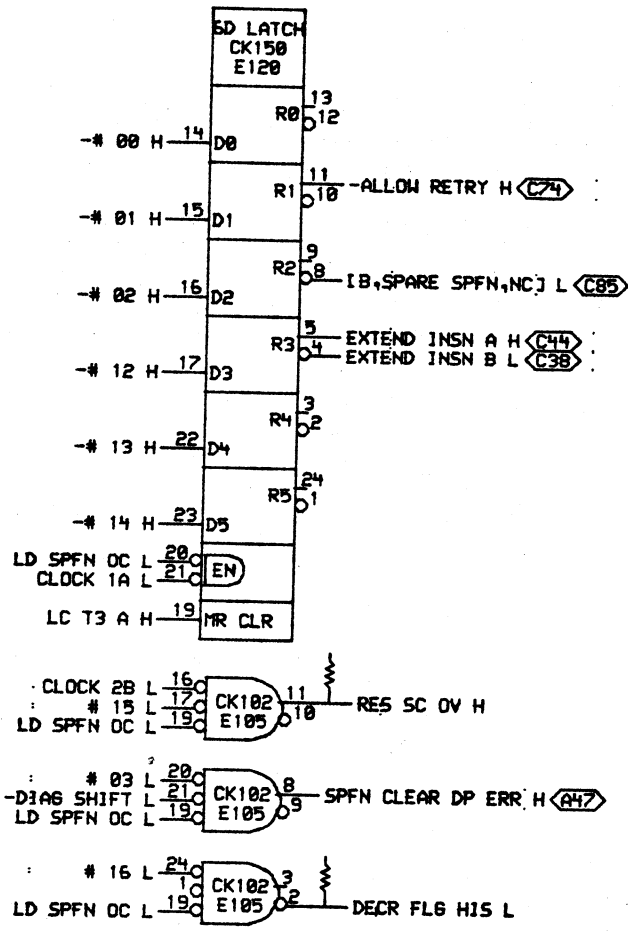


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REVISIONS	
CHK	CHANGE NO. REV

8	7	6	5	4	3	2	1
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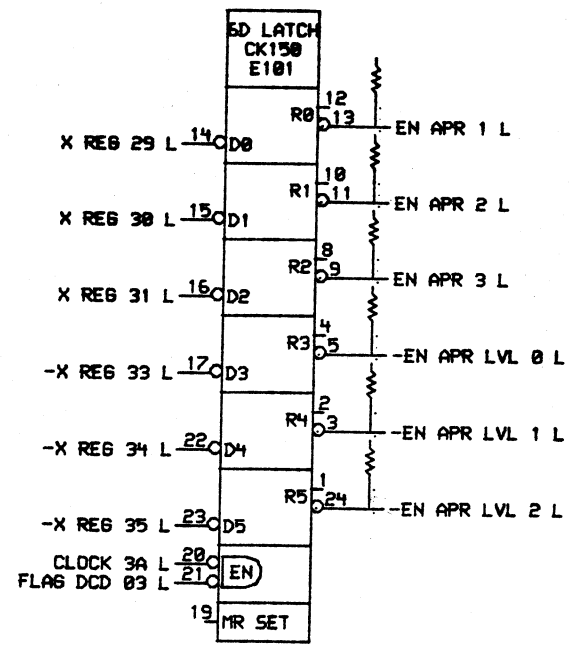
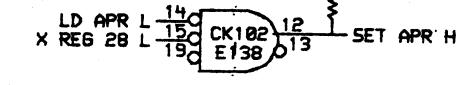
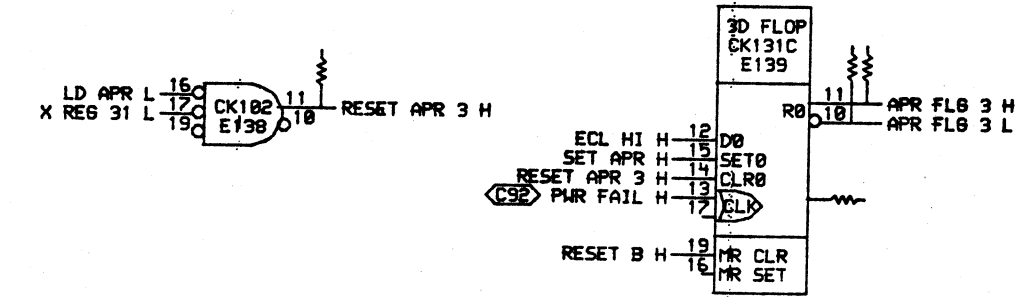
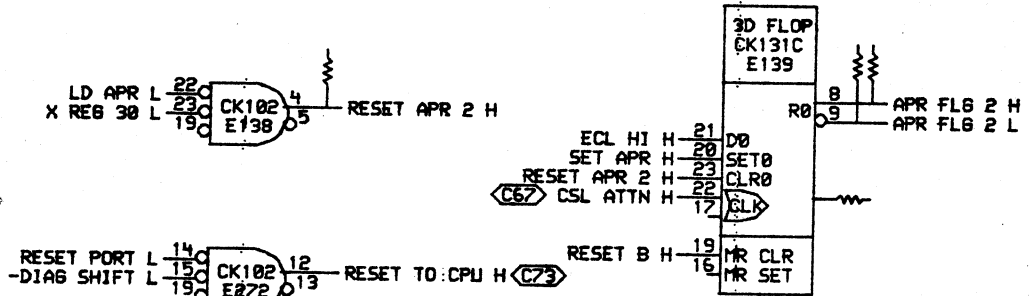
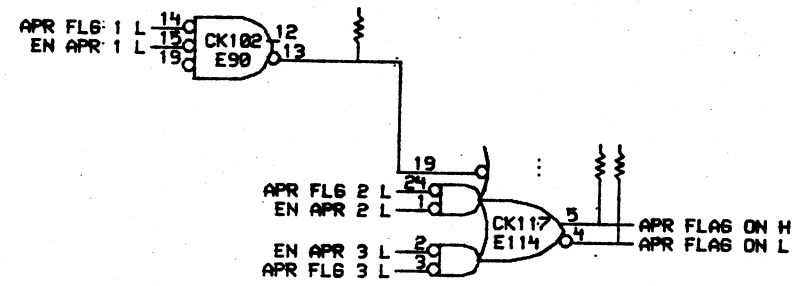
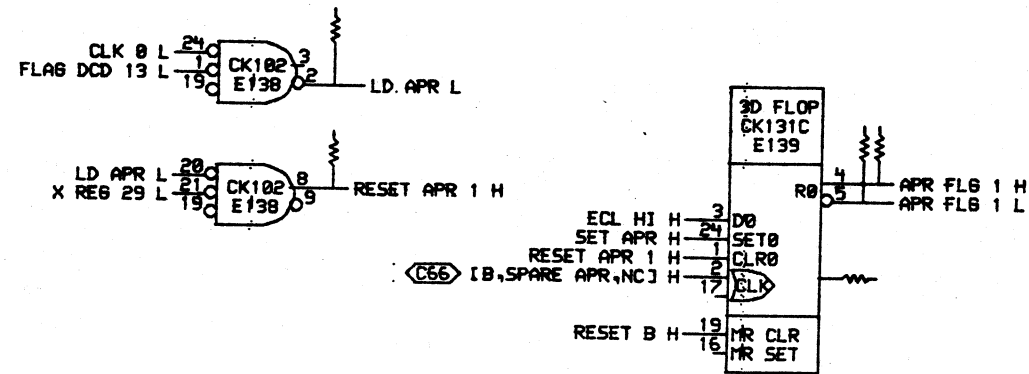
	DESIGNED BY: <i>J. Early</i>	DATE: 05-11-68	ENG.:	DATE:	TITLE: TRAP 1, 2, NO DIV
	CHK'D BY: <i>J. Early</i>	DATE: 05-11-68	BOARD LOCATION:	REV.:	PCU, USER FLAGS
FIRST USED ON OPTION MODEL:					SIZE: D CODE: CS NUMBER: L0046-0-SCAG



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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE 11-NOV-82	ENG.	DATE	TITLE: SPFN, IO REG, MAC ADDR	SIZE D	CODE CS	NUMBER L0046-0-SCAH	REV.
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0046-0						



UNVSAL DENUX/ DECODE CK170 E126	
0F	APR REG 1 H
1F	APR REG 2 H
2F	APR REG 3 H
3F	APR REG 4 H
4F	APR REG 5 H
5F	APR REG 6 H
6F	APR REG 7 H
7F	APR REG 8 H

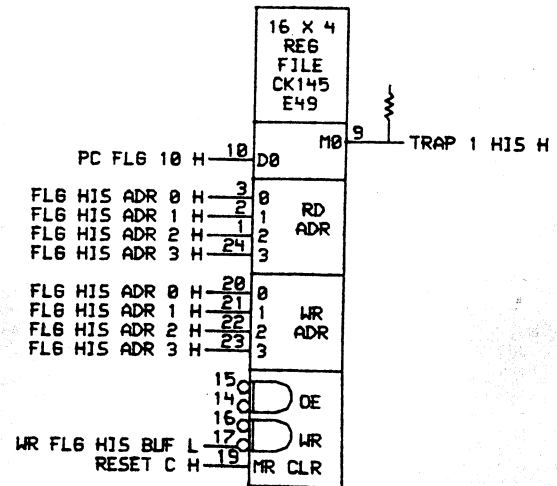
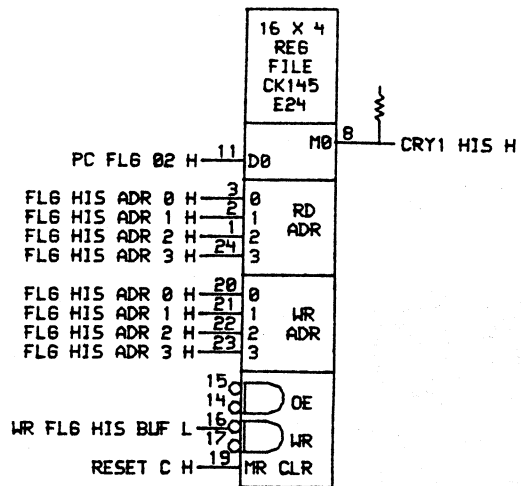
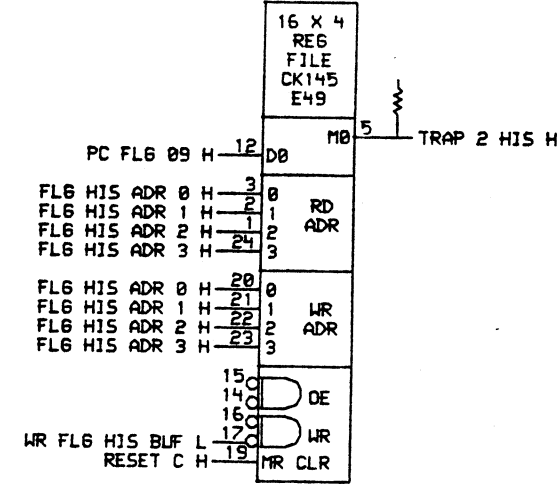
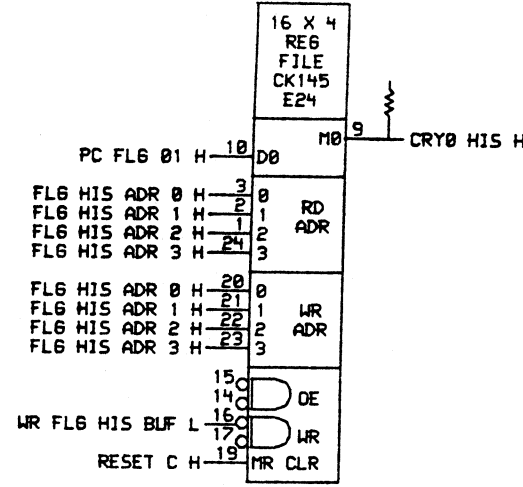
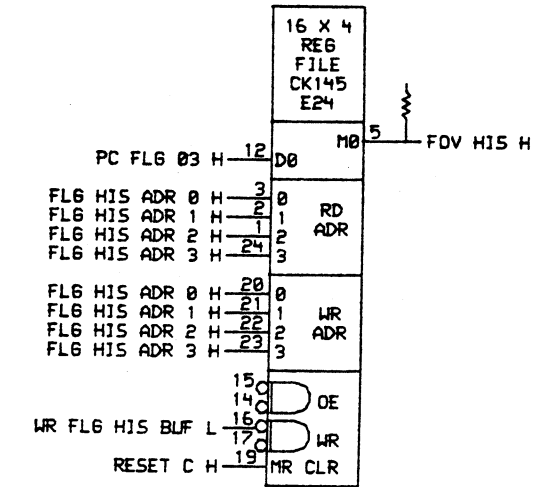
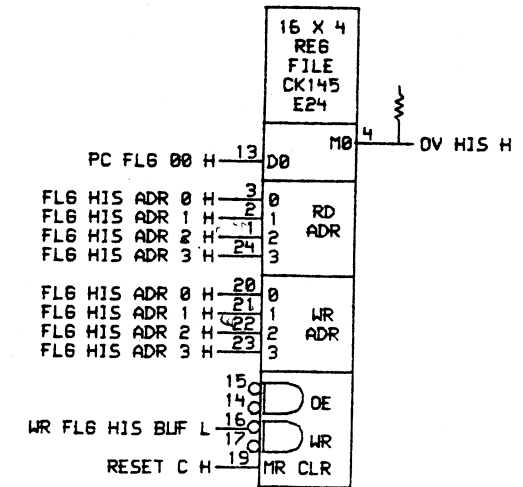
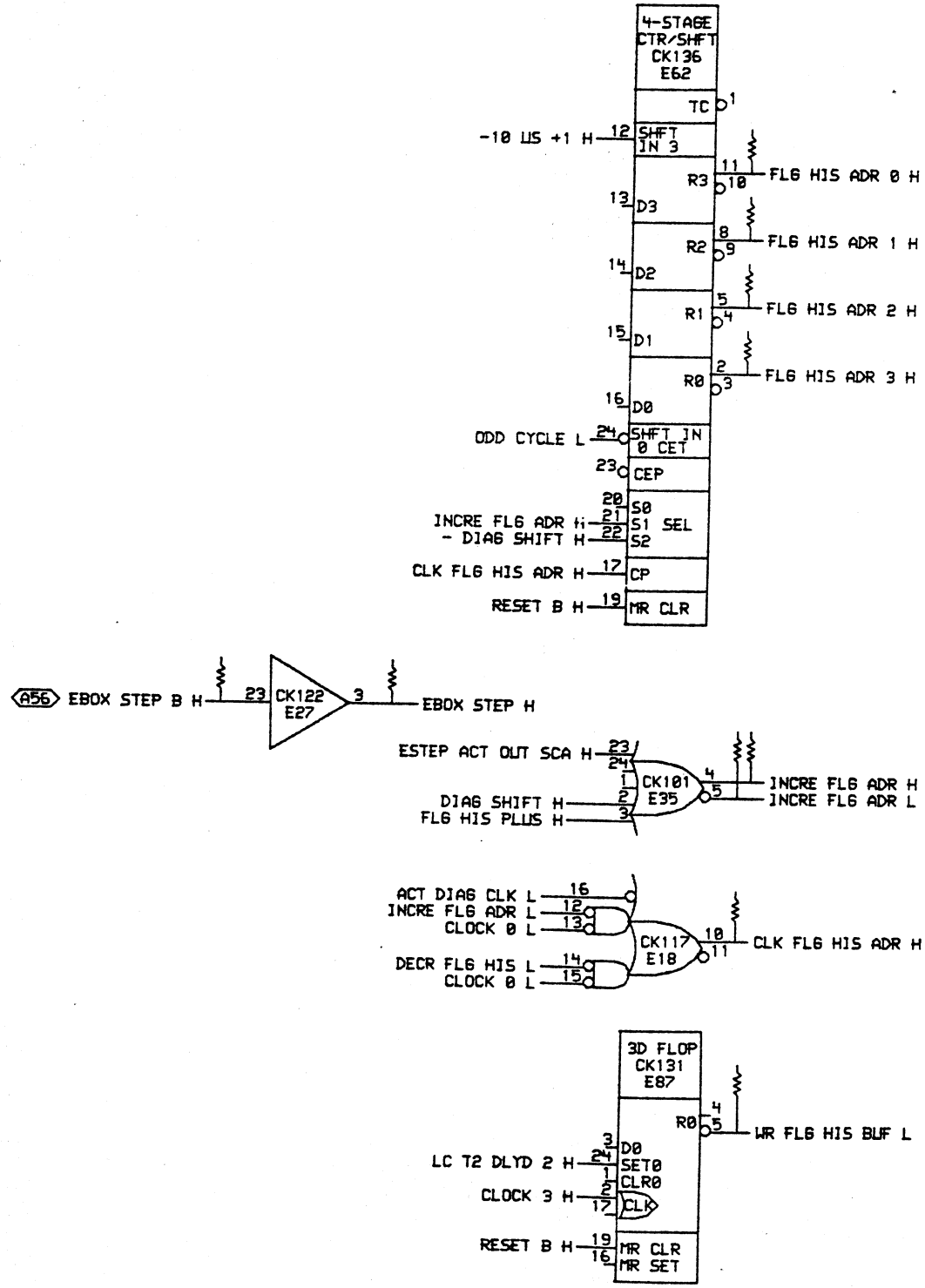
EN APR LVL 2 H	A0
EN APR LVL 1 H	A1
EN APR LVL 0 H	A2
APR FLAG ON L	ENA
APR FLAG ON L	ENB
ECL HI H	OUTPUT TURE HIGH
ECL HI H	M CNTRL

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:
	11-NOV-82			APR FLAGS 1-4
CHK'D.	DATE	BOARD LOCATION:	SHEET OF	
VULCAN (2000-10046-0-SCAI) DRAW	11-NOV-82 17:52	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL:			D	CS
			NUMBER	REV.
			L0046-0-SCAI	

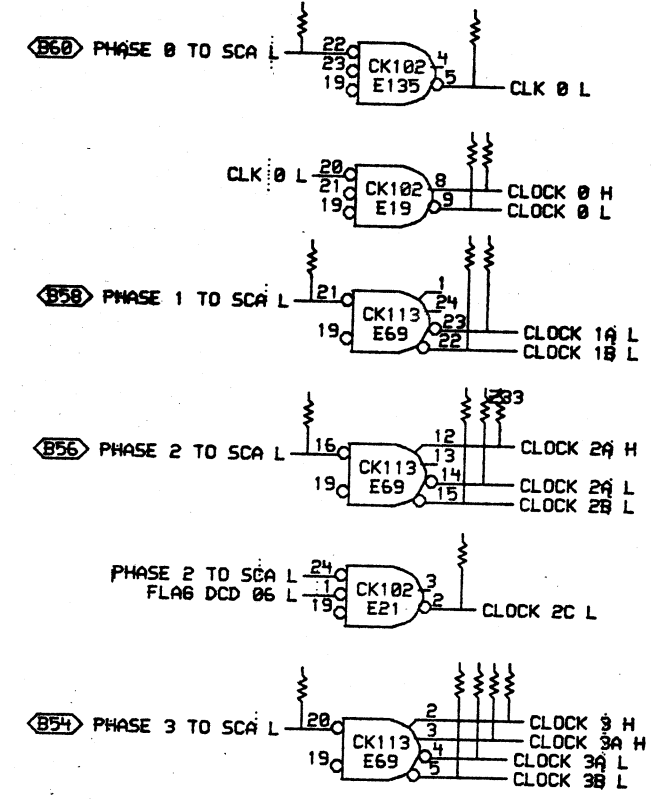
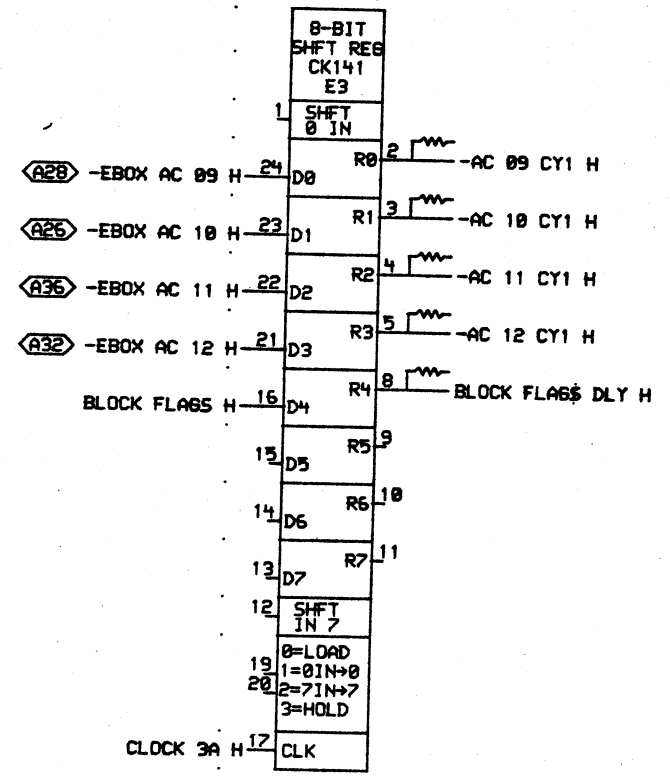
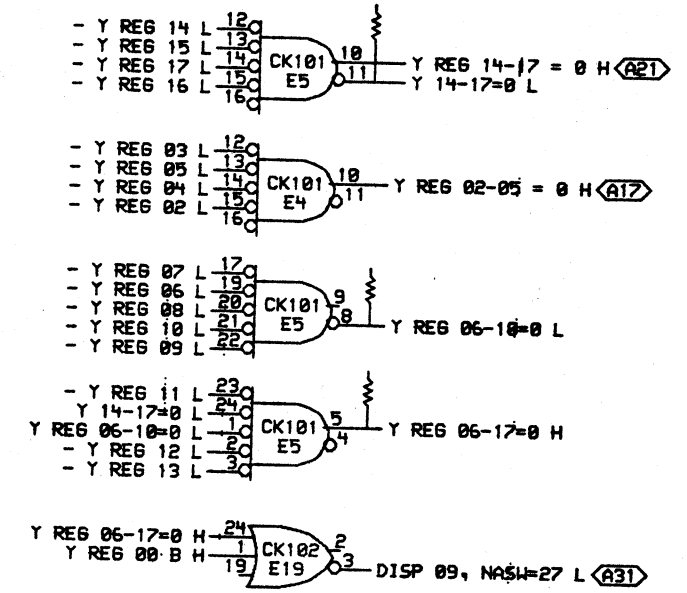
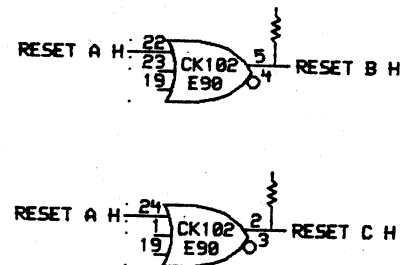
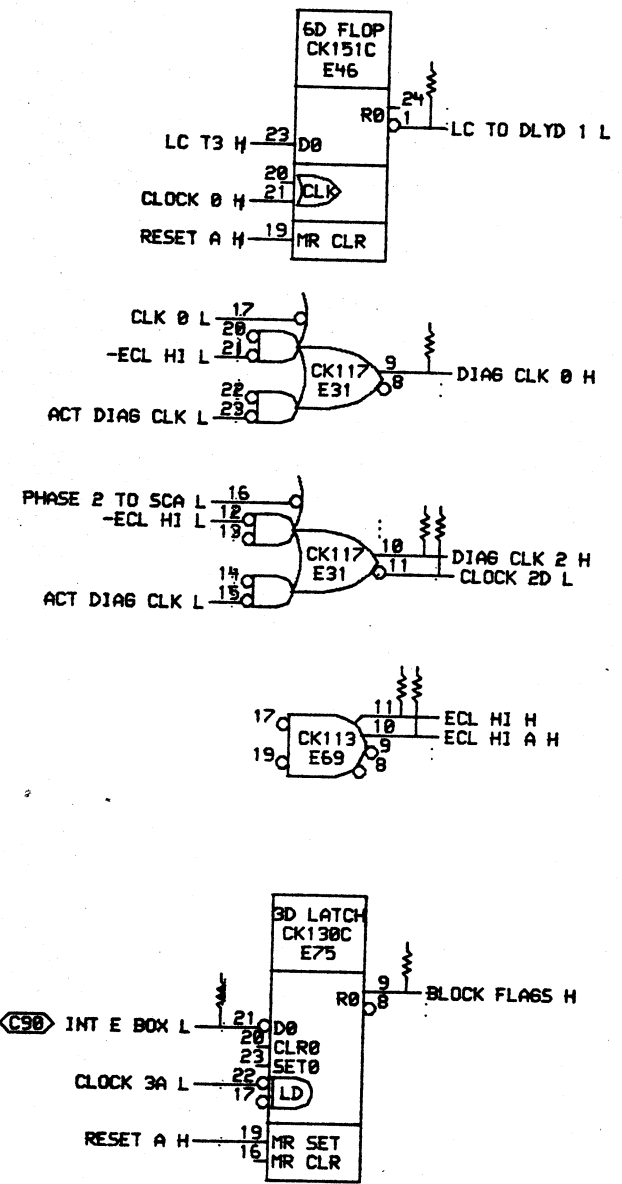
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	05-FEB-82	J. J. J.		FLAG HISTORY BUFFER
CHK'D.	DATE	BOARD LOCATION:		
		SHEET		
FIRST USED ON OPTION/MODEL:			SIZE	CODE
			D	CS
			NUMBER	REV.
			1.0046-A-SCAJ	



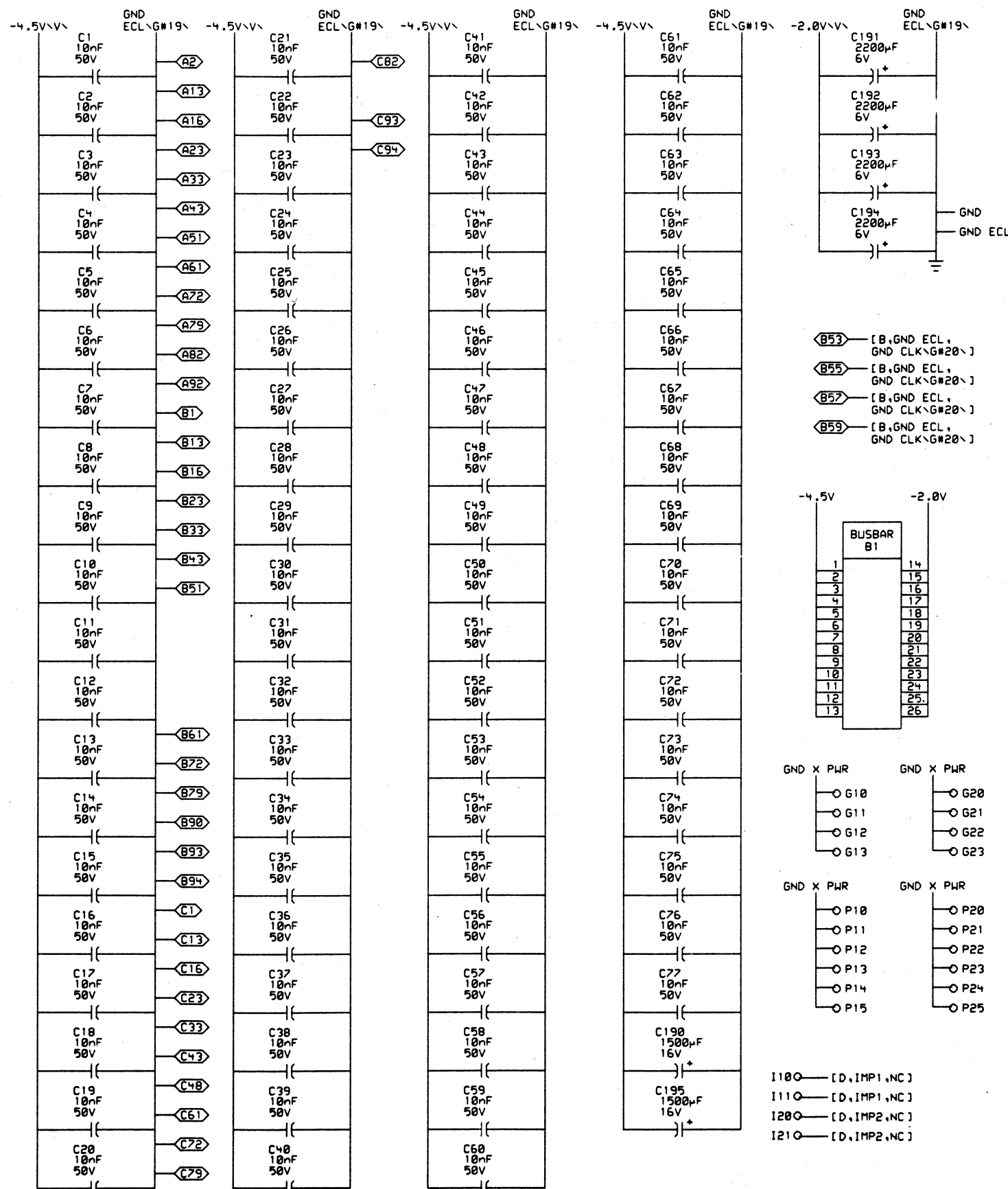
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REVISIONS	
CHK	CHANGE NO. REV

	DATE	ENG.	DATE	TITLE:
	11-NOV-82			IO DRIVE
CHK'D.	DATE	BOARD LOCATION:	SHEET OF	
VUL DNN (2888.L0046)66SCAK.DRW	10-NOV-82 07:03	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 2080			D	CS
			NUMBER	REV.
			L0046-0-SCAK	

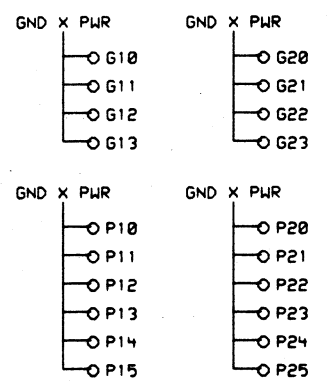
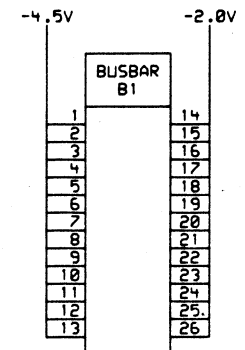
8	7	6	5	4	3	2	1
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D
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 REV.
 NUMBER
 L0046-0-SCAK
 CS
 D
 B
 A



- (B89) [B,GND ECL, NC]
- (B18) NC B18
- (B24) NC B24
- (B27) NC B27
- (B89) NC B89

- (B53) [B,GND ECL, GND CLK\G#20\]
- (B55) [B,GND ECL, GND CLK\G#20\]
- (B57) [B,GND ECL, GND CLK\G#20\]
- (B59) [B,GND ECL, GND CLK\G#20\]

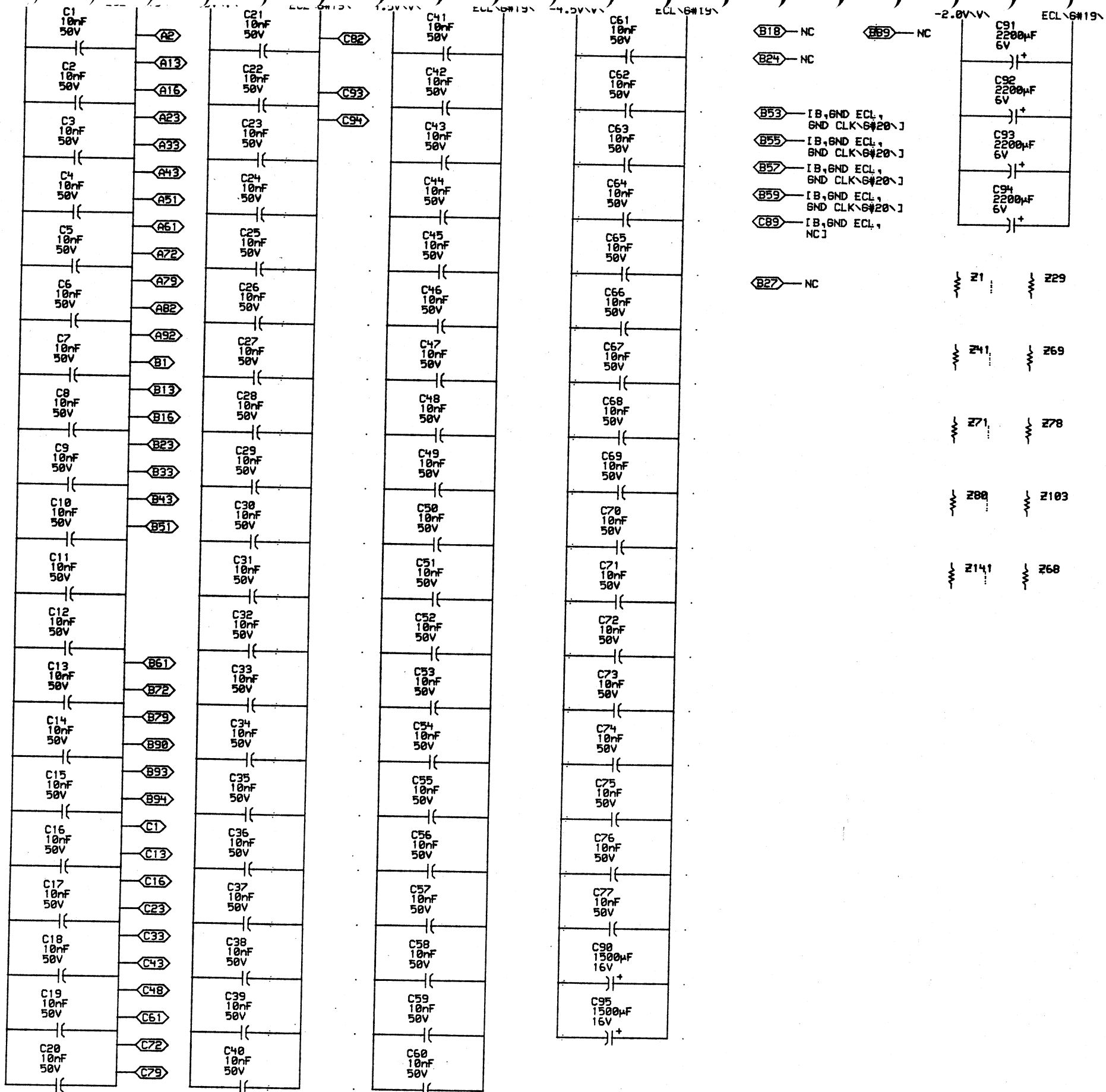


- 1180 [D,IMP1,NC]
- 1110 [D,IMP1,NC]
- 1200 [D,IMP2,NC]
- 1210 [D,IMP2,NC]

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REVISIONS	
CHK	CHANGE NO. REV

	DATE 22-DEC-82	ENG.	DATE	TITLE: SCA POWER & GROUND
	CHK'D.		SHEET 1 OF 1	
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0046-0		SIZE CODE D CS NUMBER L0046-0-SCAL REV. *



REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. <i>J. J. J.</i>	DATE 11-NOV-82	ENG.	DATE	TITLE: SCA POWER & GROUND
	CHK'D.	DATE	BOARD LOCATION: B-DD-L0046-0	SHEET 1 OF 1	
VIL CAN: C2000_L0046_0665201.DRW 109-NOV-82 00:45		NEXT HIGHER ASSEMBLY: B-DD-L0046-0		SIZE D	CODE CS
FIRST USED ON OPTION/MODEL: 2080				NUMBER L0046-0-SCAEX	REV.

D
 C
 B
 A


VULCAN:<2080.L0049>83CRA1
 VULCAN:<2080.L0049>29CRA2
 VULCAN:<2080.L0049>4CRA3
 VULCAN:<2080.L0049>72CRA4
 VULCAN:<2080.L0049>83CRA5
 VULCAN:<2080.L0049>83CRA6
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 VULCAN:<2080.L0049>83CRA8
 VULCAN:<2080.L0049>83CRA9
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 VULCAN:<2080.L0049>28CRAB
 VULCAN:<2080.L0049>83CRAC
 VULCAN:<2080.L0049>83CRAD
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 VULCAN:<2080.L0049>81CRAG
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 VULCAN:<2080.L0049>84CRAJ

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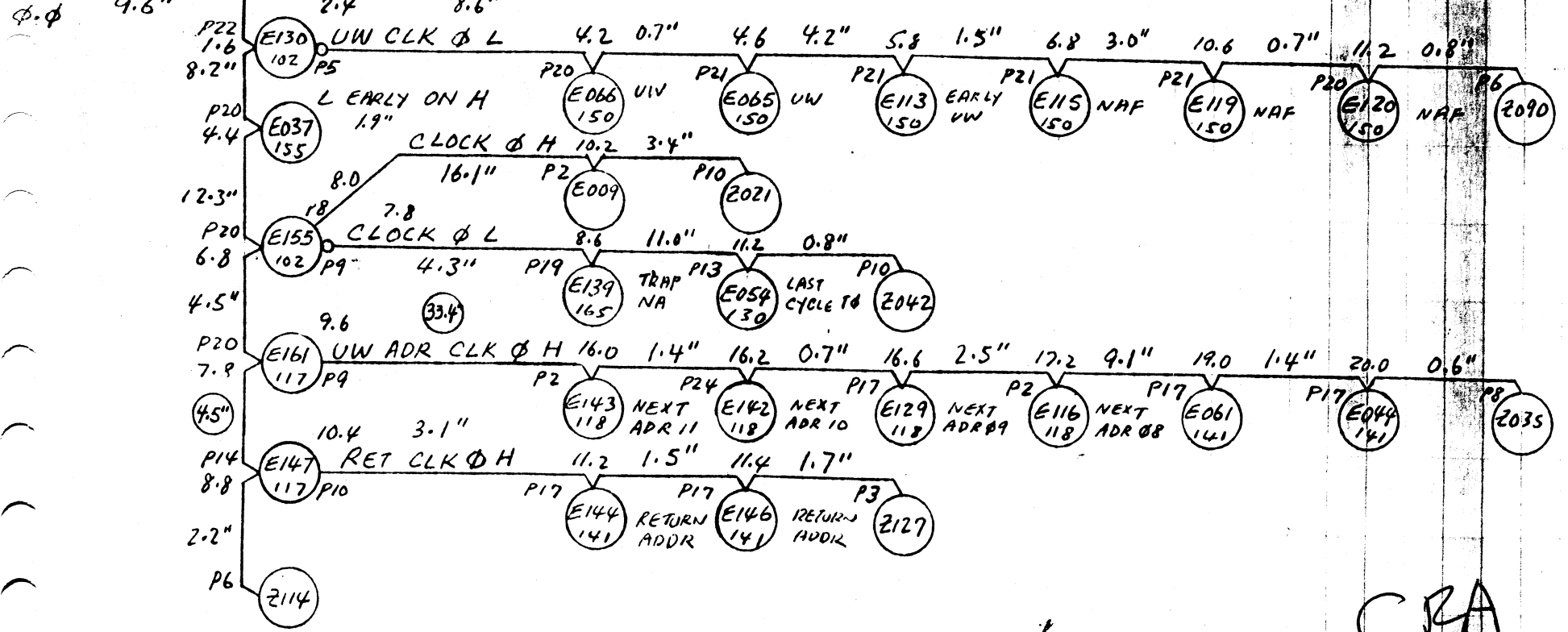
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REVISIONS		
CHK	CHANGE NO.	REV

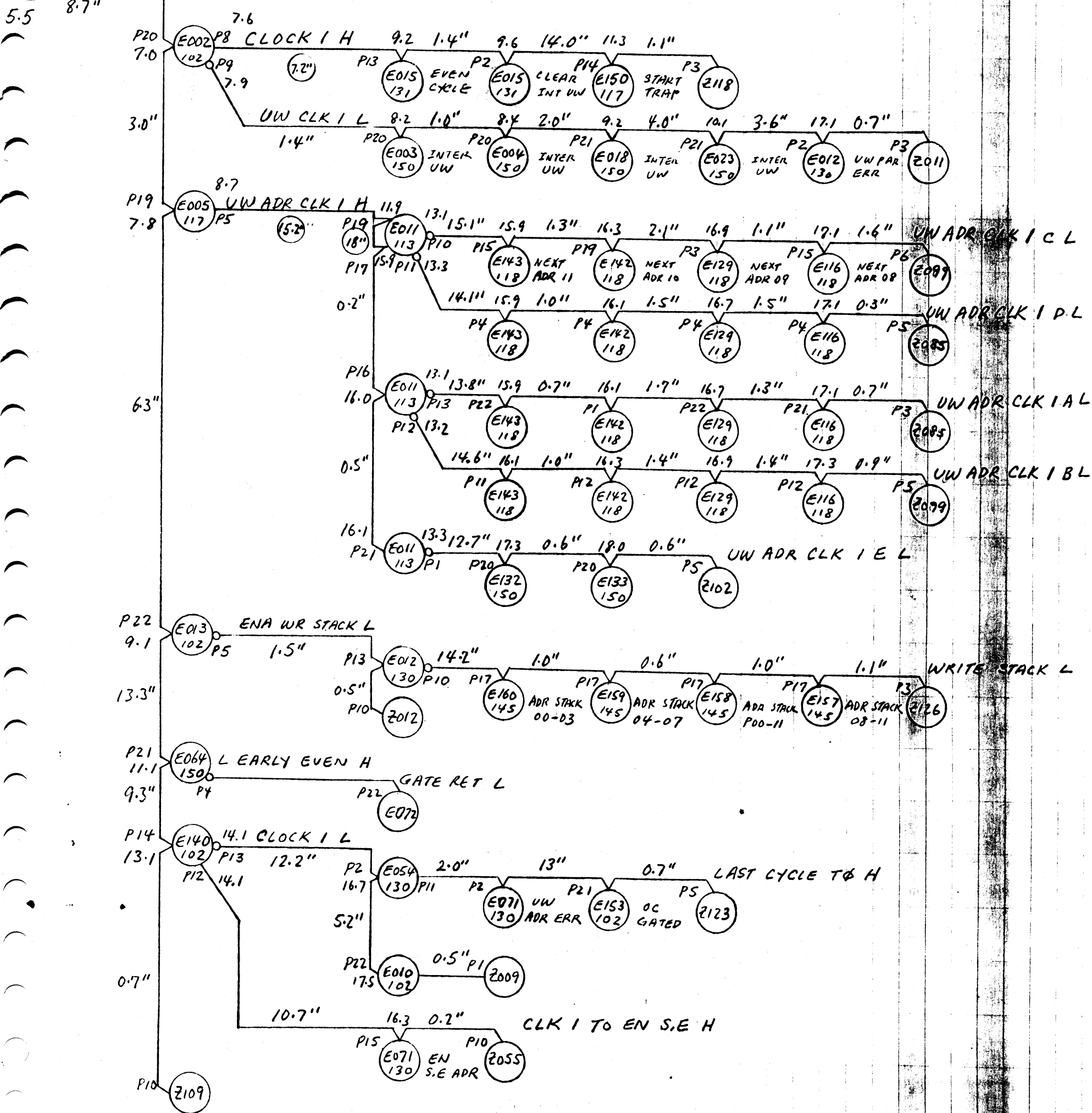
 DRN: <i>J. Family</i> CHK'D.	DATE 04-FEB-83	ENG.	DATE	TITLE:
	DATE	BOARD LOCATION:	SHEET	OF
ARCH: C:\MLER.PLOTS\LCRA.DPL.1 04-FEB-83 03:05 NEXT HIGHER ASSEMBLY:				SIZE D CODE SP
FIRST USED ON OPTION/MODEL:				NUMBER REV.

PHASE 0 TO CRA L



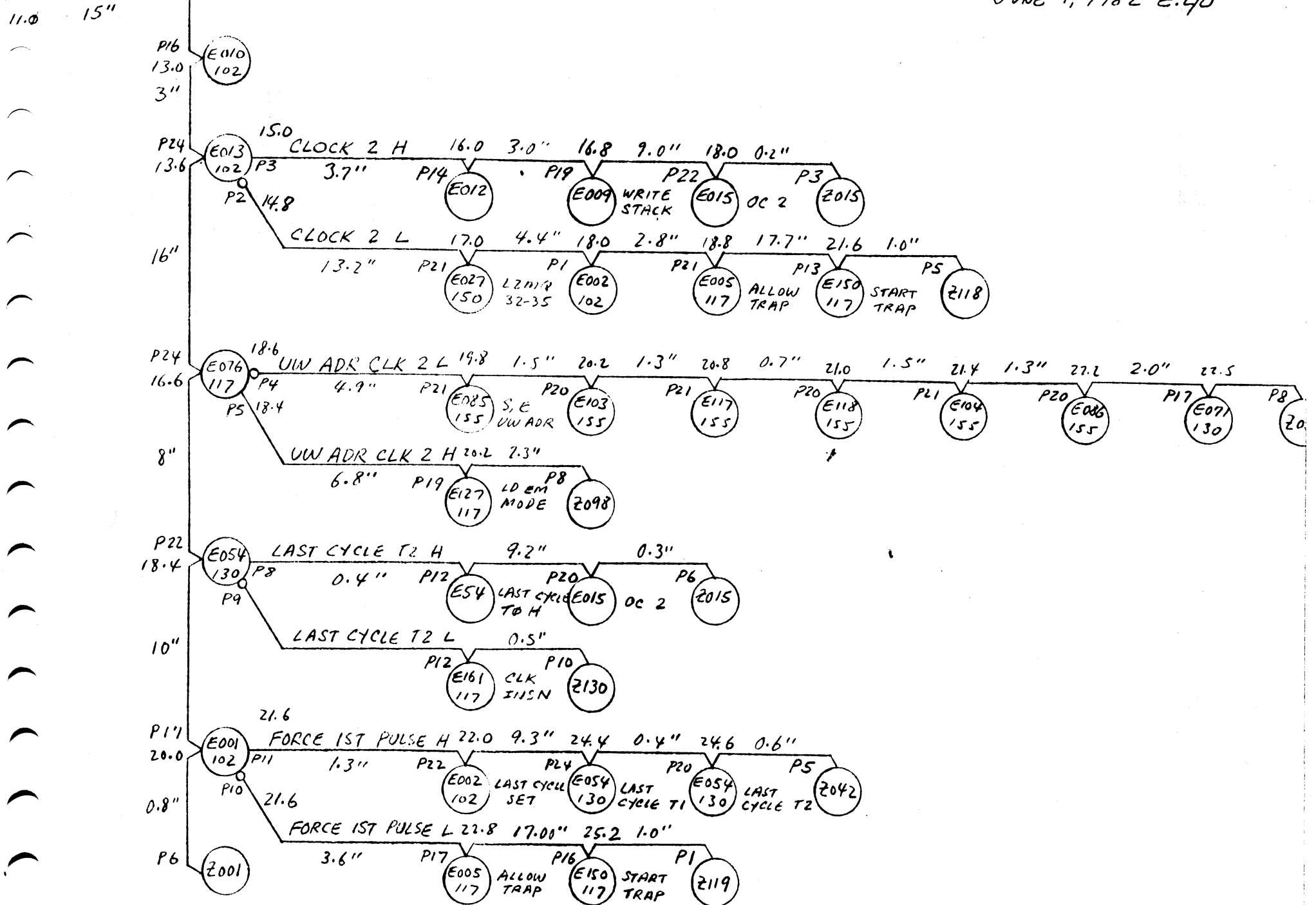
CRA

PHASE 1 TO CRA L

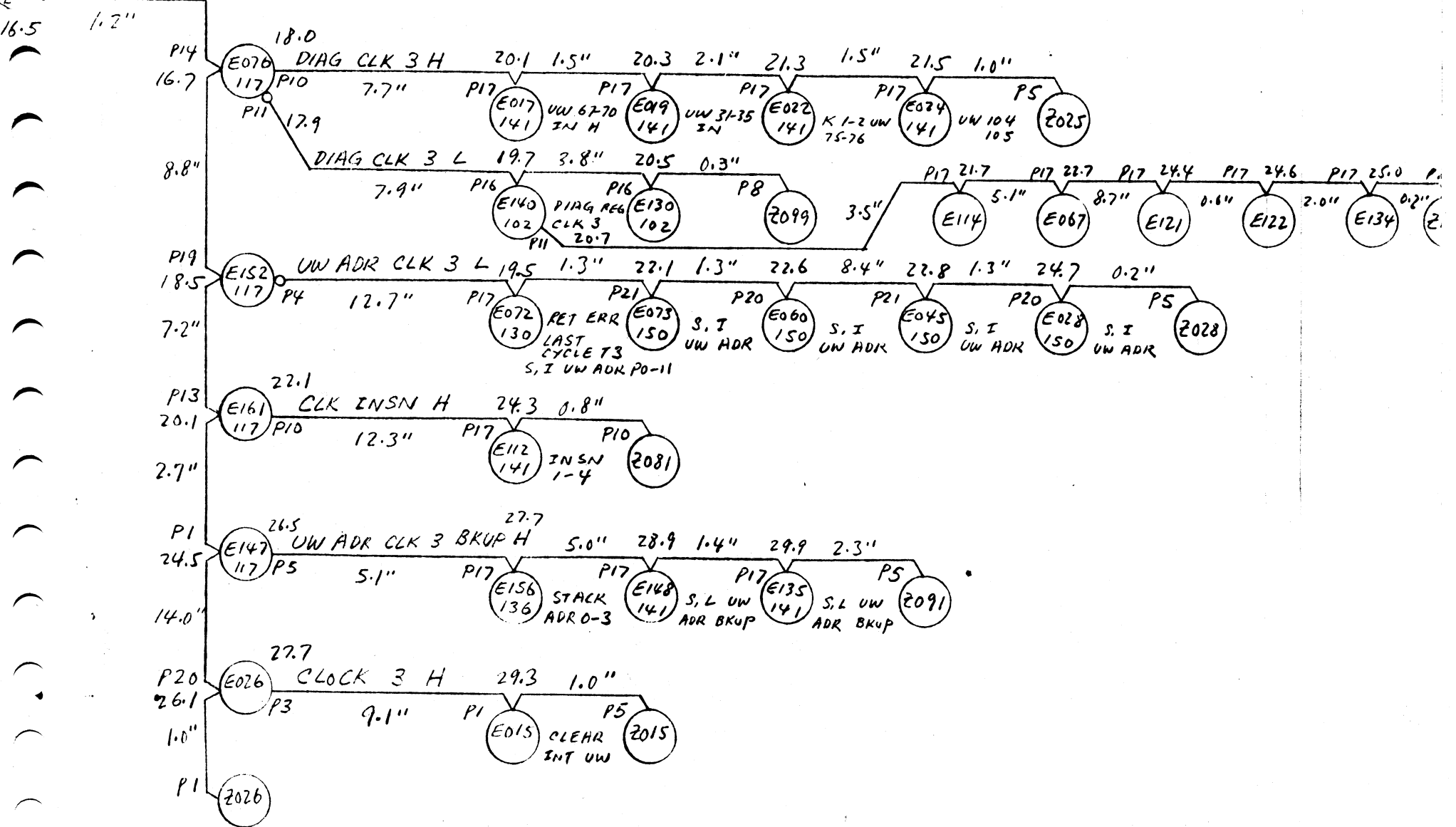


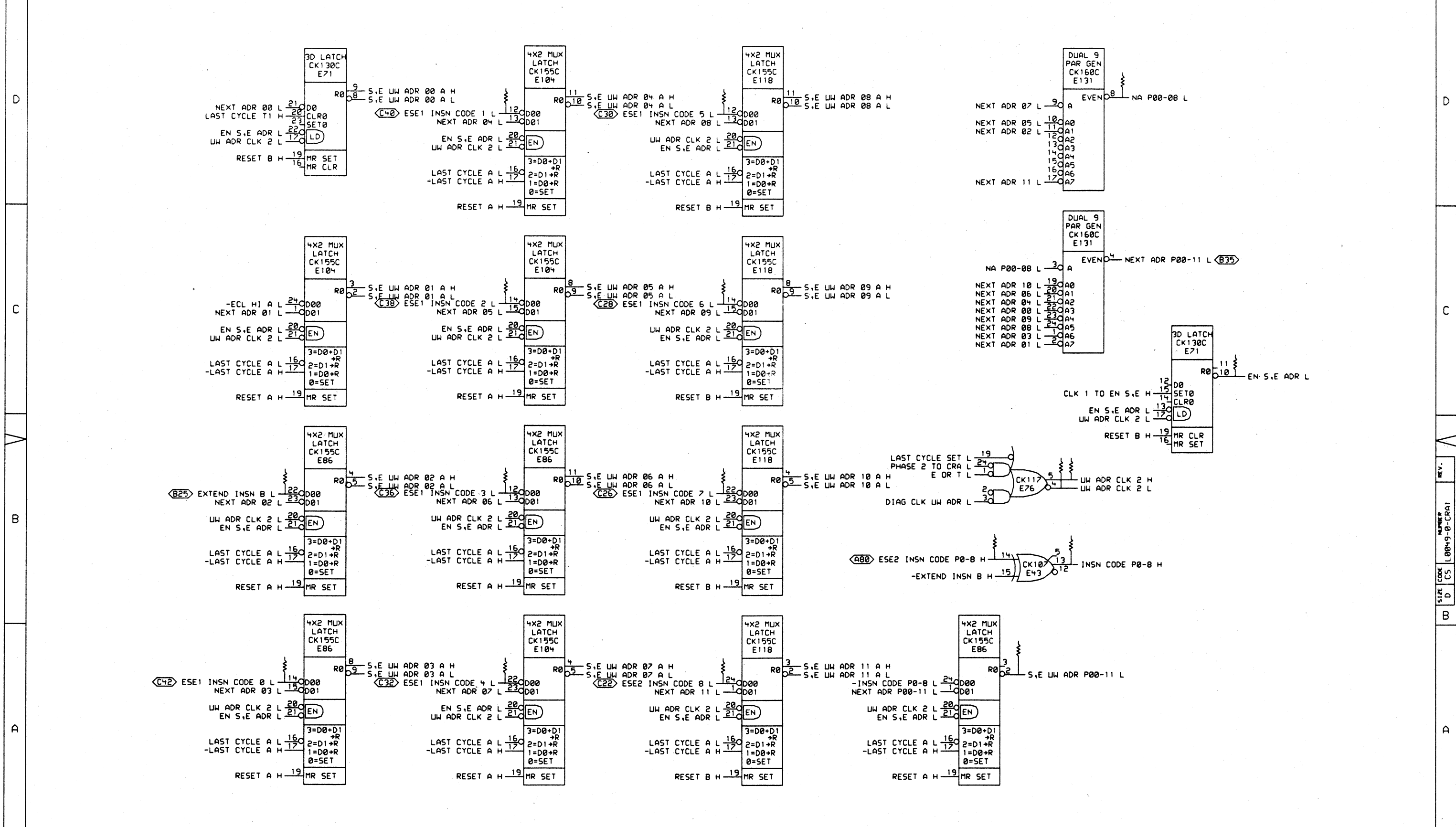
PHASE 2 TO CRA L

JUNE 1, 1982 E.L.U



PHASE 3 TO CRA L

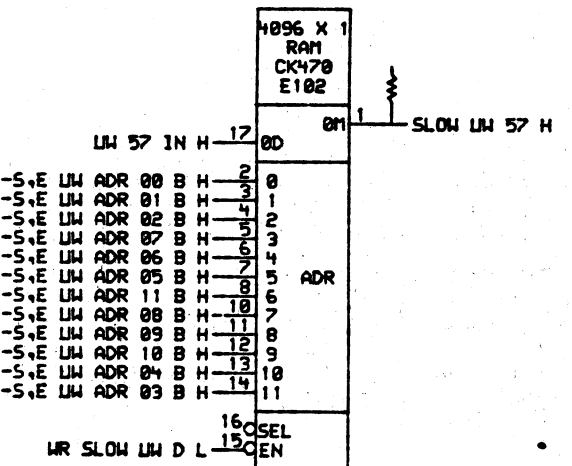
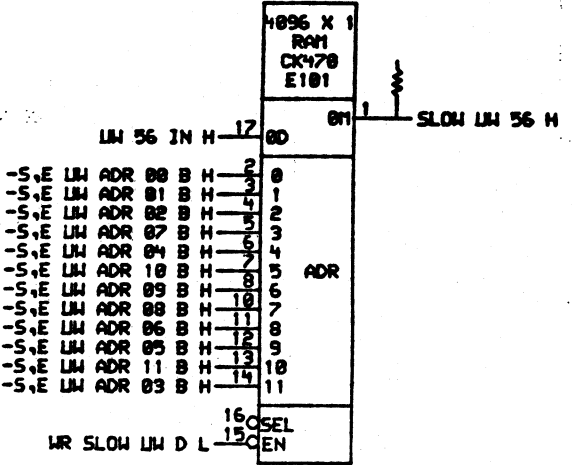
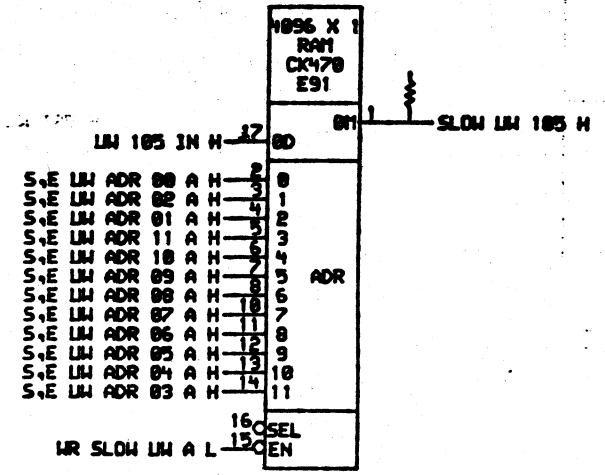
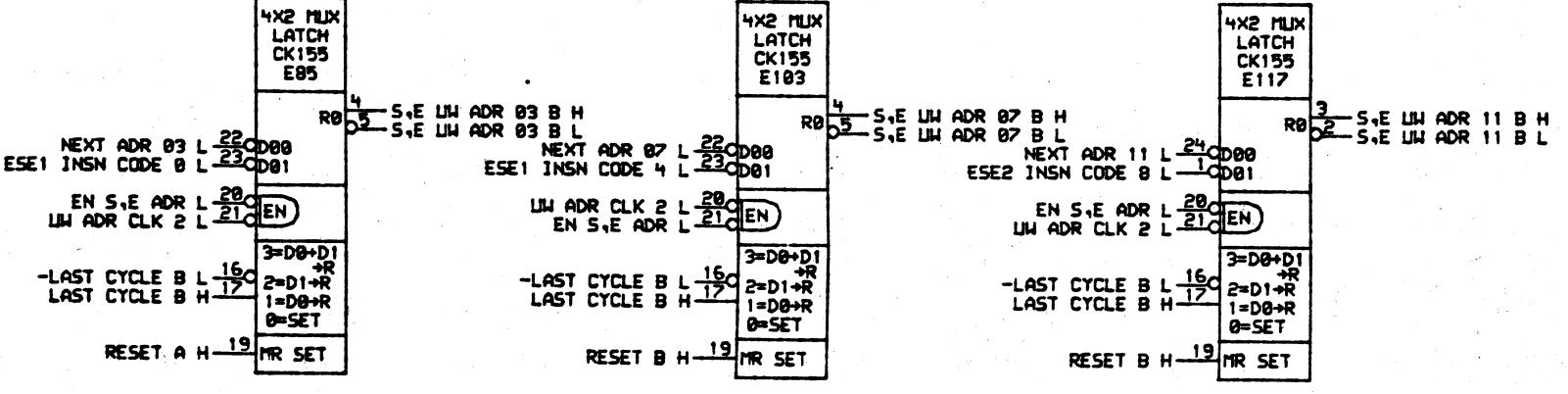
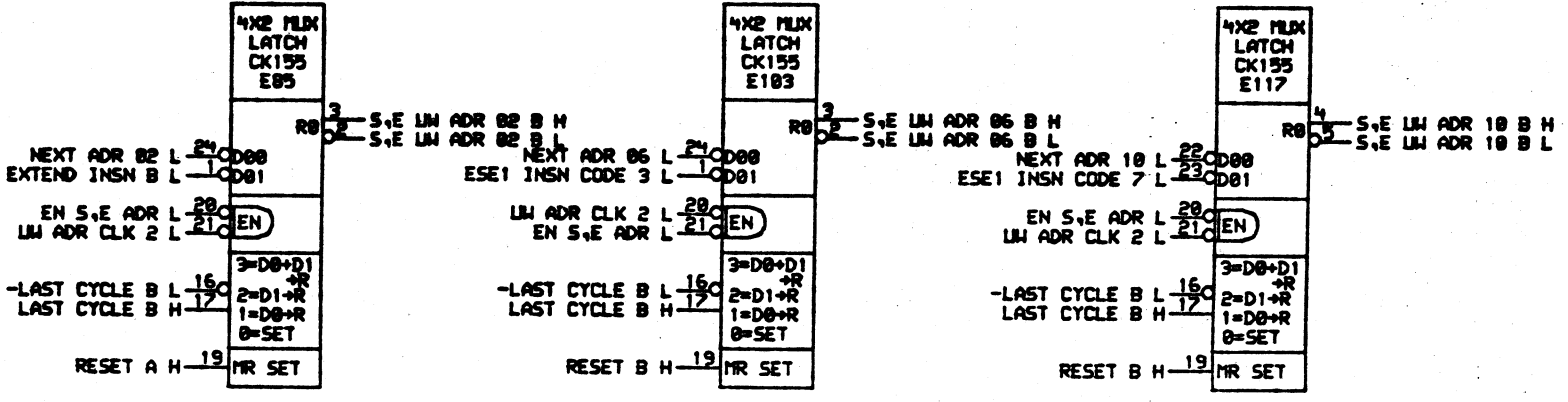
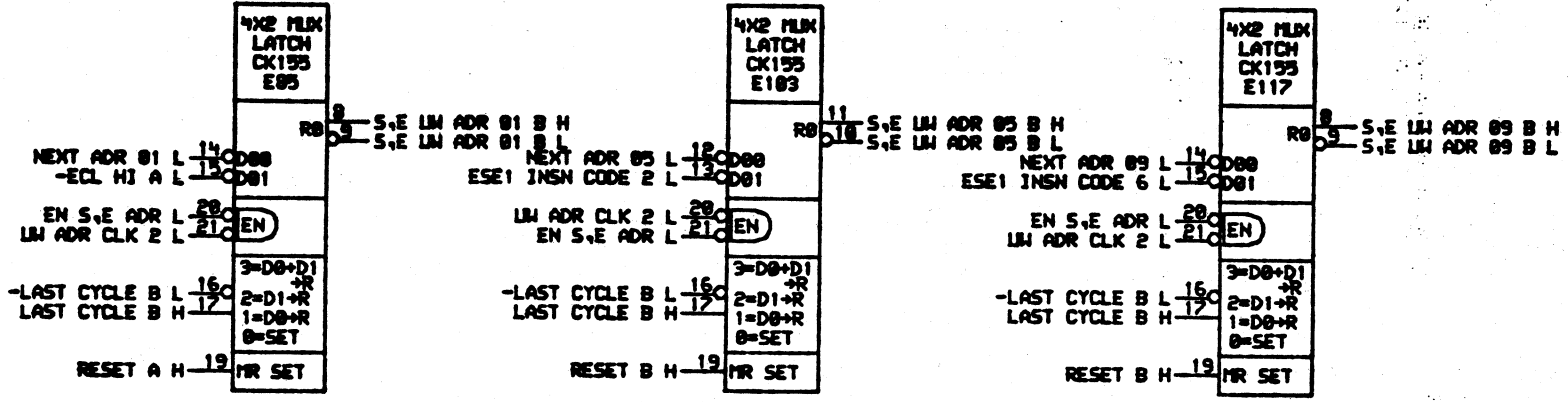
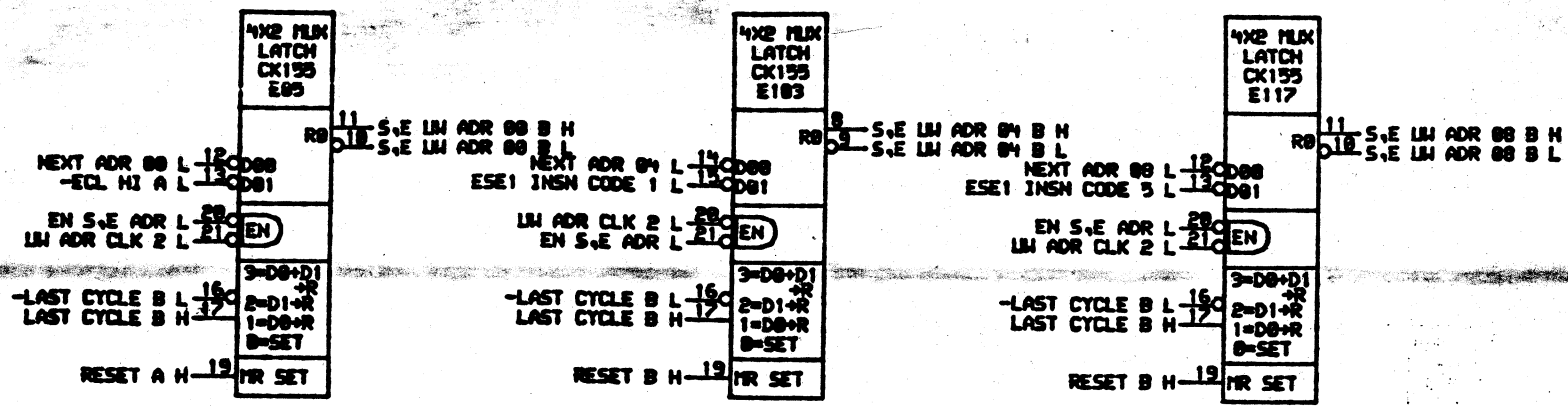




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REVISIONS		
CHK	CHANGE NO.	REV.

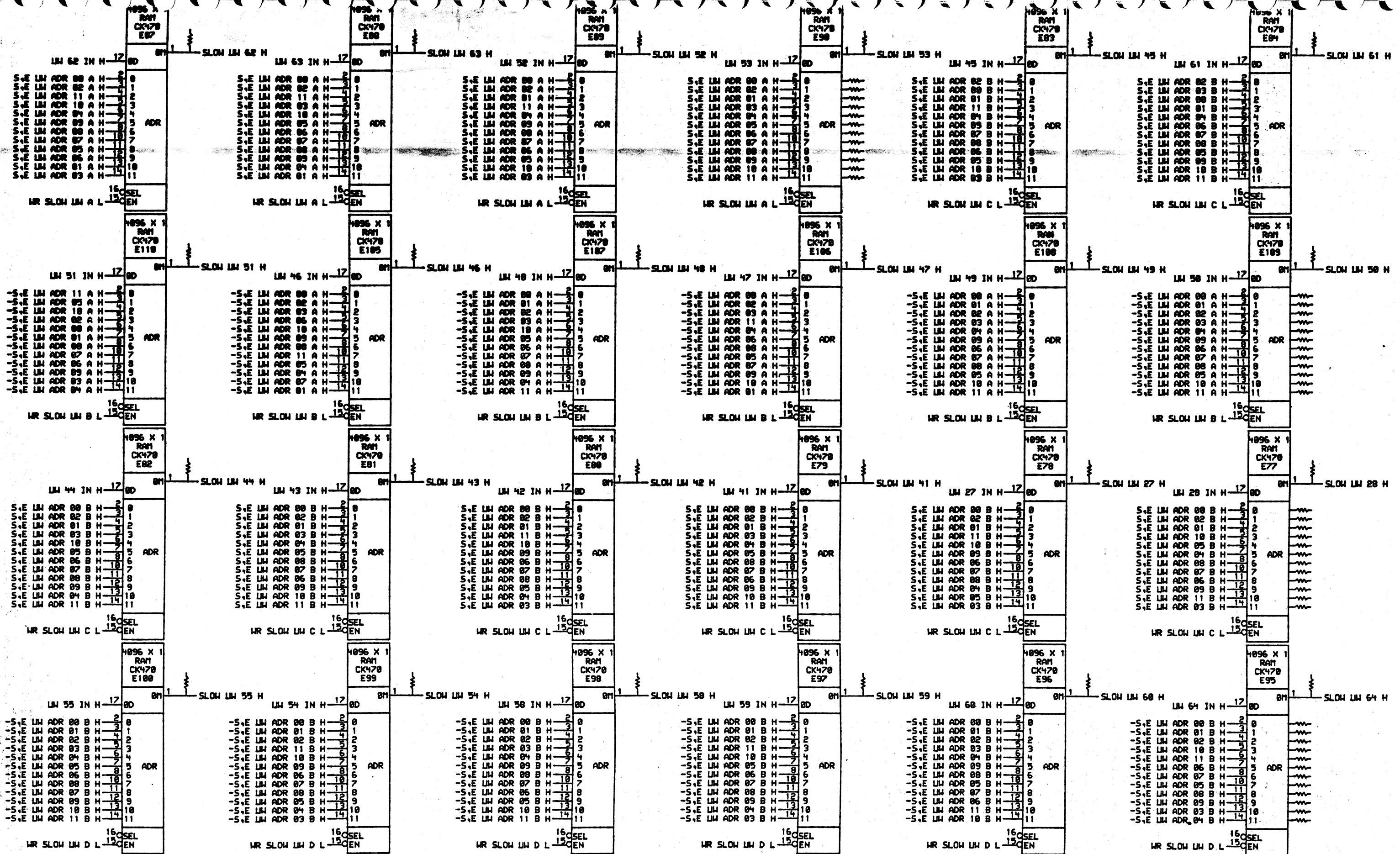
	DATE	ENG.	DATE	TITLE:
	25-JAN-83	J. J. J.	25-JAN-83	SLOW, EARLY CONTROL ADR
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		SIZE CODE
L0049-0-CRA1		L0049-0-CRA1		D CS
NUMBER			REV.	
L0049-0-CRA1				



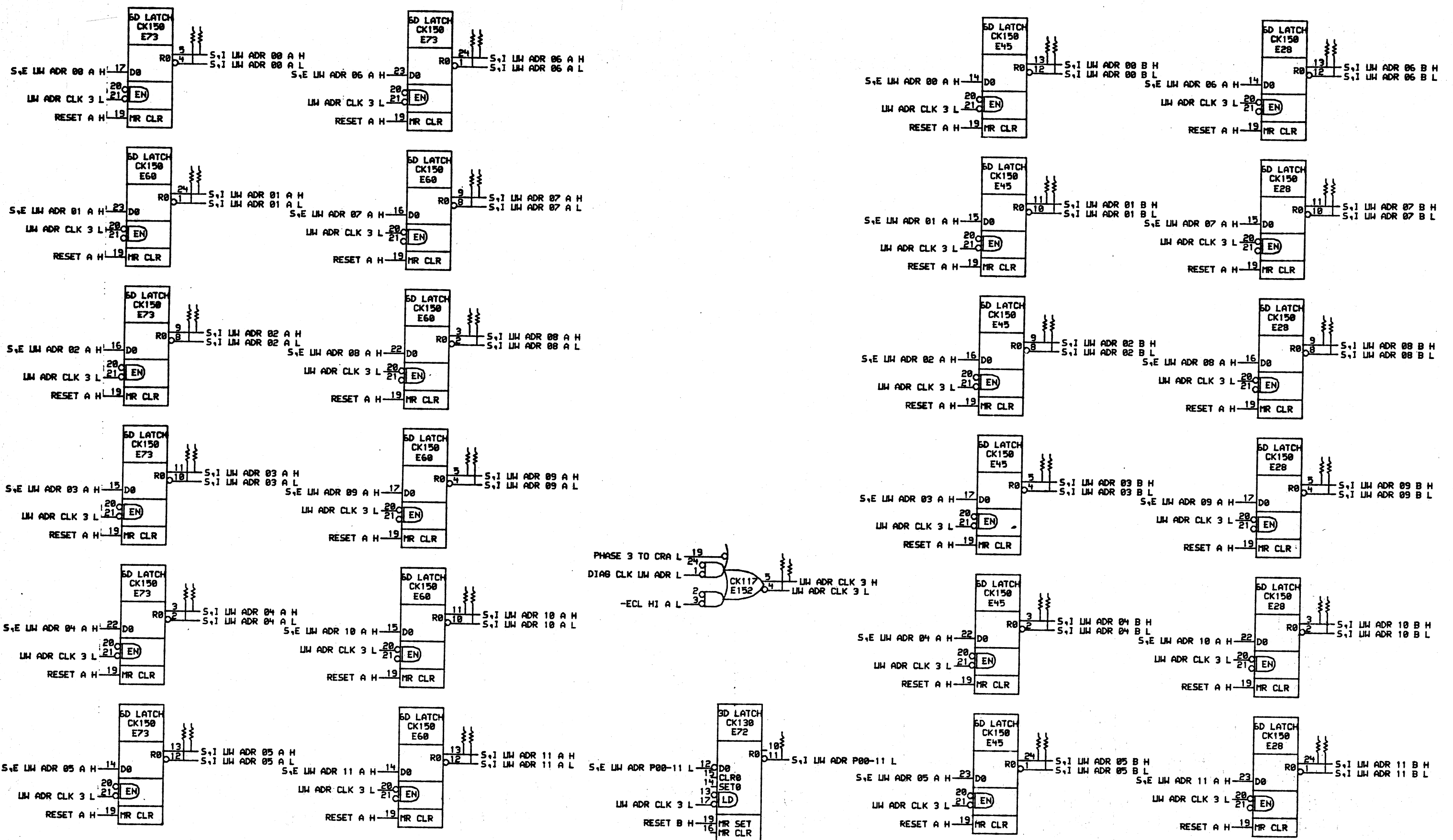
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REVISIONS	
CHK	CHANGE NO. REV

	DATE 19-JAN-82 DATE BOARD LOCATION SHEET OF	ENG. DATE TITLE: SLOW, EARLY CONTROL ADR
	FIRST USED ON OPTION MODEL:	NEXT HIGHER ASSEMBLY:



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		SLOW, EARLY CTLS
VIL CAN (2000, L0049) CR43, DRW 00-OCT-81 18:05 NEXT HIGHER ASSEMBLY:				00-17,28,37,38
FIRST USED ON OPTION MODEL:		SIZE CODE	NUMBER	REV.
		D	CS L0049-0-CRA3	

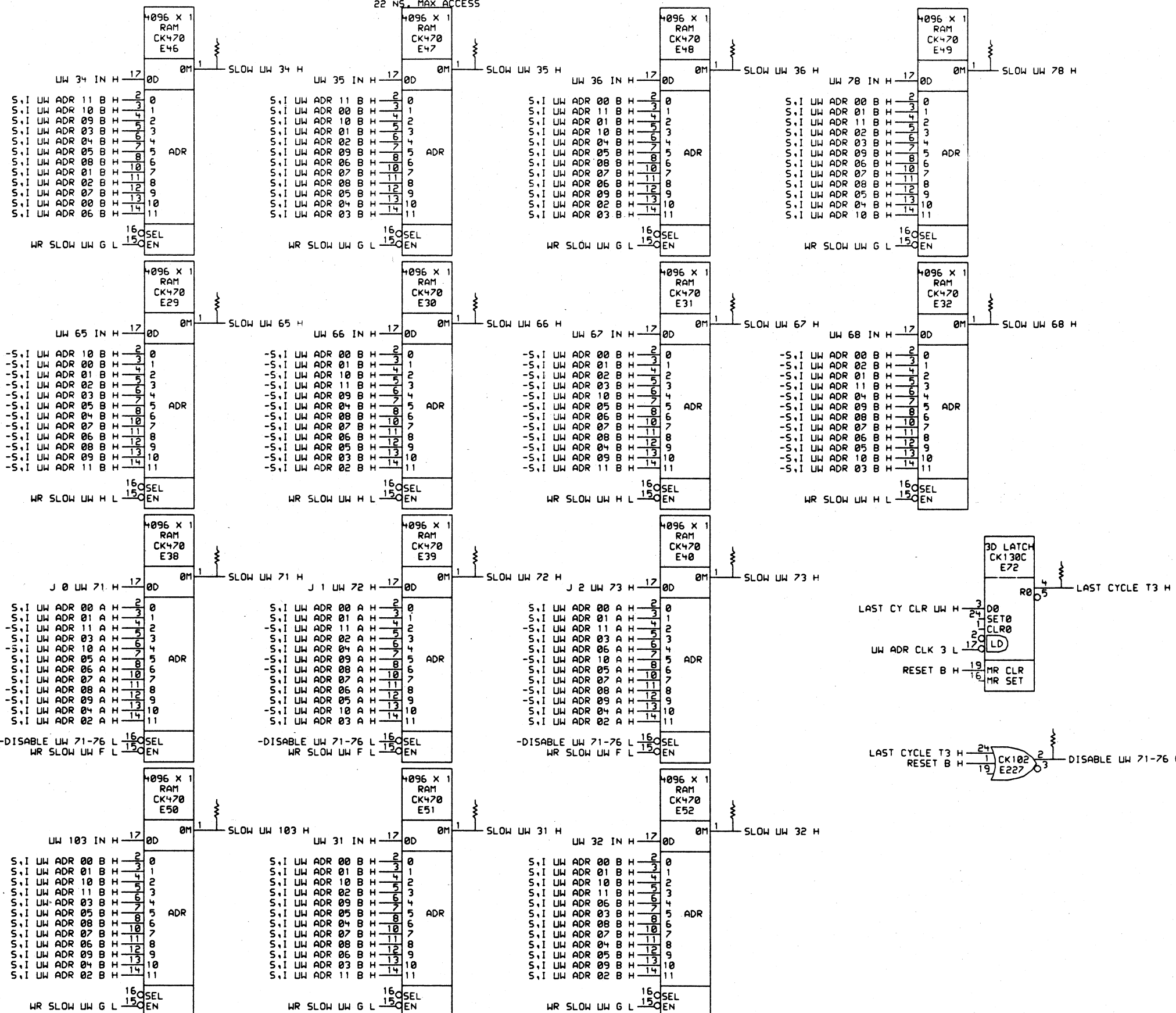


REV. NUMBER BOARD CODE SIZE D B A
 CS L0049-B-CRAY

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REVISIONS	
CHK	CHANGE NO. REV

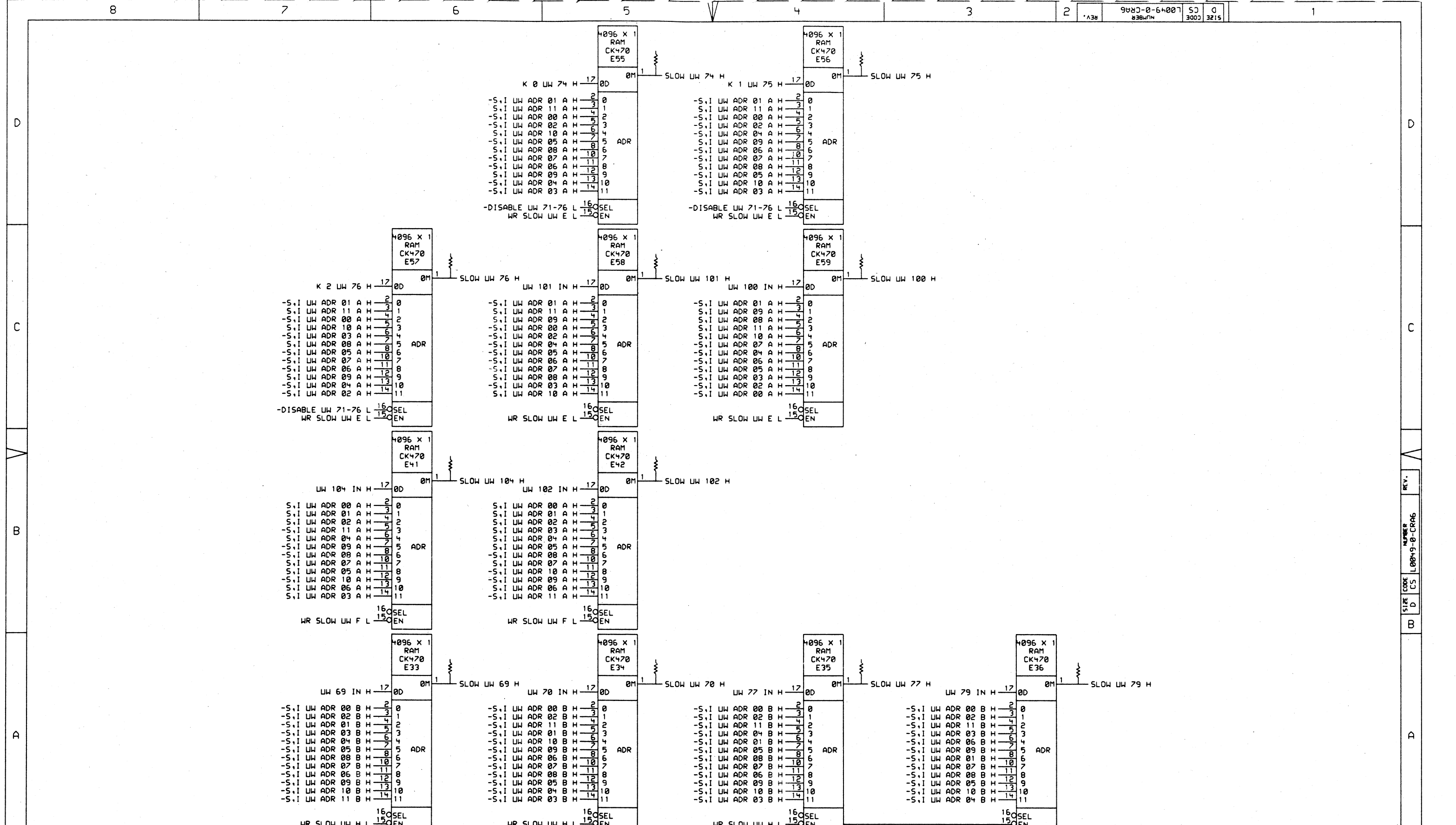
	DR. J. Family CHK'D.	DATE 14-AUG-82 DATE	ENG.	DATE	TITLE: SLOW, INTER CTL ADR REG
	VLSI (2000) L0049-B-CRAY DRW 112-AUG-82 14:20	NEXT HIGHER ASSEMBLY:	BOARD LOCATION:	SHEET OF:	SIZE CODE D CS L0049-B-CRAY



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REVISIONS		
CHK	CHANGE NO.	REV.

digital DRN. J. J. J. DATE 26-JAN-83 ENG. DATE 26-JAN-83
 CHK'D. BOARD LOCATION: SHEET OF
 VULCAN: (2000) L0049-0-CRA5.DRW 25-JAN-83 10:29 NEXT HIGHER ASSEMBLY:
 FIRST USED ON OPTION/MODEL: TITLE: SLOW, INTER 18,24,29-34
 39,76-7,80-1,83
 SIZE CODE D CS NUMBER L0049-0-CRA5 REV.



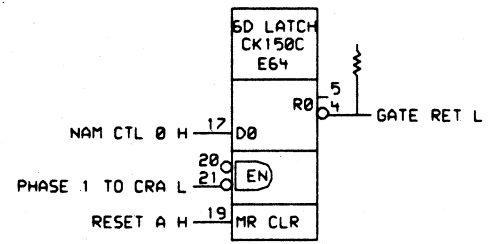
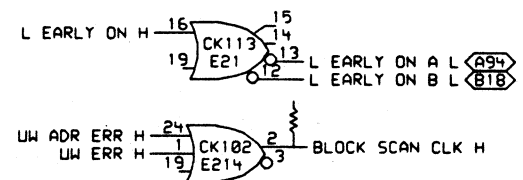
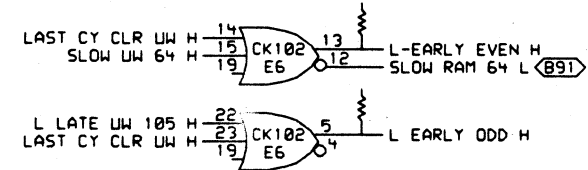
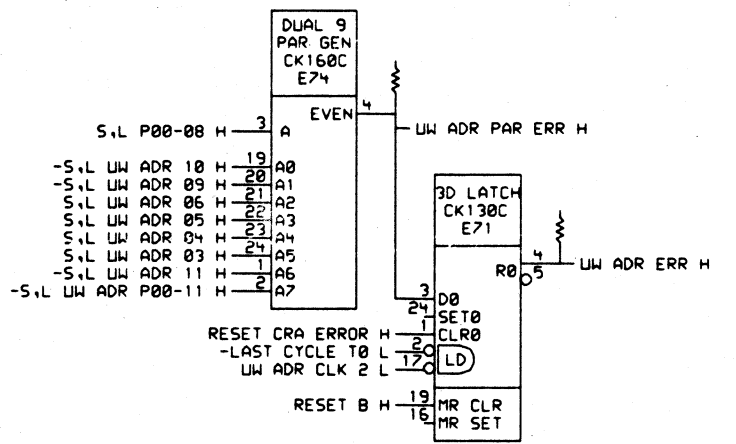
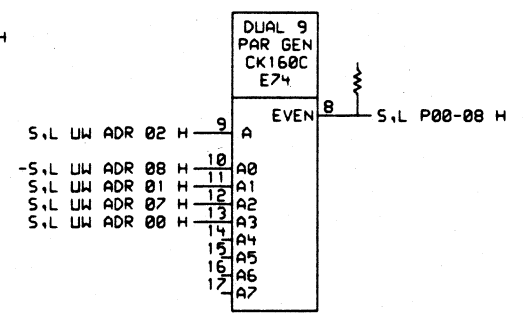
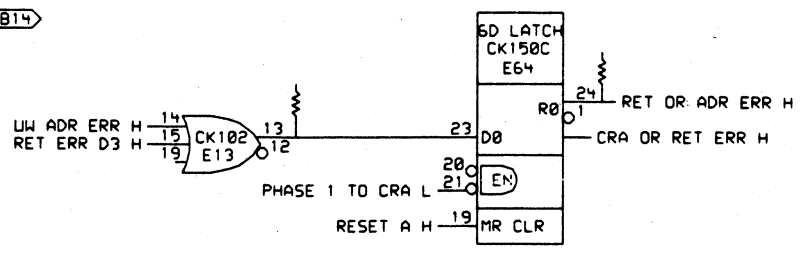
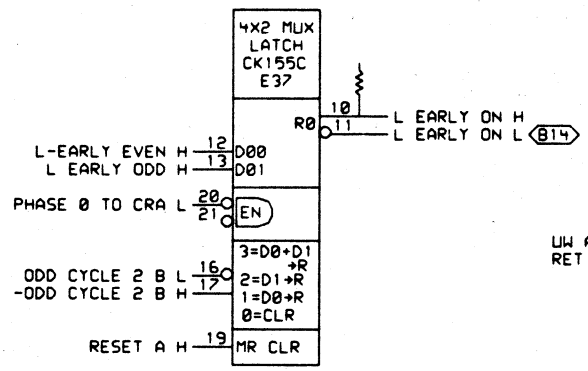
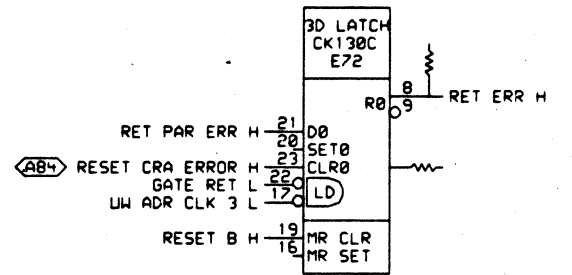
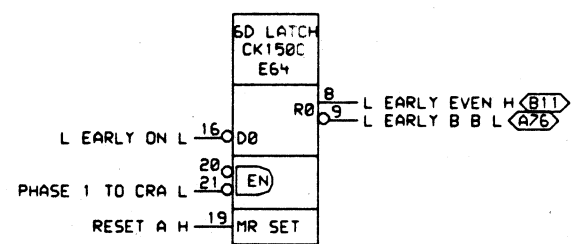
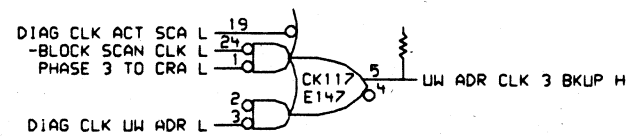
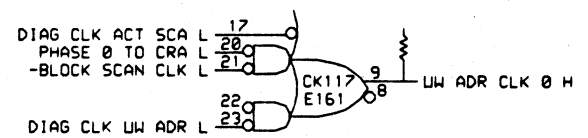
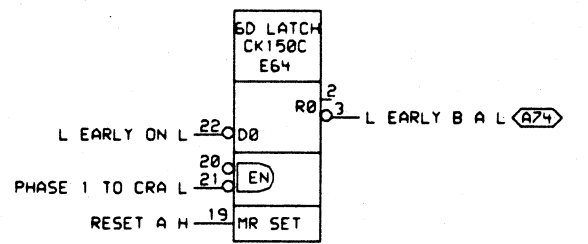
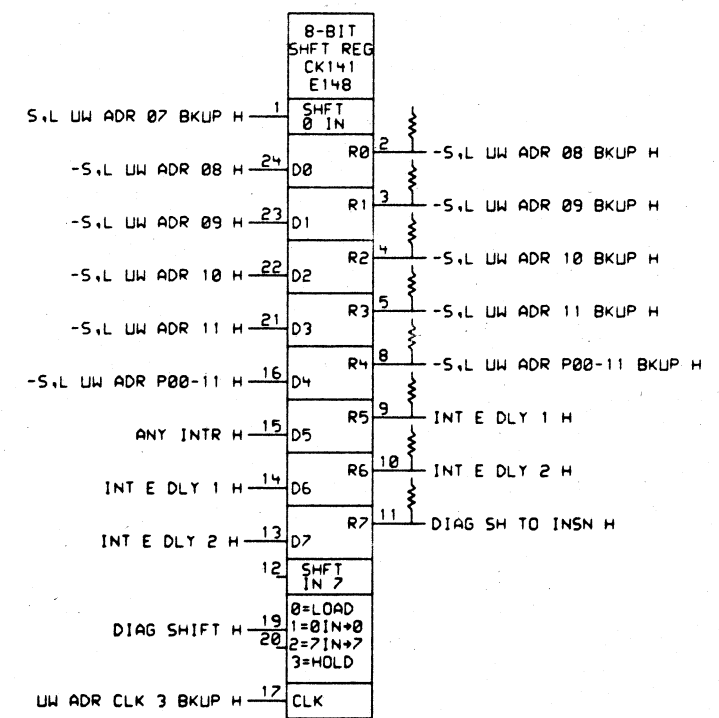
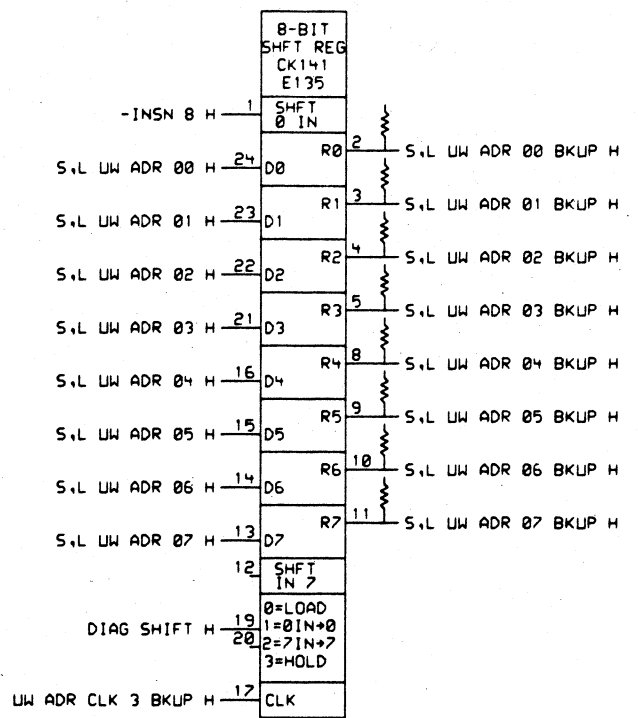
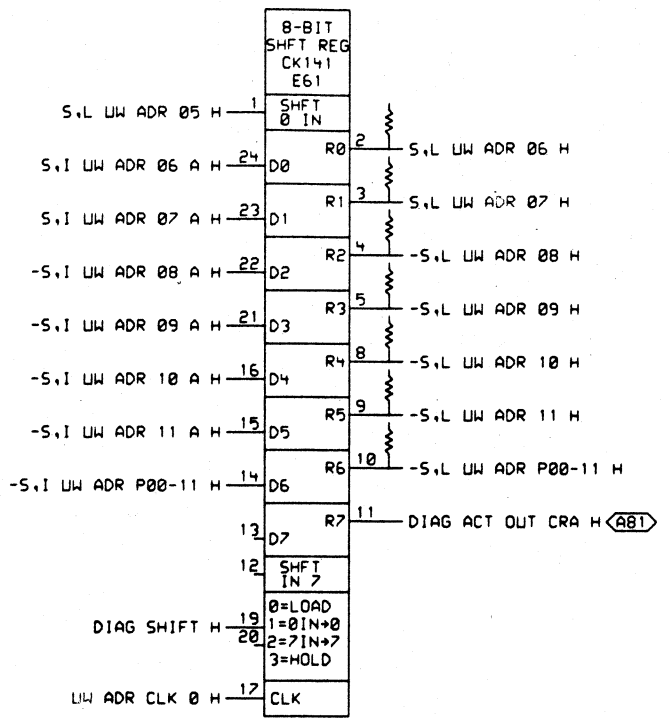
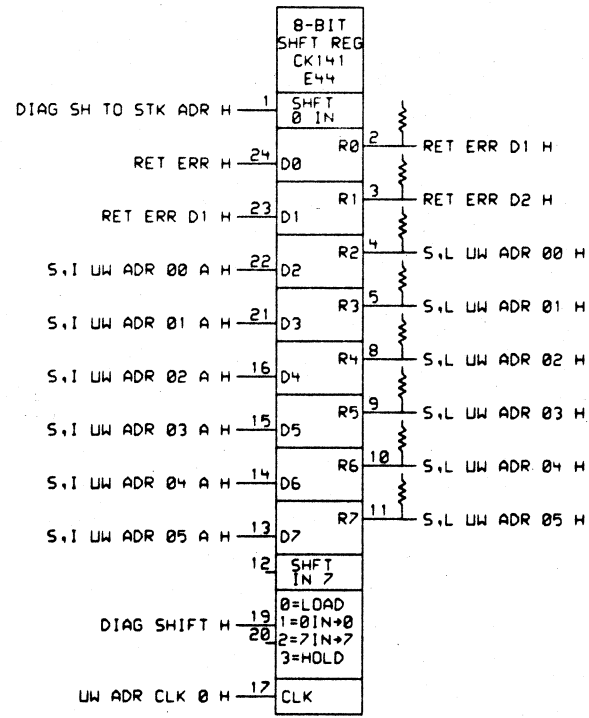
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REVISIONS		
CHK	CHANGE NO.	REV.

digital *DR. J. J. J.*
 DATE 26-JAN-83
 DATE 26-JAN-83
 CHECK'D.
 VULCAN: (2000.L0049)3CRA6.DRW 125-JAN-83 08:36
 FIRST USED ON OPTION/MODEL:

ENG.
 DATE
 BOARD LOCATION:
 SHEET OF
 NEXT HIGHER ASSEMBLY:

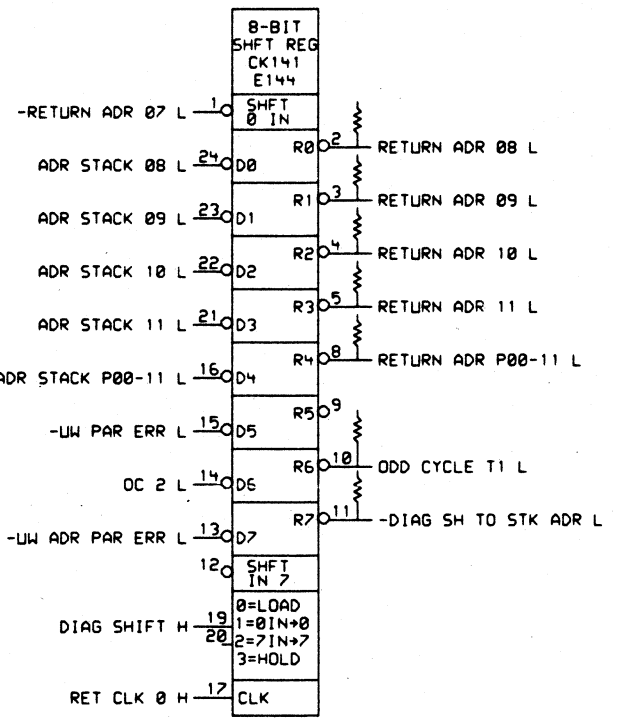
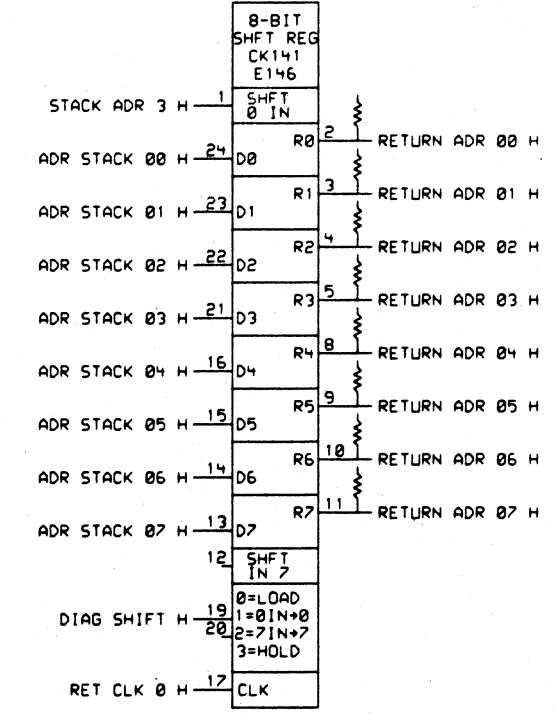
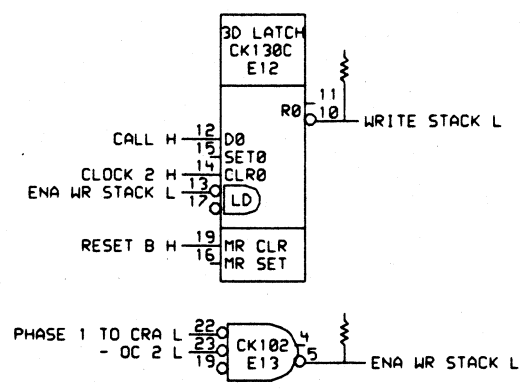
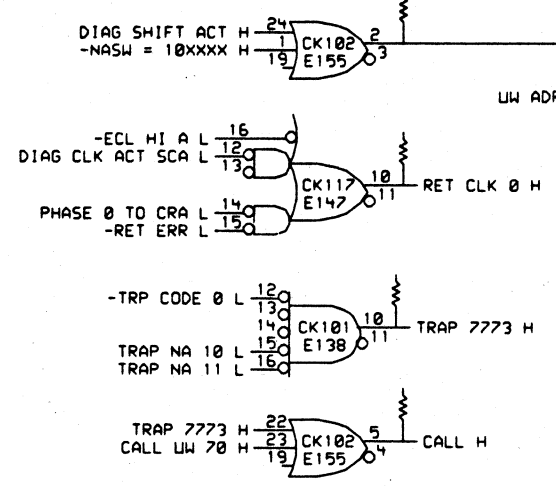
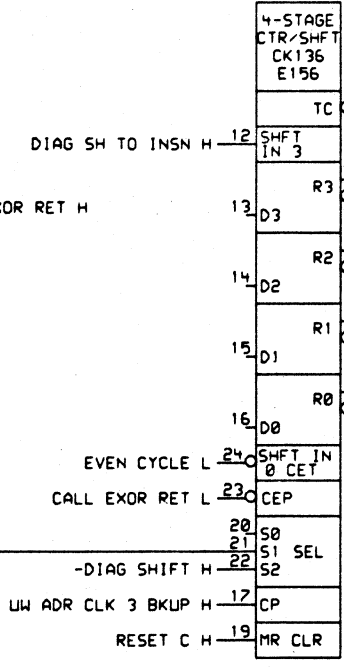
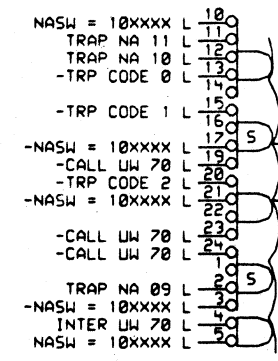
TITLE: SLOW, LATE CTLS
 19-23, 25-7, 40-7
 SIZE CODE D CS
 NUMBER L0049-0-CRA6
 REV.



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REVISIONS	CHK	CHANGE NO.	REV

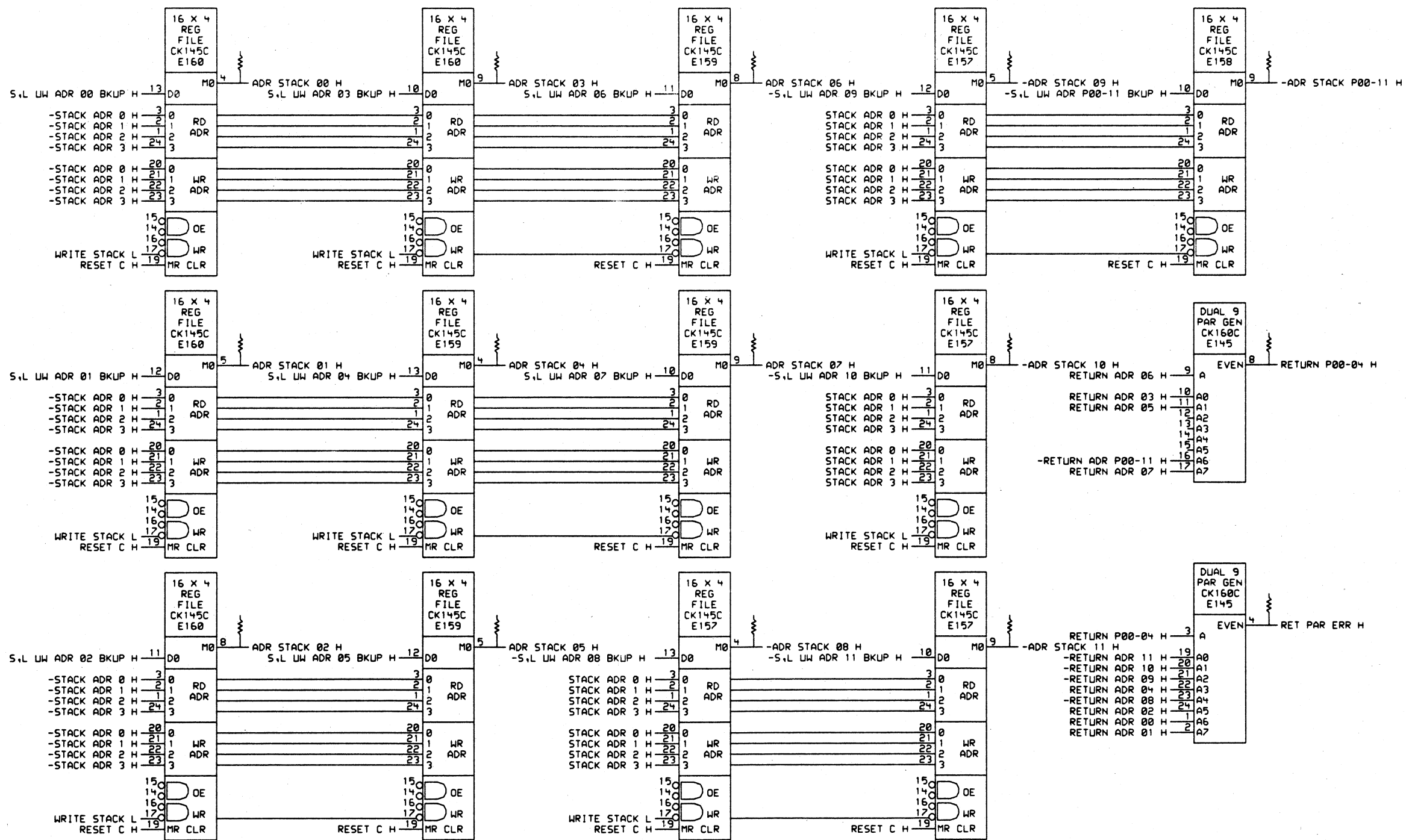
digital	DATE	ENG.	DATE	TITLE:
	26-JAN-83	J. Family		SLOW LATE ADDRESS ERROR
	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL:				SIZE CODE
				D CS
				NUMBER
				L0049-0-CRA7
				REV.



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REVISIONS	
CHK	CHANGE NO. REV

digital
 DRW. J. Family
 DATE 26-JAN-83
 DATE 25-JAN-83 10:26
 TITLE: NEXT ADDRESS PARITY MODIFY
 BOARD LOCATION: SHEET OF
 SIZE CODE D CS
 NUMBER L0049-0-CRAB
 REV.



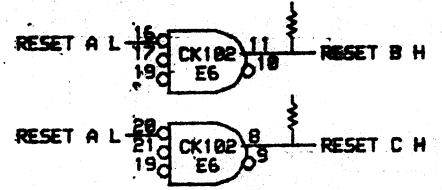
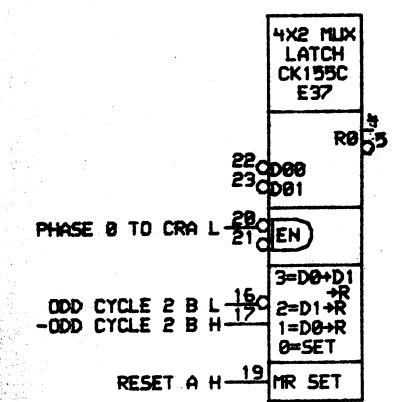
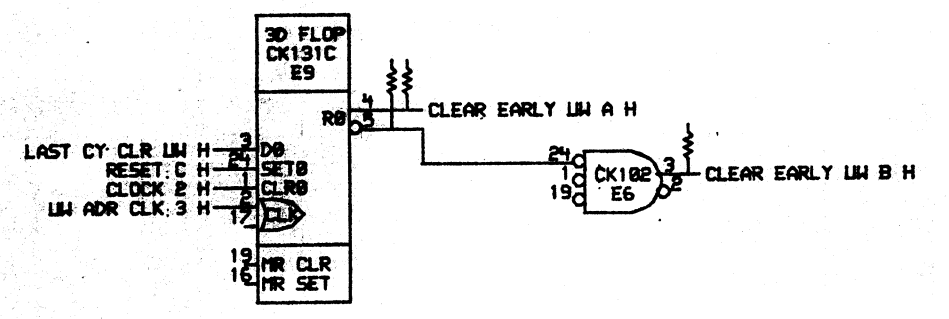
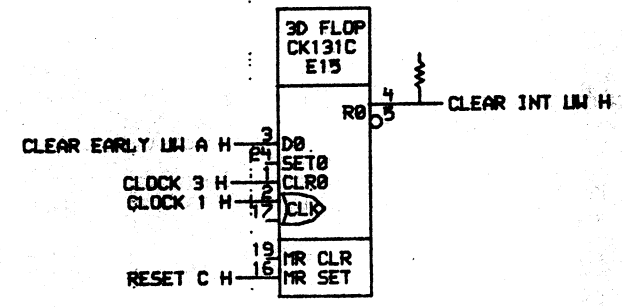
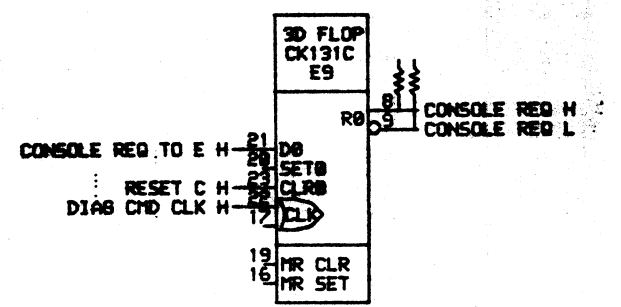
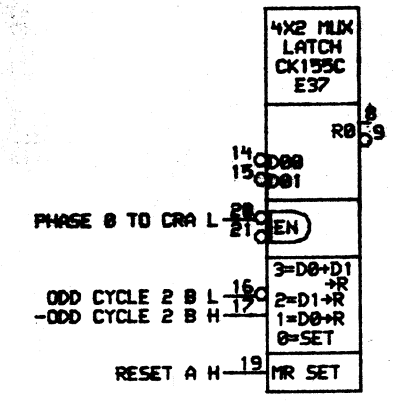
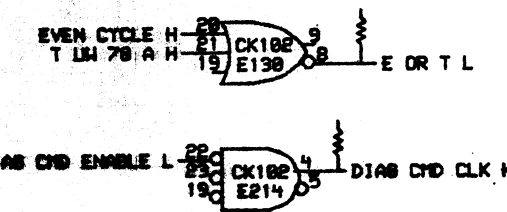
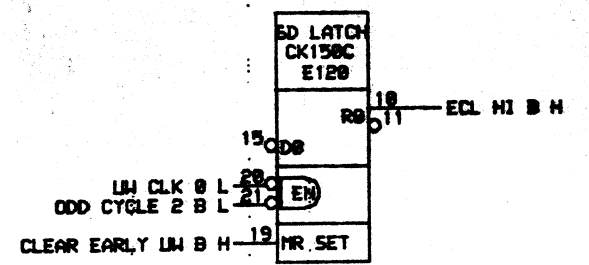
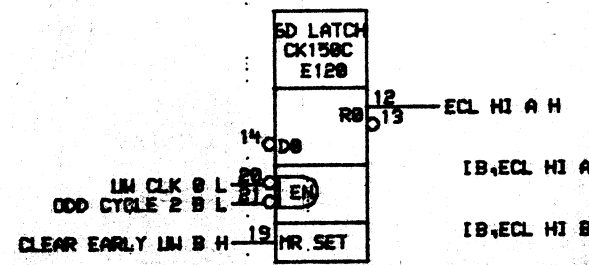
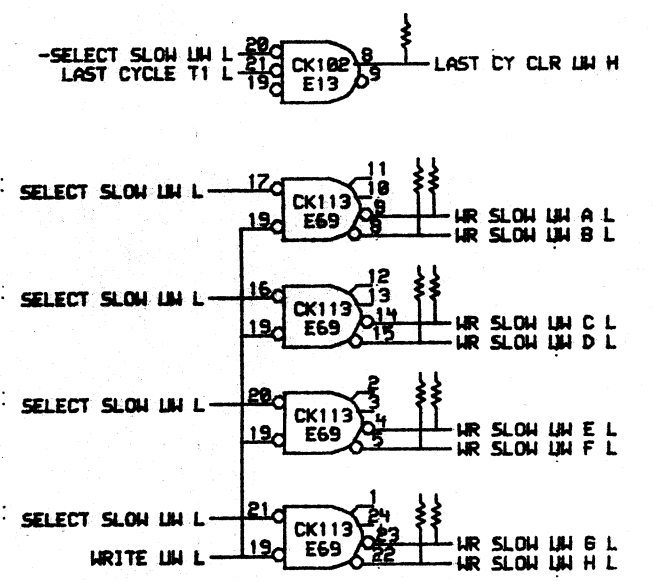
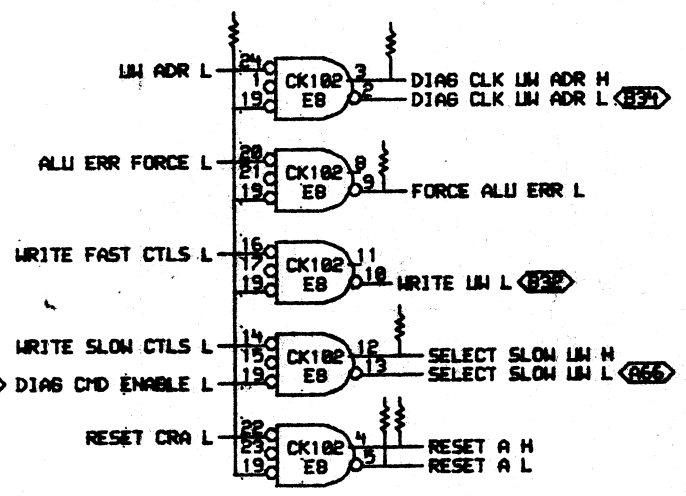
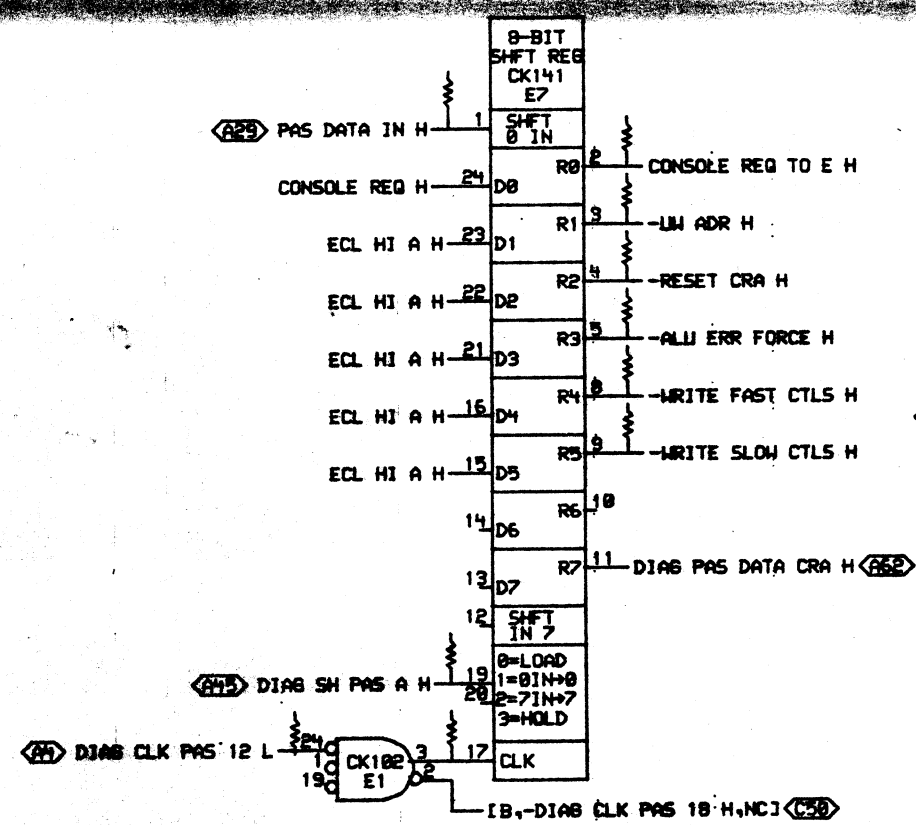
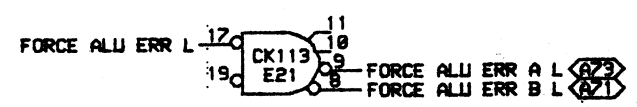
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REVISIONS	
CHK	CHANGE NO. REV.

digital
 DATE 26-JAN-83
 DATE 25-JAN-83 09:17
 FIRST USED ON OPTION/MODEL:

ENG. DATE
 BOARD LOCATION: SHEET OF
 NEXT HIGHER ASSEMBLY:

TITLE: ADR STACK PAR CHK
 NUMBER L0049-0-CRA9
 SIZE CODE D CS
 REV.



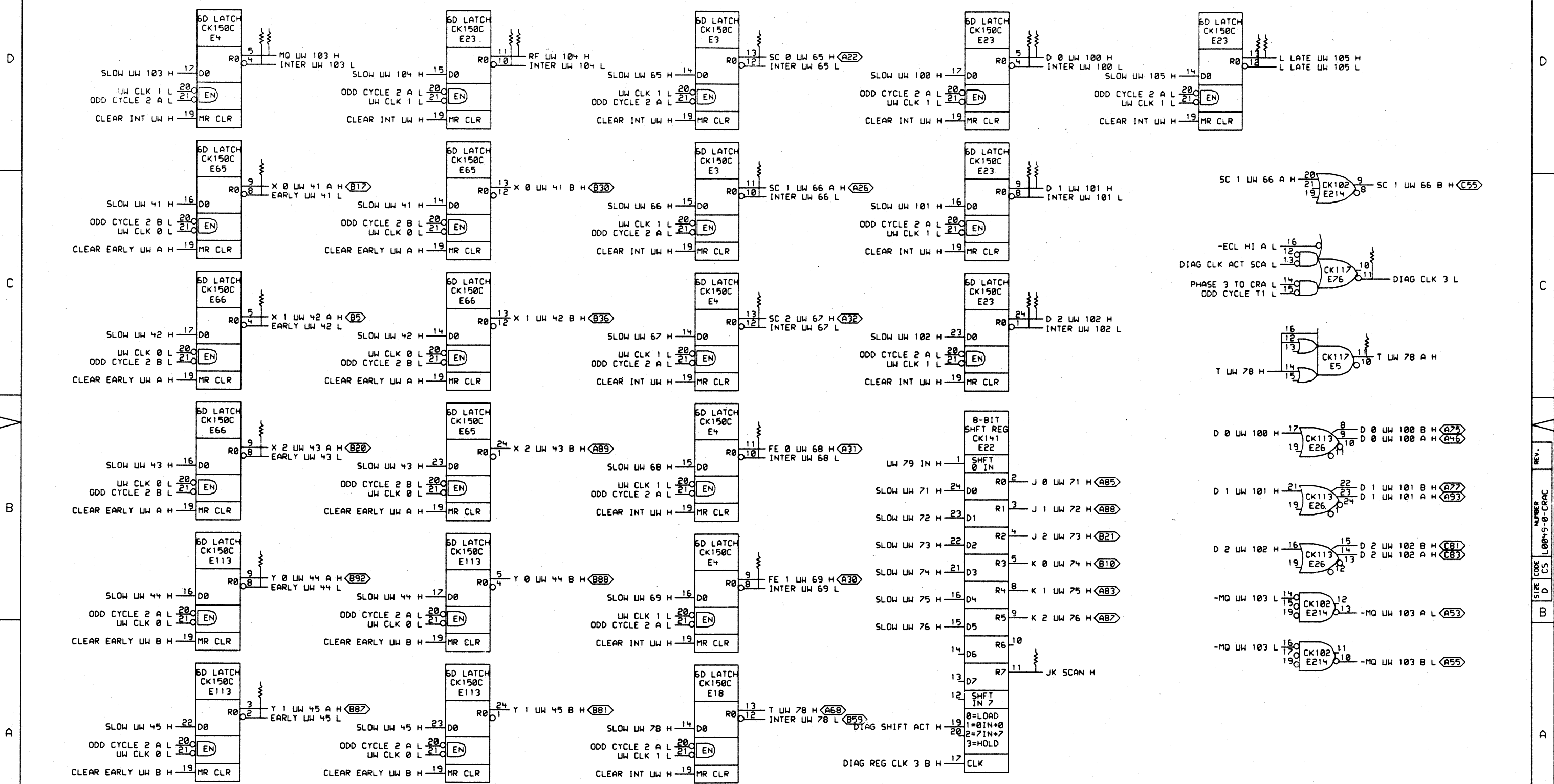
D
C
B
A

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REVISIONS
CHK CHANGE NO. REV

digital	DATE 09-OCT-82	ENG.	DATE
	DATE	BOARD LOCATION	DATE
FIRST USED ON OPTION MODEL:		NEXT HIGHER ASSEMBLY:	

TITLE: MISC CLKS, CTLS & BUFFERING			
SIZE D	CODE CS	NUMBER L0049-0-CRAA	REV.

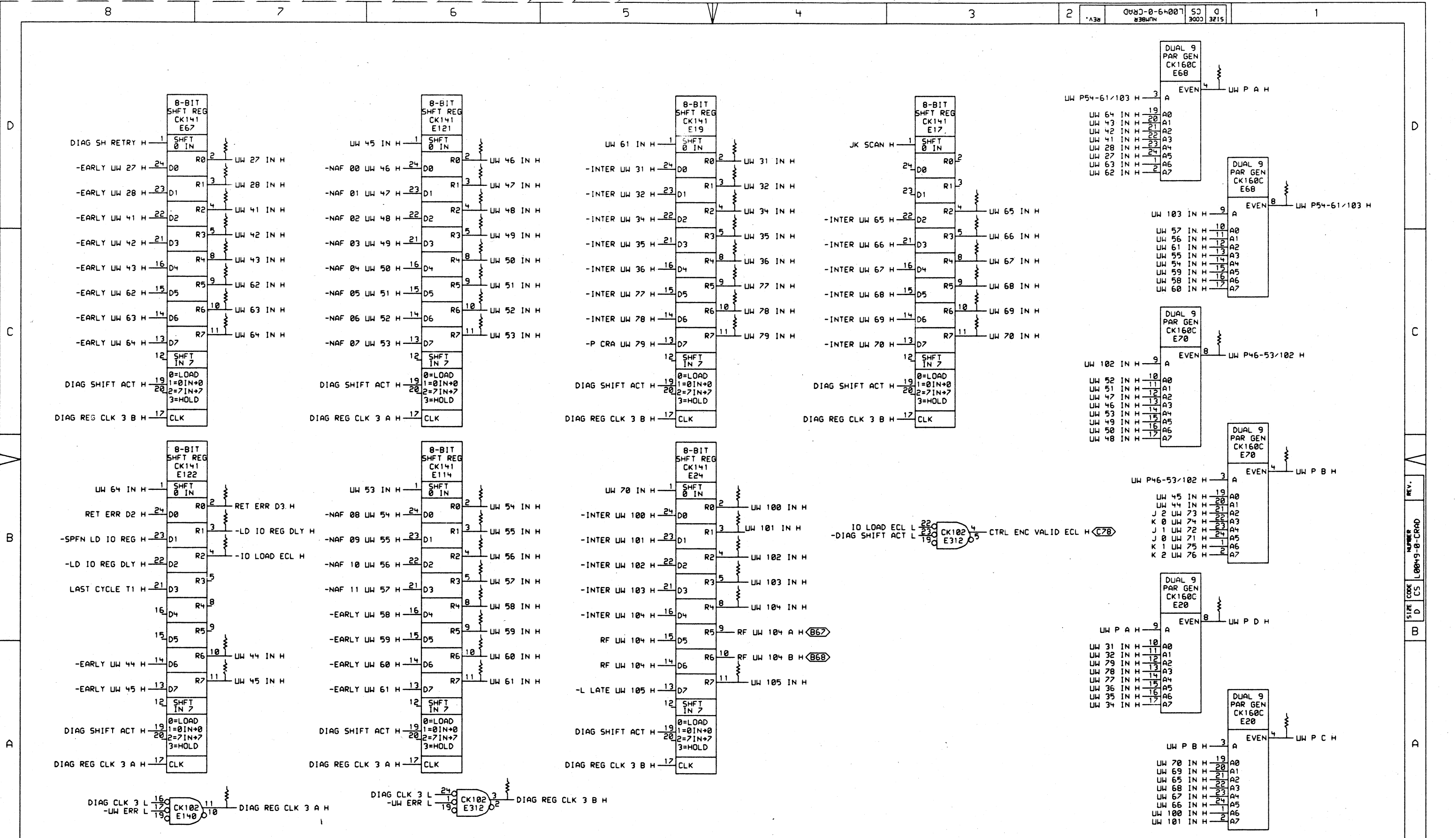


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REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	26-JAN-83			LATE CONTROLS
CHK'D.	DATE	BOARD LOCATION:	SHEET	19-23,25-7,40-7
VULCAN(2000.L0049)03CRAC.DRW	25-JAN-83 09:21	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL:			D CS	L0049-0-CRAC
			REV.	

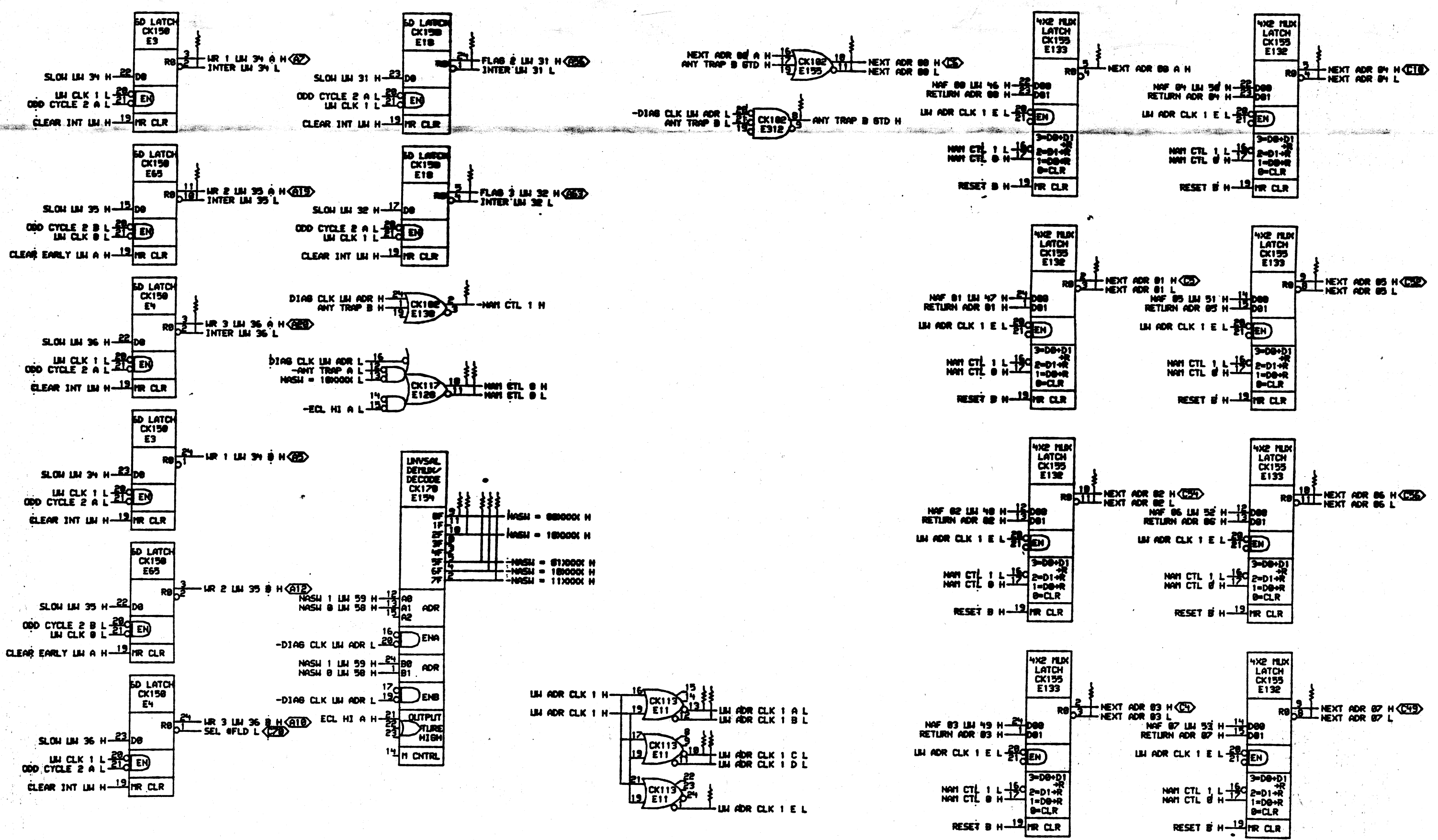
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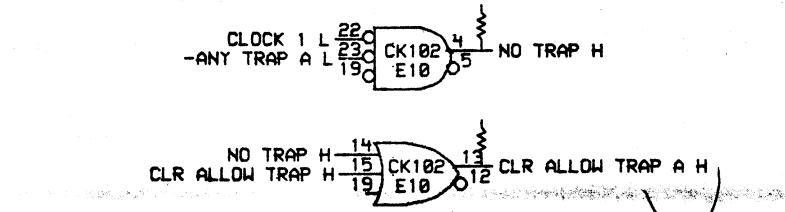
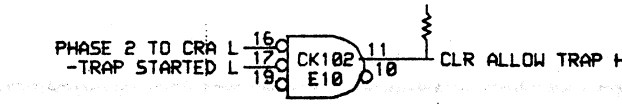
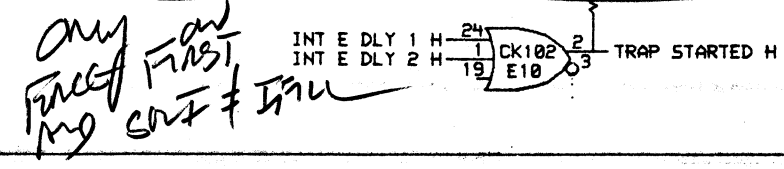
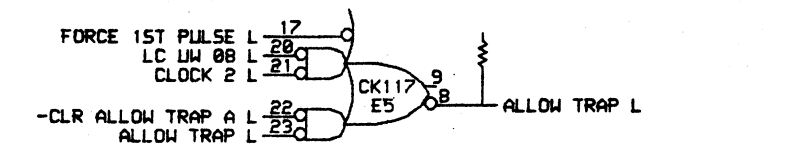
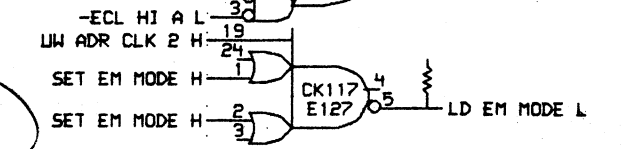
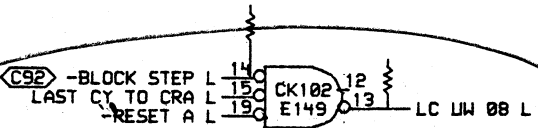
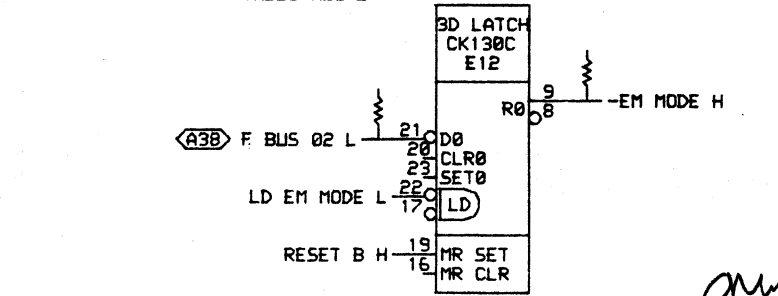
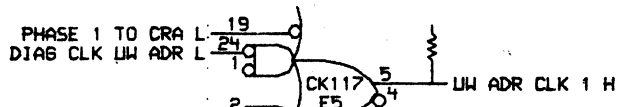
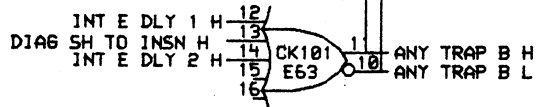
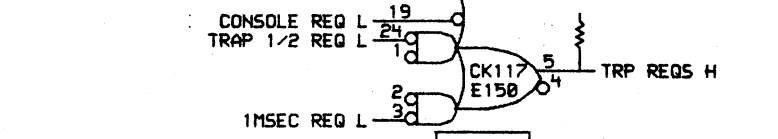
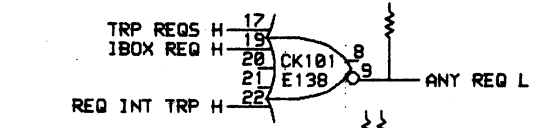
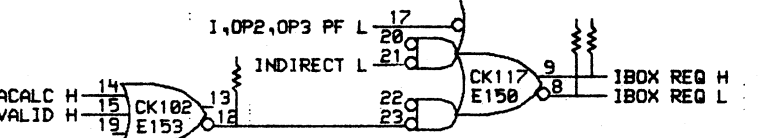
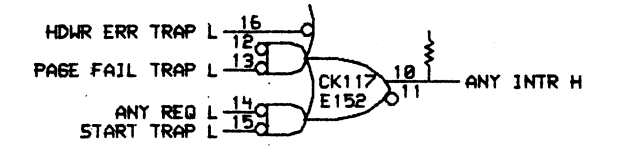
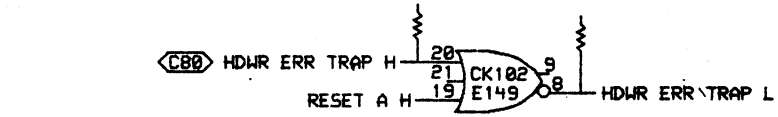
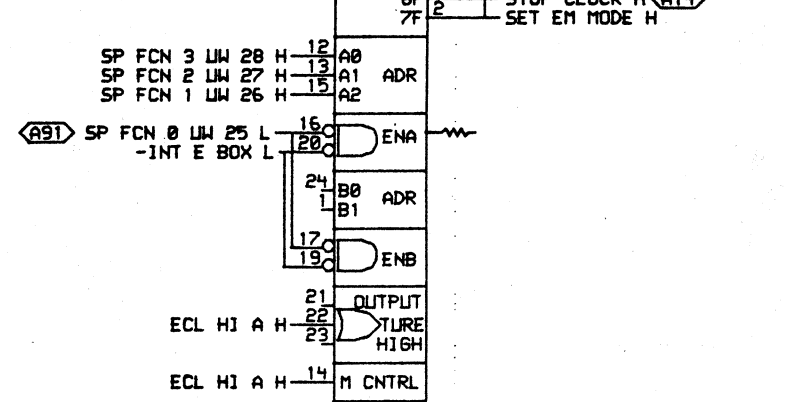
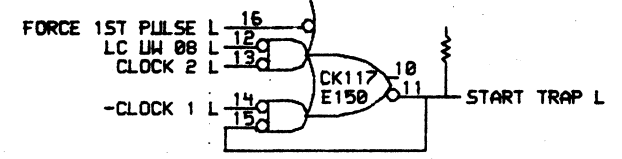
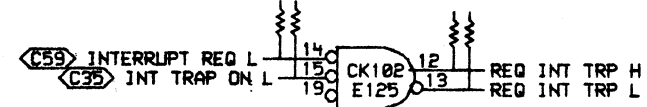
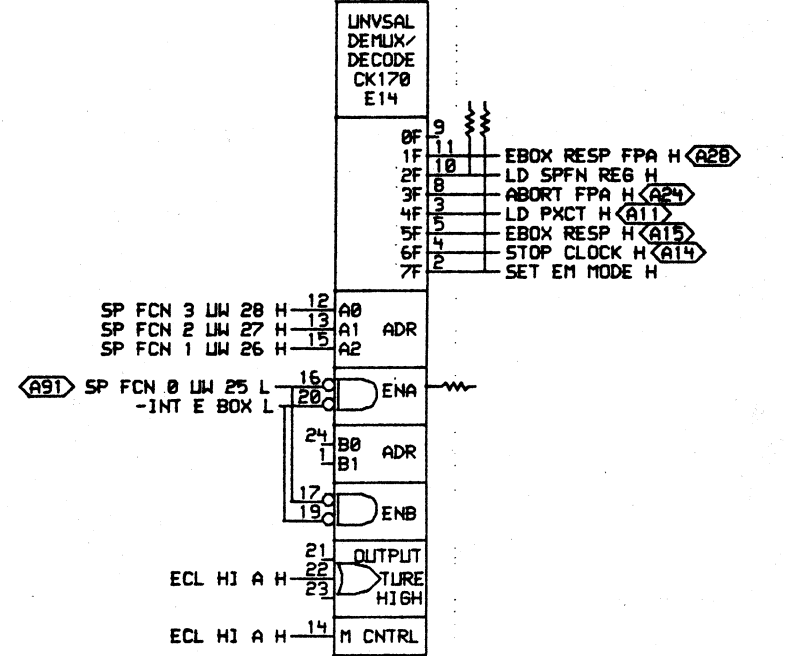
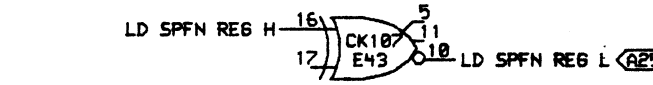
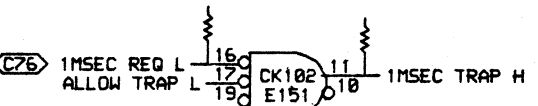
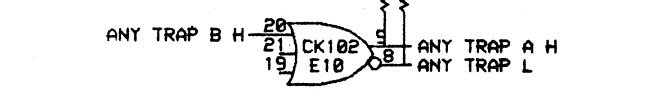
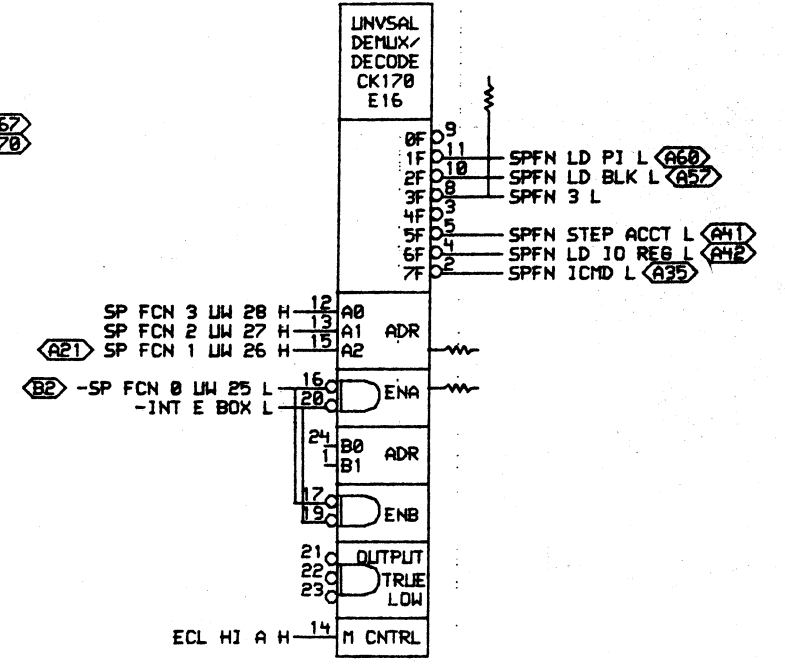
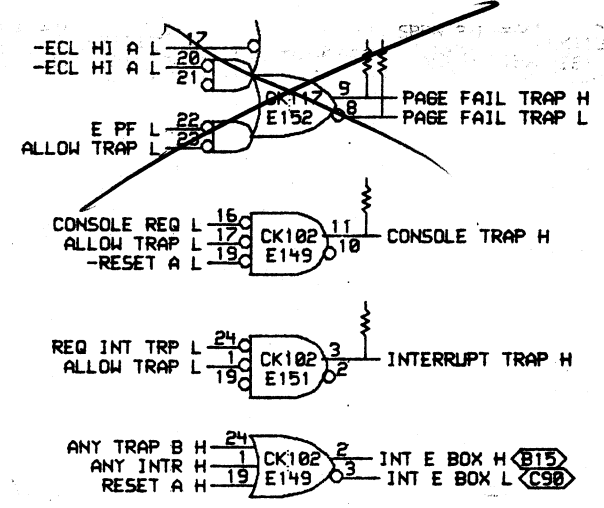
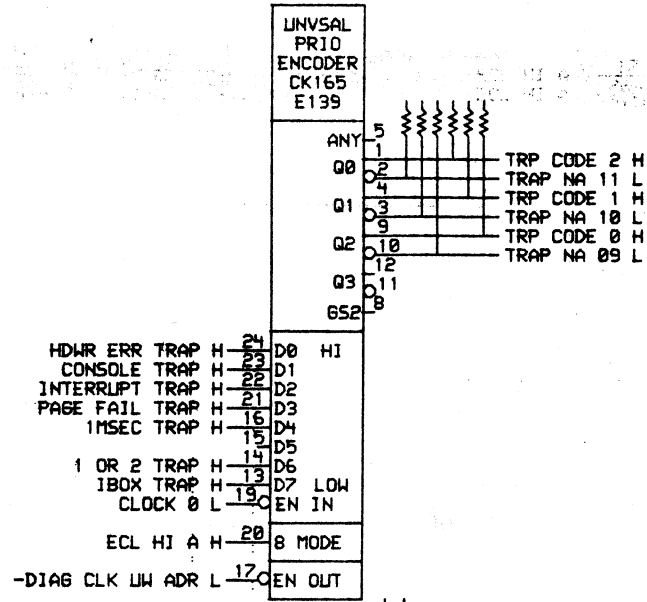
REVISIONS		
CHK	CHANGE NO.	REV

digital	DRW. J. J. J.	DATE 26-JAN-83	ENG.	DATE	TITLE: DIAG CONTROL
	CHK'D.	DATE	BOARD LOCATION:	SHEET	REG 00-34, 39-47
FIRST USED ON OPTION/MODEL:			25-JAN-83 13:03	NEXT HIGHER ASSEMBLY:	SIZE CODE NUMBER REV.
				D	CS L0049-0-CRAD

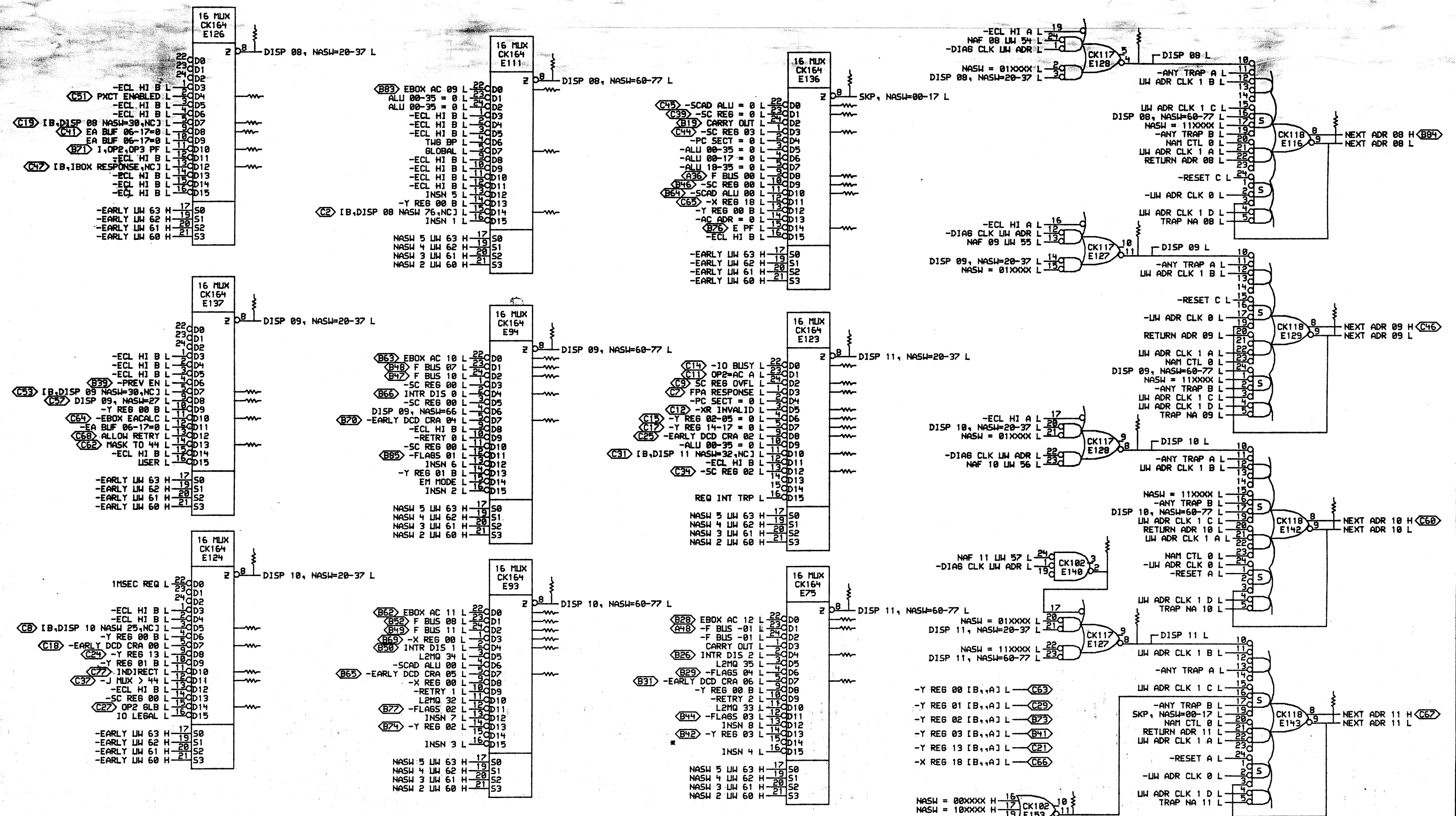


REVISIONS	
CHK	CHANGE NO. REV

	DR. J. L. L...	DATE 12-11-68	ENG.	DATE	TITLE: INTER CONTROLS 76,77,80,81,83
	CK'D.	DATE	BOARD LOCATION		
FIRST USED ON OPTION MODEL: 116-JL-82 2048 NEXT HIGHER ASSEMBLY:					SIZE CODE D CS NUMBER L0049-0-CRAE REV.



*any
force
first
no
clk # 1/2*

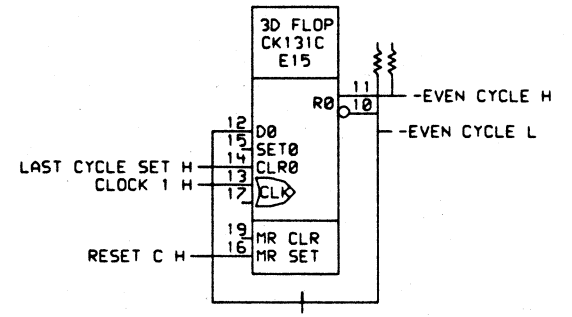
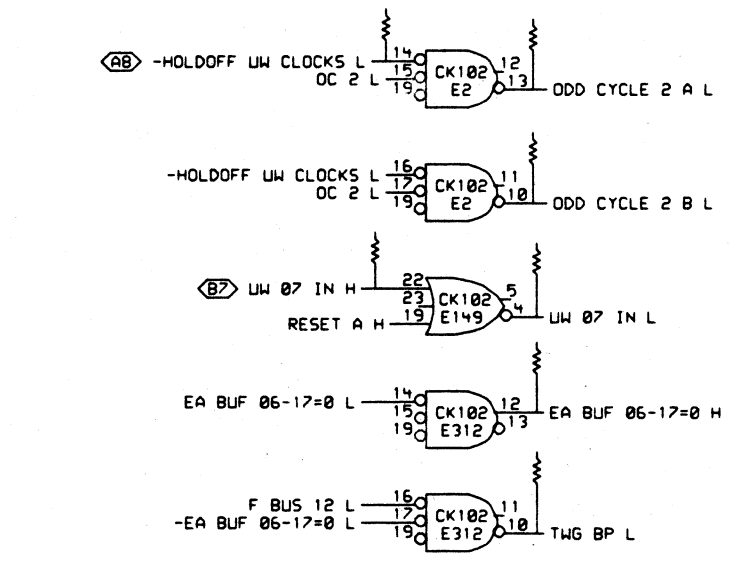
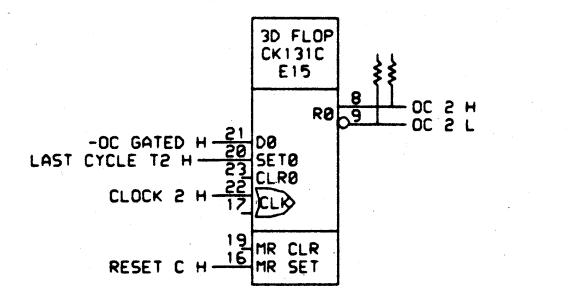
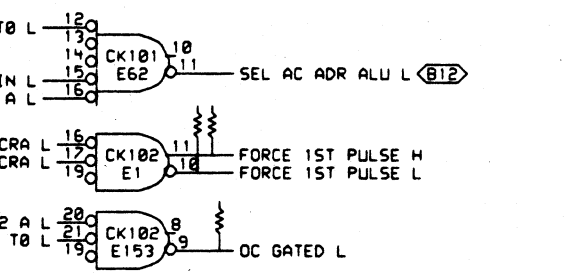
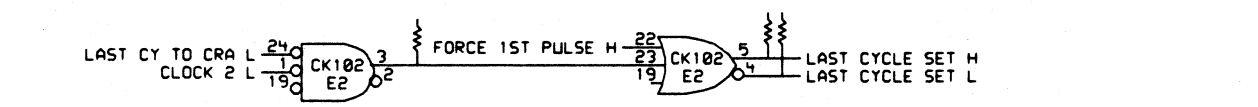
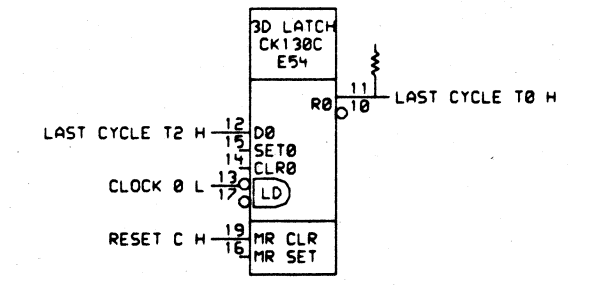
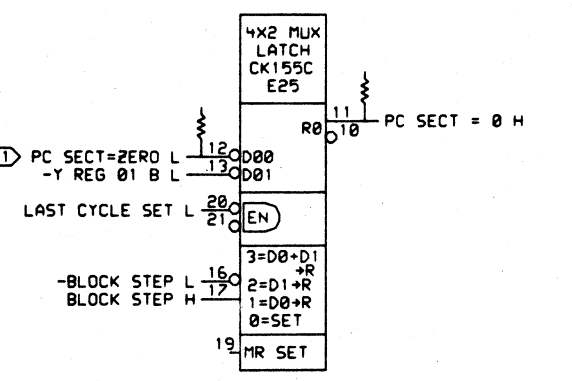
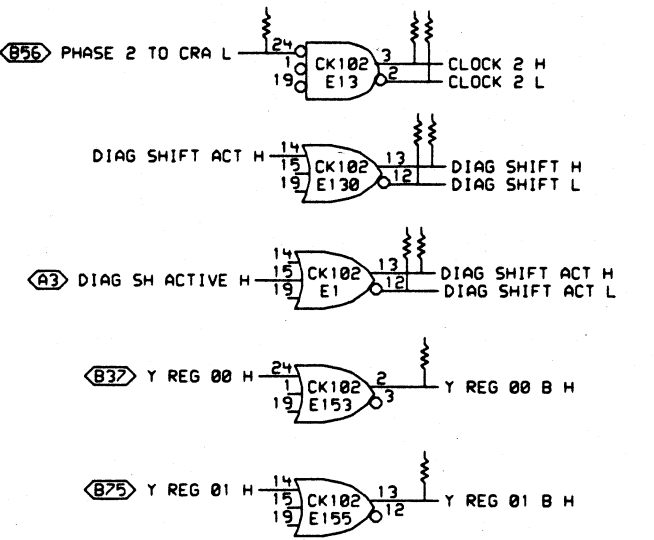
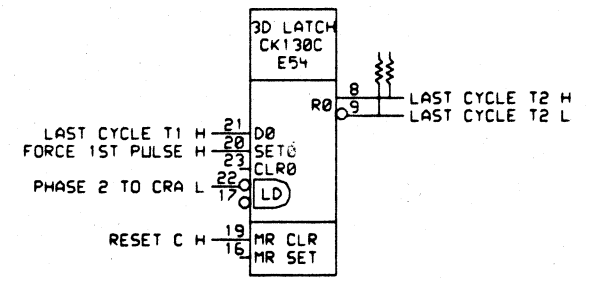
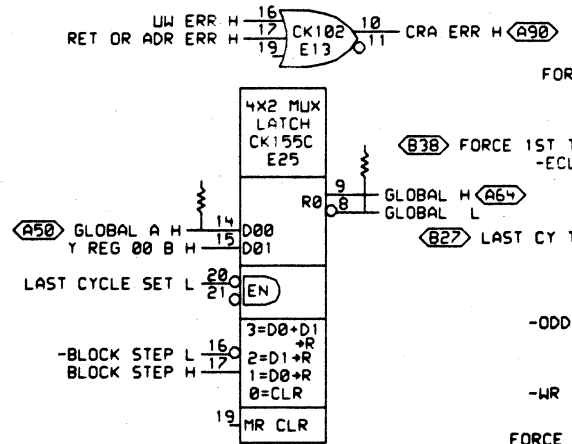
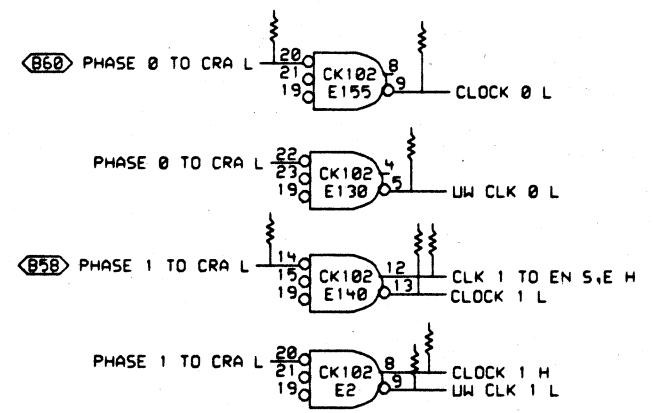
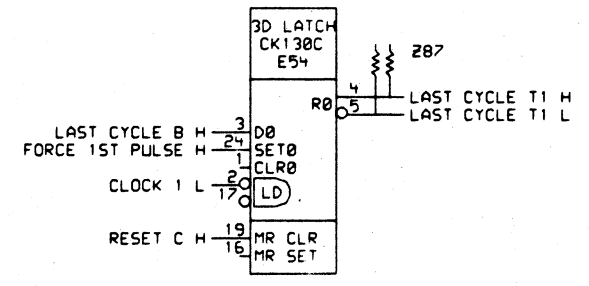
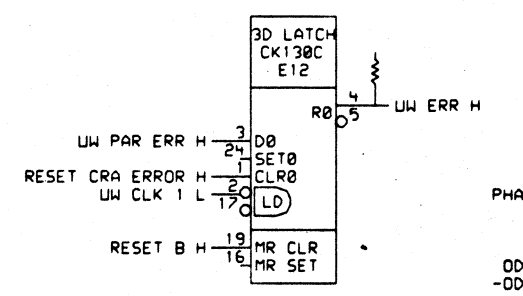
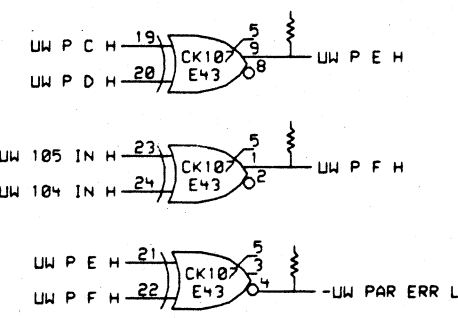


*NOTE: STUBS HERE TERMINATE ON SCA.

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REVISIONS	
CHK	CHANGE NO. REV

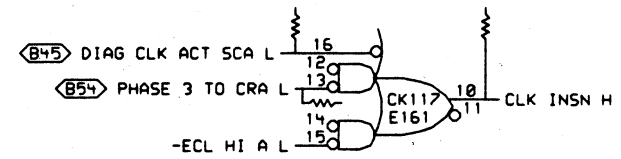
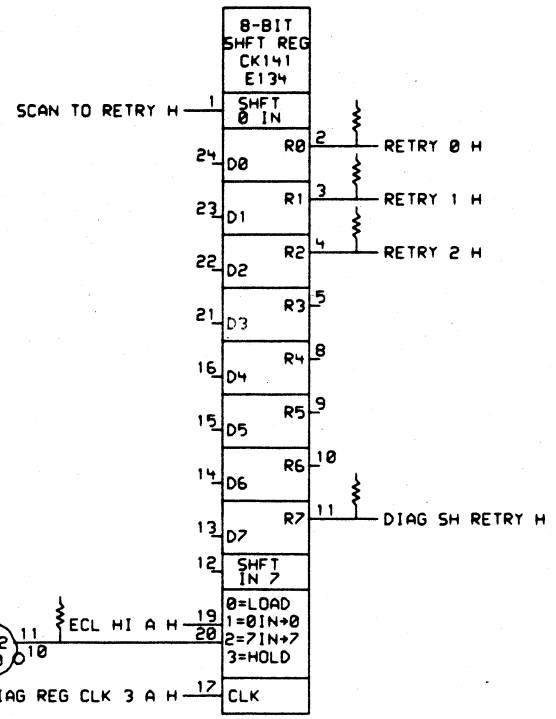
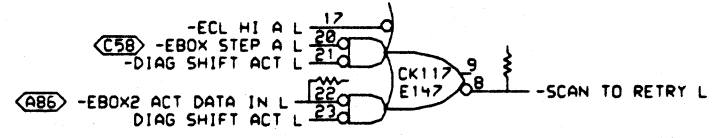
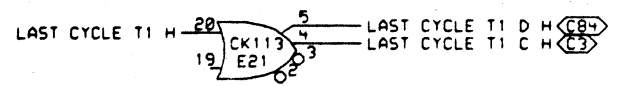
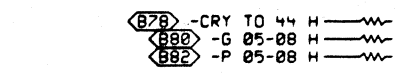
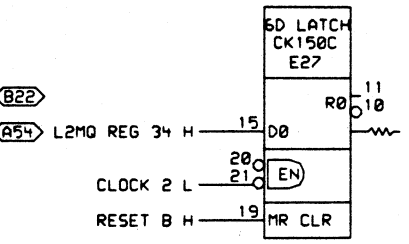
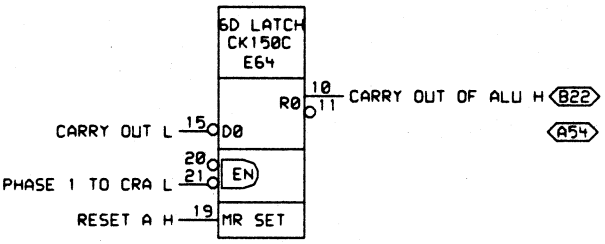
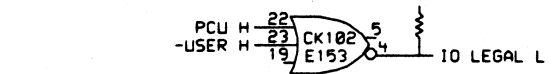
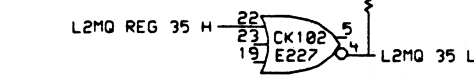
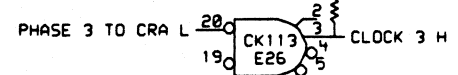
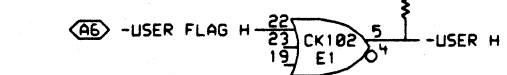
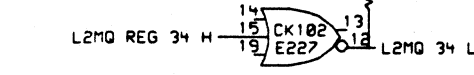
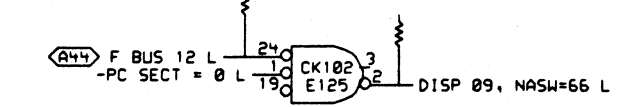
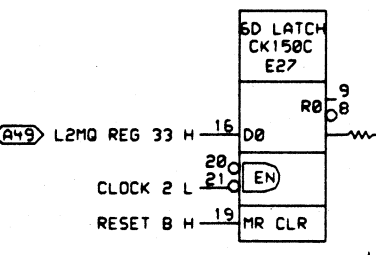
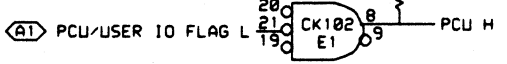
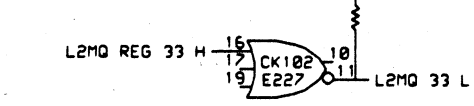
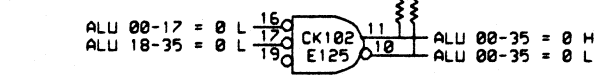
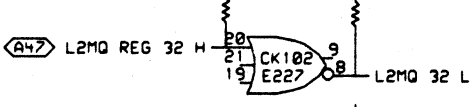
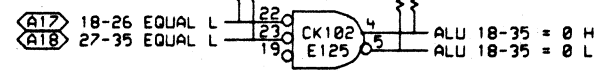
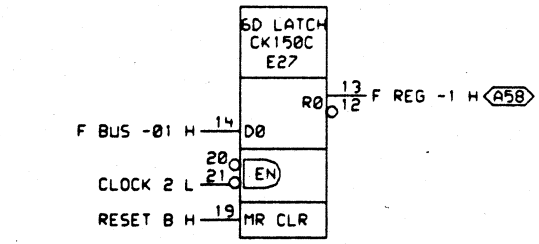
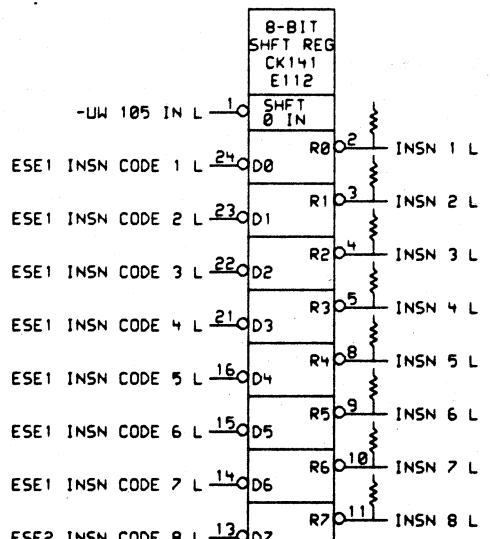
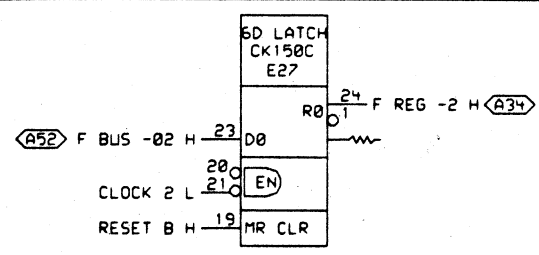
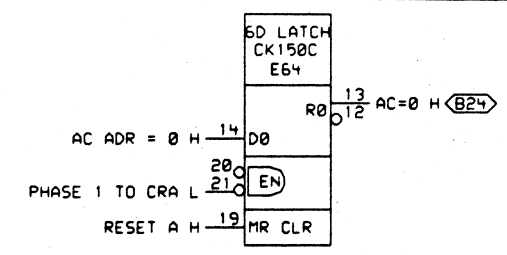
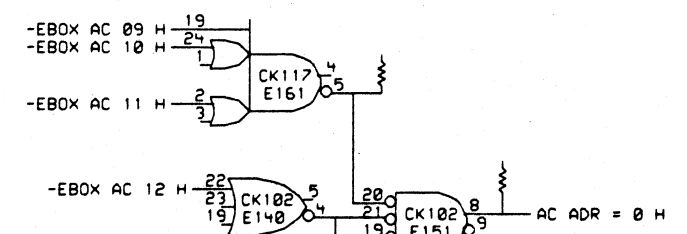
	DRW. <i>J. J. J.</i>	DATE 06-JAN-89	ENG.	DATE	TITLE: NEXT ADDRESS
	CHK'D.				00-11
VULCAN 4888.L0049-0-CRAG.DRW 05-JAN-83 09:05 NEXT HIGHER ASSEMBLY:		SHEET OF		SIZE CODE D C5	NUMBER L0049-0-CRAG
FIRST USED ON OPTION/MODEL:				REV.	



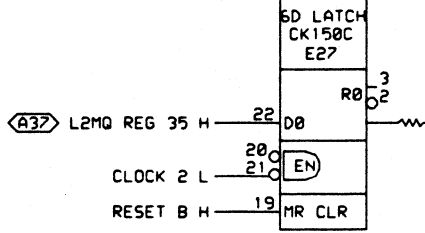
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REVISIONS	CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	25-JAN-83	J. Jansky		MISC CLOCKS, ERRORS, CTLS
	DATE	BOARD LOCATION:	SHEET	OF
	25-JAN-83 10:36			
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
			D CS	L0049-0-CRAH
			REV.	



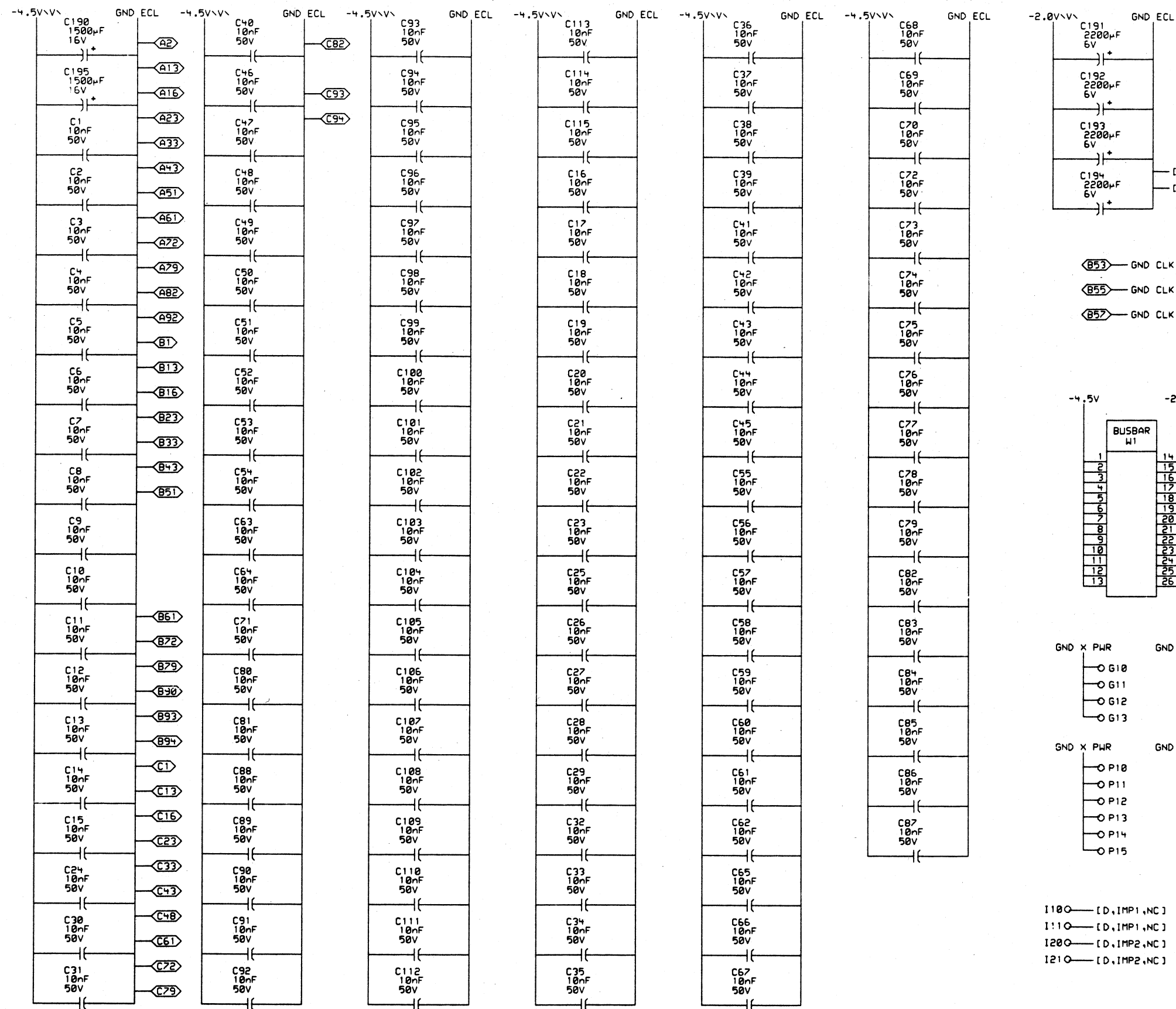
[B,CARRY OUT,CRY OUT] L - B40
 [B,EBOX STEP A,EBOX STEP B] H - C88



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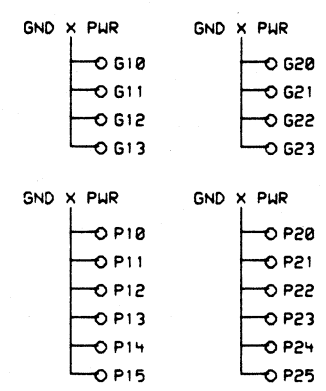
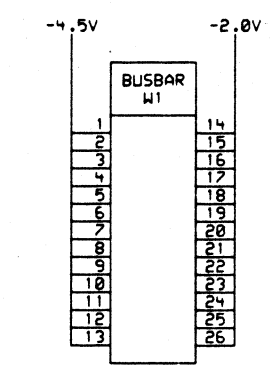
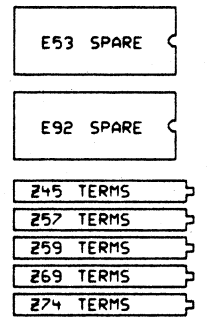
REVISIONS		
CHK	CHANGE NO.	REV

digital	DATE	ENG.	DATE	TITLE:
	26-JAN-83			
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
	25-JAN-83 10:01	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL:			D CS L0049-0-CRAI	REV.



- (A69) NC A69
- (A78) NC A78
- (B89) NC B89
- (C73) NC C73
- (C74) NC C74
- (C75) NC C75
- (C85) NC C85
- (C86) NC C86
- (C87) NC C87
- (C91) NC C91
- (C89) [B,GND ECL,NC]

NOTE: E53 AND E92 ARE 18 PIN SLOTS



- I180 [D,IMP1,NC]
- I110 [D,IMP1,NC]
- I200 [D,IMP2,NC]
- I210 [D,IMP2,NC]

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REVISIONS		
CHK	CHANGE NO.	REV

	DATE 08-FEB-83	ENG.	DATE	TITLE: POWER & GROUND CAPS
	DATE 08-FEB-83	CHK'D. J. J. J.	BOARD LOCATION: 1 OF 1	
VULCAN (2000, L0049-0-CRAJ), DRW 103-FEB-83 14:37 NEXT HIGHER ASSEMBLY: B-DD-L0046-0		SIZE CODE D CS	NUMBER L0049-0-CRAJ	REV. 0

PROTOJ:<2080.L0047.REVD>3DSHL1
 PROTOJ:<2080.L0047.REVD>3DSHL2
 PROTOJ:<2080.L0047.REVD>3DSHL3
 PROTOJ:<2080.L0047.REVD>3DSHL4
 PROTOJ:<2080.L0047.REVD>3DSHL5
 PROTOJ:<2080.L0047.REVD>3DSHL6
 PROTOJ:<2080.L0047.REVD>8DSHL7
 PROTOJ:<2080.L0047.REVD>3DSHL8
 PROTOJ:<2080.L0047.REVD>5DSHL9
 PROTOJ:<2080.L0047.REVD>3DSHLA
 PROTOJ:<2080.L0047.REVD>3DSHLB
 PROTOJ:<2080.L0047.REVD>3DSHLC
 PROTOJ:<2080.L0047.REVD>3DSHLD
 PROTOJ:<2080.L0047.REVD>3DSHLE
 PROTOJ:<2080.L0047.REVD>3DSHLF
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 PROTOJ:<2080.L0047.REVD>15DSHLI
 PROTOJ:<2080.L0047.REVD>17DSHLJ
 PROTOJ:<2080.L0047.REVD>10DSHLK
 PROTOJ:<2080.L0047.REVD>18DSHLL
 PROTOJ:<2080.L0047.REVD>19DSHLM

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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN: <i>J. J. J.</i>	DATE: 02-FEB-83	ENG:	DATE:	TITLE:
	CHK'D:	DATE:	BOARD LOCATION:	DATE:	
ARCH: (LIMLER.PLOTS)\DASH.DPL.1		02-FEB-83 03:07	NEXT HIGHER ASSEMBLY:		SIZE: D SP
FIRST USED ON OPTION/MODEL:					NUMBER
					REV.

REV. A
NUMBER
SIZE CODE

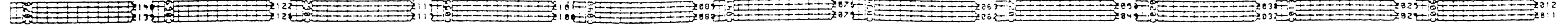
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E144	E131	E118	E105	E093	E079	E065	E051	E039	E025	E011
E143	E130	E117	E104	E092	E078	E064	E050	E038	E024	E010
E142	E129	E116	E103	E091	E077	E063	E050	E037	E023	E010
E141	E128	E115	E102	E090	E076	E062	E049	E036	E022	E009
E140	E127	E114	E101	E089	E075	E061	E048	E035	E021	E008
E139	E126	E113	E100	E088	E074	E060	E047	E034	E020	E007
E138	E125	E112	E099	E087	E073	E059	E046	E033	E019	E006
E137	E124	E111	E098	E086	E072	E058	E045	E032	E018	E005
E136	E123	E110	E097	E085	E071	E057	E044	E031	E017	E004
E135	E122	E109	E096	E084	E070	E056	E043	E030	E016	E003
E134	E121	E108	E095	E083	E069	E055	E042	E029	E015	E002
E140	E127	E114	E101	E088	E074	E060	E047	E034	E020	E007
E139	E126	E113	E100	E087	E073	E059	E046	E033	E019	E006
E138	E125	E112	E099	E086	E072	E058	E045	E032	E018	E005
E137	E124	E111	E098	E085	E071	E057	E044	E031	E017	E004
E136	E123	E110	E097	E084	E070	E056	E043	E030	E016	E003
E135	E122	E109	E096	E083	E069	E055	E042	E029	E015	E002
E134	E121	E108	E095	E082	E068	E054	E041	E028	E014	E001

+ C095 + C094 + C093 + C092 + C091 + C090 +

SHL/SFR

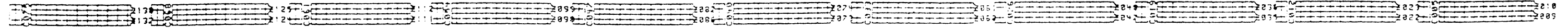
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E140 E127 E114 E101 E089 E076 E063 E050 E038 E025 E012



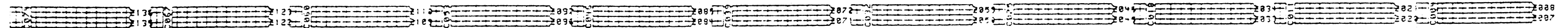
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E138 E125 E112 E099 E087 E074 E061 E048 E036 E023 E010



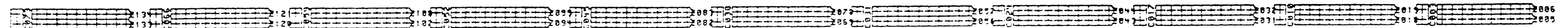
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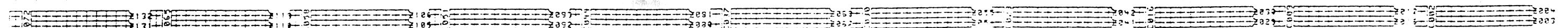
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E133 E120 E107 E094 E082 E069 E056 E043 E031 E018 E005

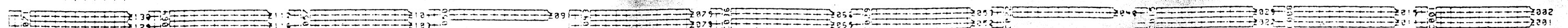
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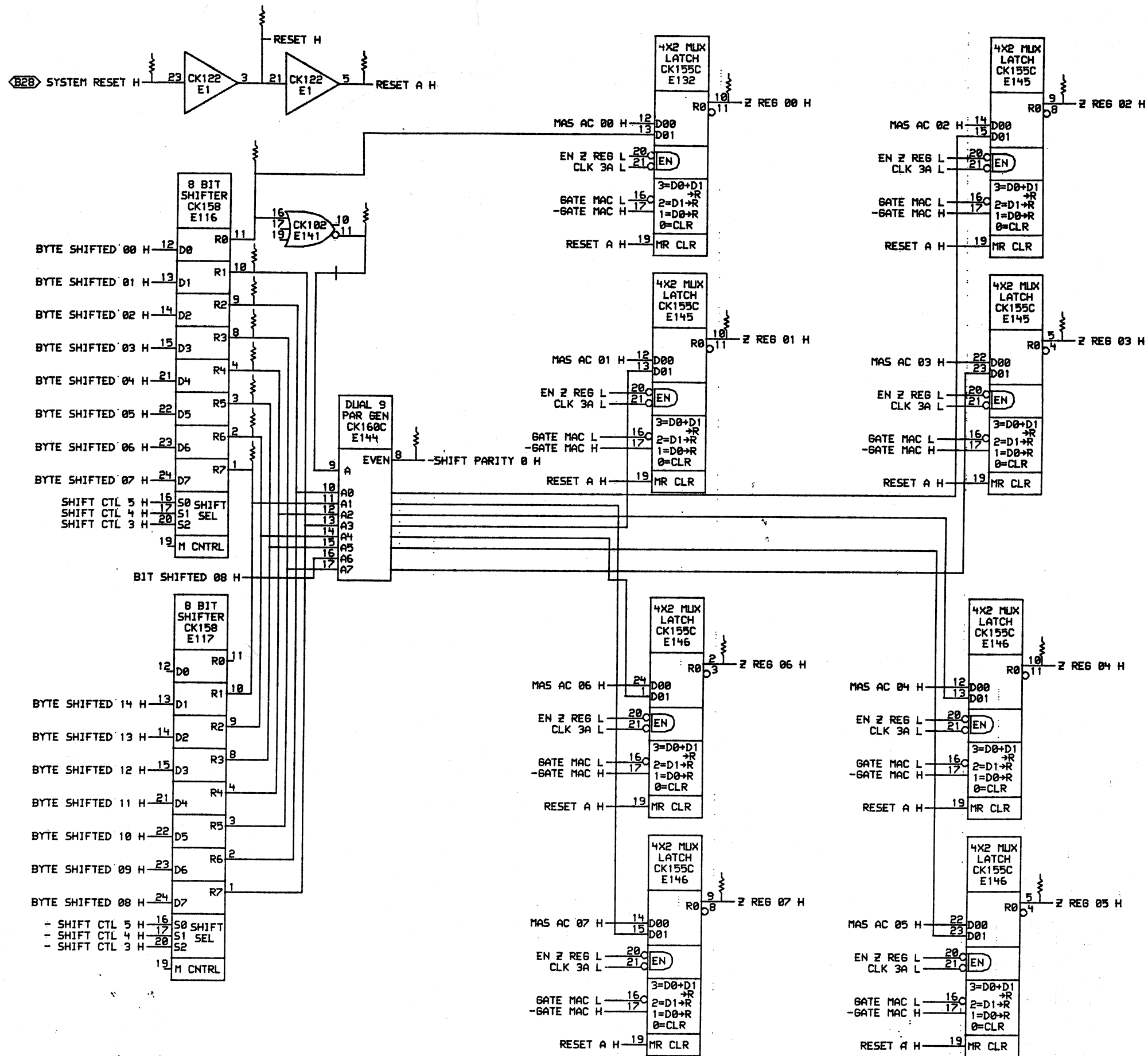
+ C095 + + C094 + + C093 + + C092 + + C091 + + C090 +

E130 E117 E104 E091 E079 E066 E053 E040 E028 E015 E002

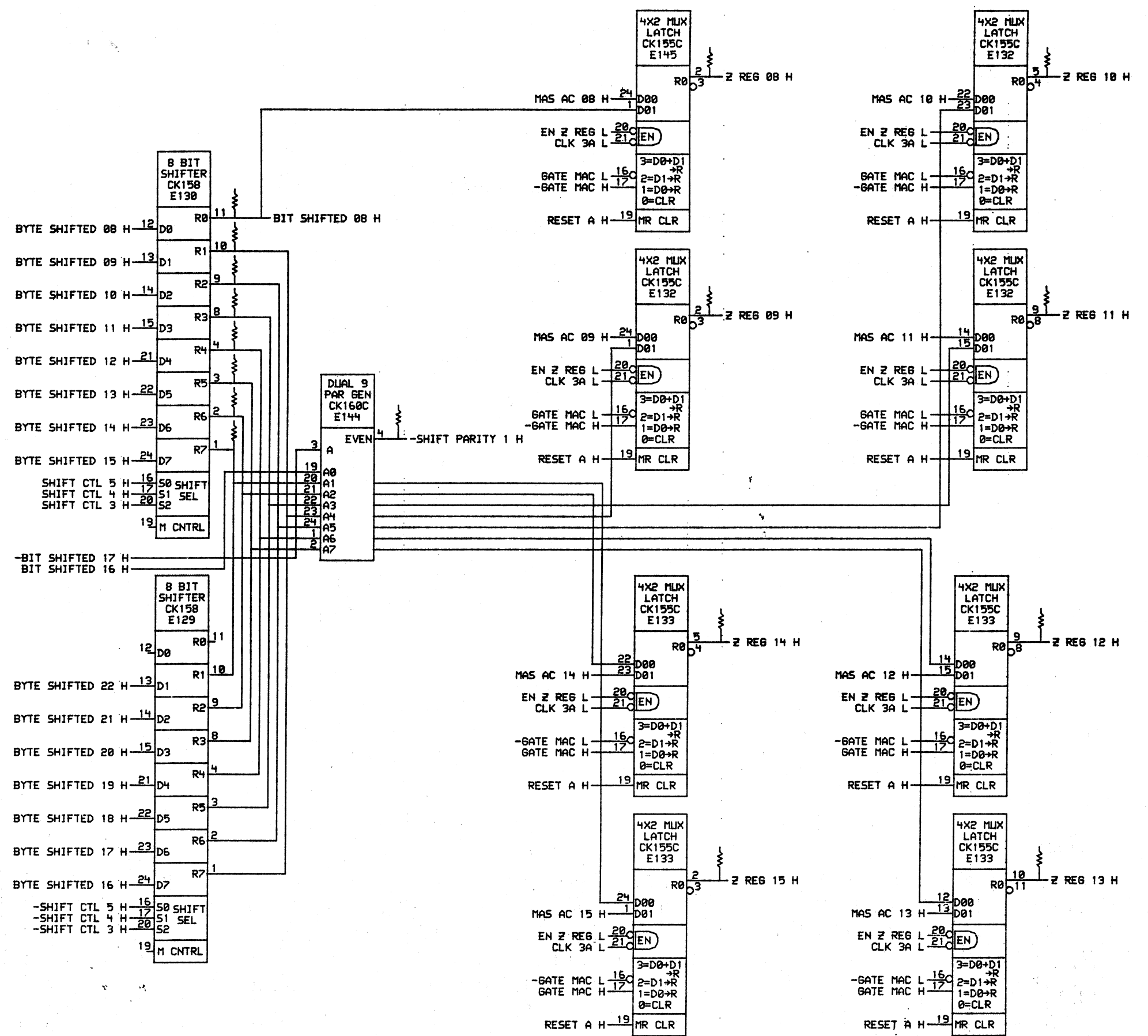


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SHL

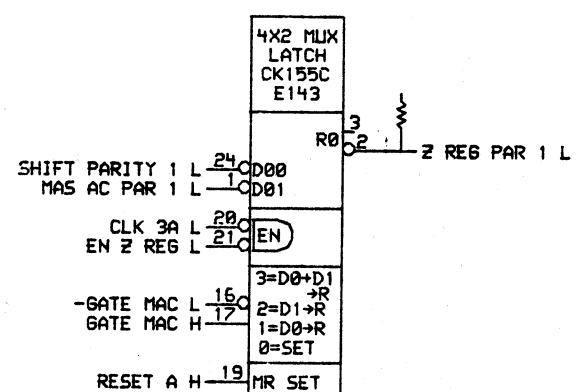
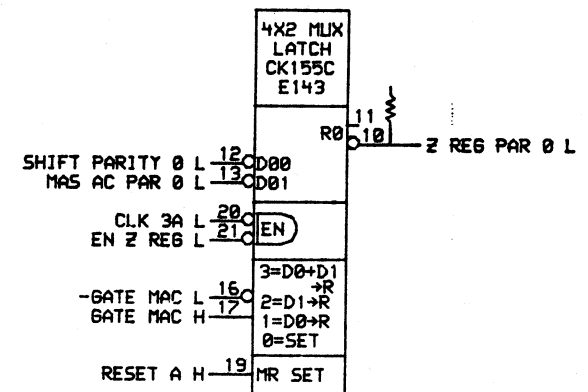
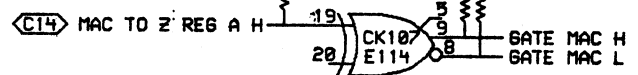
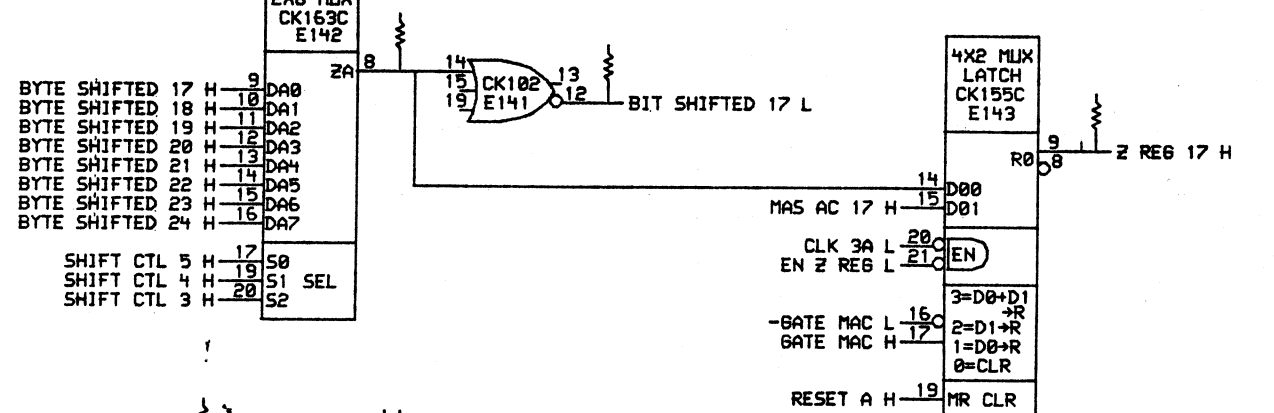
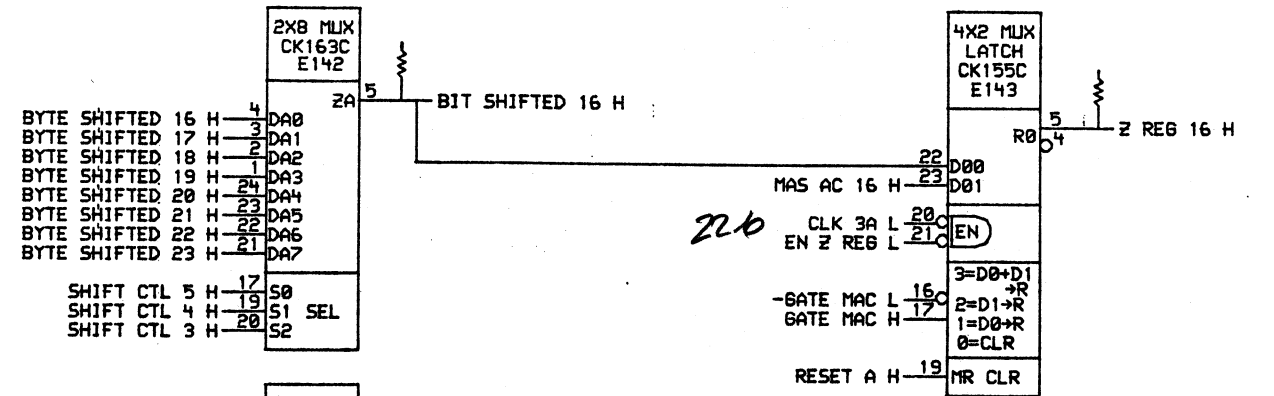
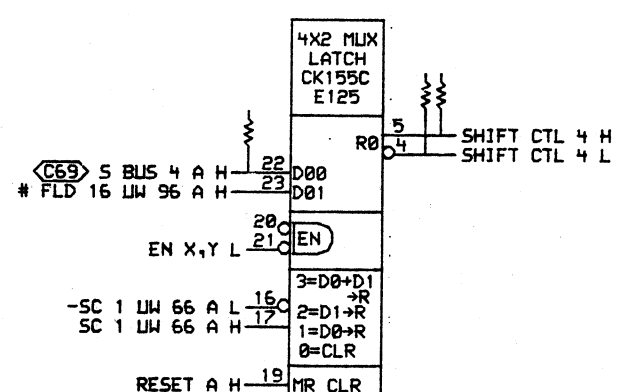
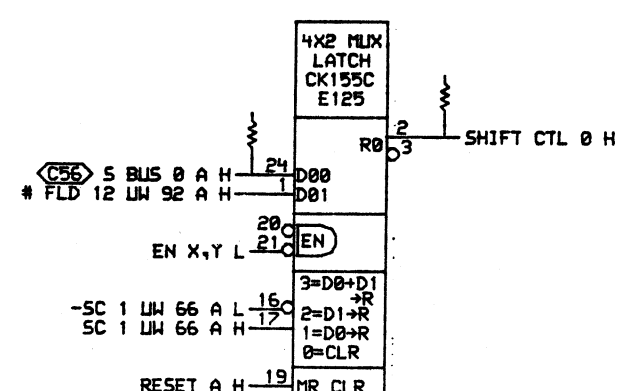
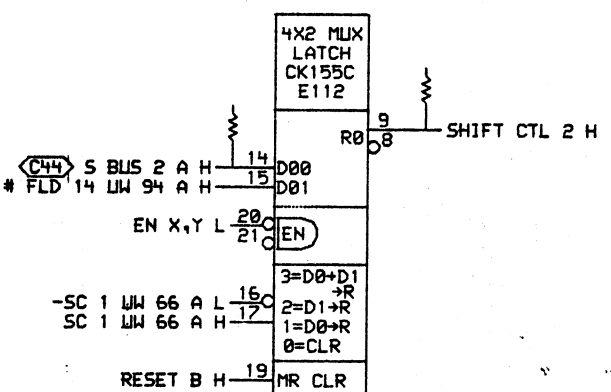
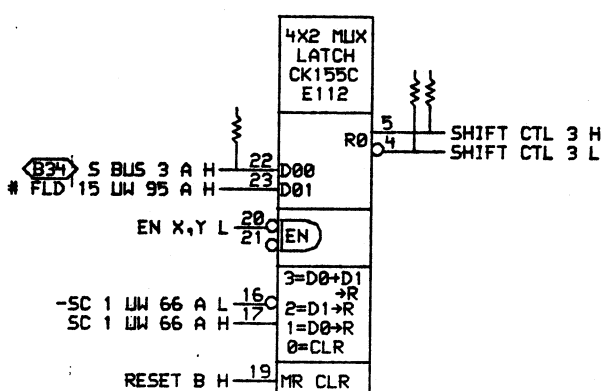
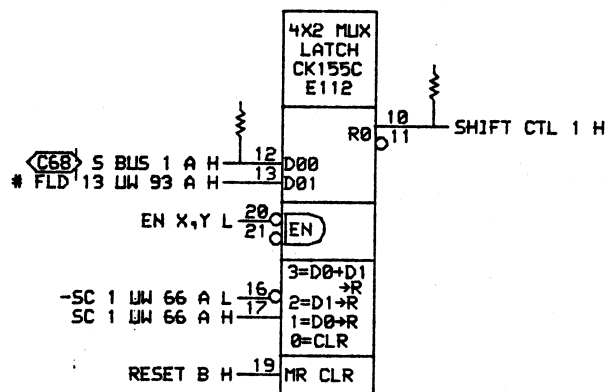
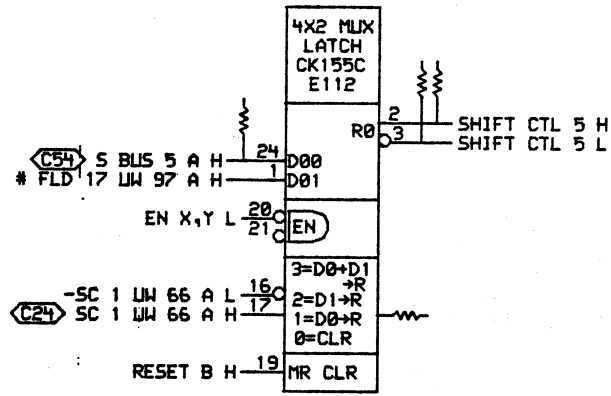


DRN.	DATE	ENG.	DATE	TITLE:	0-7 BIT SHIFT
CHK'D.	DATE	BOARD LOCATION		Z REG 00-07	
FIRST USED ON OPTION/MODEL: 2080				SIZE	NUMBER
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				REV.	*

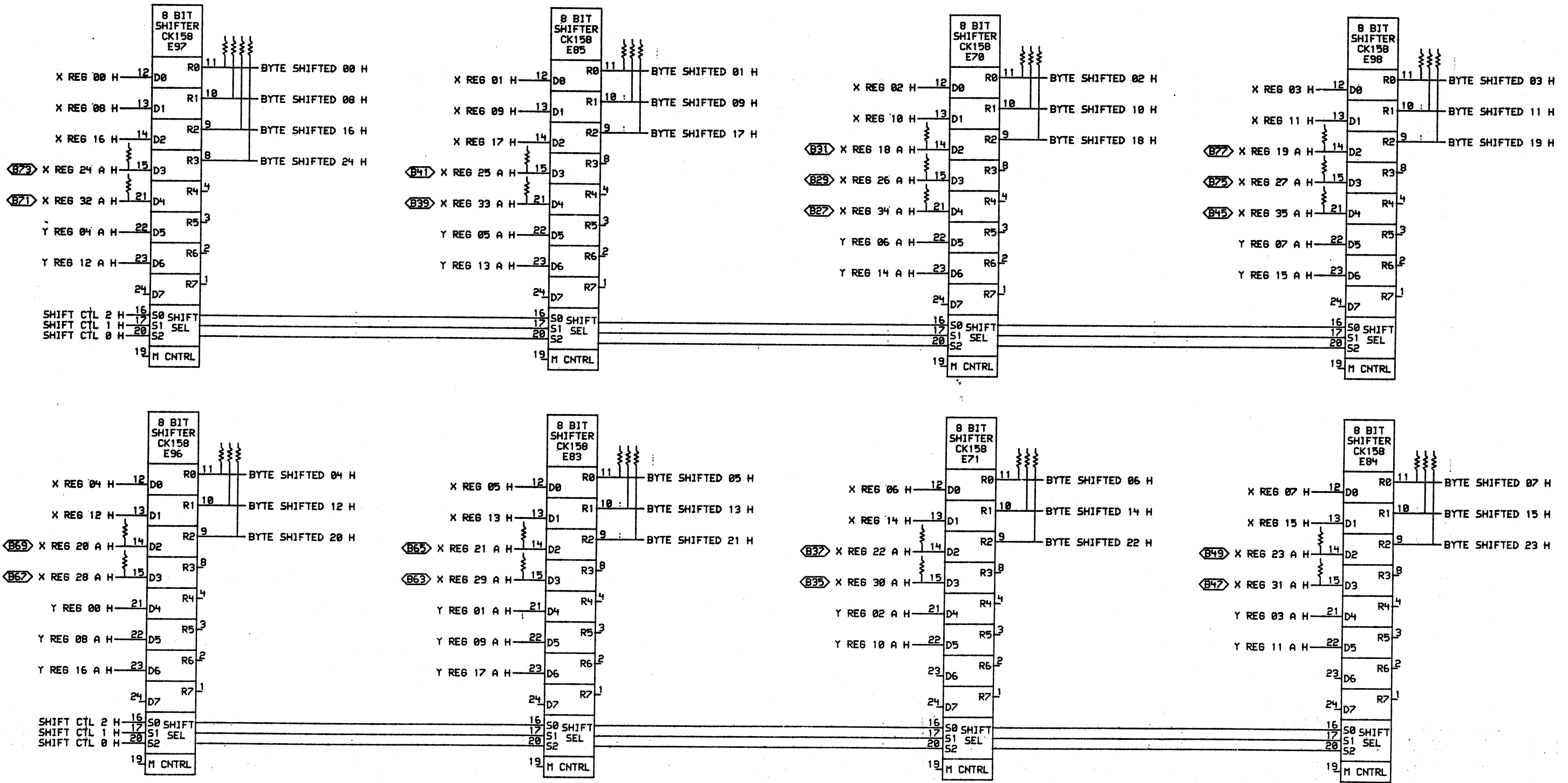


DRN.	DATE	ENG.	DATE	TITLE:	0-7 BIT SHIFT Z REG 08-15		
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2080				NEXT HIGHER ASSEMBLY: B-DD-L0047-0	D	CS	L0047-0-SHL2
							REV. *

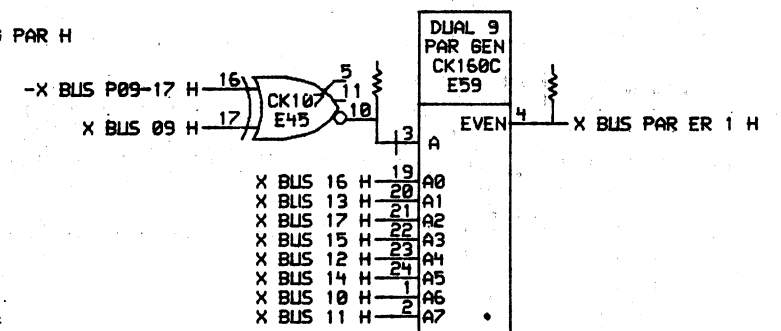
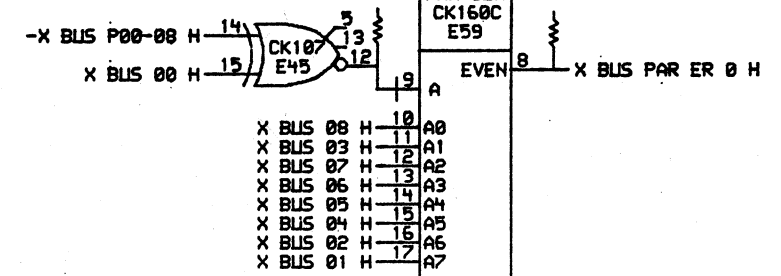
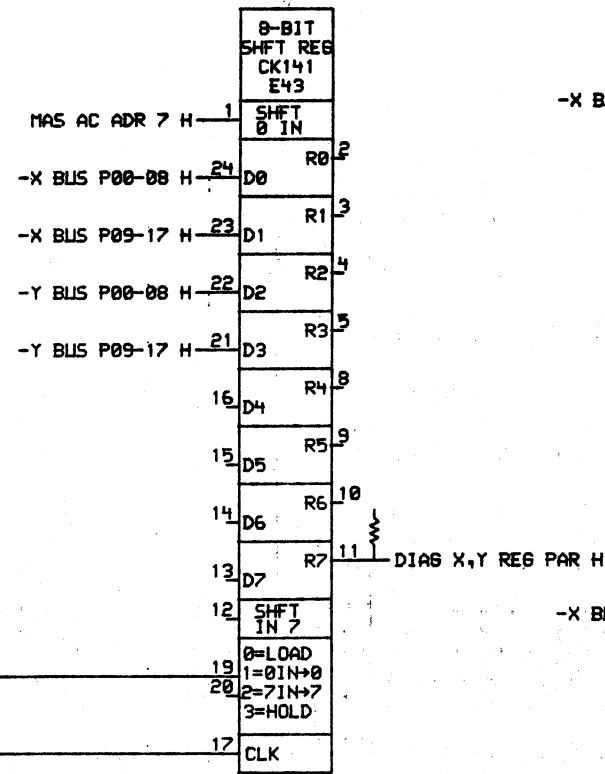
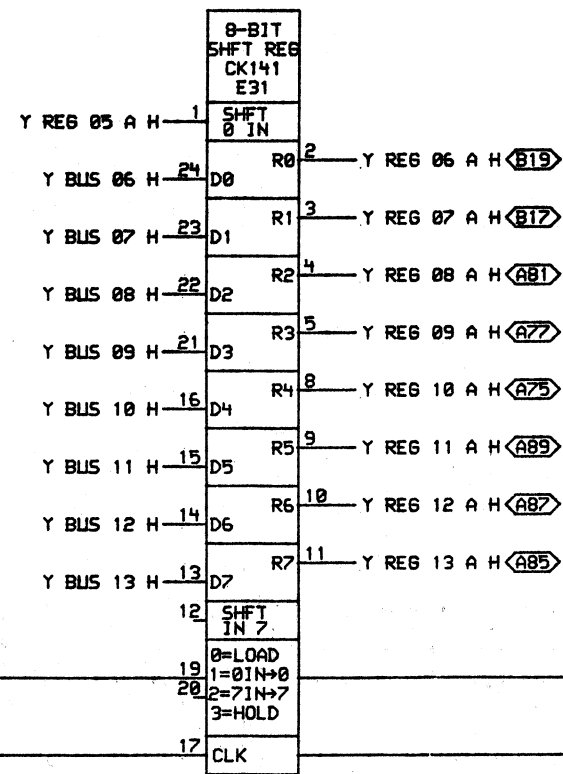
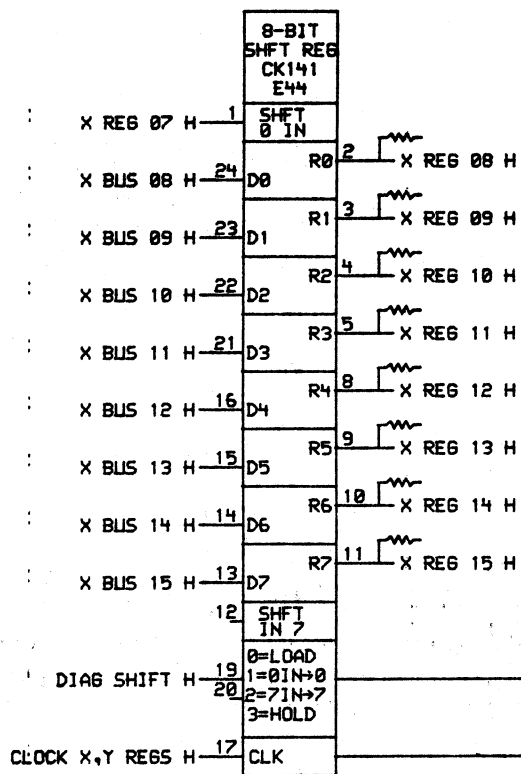
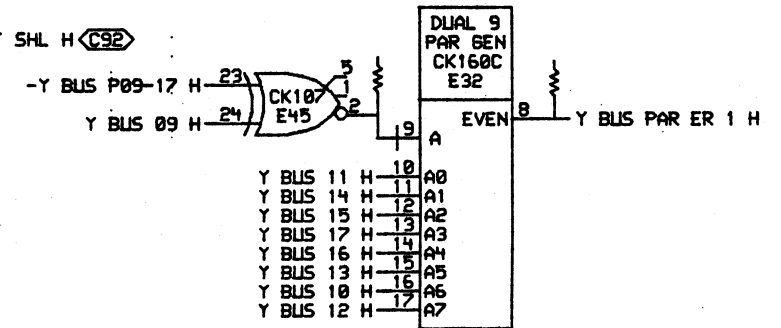
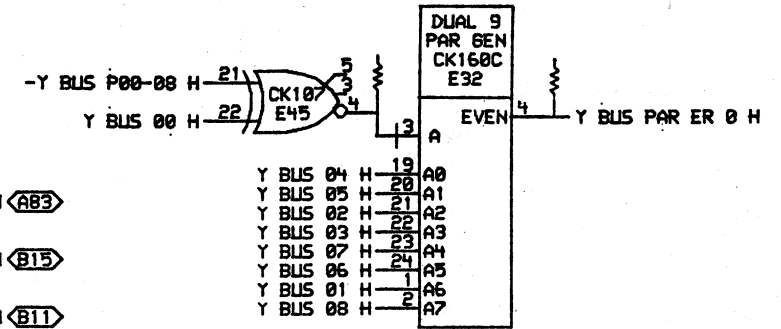
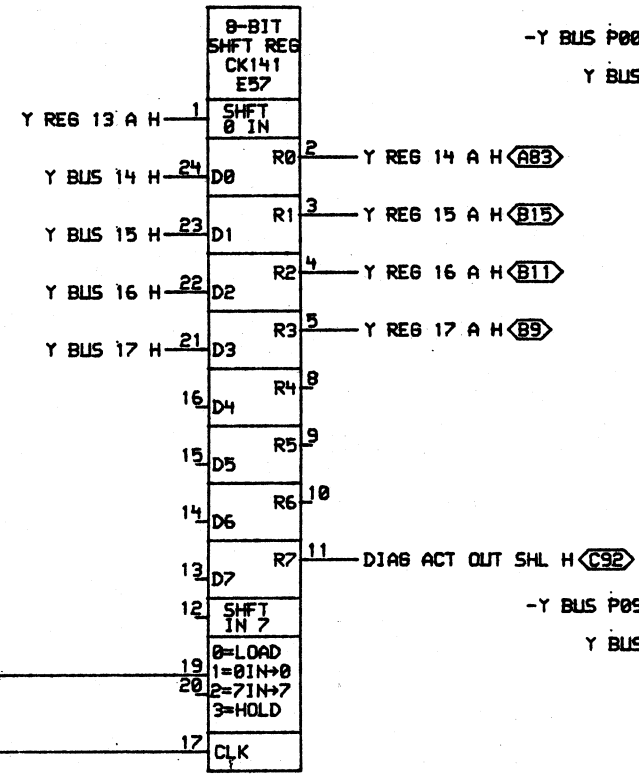
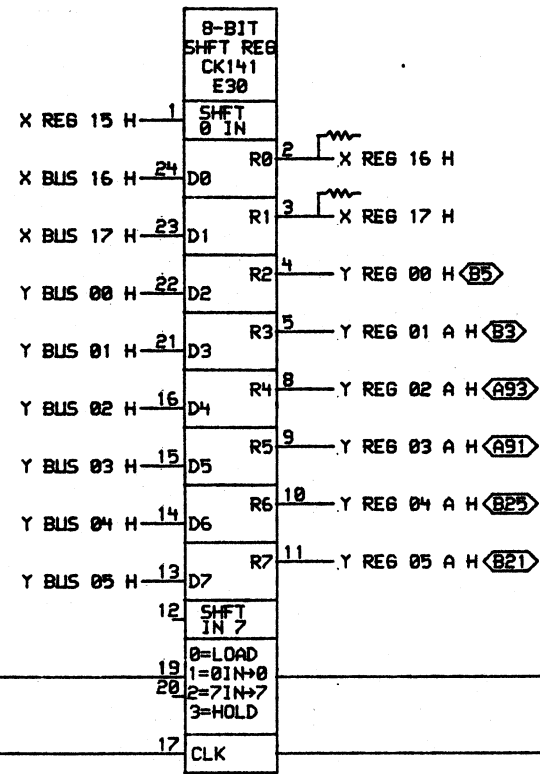
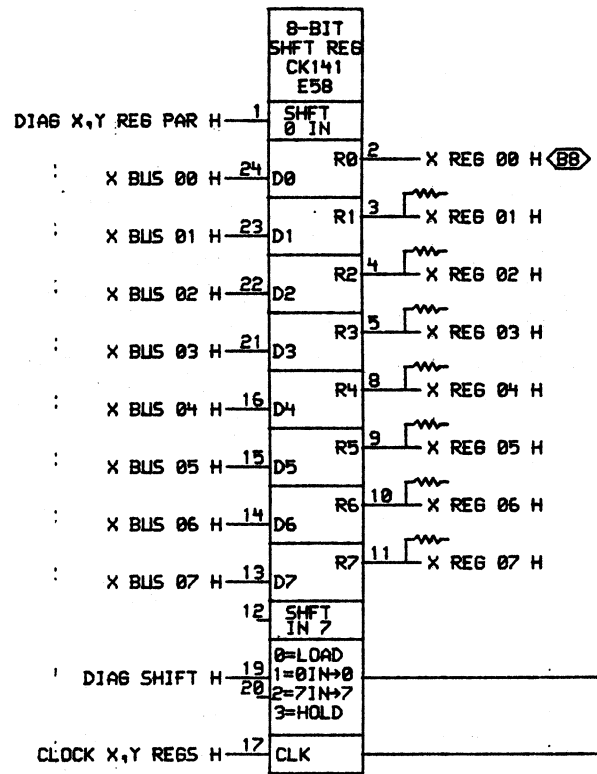
D
C
B
A



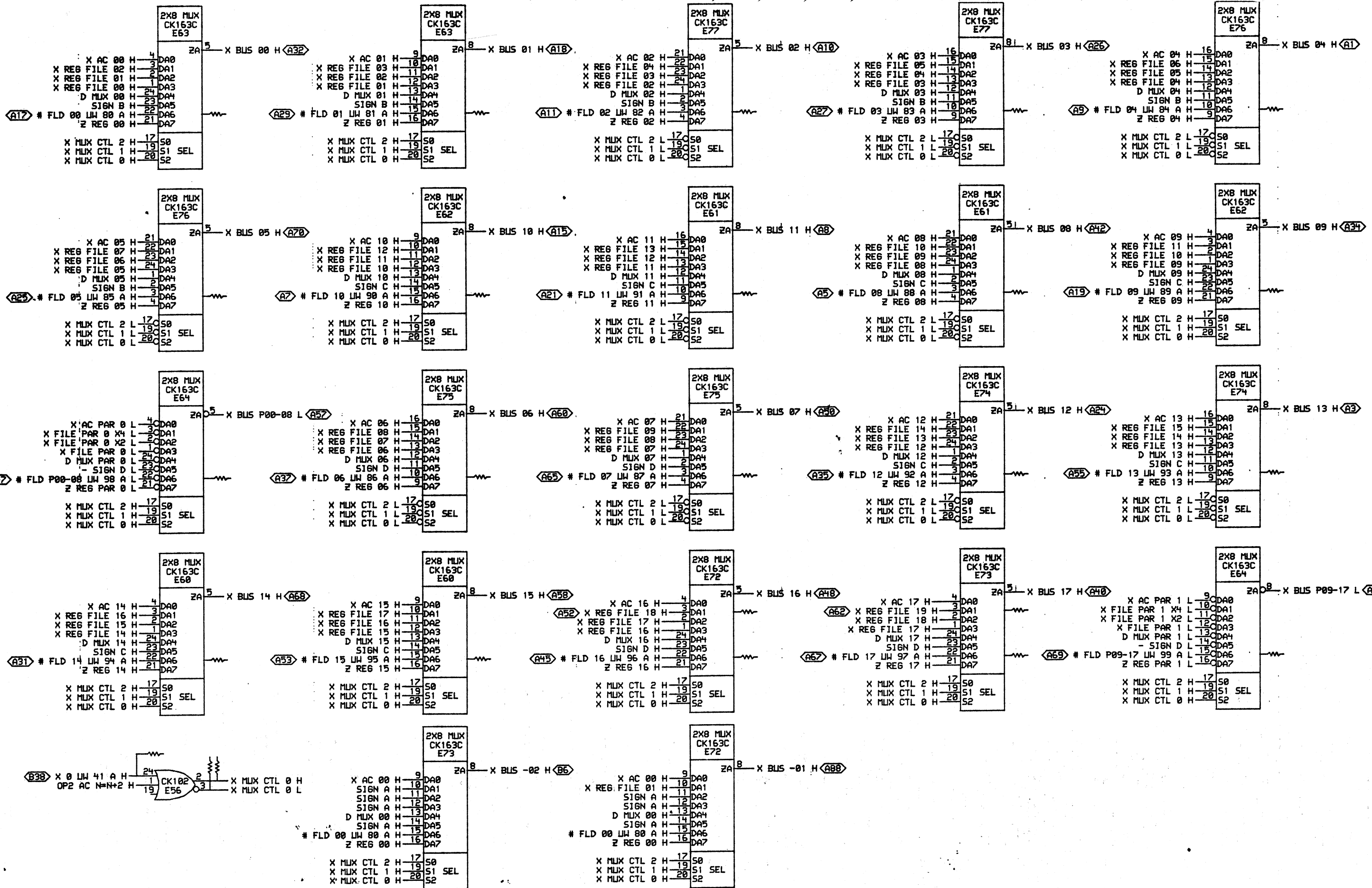
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		0-7 BIT SHIFT
		SHEET	OF '1	Z REG 16,17
FIRST USED ON OPTION/MODEL:				SIZE CODE
NEXT HIGHER ASSEMBLY:				NUMBER
B-DD-L0047-0				L0047-0-SHL3
				REV. *



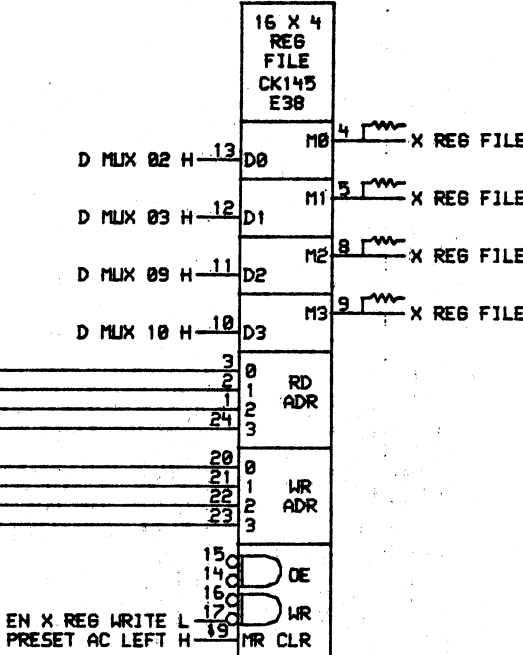
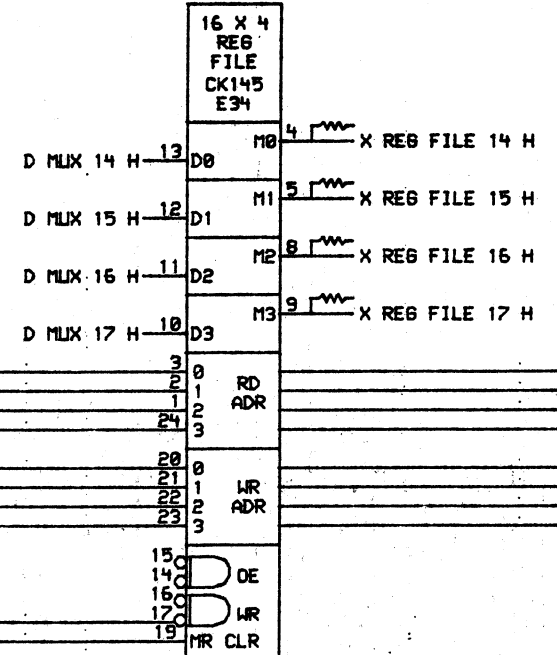
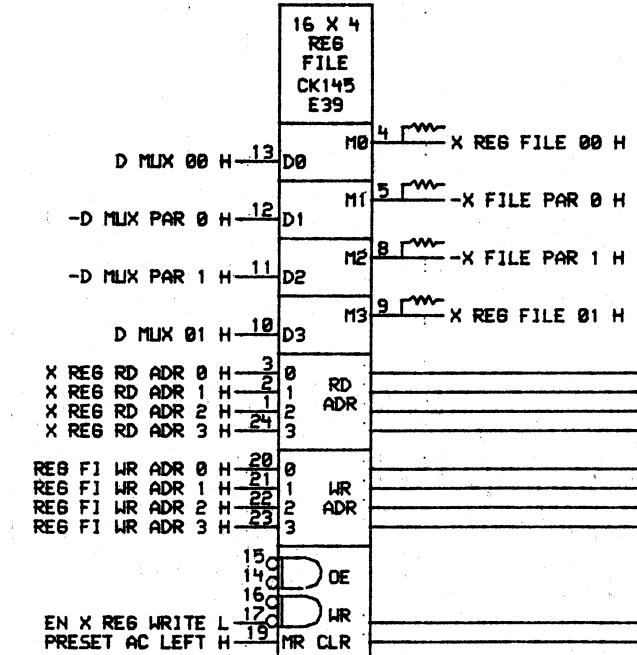
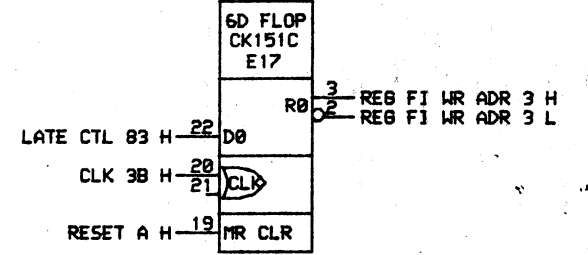
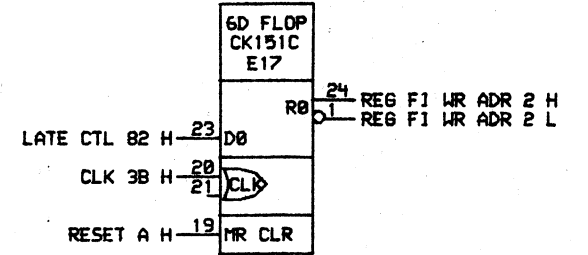
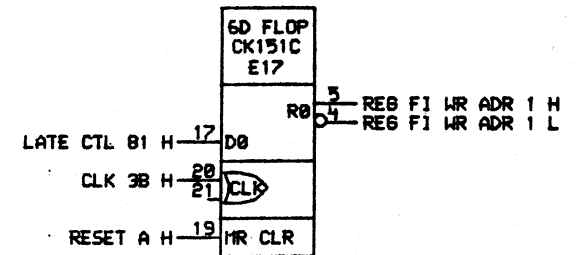
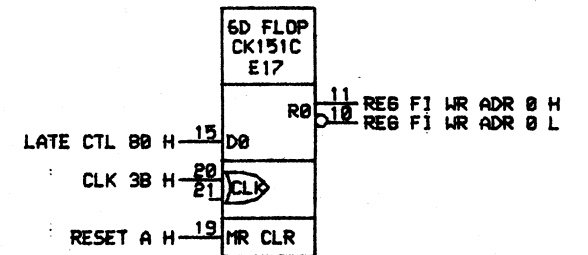
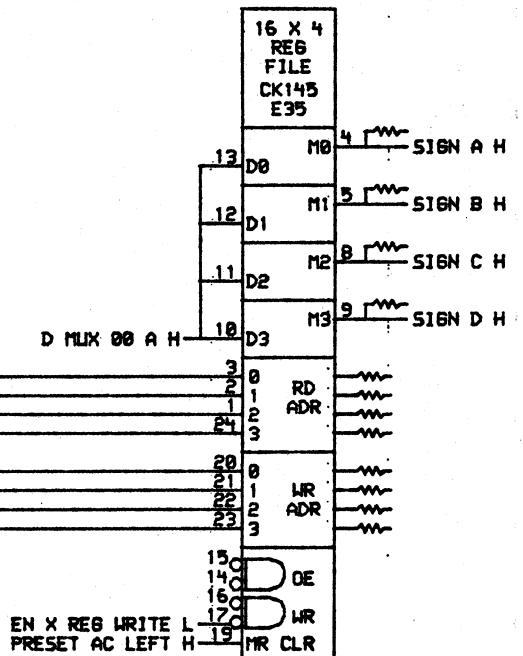
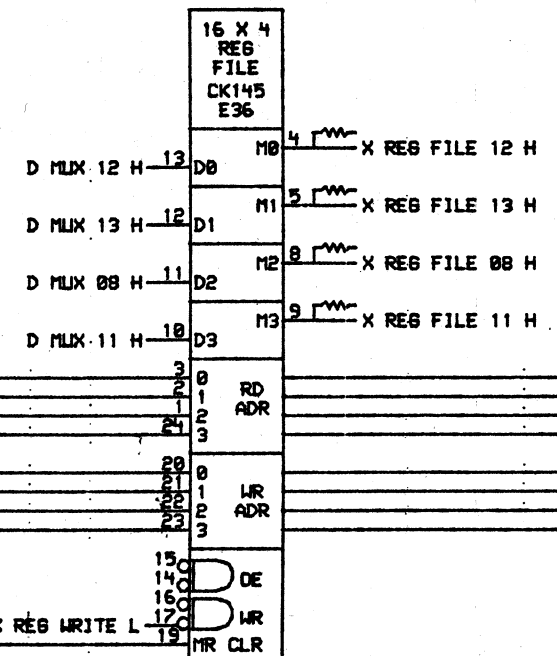
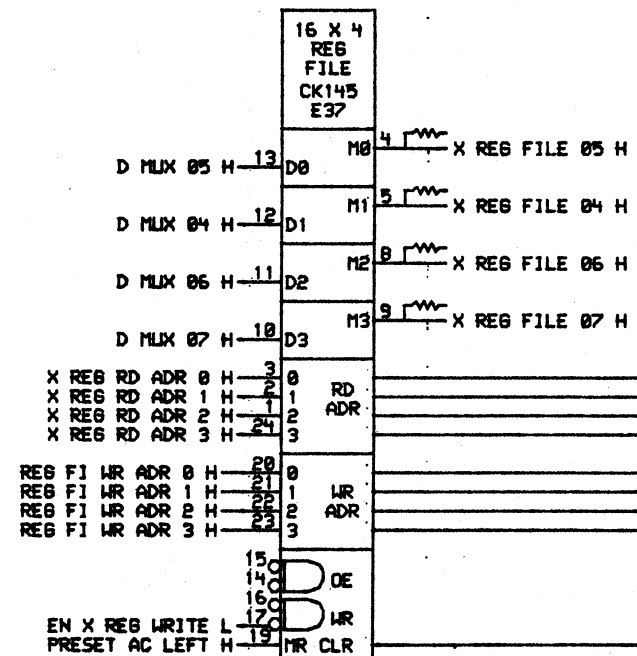
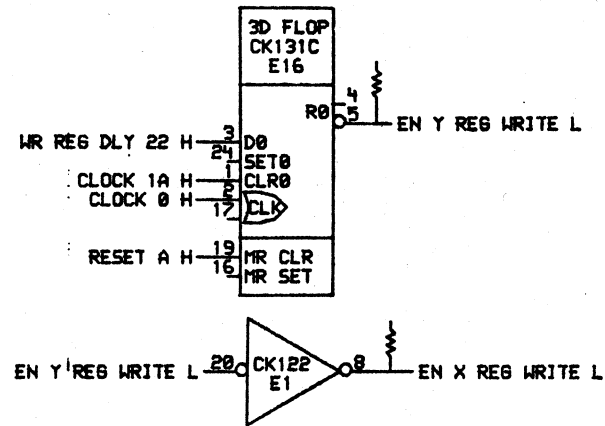
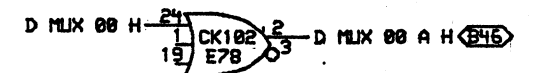
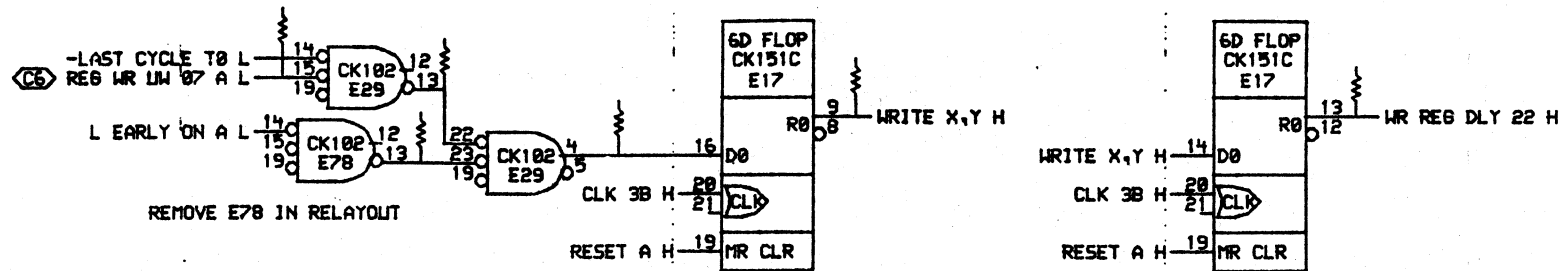
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	0-4 BYTE SHIFT BITS 00-24
FIRST USED ON OPTION/MODEL:				NUMBER
B-DD-L0047-0				L0047-0-SHL4
NEXT HIGHER ASSEMBLY:				REV.
D				*



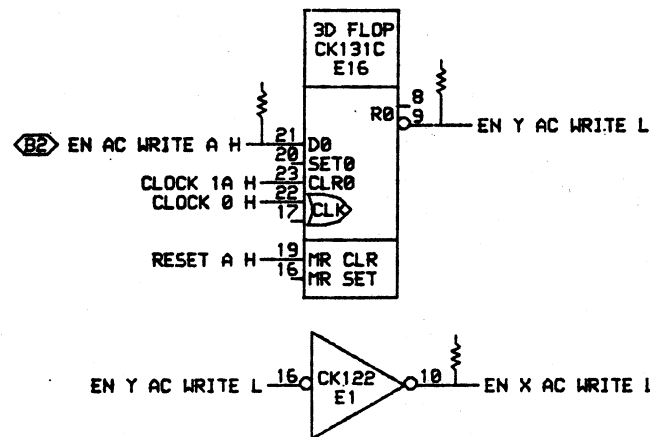
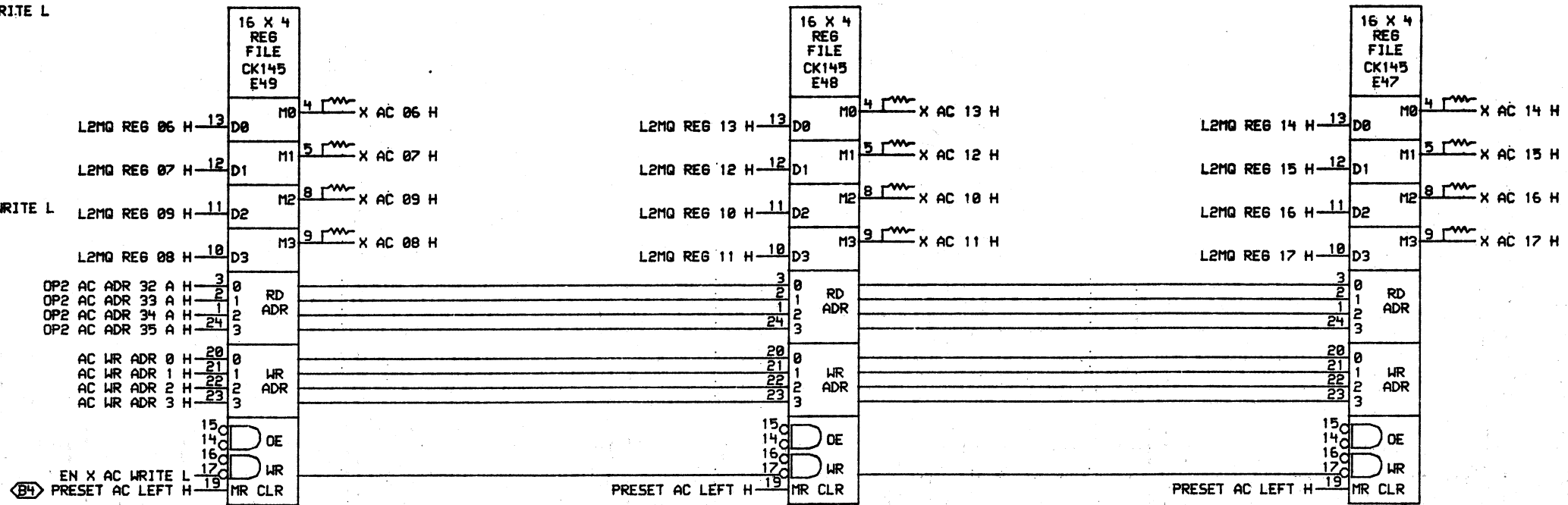
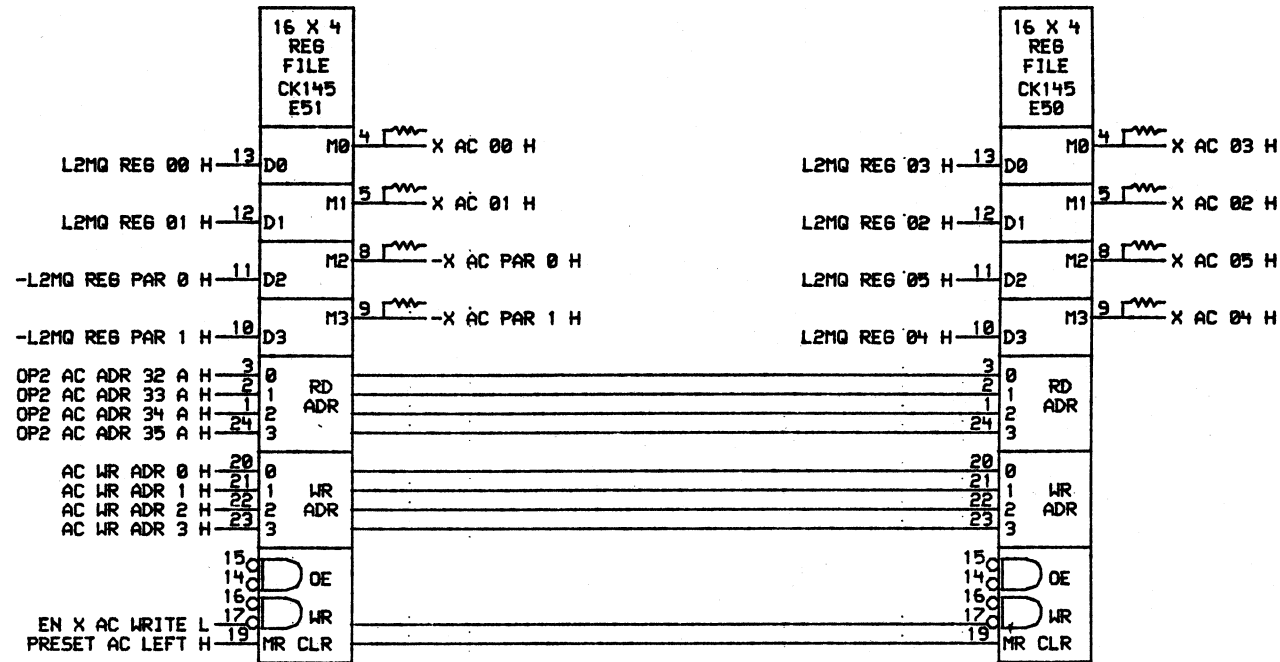
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		X,Y REGS 00-17
		SHEET	OF	X,Y DIAG SCAN
PROT. # (CROSS-REF. NEW) 30443.DRW	19-SEP-82 10:36	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL:	B-DD-L0047-0		D	CS
			NUMBER	REV.
			L0047-0-SHL5	*



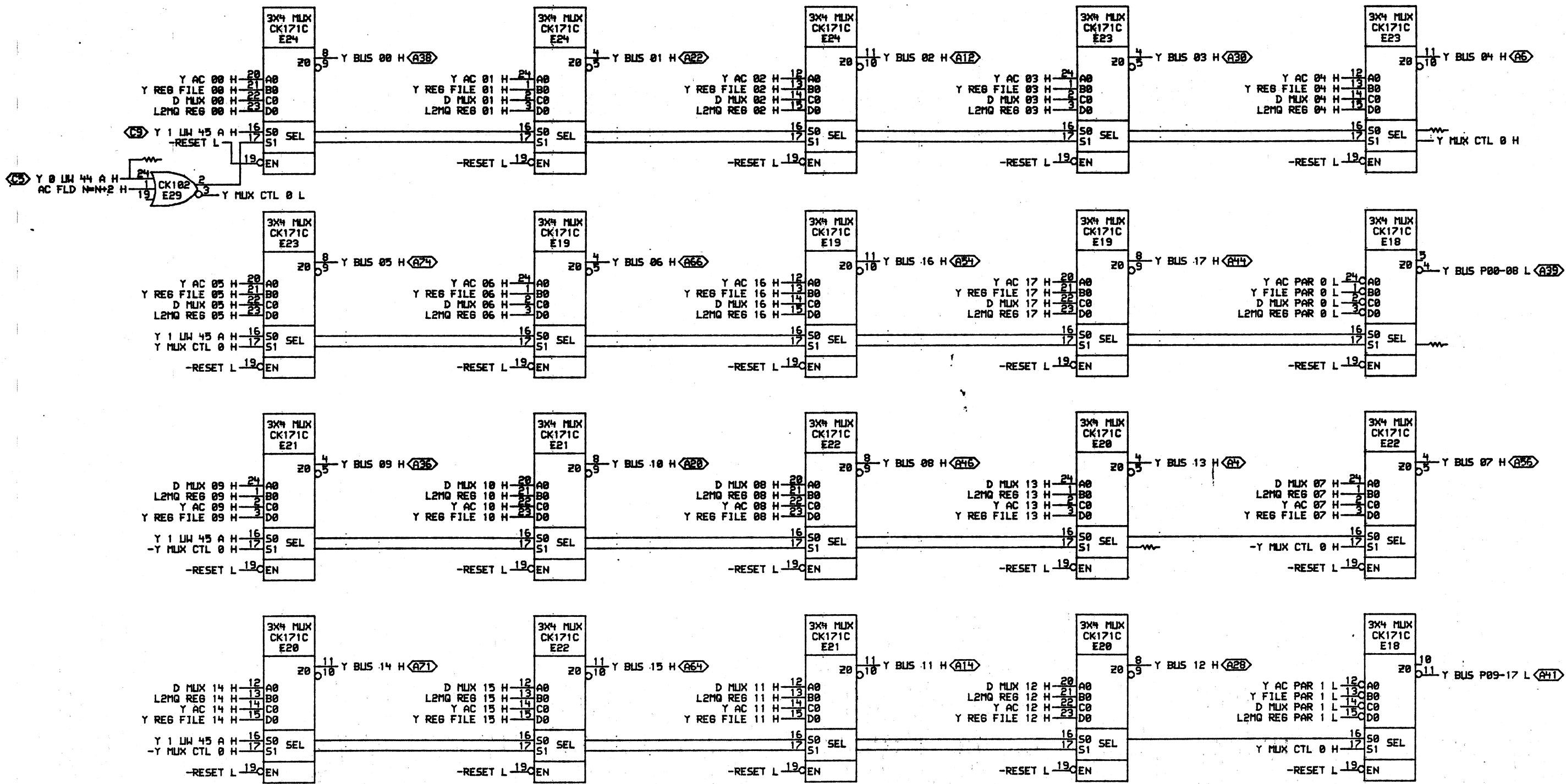
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CHK'D.	DATE	BOARD LOCATION:	SHEET 1 OF 1	
FIRST USED ON OPTION/MODEL:				SIZE CODE NUMBER REV. D CS L0047-0-SHL6 *



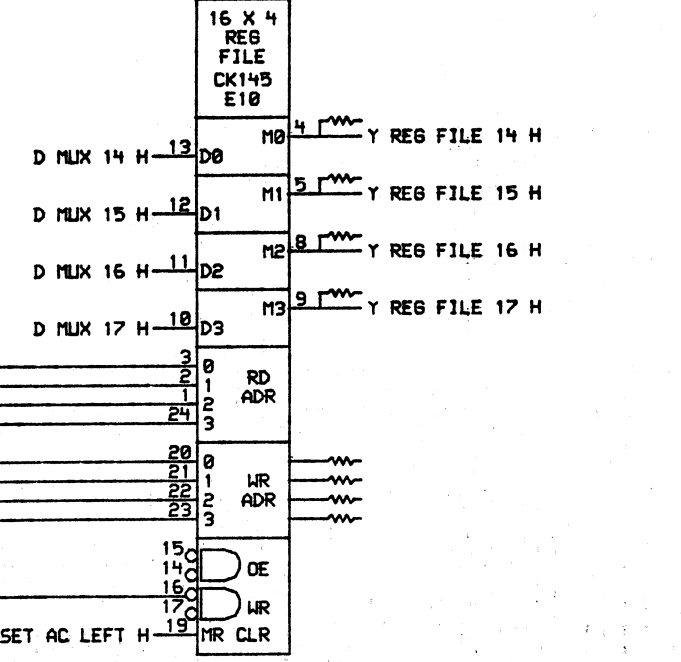
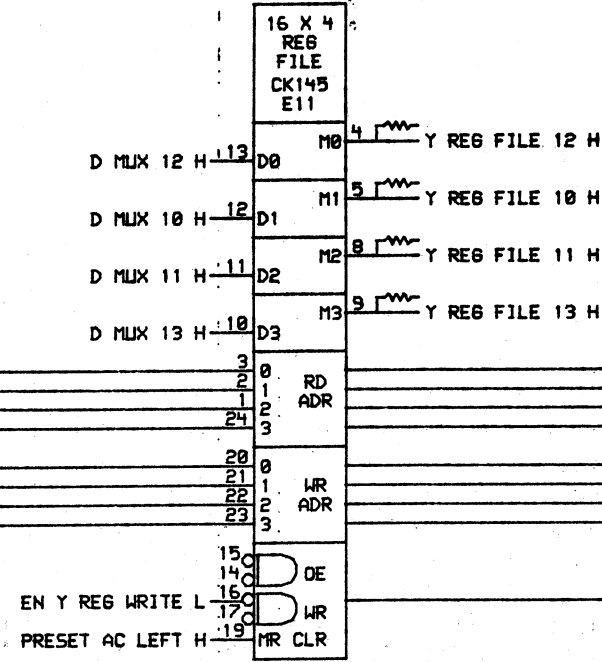
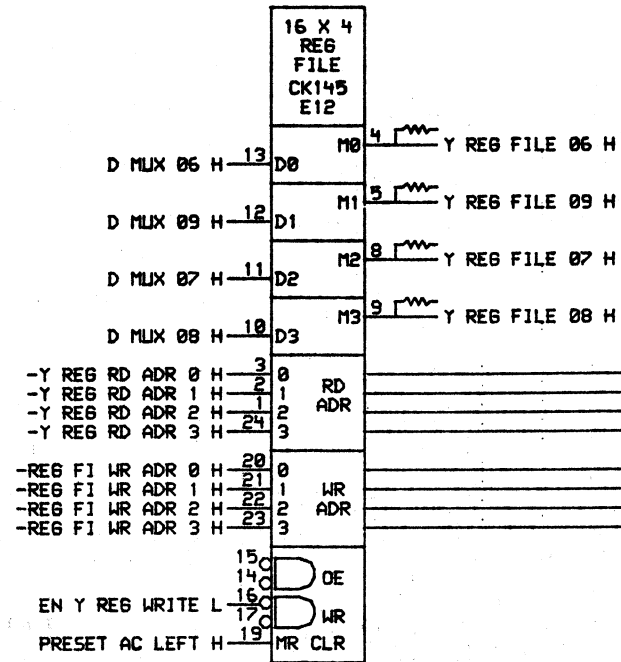
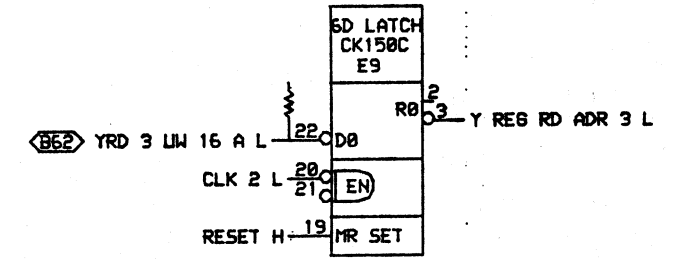
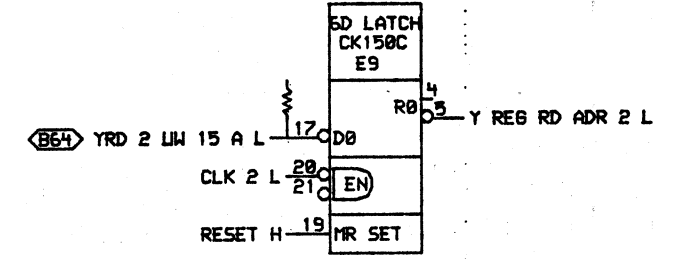
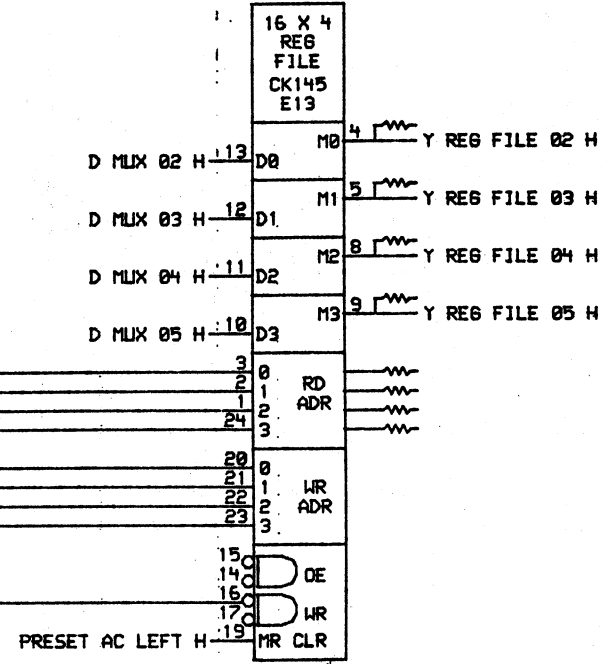
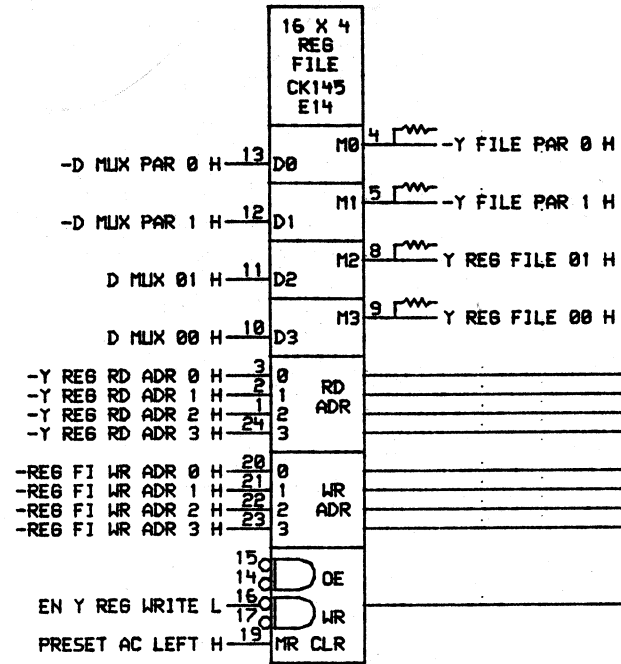
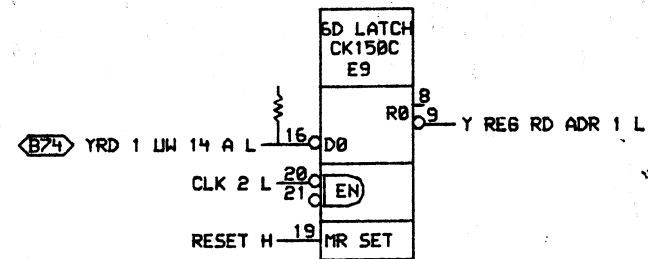
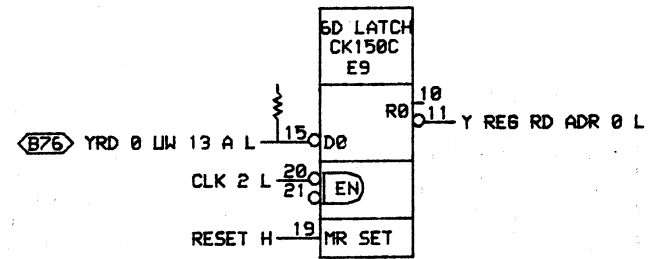
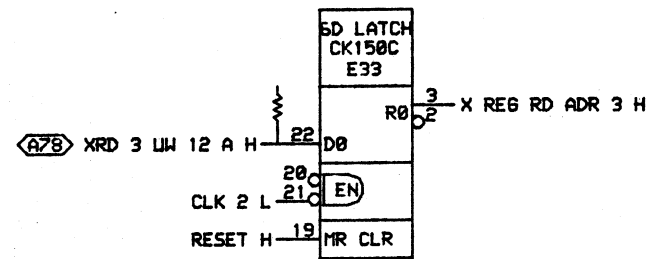
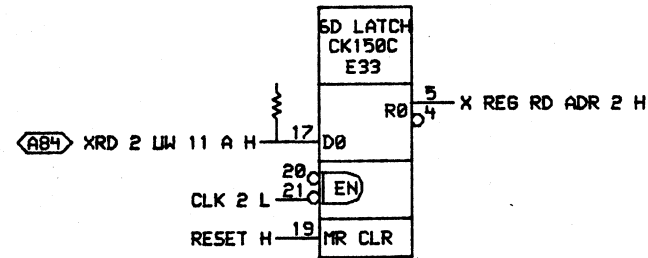
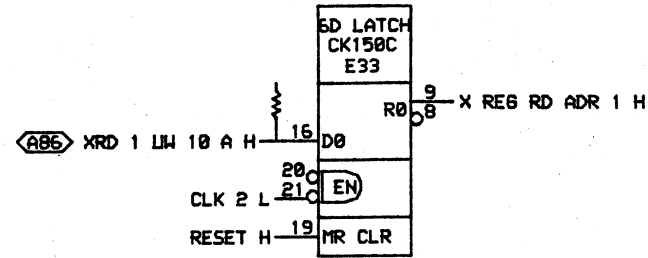
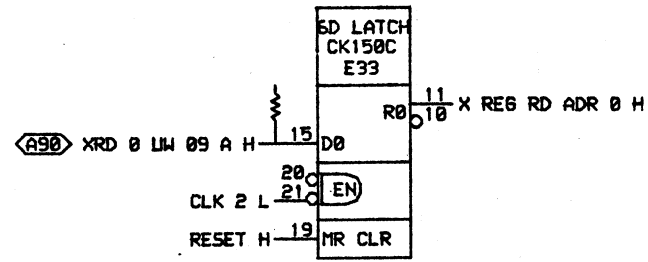
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	X REG FILE BYTES 0,1
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		SIZE CODE
B-DD-L0047-0		B-DD-L0047-0		NUMBER
				L0047-0-SHL7
				REV.
				*



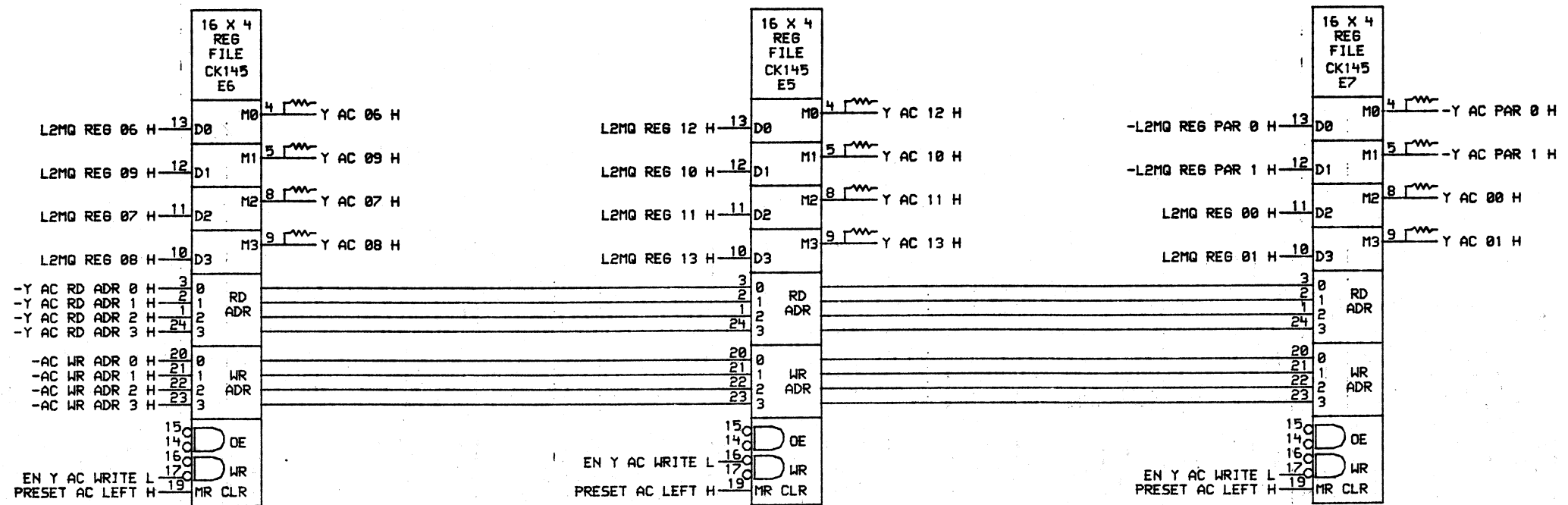
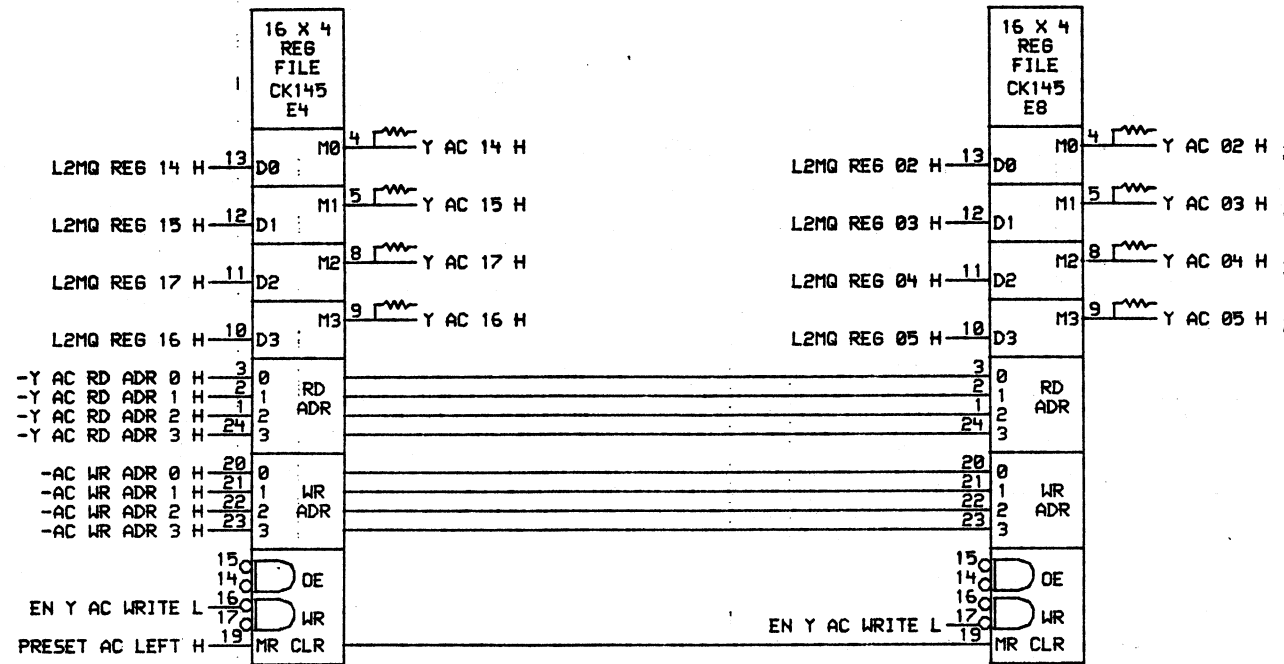
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		X AC FILE
		SHEET	OF	BYTES 0,1
PROTOLIB CERN0.LIB17.REV030418.DRW	19-SEP-82 10:36	NEXT HIGHER ASSEMBLY:		REV. *
FIRST USED ON OPTION/MODEL:	B-DD-L0047-0	SIZE	CODE	NUMBER
		D	CS	L0047-0-SHL8



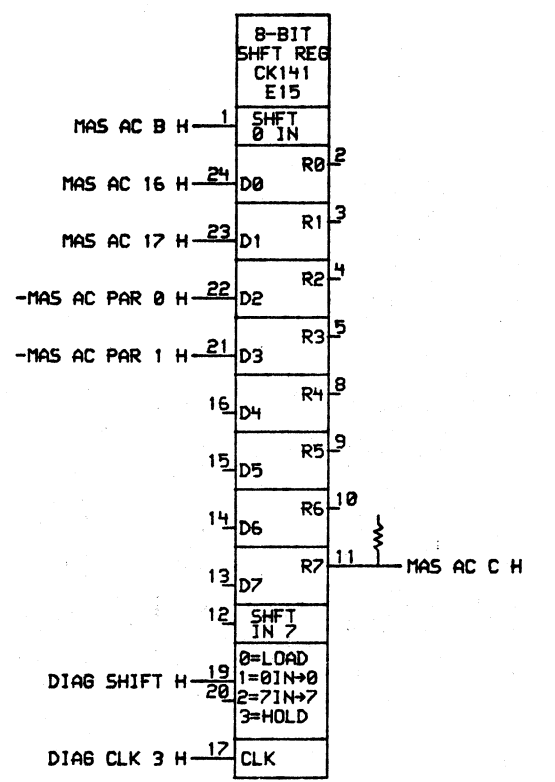
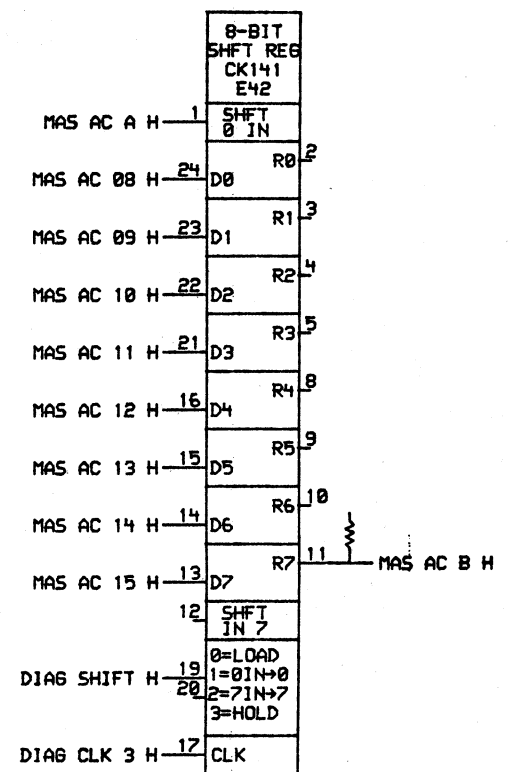
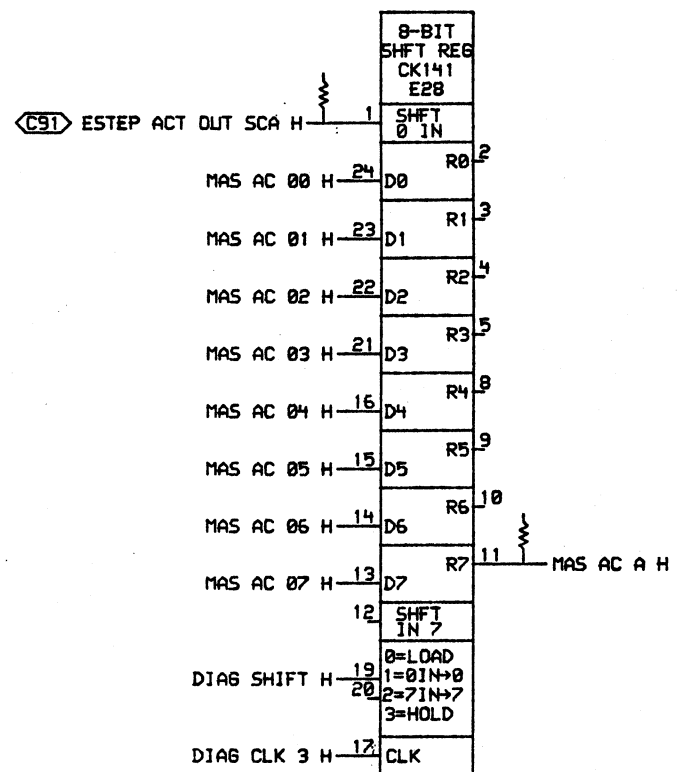
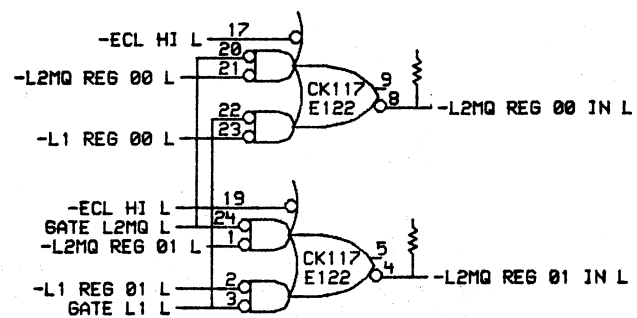
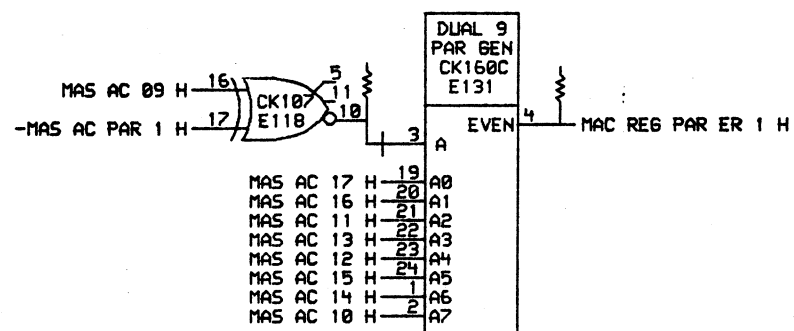
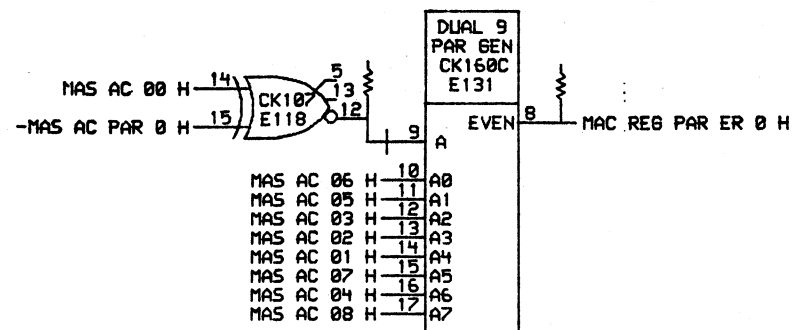
DRN.	DATE	ENG.	DATE	TITLE:	Y MUX 00-17			
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER	REV.
PROD. IN CHARGE	DATE	25-SEP-82 10:22	NEXT HIGHER ASSEMBLY:	B-DD-L0047-0	D	CS	L0047-0-SHL9	*
FIRST USED ON OPTION/MODEL:								



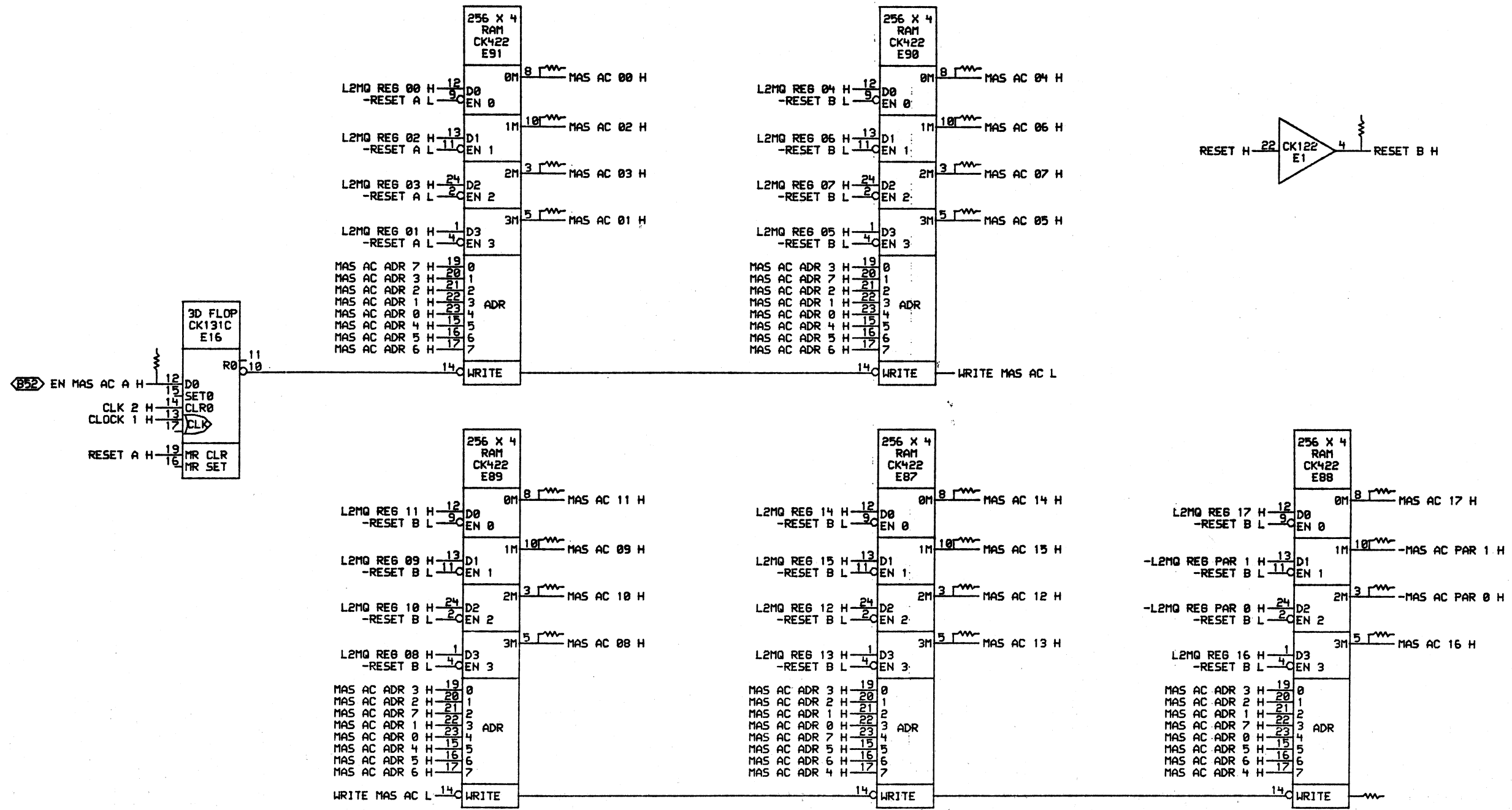
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL:				B-DD-L0047-0
SIZE	CODE	NUMBER	REV.	
D	CS	L0047-0-SHLA	*	



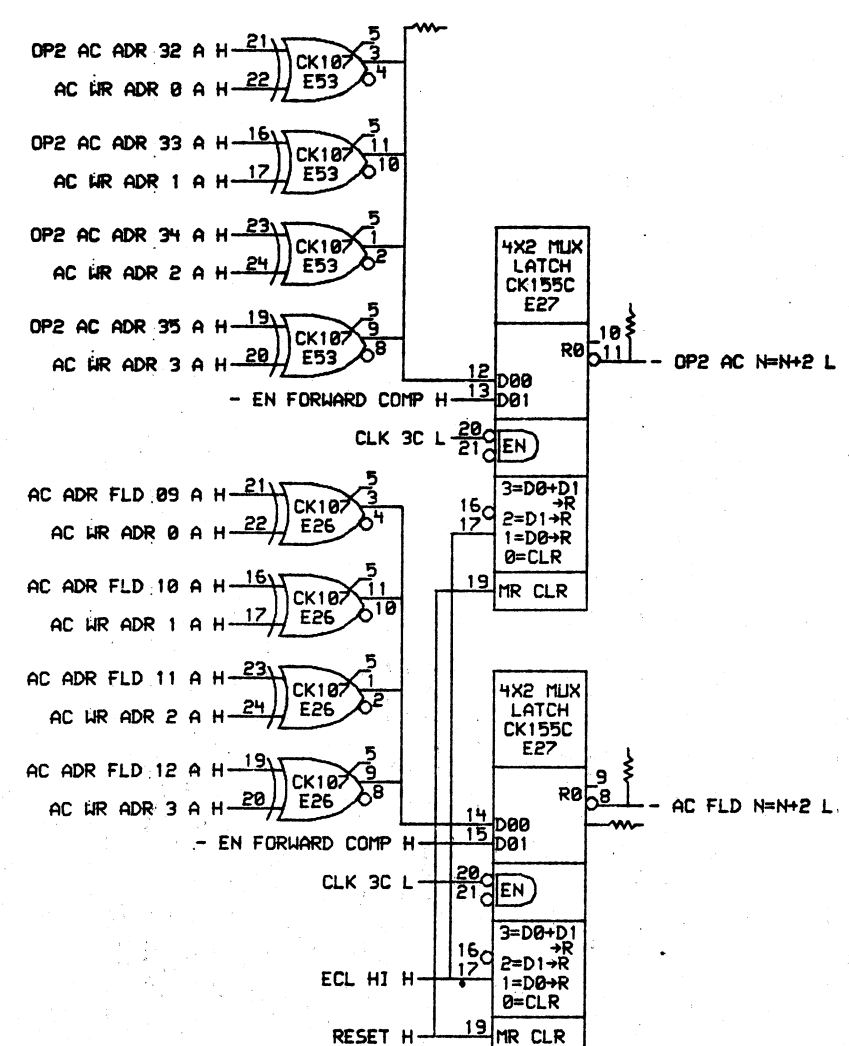
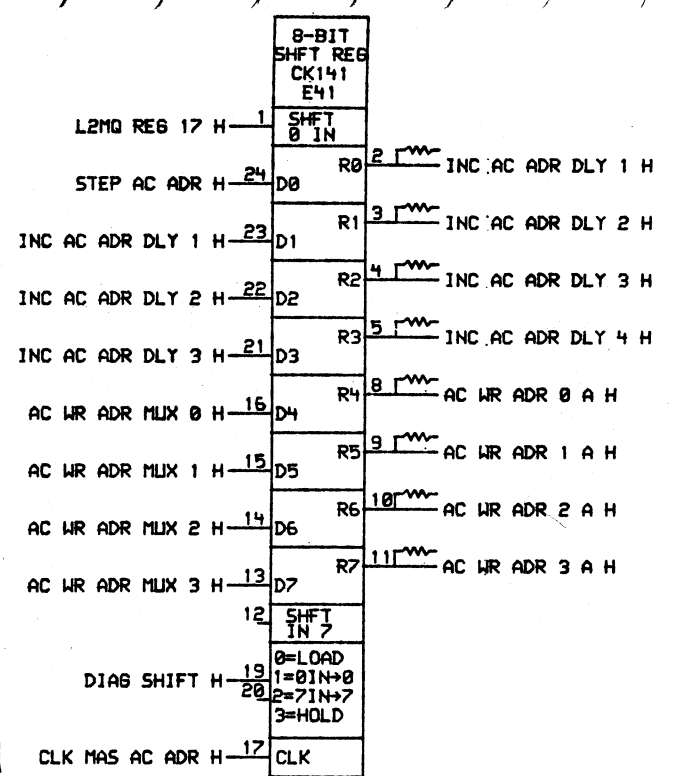
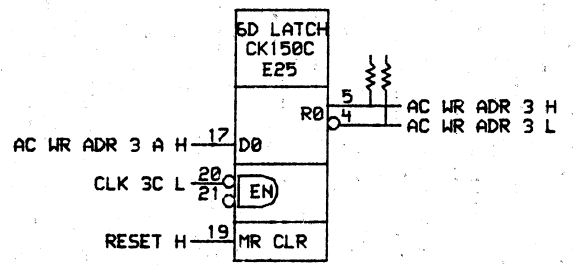
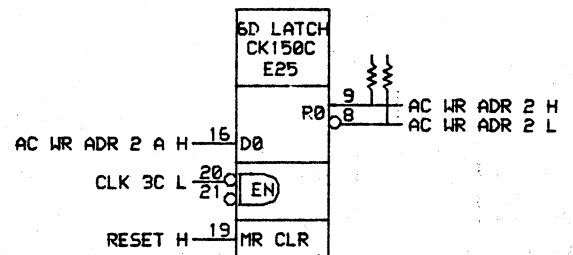
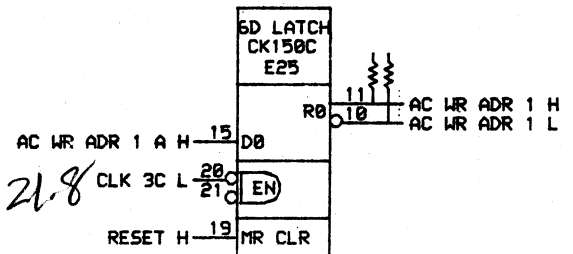
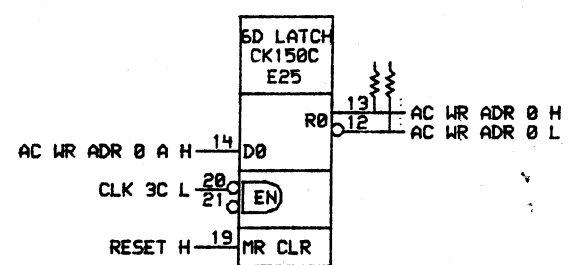
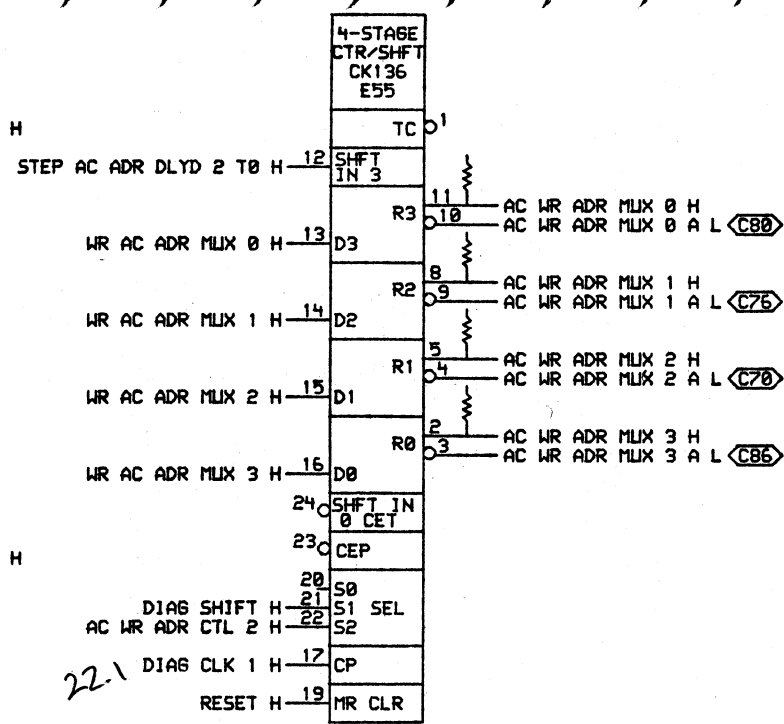
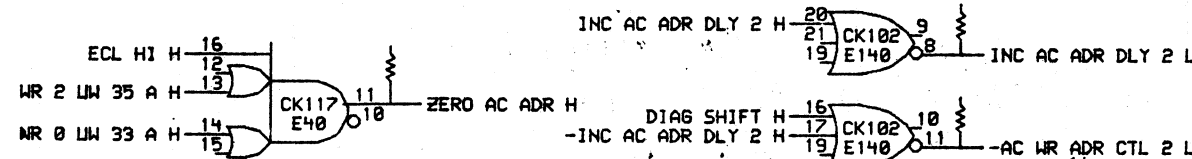
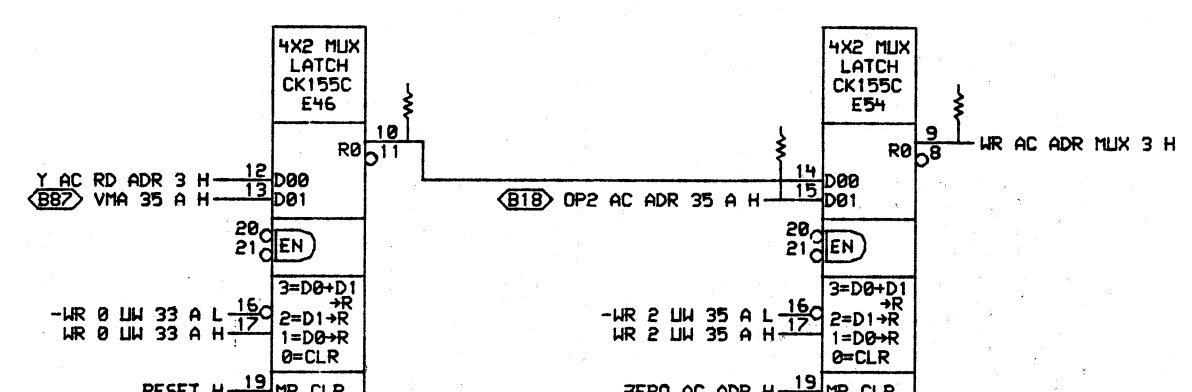
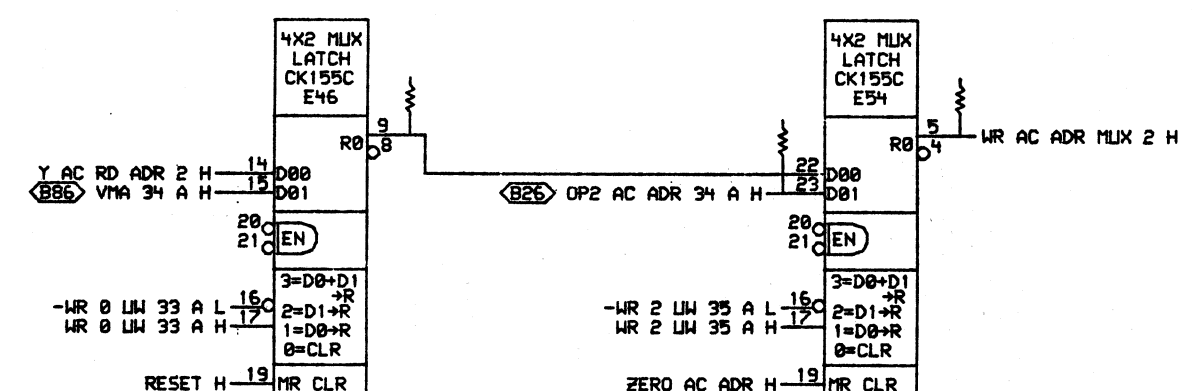
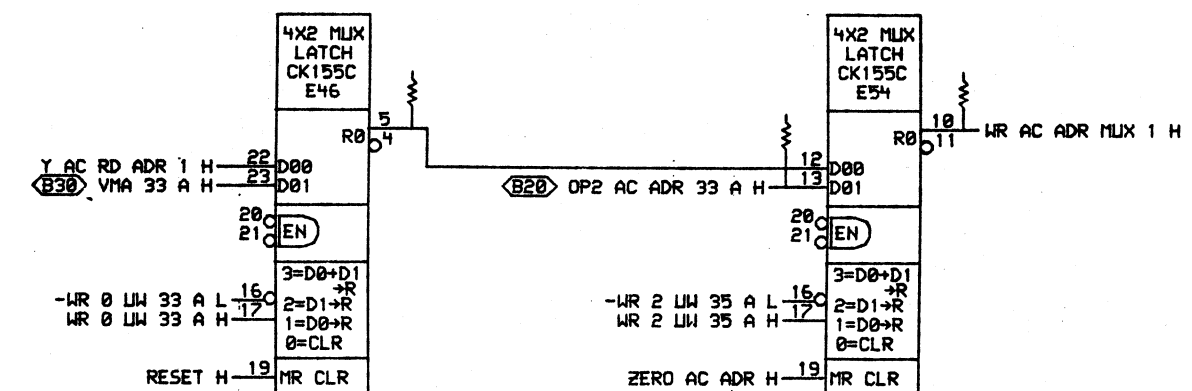
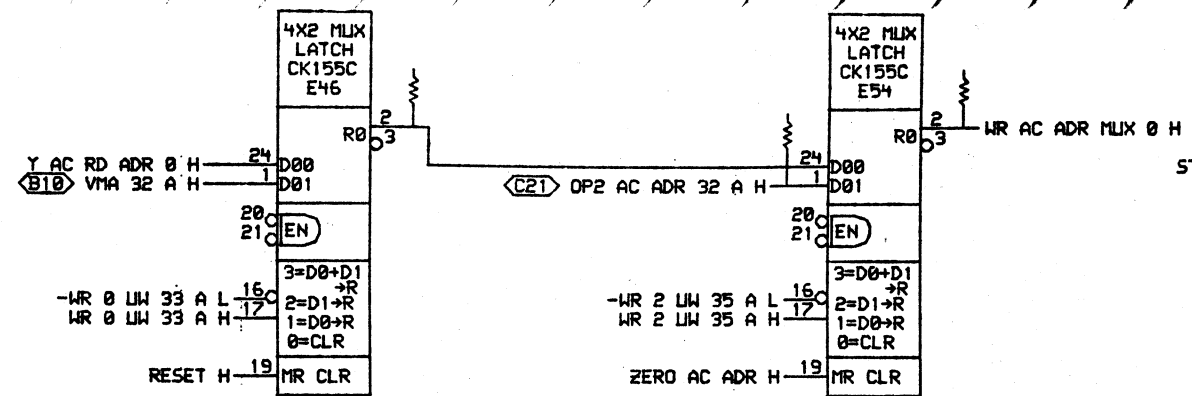
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL:				NEXT HIGHER ASSEMBLY:
SIZE	CODE	NUMBER	REV.	
D	CS	L0047-0-SHLB	*	



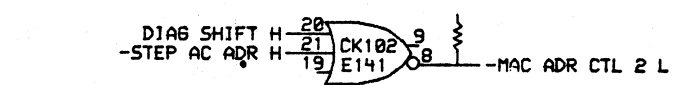
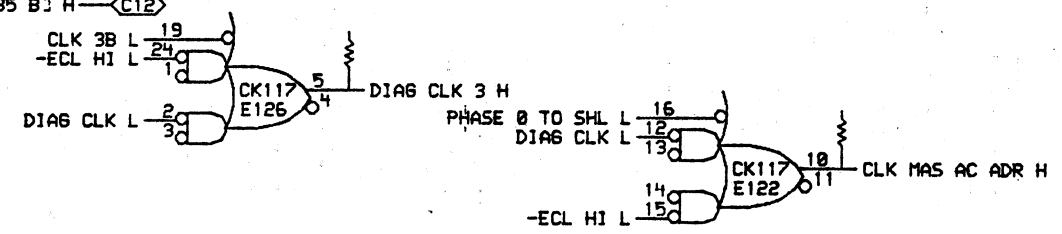
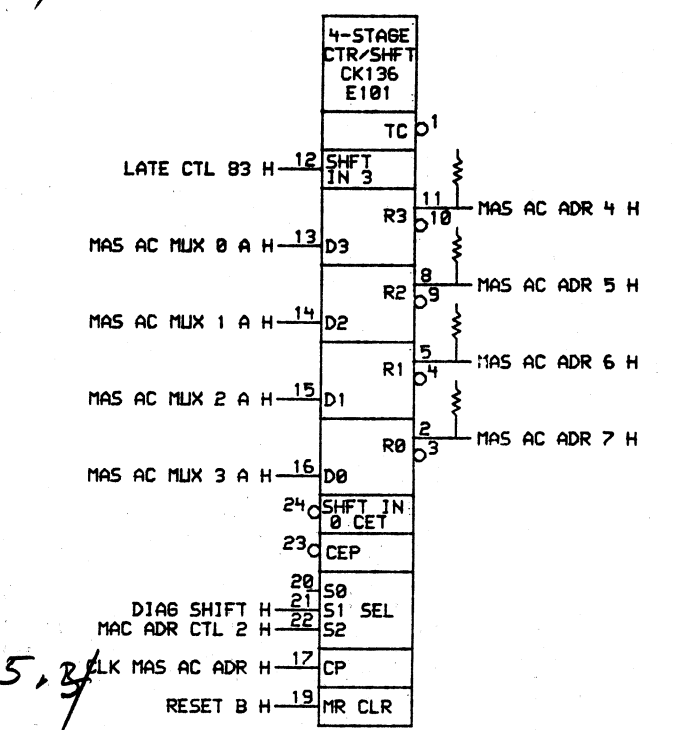
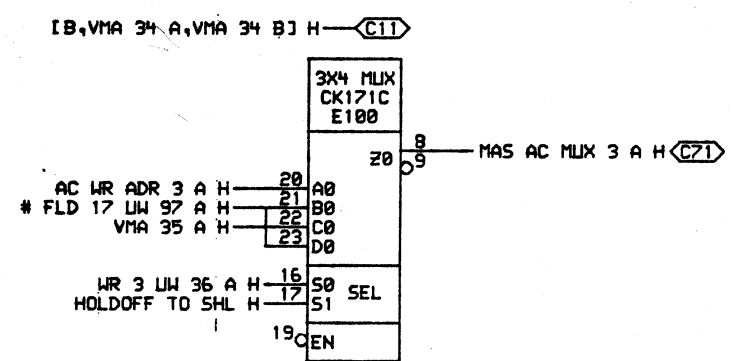
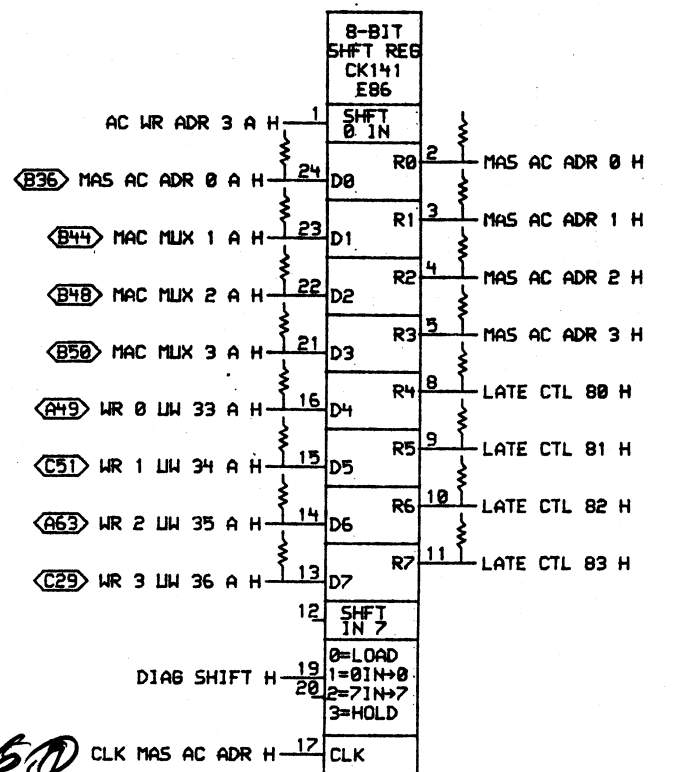
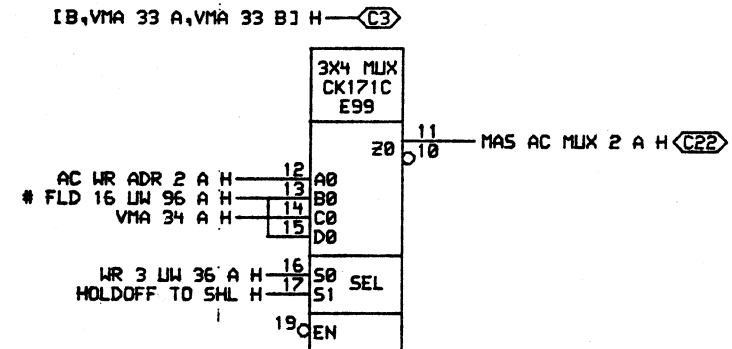
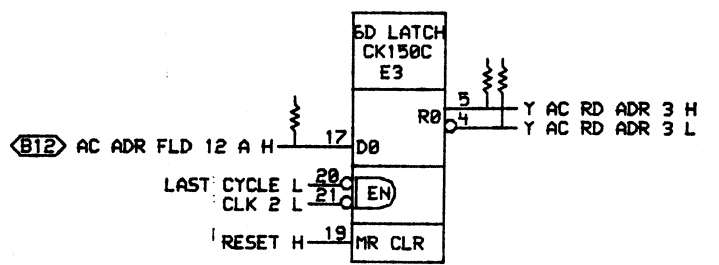
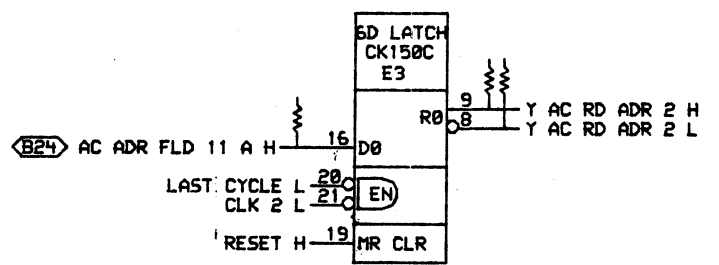
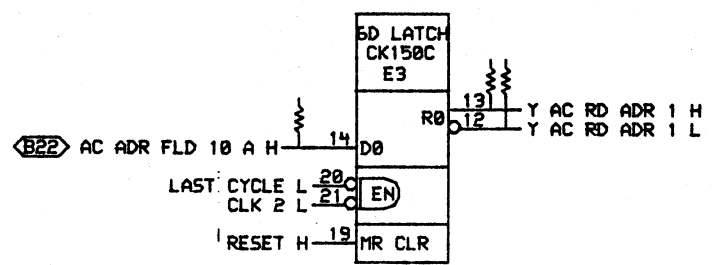
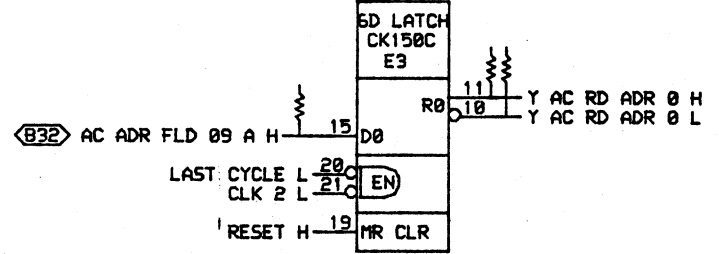
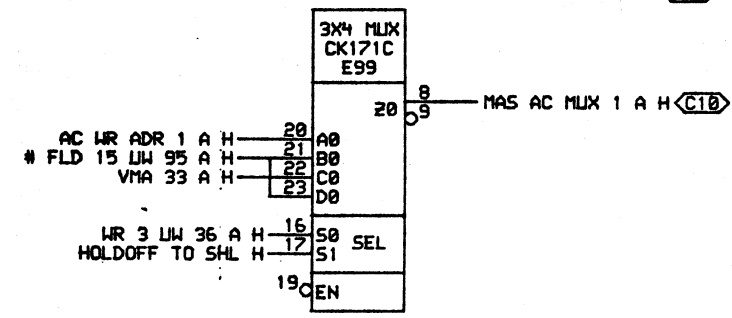
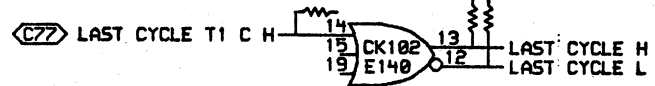
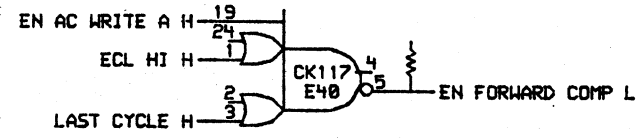
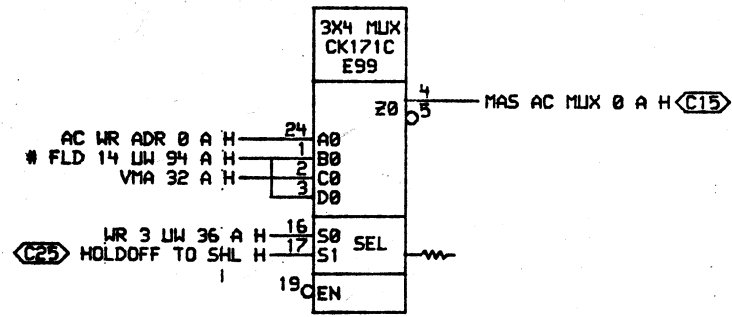
DRN.	DATE	ENG.	DATE	TITLE:	MAC REG BY 0,1 PC,L2MQ 0,1 IN		
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	1	1	
FIRST USED ON OPTION/MODEL:				SIZE	CODE	NUMBER	REV.
B-DD-L0047-0				D	CS	L0047-0-SHLC	*



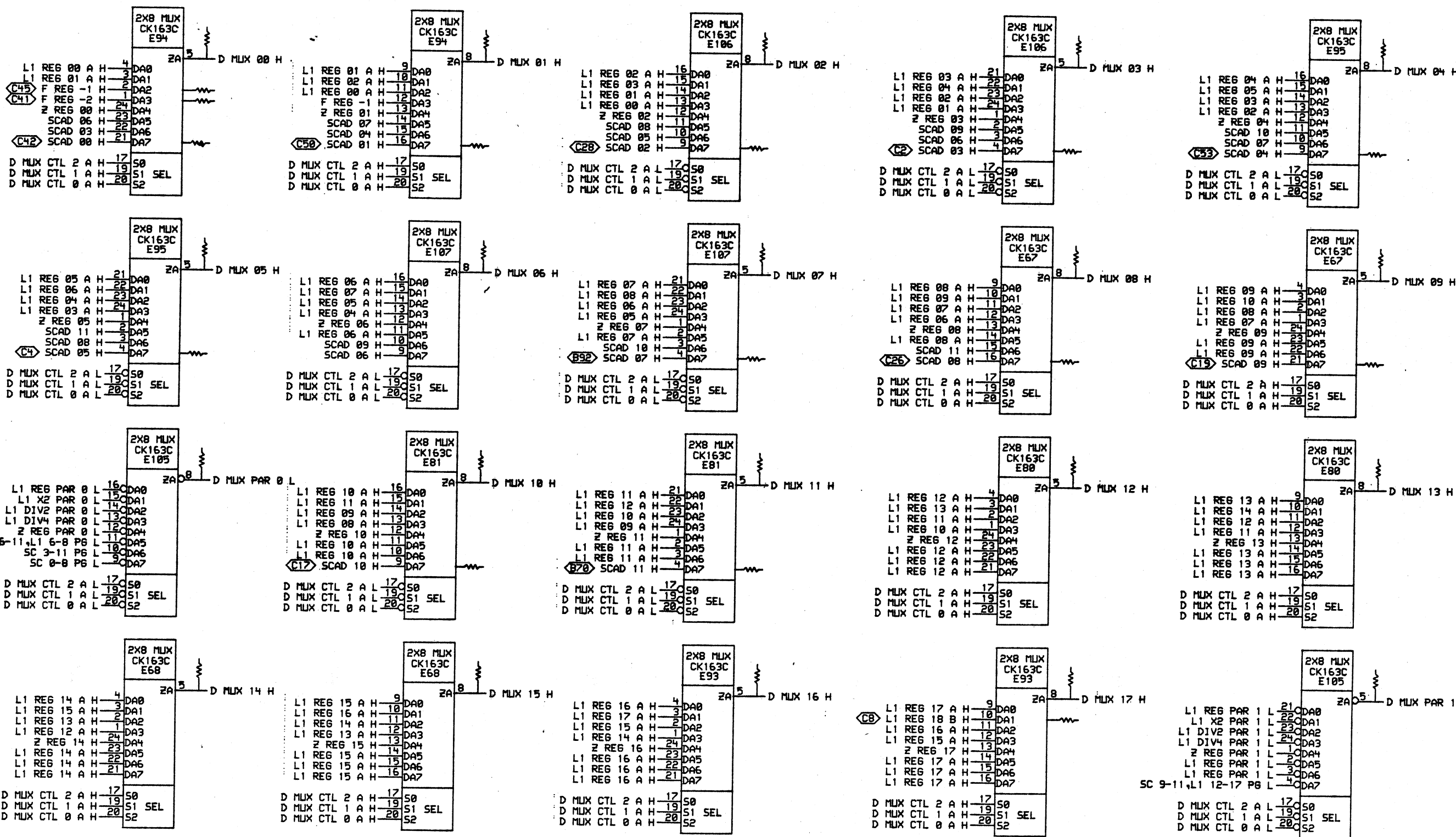
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL:				NUMBER
NEXT HIGHER ASSEMBLY:				REV.
B-DD-L0047-0				*
D CS L0047-0-SHLD				



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		AC RD, WR ADR
		SHEET		ALL, MUX, & REG
PROJ: J. C. B. L. 10047-REV 2-20-82	19-SEP-82 10:38	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION MODEL:	B-DD-L0047-0		D	C5 L0047-0-SHLE
				REV. *



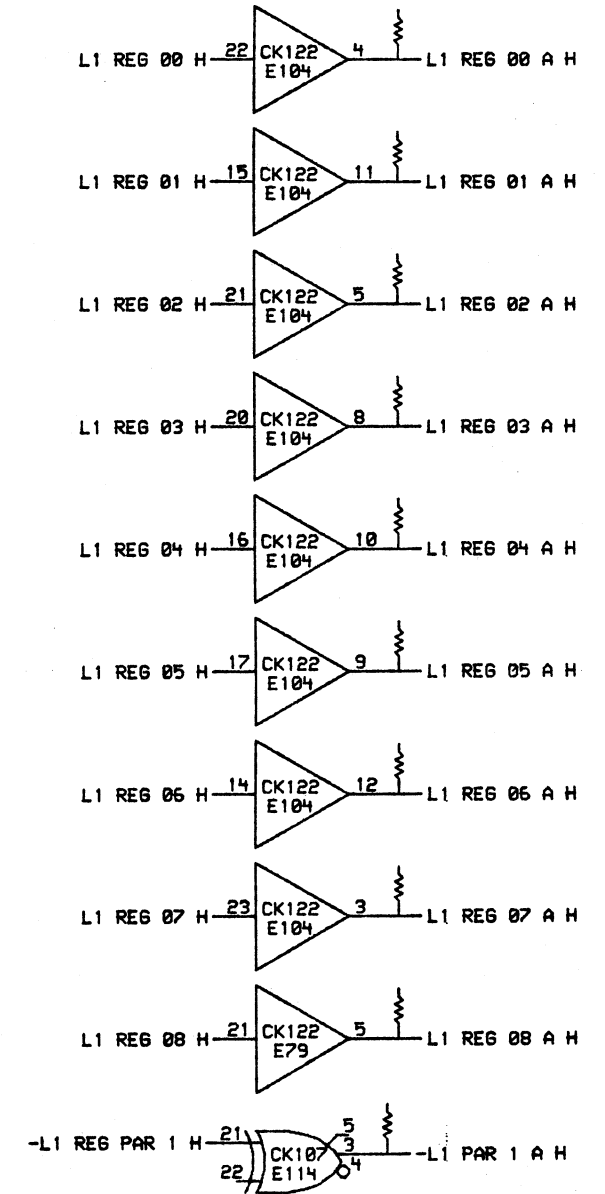
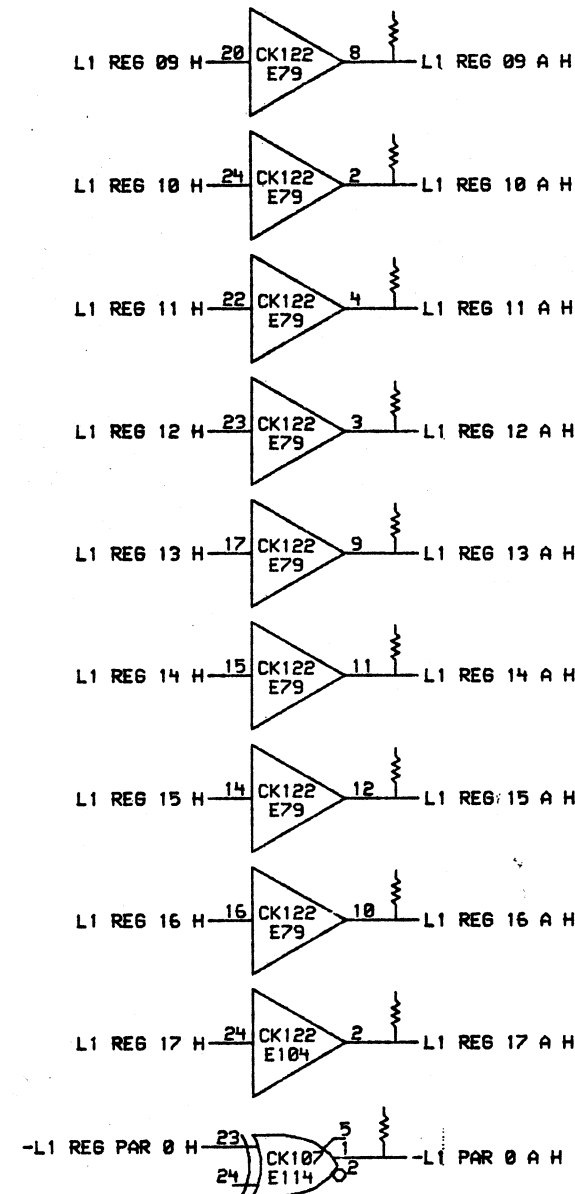
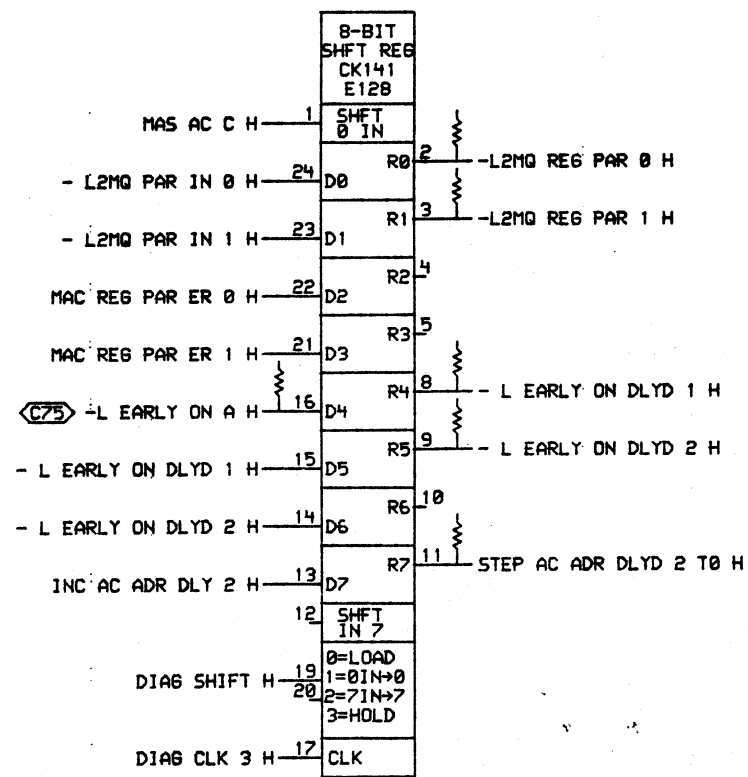
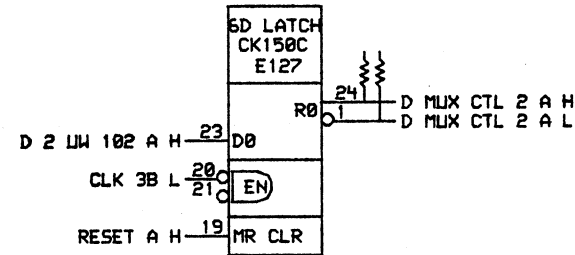
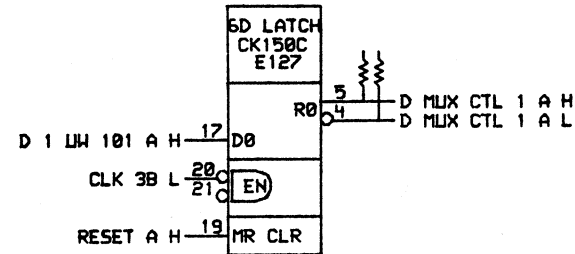
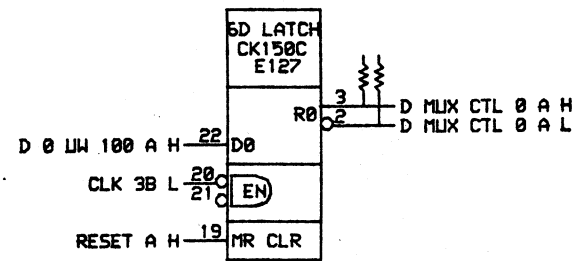
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		Y AC ADR MUX
		SHEET 1 OF 1		MAC WR MUX
PROTOL (C800, L0047, REV0) 30547.DRW	19-SEP-82 10:38	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL:	B-DD-L0047-0		D CS	L0047-0-SHLF
				REV. *



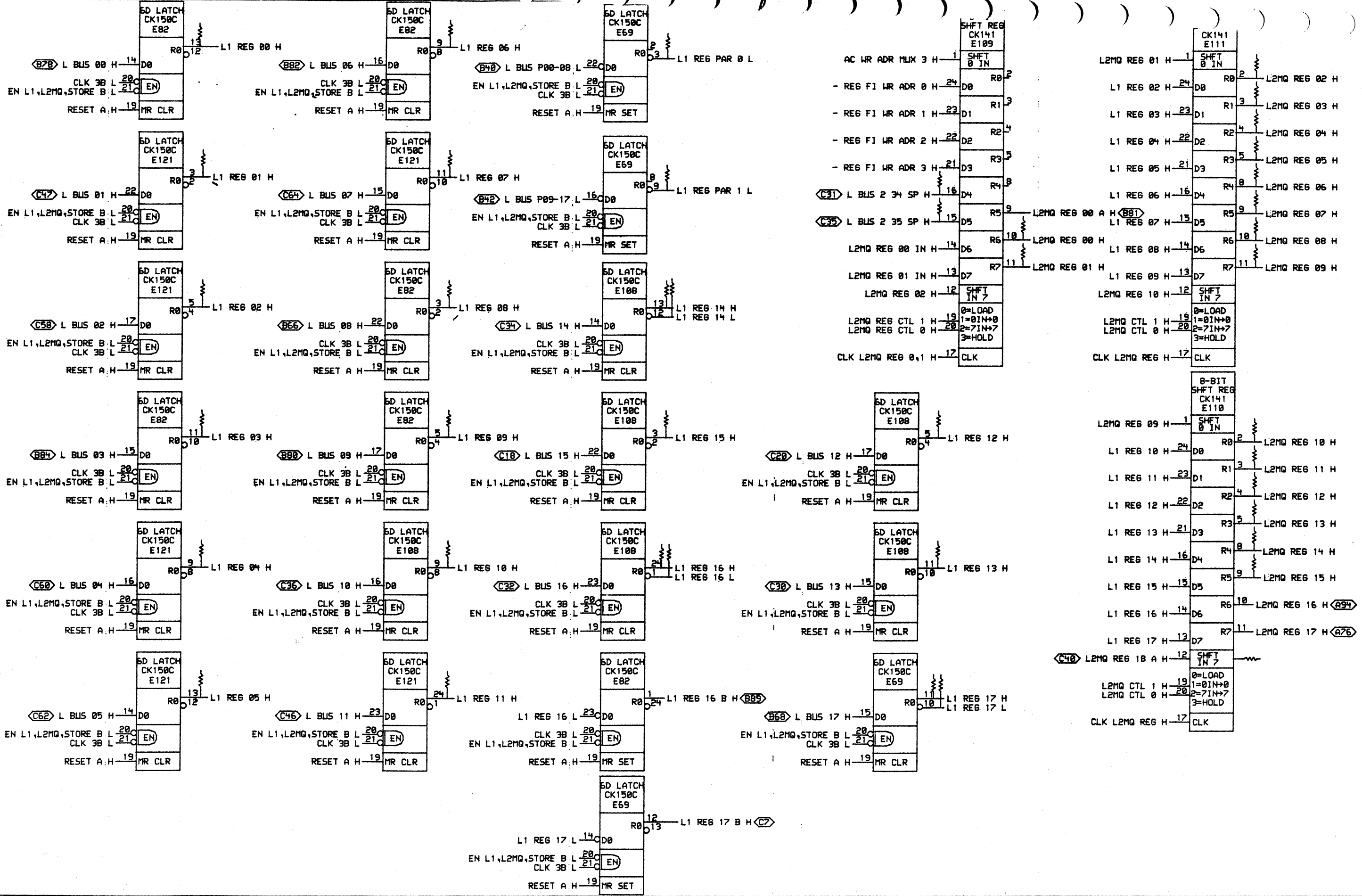
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REVISIONS		
CHK	CHANGE NO.	REV

	DATE	ENG.	DATE	TITLE:
	04-NOV-82			D MUX 00-17
CHK'D.	DATE	BOARD LOCATION:		
		SHEET 1 OF 1		
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		SIZE CODE
B-DD-L0047-0		B-DD-L0047-0		D CS
				NUMBER
				L0047-0-SHLG
				REV. *



DRN.	DATE	ENG.	DATE	TITLE:	SCAD MUX		
CHK'D.	DATE	BOARD LOCATION:					
FIRST USED ON OPTION/MODEL:				NEXT HIGHER ASSEMBLY:		SIZE	CODE
B-DD-L0047-0				B-DD-L0047-0		D	CS
						NUMBER	REV.
						L0047-0-SHLH	*



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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:
	04-NOV-82			L1, L2MQ REGS
DRW. J. J. J.	DATE	BOARD LOCATION:	NUMBER	
CHK'D.		SHEET 1 OF 1	L0047-0-SHLI	
PROTOD (2800, L0047, REV) 1505M, 2, DRW 03-NOV-82 09:03	NEXT HIGHER ASSEMBLY:		SIZE	CODE
FIRST USED ON OPTION/MODEL:	B-DD-L0047-0		D	CS

D

D

C

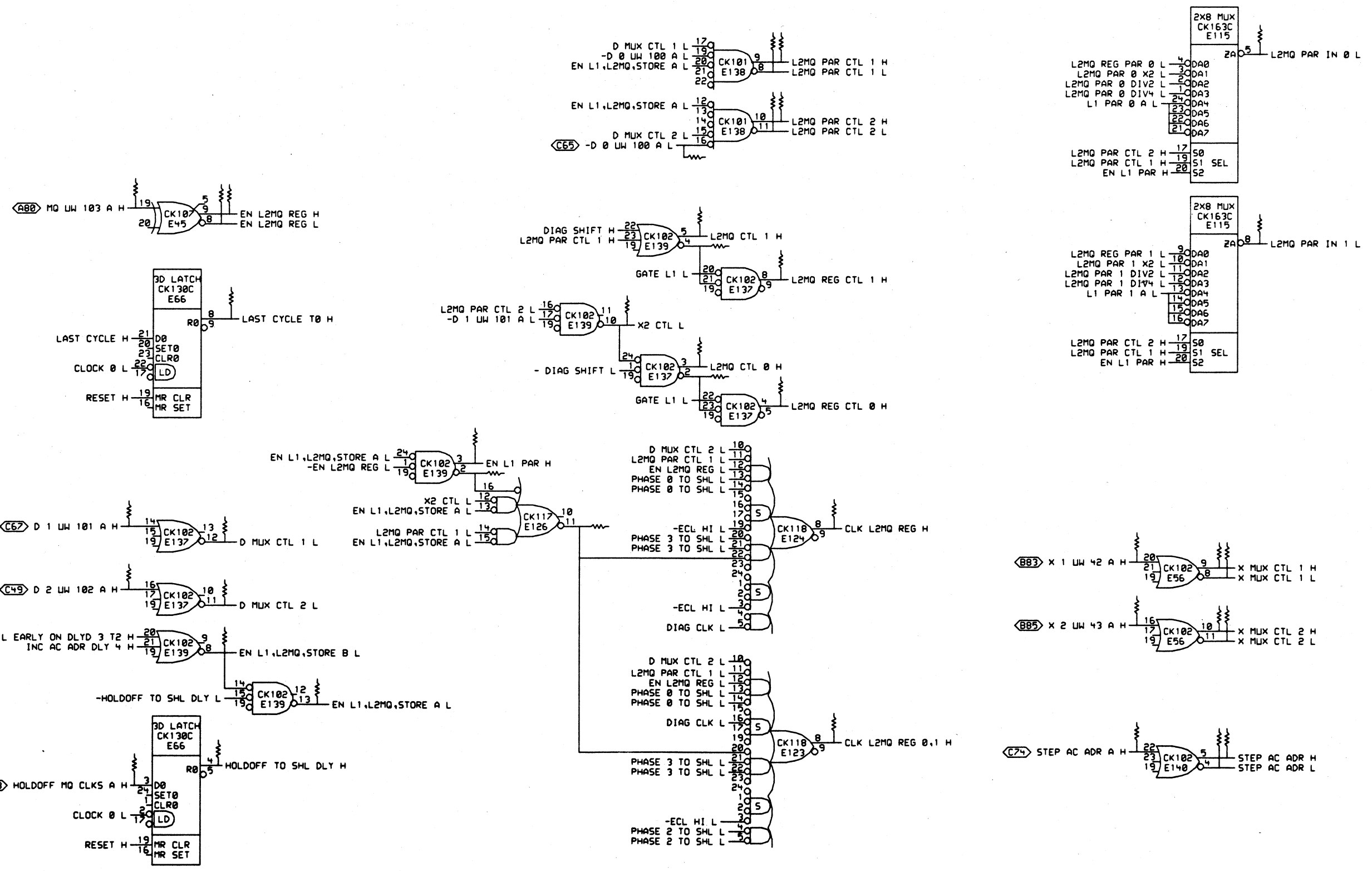
C

B

B

A

A

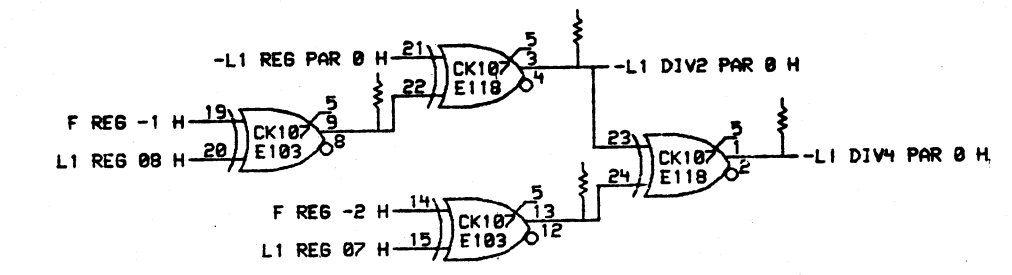
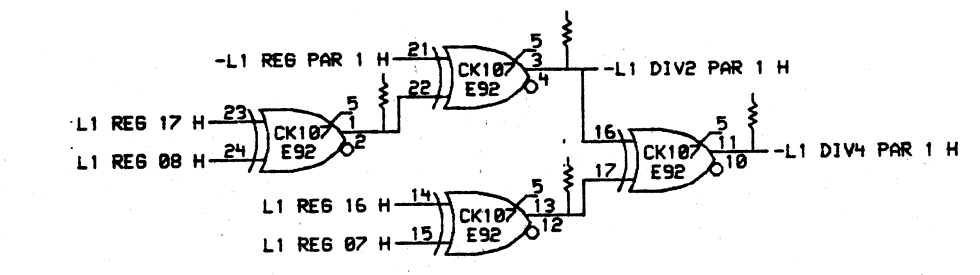
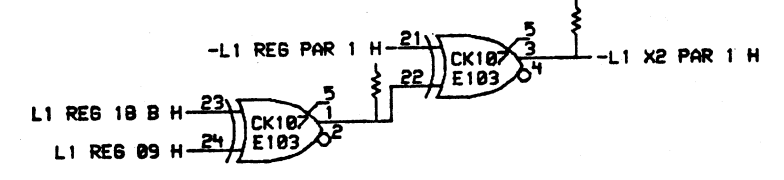
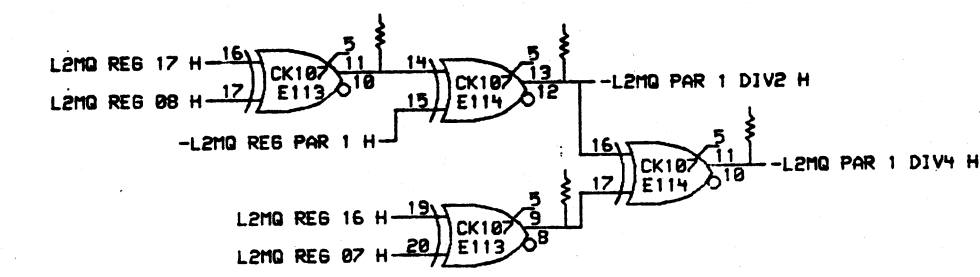
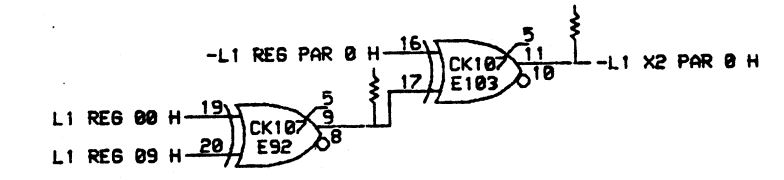
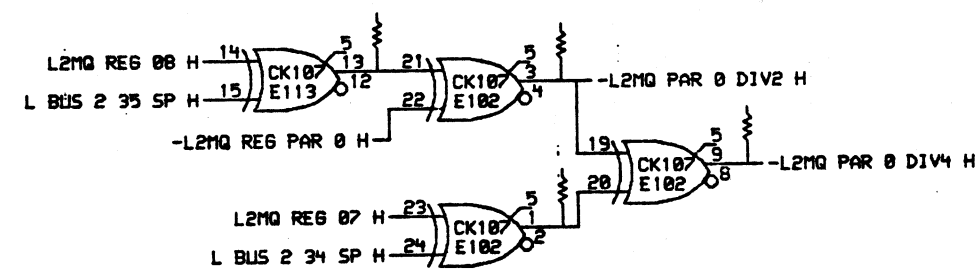
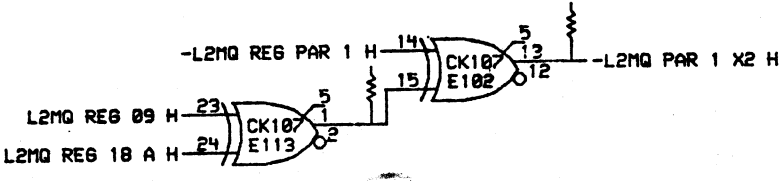
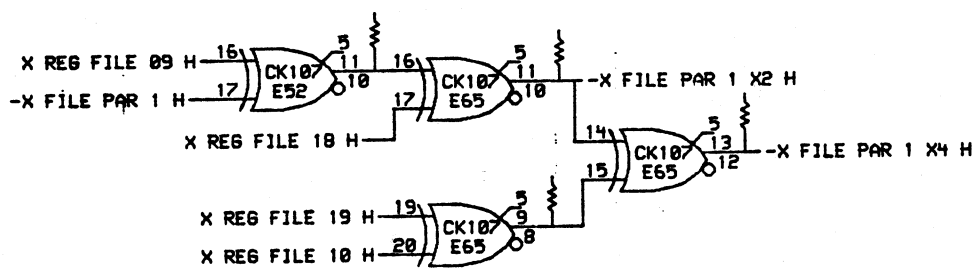
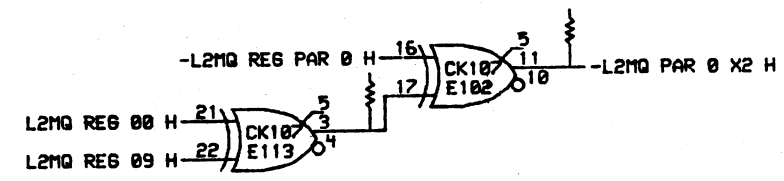
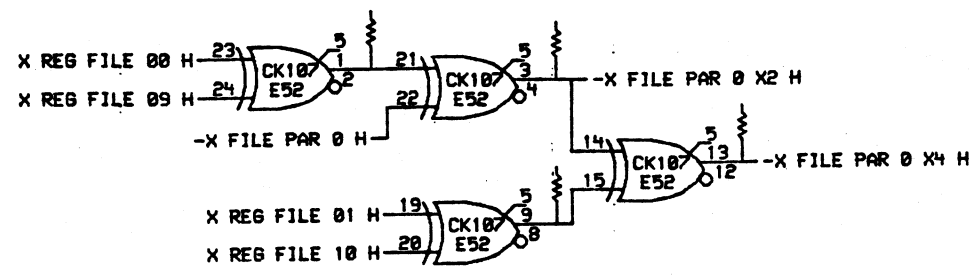


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REVISIONS	
CHK	CHANGE NO. REV

DATE	ENG.	DATE	TITLE:
23-DEC-82	J. Jansky		D MUX, L2MQ CTL
DATE	BOARD LOCATION:	SHEET	L2MQ REG PAR
12-DEC-82 17:00	B-DD-L0047-0	OF	
FIRST USED ON OPTION/MODEL:		SIZE CODE	NUMBER
		D CS	L0047-0-SHLJ

DATE	ENG.	DATE	TITLE:
23-DEC-82	J. Jansky		D MUX, L2MQ CTL
DATE	BOARD LOCATION:	SHEET	L2MQ REG PAR
12-DEC-82 17:00	B-DD-L0047-0	OF	
FIRST USED ON OPTION/MODEL:		SIZE CODE	NUMBER
		D CS	L0047-0-SHLJ



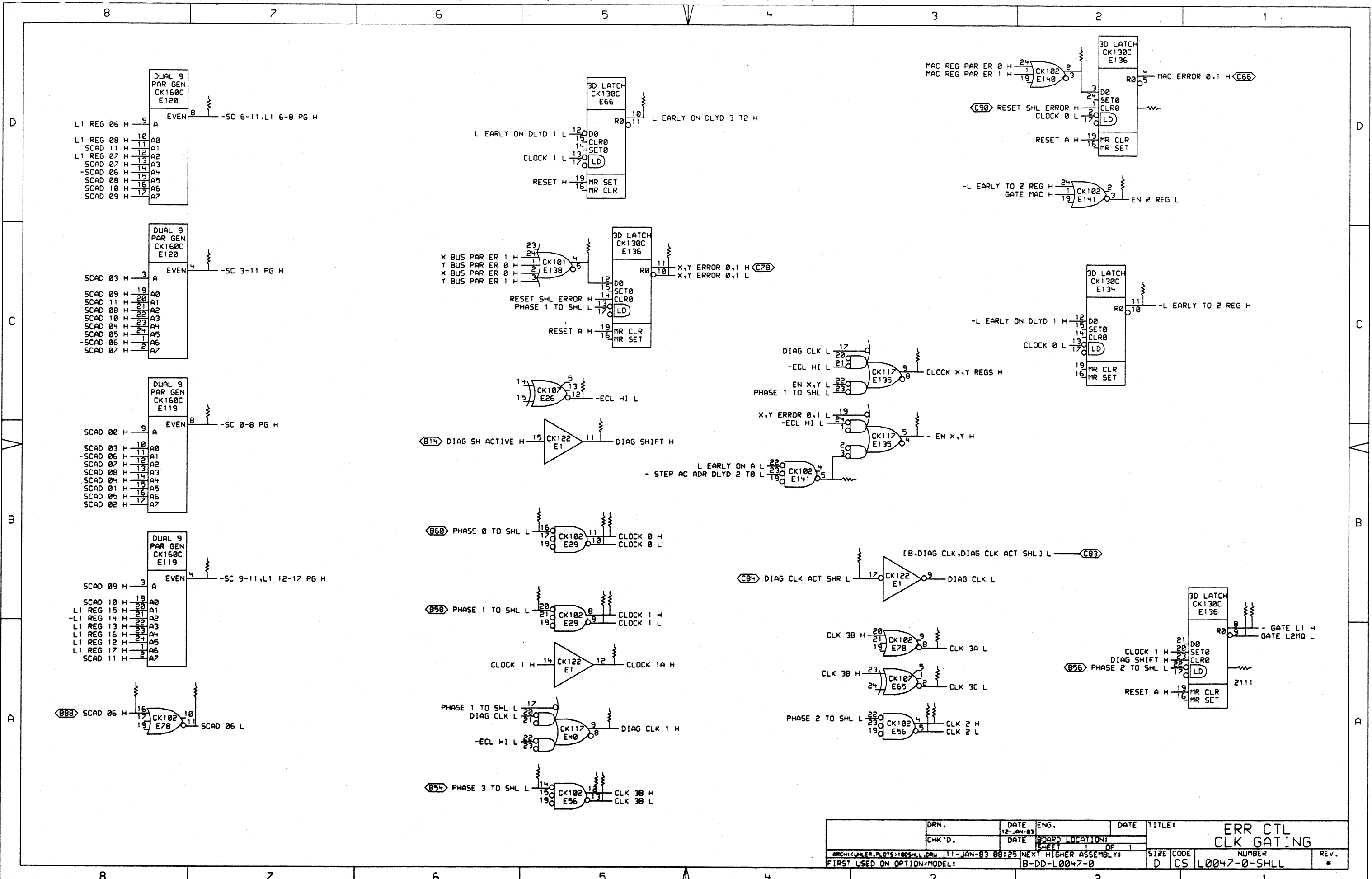
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REVISIONS	
CHK	CHANGE NO. REV

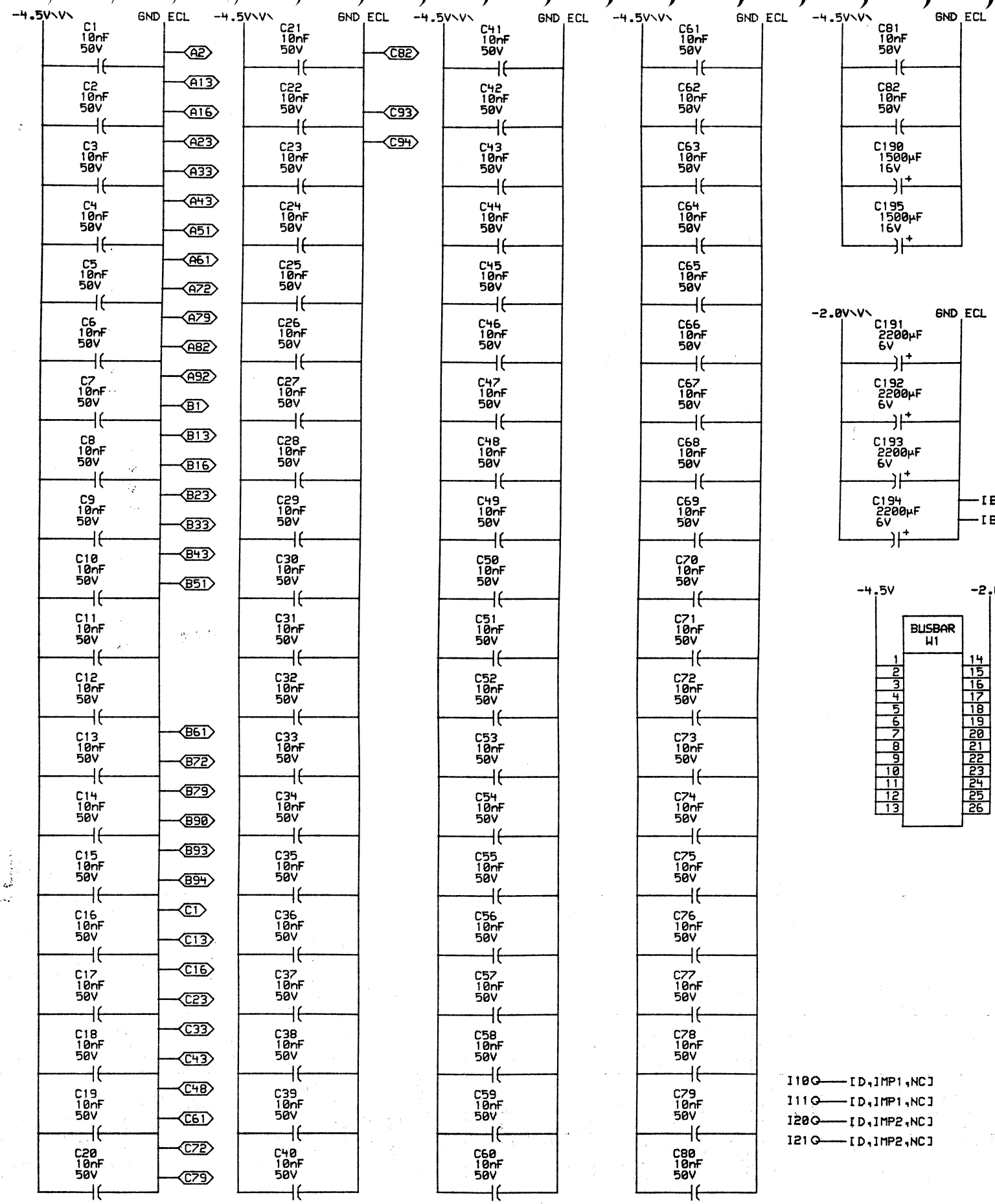
8	7	6	5	4	3	2	1
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digital	DATE	ENG.	DATE	TITLE:
	20-OCT-82			SHIFT PARITY ADJUSTMENT
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
	19-OCT-82 11:33	NEXT HIGHER ASSEMBLY:		
FIRST USED ON OPTION/MODEL:	B-DD-L0047-0		SIZE	CODE
			D	CS

NUMBER	REV.
L0047-0-SHLK	*



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	ERR CTL CLK GATING
ARCH: C:\PLR_PLOTS\1054\DRN_111-JAN-83 08:25		NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL:		B-DD-L0047-0		D CS
			NUMBER	REV.
			L0047-0-SHLL	*

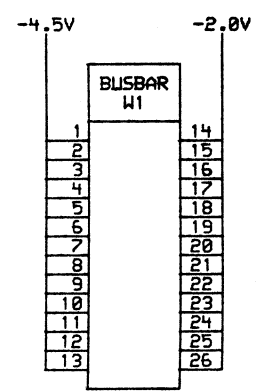
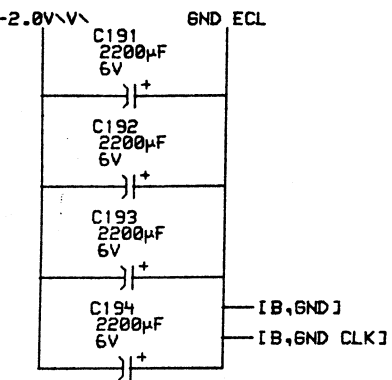


- Ⓢ53 GND CLK
- Ⓢ55 GND CLK
- Ⓢ57 GND CLK
- Ⓢ59 GND CLK

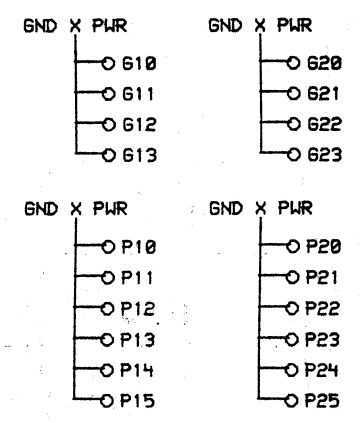
- Ⓢ73 NC A73
- Ⓢ77 NC B7
- Ⓢ27 NC C27
- Ⓢ37 NC C37
- Ⓢ38 NC C38
- Ⓢ39 NC C39
- Ⓢ52 NC C52
- Ⓢ55 NC C55
- Ⓢ57 NC C57
- Ⓢ59 NC C59
- Ⓢ63 NC C63
- Ⓢ61 NC C81
- Ⓢ85 NC C85
- Ⓢ87 NC C87
- Ⓢ88 NC C88
- Ⓢ89 NC C89

E2 SPARE

- Z2 TERMS
- Z48 TERMS
- Z122 TERMS



- I100 ID,IMP1,NC
- I110 ID,IMP1,NC
- I200 ID,IMP2,NC
- I210 ID,IMP2,NC



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REVISIONS	
CHK	CHANGE NO. REV

	DATE 02-FEB-83 DATE 02-FEB-83 BOARD LOCATION: SHEET 1 OF 1 NEXT HIGHER ASSEMBLY: B-DD-L0047-0	TITLE: CSHL POWER & GROUND NUMBER L0047-0-SHLM REV. *
	FIRST USED ON OPTION/MODEL: 2080	SIZE CODE D CS

D

C

B

A

D

C

REV.

NUMBER

SIZE CODE SP

B

A

PROTOJ:<2080.L0071.REVC>2CSHR1
 PROTOJ:<2080.L0071.REVC>2CSHR2
 PROTOJ:<2080.L0071.REVC>8CSHR3
 PROTOJ:<2080.L0071.REVC>2CSHR4
 PROTOJ:<2080.L0071.REVC>2CSHR5
 PROTOJ:<2080.L0071.REVC>8CSHR6
 PROTOJ:<2080.L0071.REVC>7CSHR7
 PROTOJ:<2080.L0071.REVC>2CSHR8
 PROTOJ:<2080.L0071.REVC>4CSHR9
 PROTOJ:<2080.L0071.REVC>2CSHRA
 PROTOJ:<2080.L0071.REVC>2CSHRB
 PROTOJ:<2080.L0071.REVC>2CSHRC
 PROTOJ:<2080.L0071.REVC>2CSHRD
 PROTOJ:<2080.L0071.REVC>2CSHRE
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 PROTOJ:<2080.L0071.REVC>15CSHRL
 PROTOJ:<2080.L0071.REVC>6CSHRM

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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN. *J. Jorjky*
 CHK'D.

DATE 12-JAN-83

ENG. DATE

BOARD LOCATION:

SHEET OF

TITLE:

ARCH: (L0071.PLOTS)CSHR.DPL.1 12-JAN-83 03:08
 FIRST USED ON OPTION/MODEL:

NEXT HIGHER ASSEMBLY:

SIZE CODE SP

NUMBER

REV.

E145	E132	E119	E106	E094	E081	E067	E053	E041	E027	E013
E144	E131	E118	E105	E093	E079	E065	E052	E040	E026	E012
E143	E130	E117	E104	E091	E077	E063	E050	E037	E023	E010
E142	E129	E116	E103	E090	E076	E062	E049	E036	E022	E009
E141	E128	E115	E102	E089	E075	E061	E048	E035	E021	E008
E140	E127	E114	E101	E088	E074	E060	E047	E034	E020	E007
E139	E126	E113	E100	E087	E073	E059	E046	E033	E019	E006
E138	E125	E112	E099	E086	E072	E058	E045	E032	E018	E005
E137	E124	E111	E098	E085	E071	E057	E044	E031	E017	E004
E136	E123	E110	E097	E084	E070	E056	E043	E030	E016	E003
+ C095		+ C094		+ C093		+ C092		+ C091		+ C090
E135	E122	E109	E096	E083	E069	E055	E042	E029	E015	E002
E134	E121	E108	E082	E068	E054	E028	E014	E001		

SHL/SFR

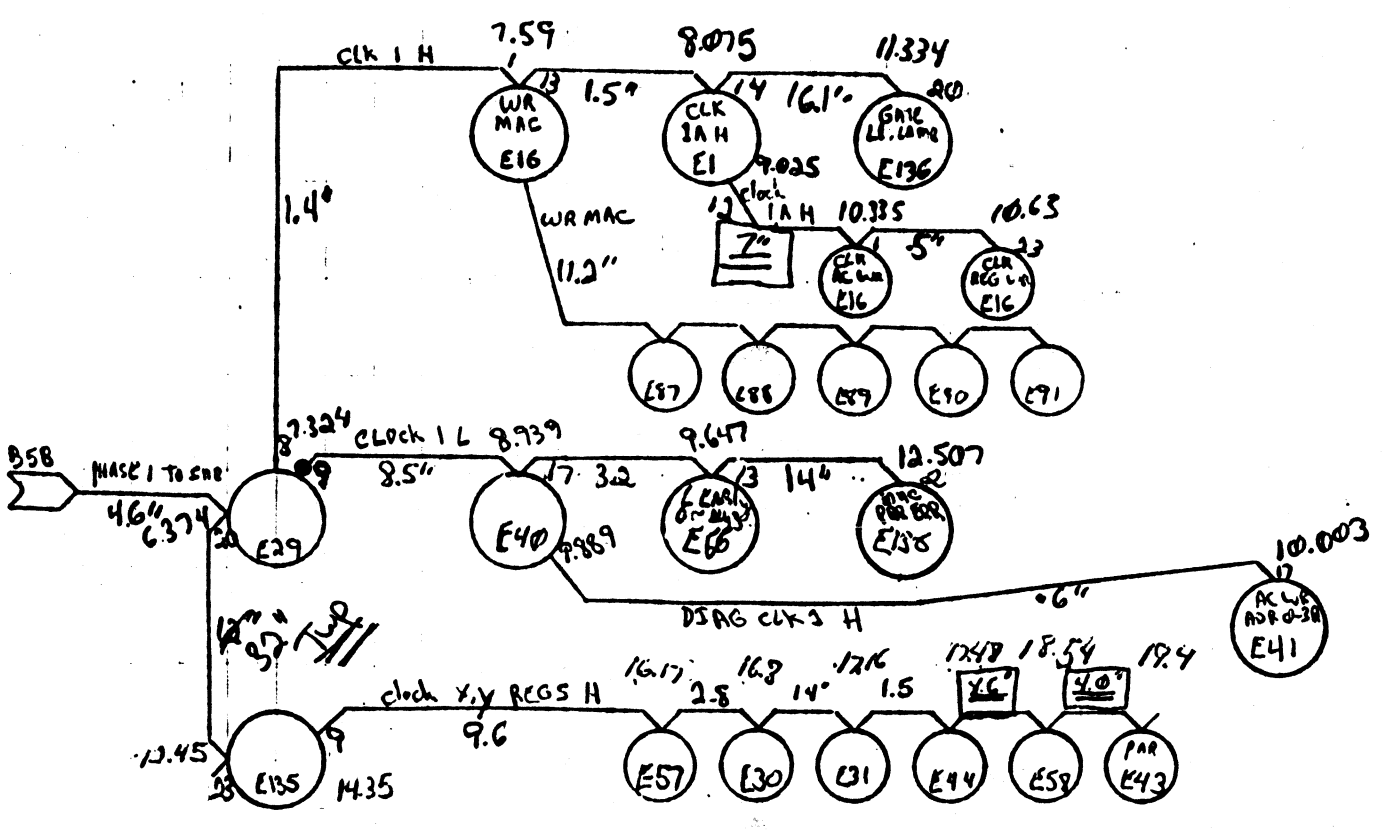
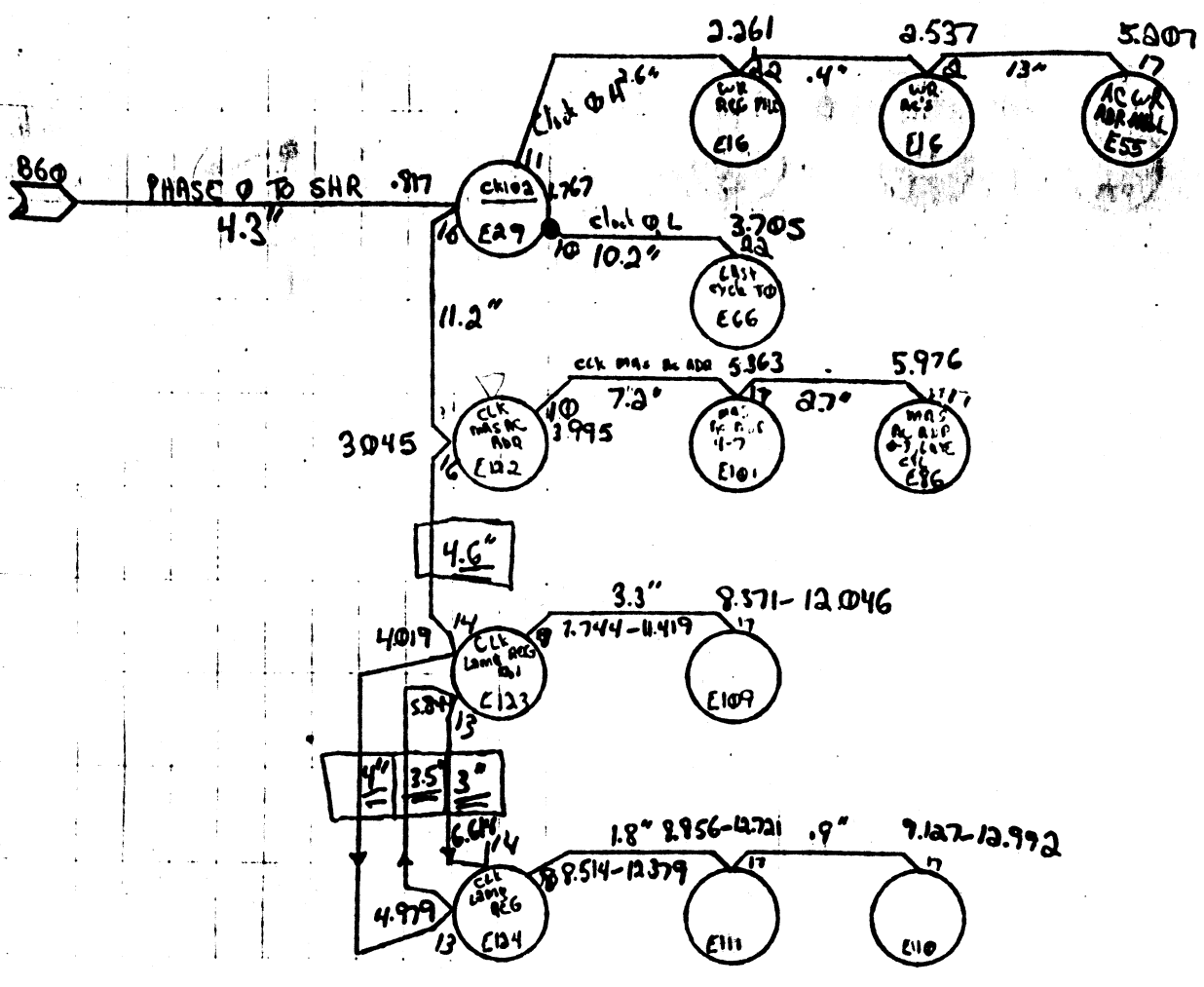
E141	E128	E115	E102	E090	E077	E064	E051	E039	E026	E013
E140	E127	E114	E101	E089	E076	E063	E050	E038	E025	E012
E139	E126	E113	E100	E088	E075	E062	E049	E037	E024	E011
E138	E125	E112	E099	E087	E074	E061	E048	E036	E023	E010
E137	E124	E111	E098	E086	E073	E060	E047	E035	E022	E009
E136	E123	E110	E097	E085	E072	E059	E046	E034	E021	E008
E135	E122	E109	E096	E084	E071	E058	E045	E033	E020	E007
E134	E121	E108	E095	E083	E070	E057	E044	E032	E019	E006
E133	E120	E107	E094	E082	E069	E056	E043	E031	E018	E005
E132	E119	E106	E093	E081	E068	E055	E042	E030	E017	E004
E131	E118	E105	E092	E080	E067	E054	E041	E029	E016	E003
+ C095		+ C094		+ C093		+ C092		+ C091		+ C090
E130	E117	E104	E091	E079	E066	E053	E040	E028	E015	E002
E129	E116	E103	E078	E065	E052	E039	E027	E014	E001	

CSHR

10/18/82

SHR TIMING D.W.H

NEW 146 LAYOUT

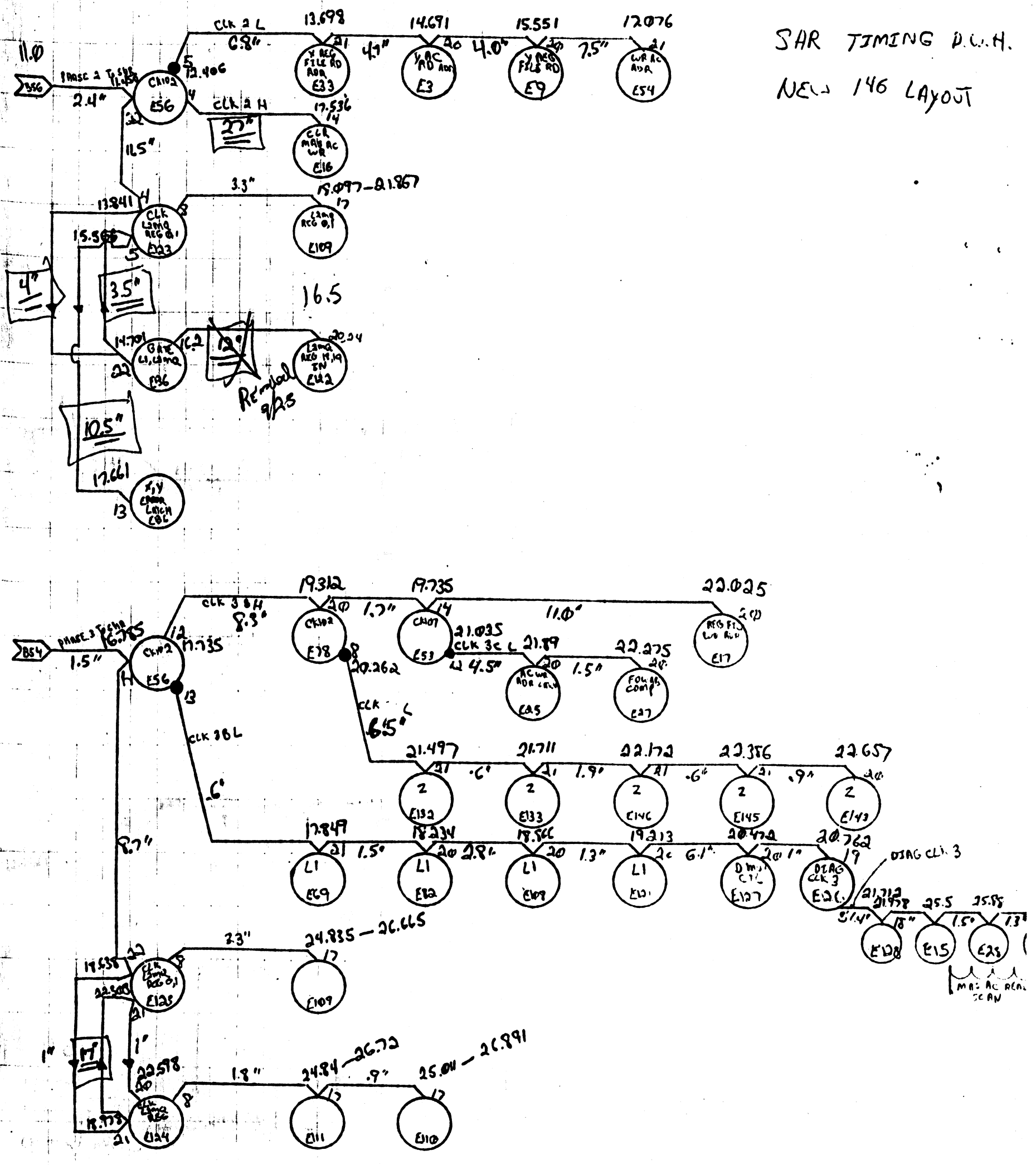


Too Late
clock approx 10.12

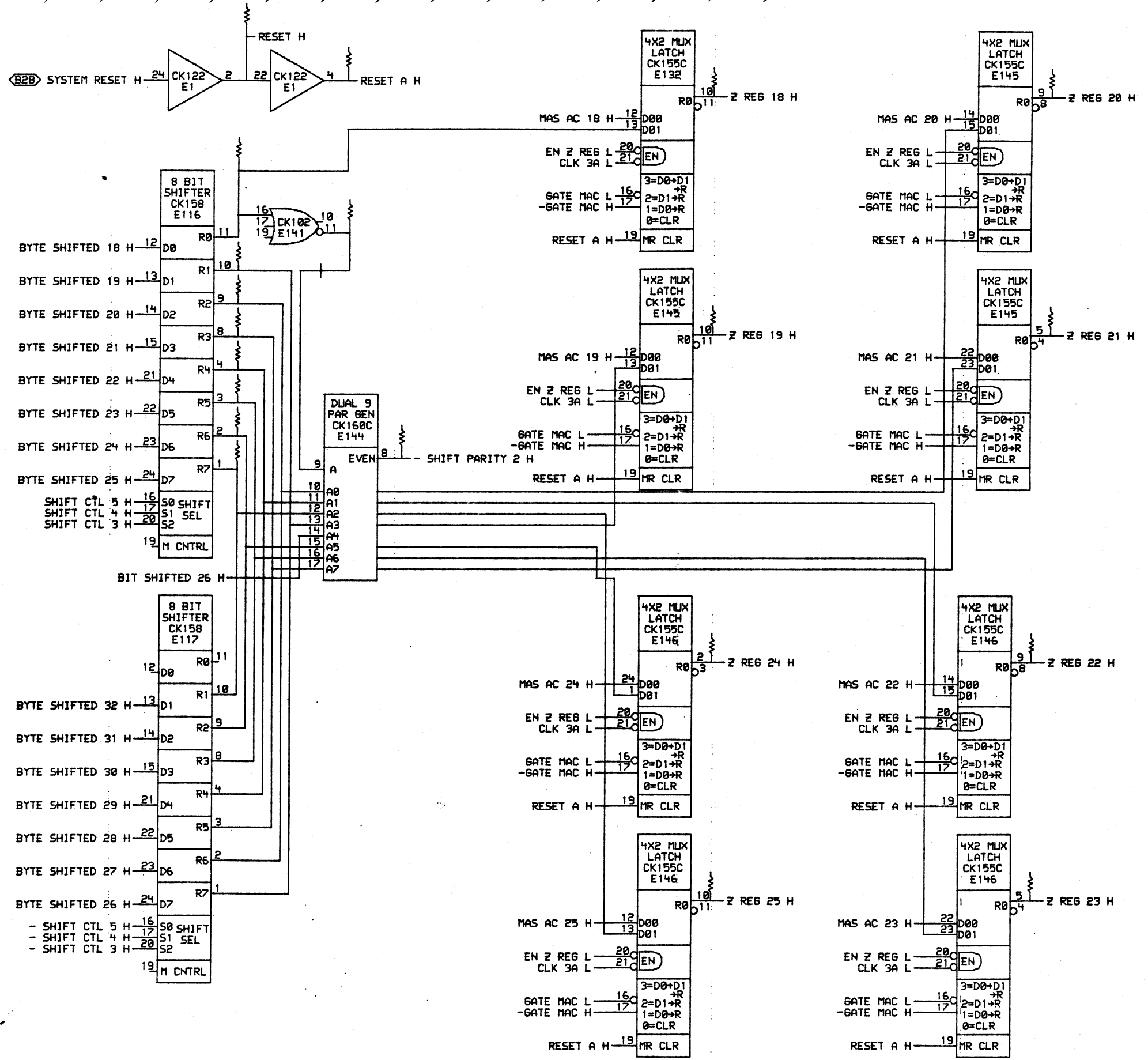
SHR
PROTO
OK by D.H. 10/18/82

5/10/82

SAR TIMING P.W.H.
NEW 146 LAYOUT



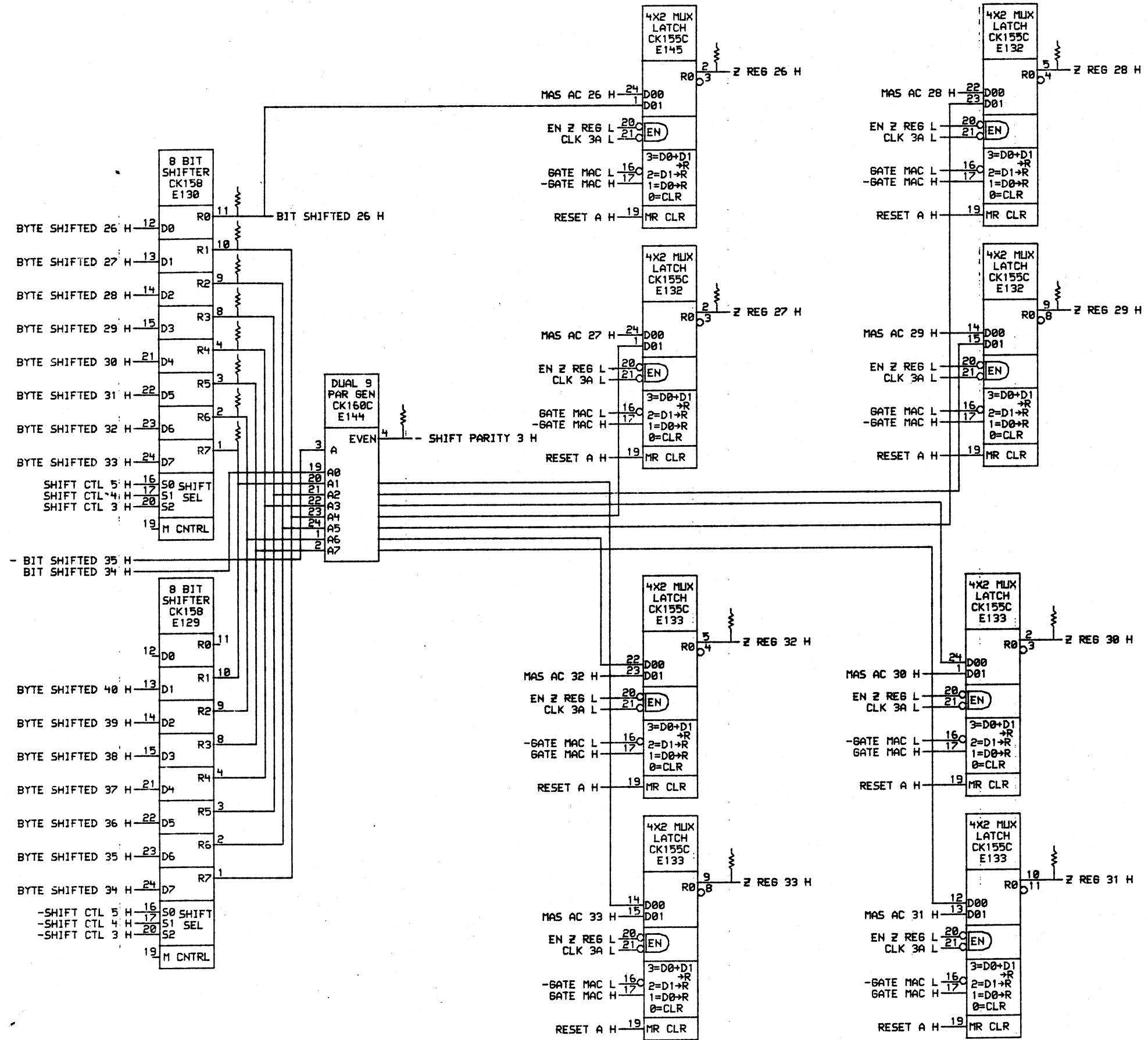
171
342
127



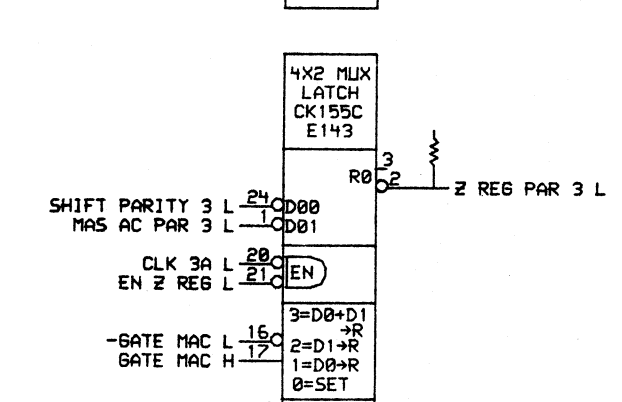
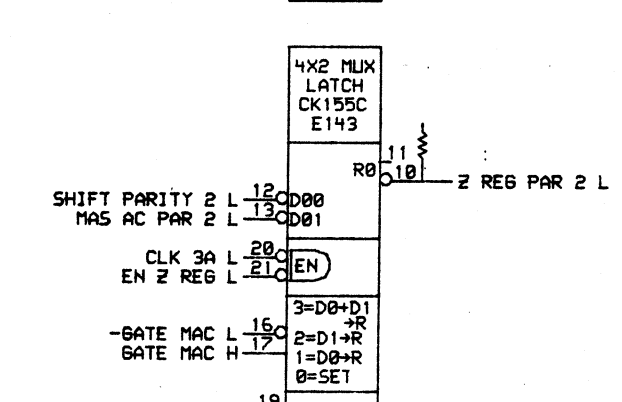
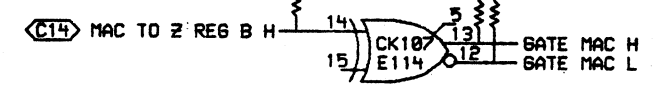
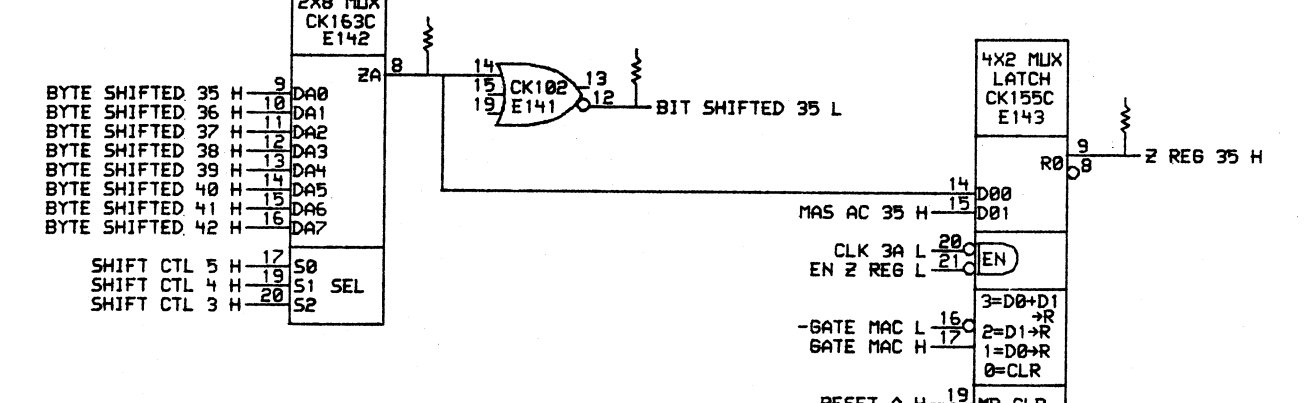
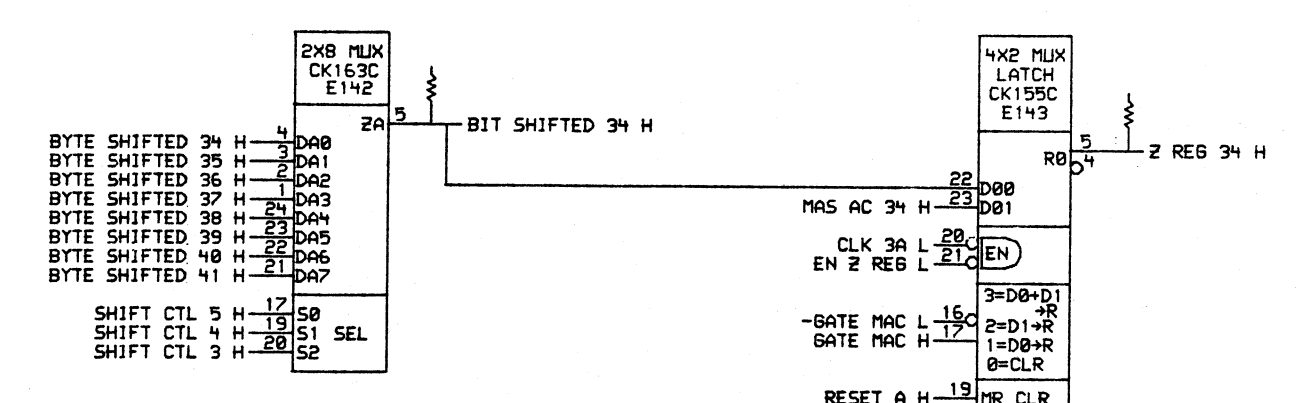
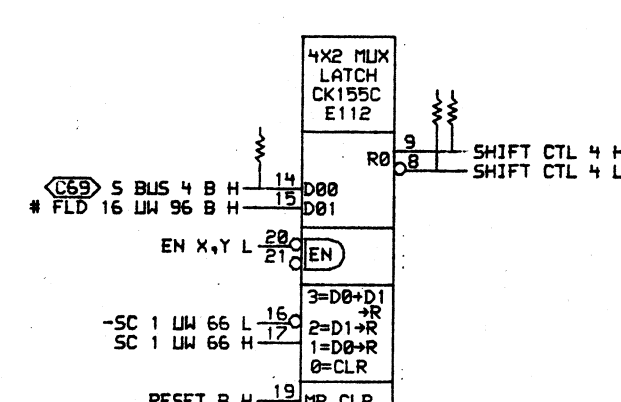
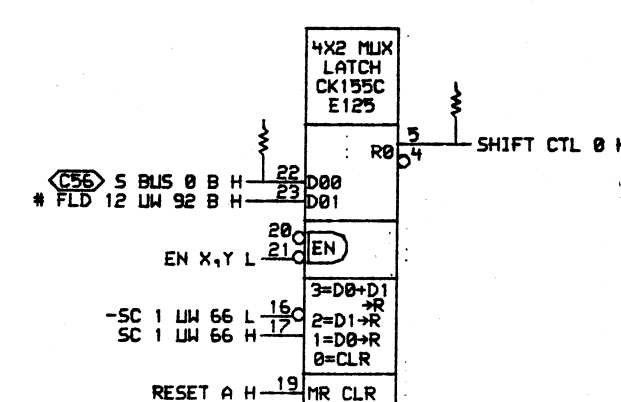
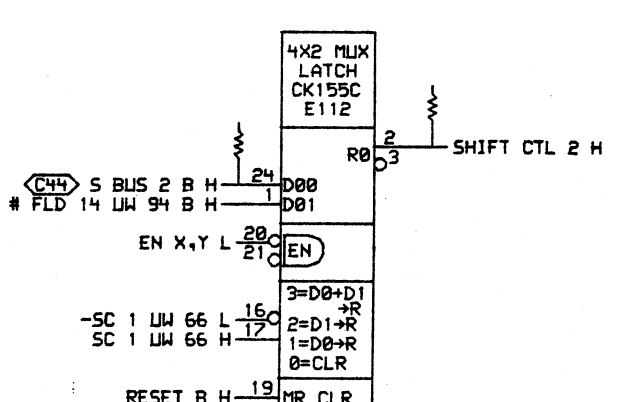
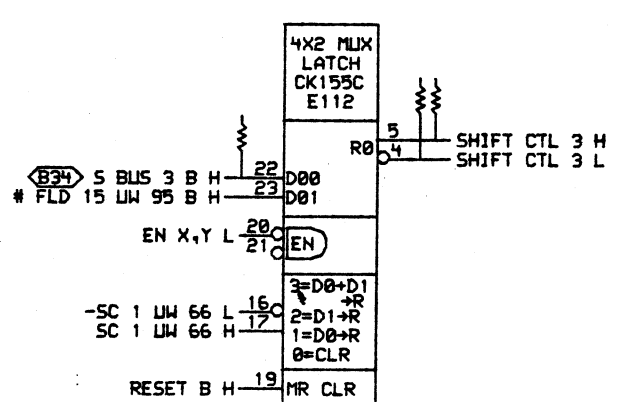
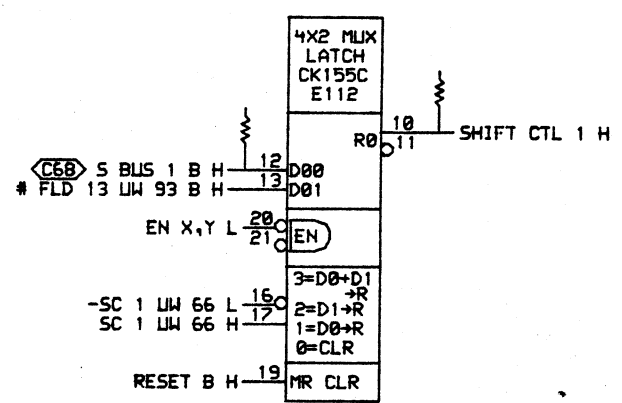
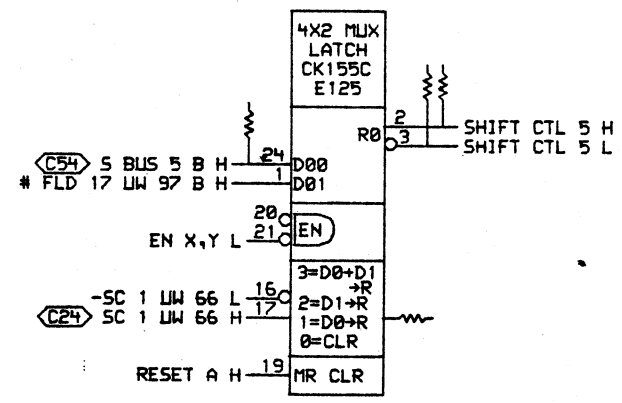
DRN.	DATE	ENG.	DATE	TITLE:	0-7 BIT SHIFT Z REG 00-07		
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2080				B-DD-L0071-0	D	CS	L0071-0-SHR1
NEXT HIGHER ASSEMBLY:				REV.	*		

D
C
B
A

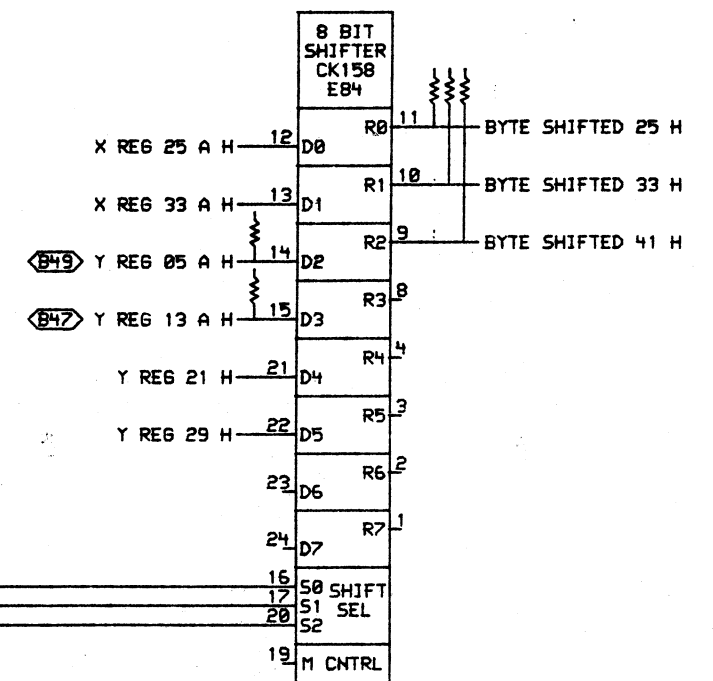
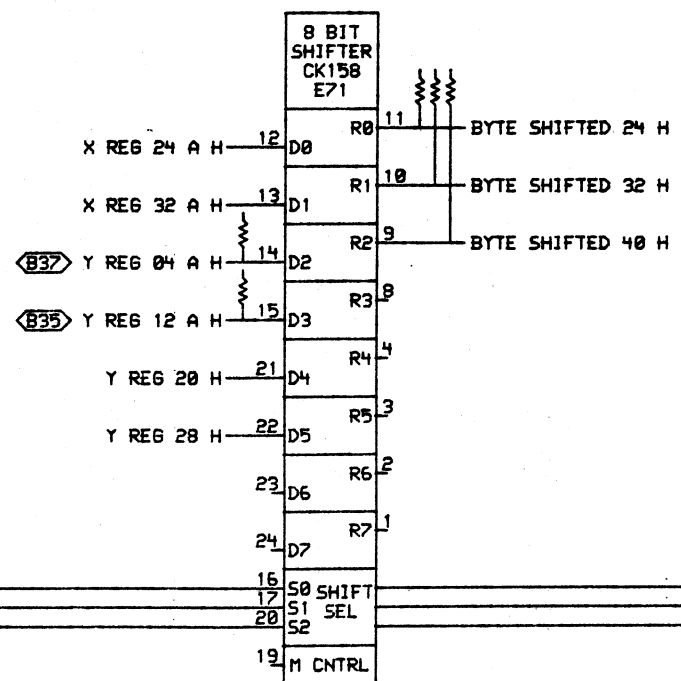
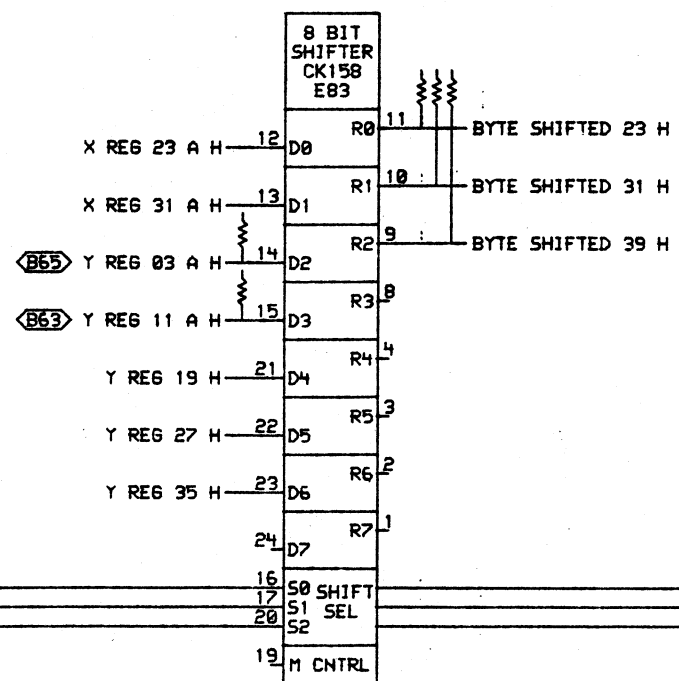
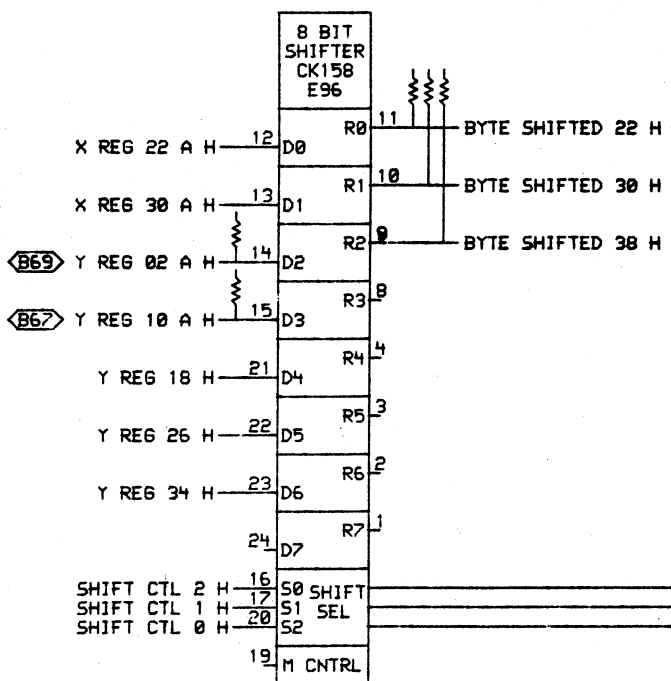
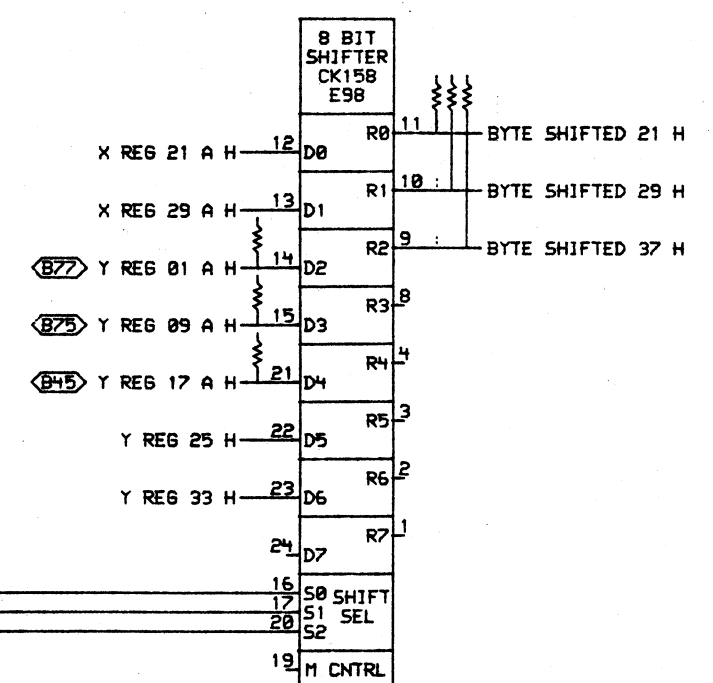
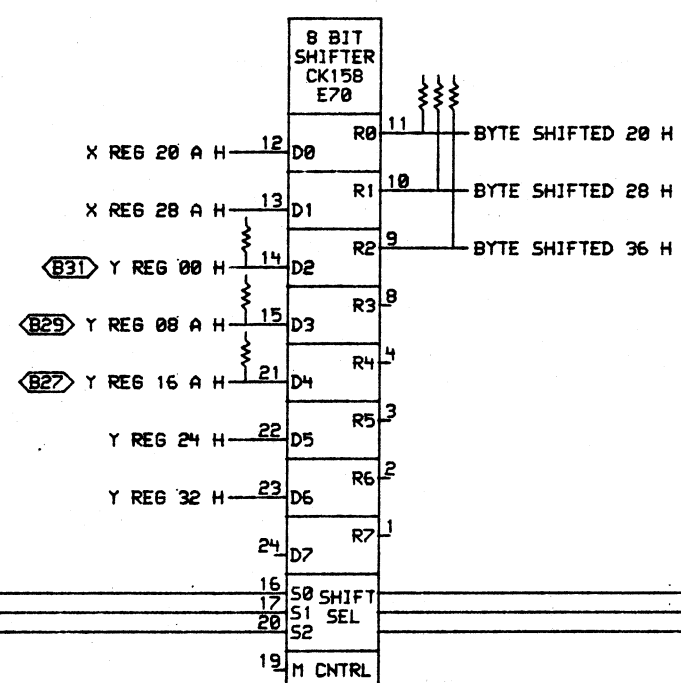
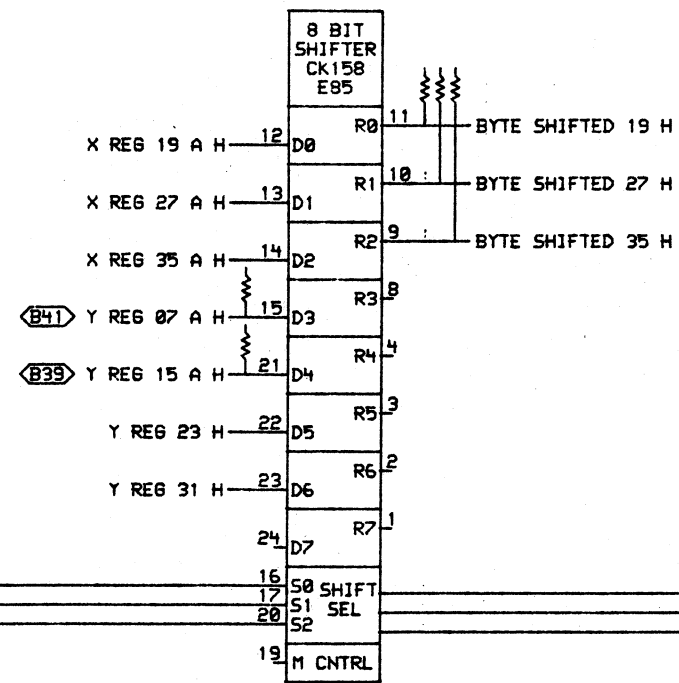
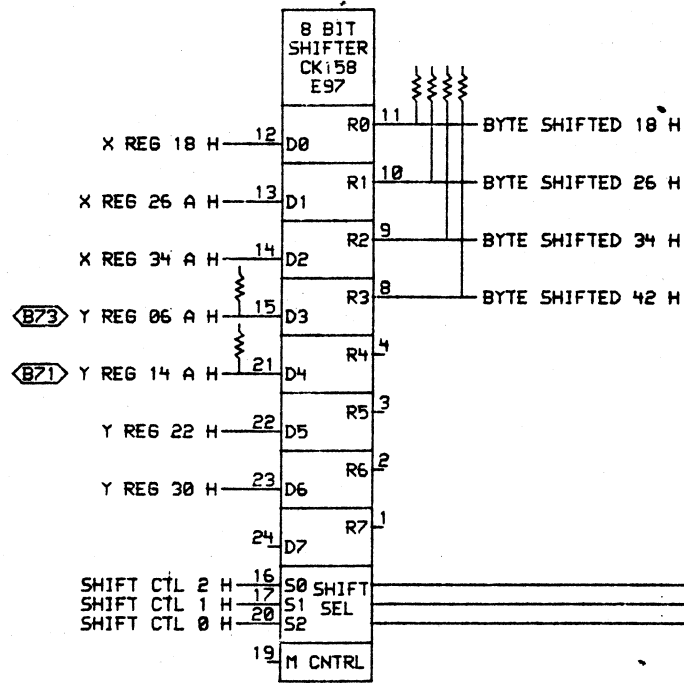
D
C
B
A

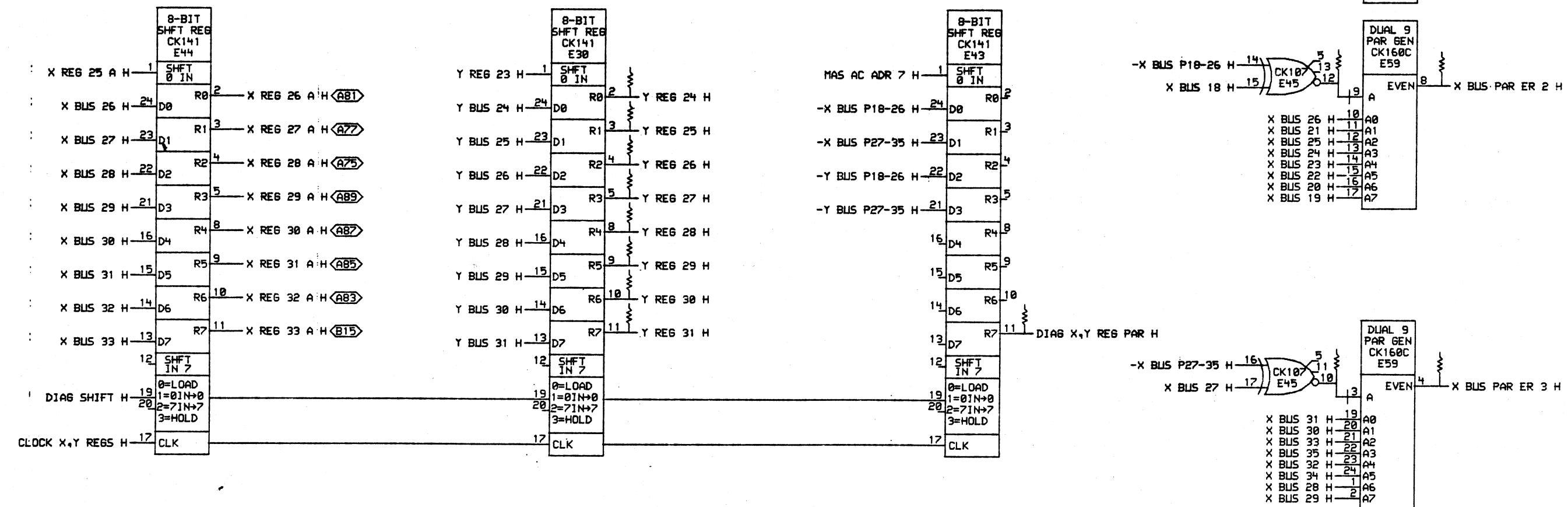
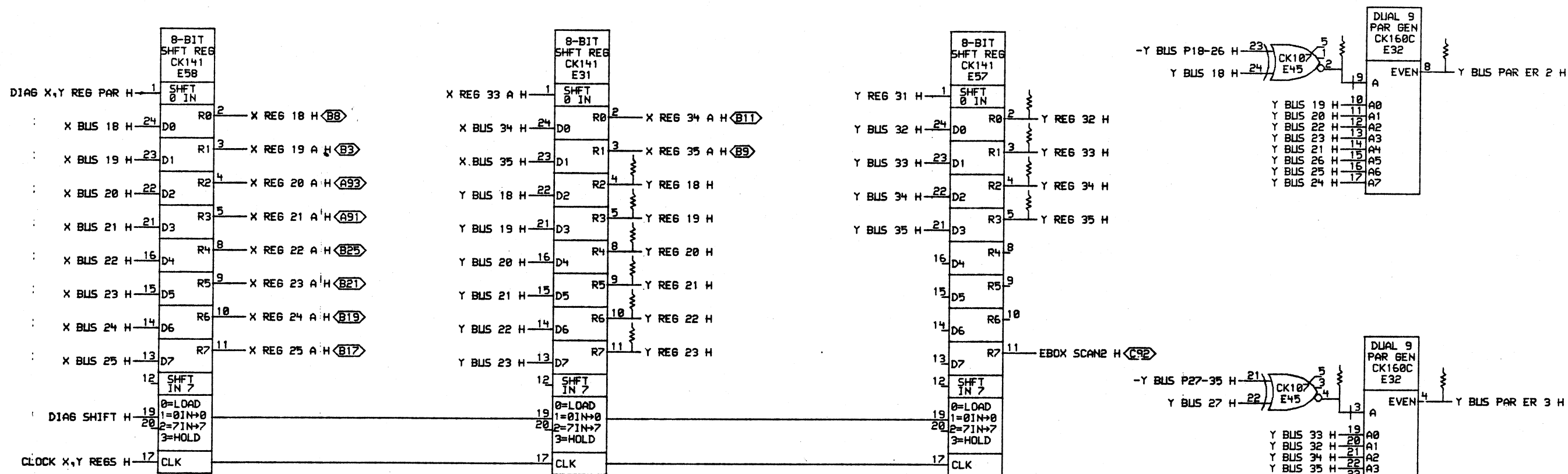


DRN.	DATE	ENG.	DATE	TITLE:	0-7 BIT SHIFT Z REG 08-15
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	
PROJ: B-2000-L0071-REV: 02-PC: SMP2.DRW				20-SEP-82 06:53	NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION/MODEL: 2000				B-DD-L0071-0	SIZE CODE
				D CS	NUMBER
				L0071-0-SHR2	REV.
				*	

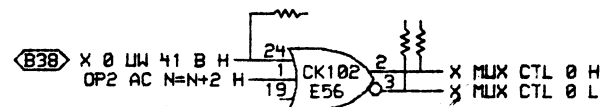
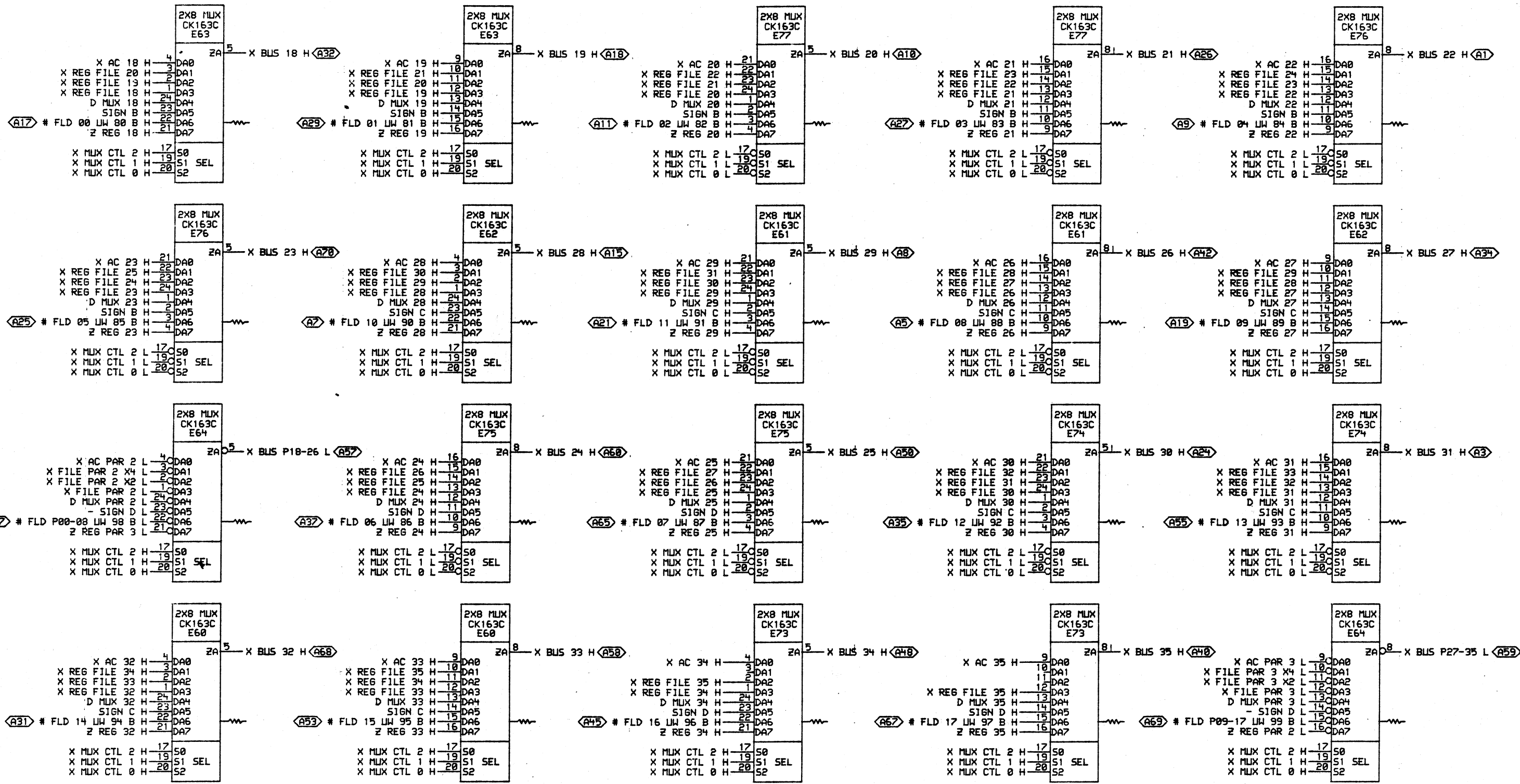


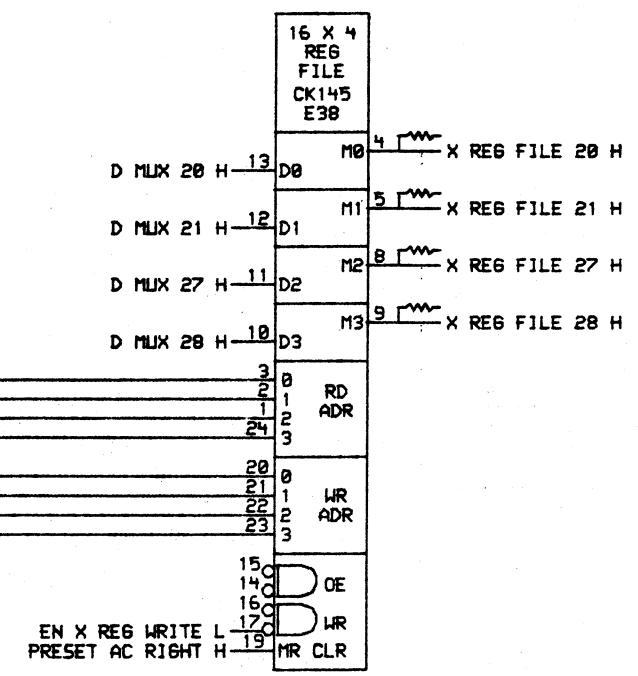
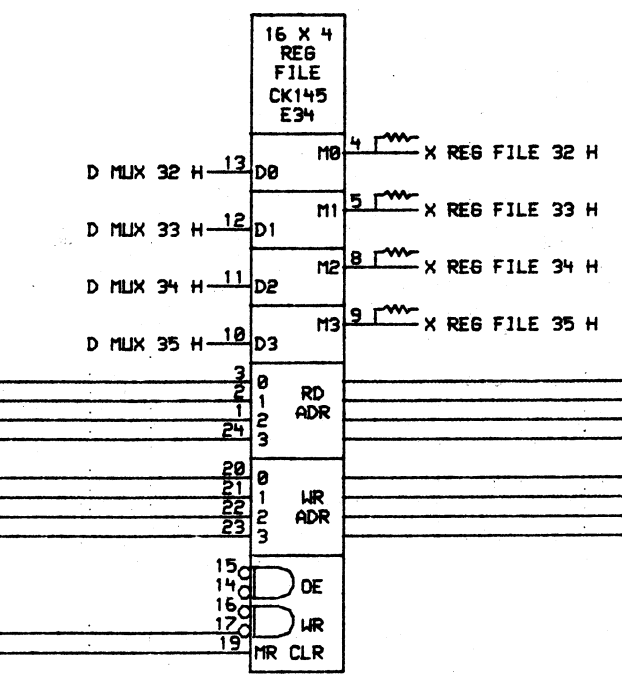
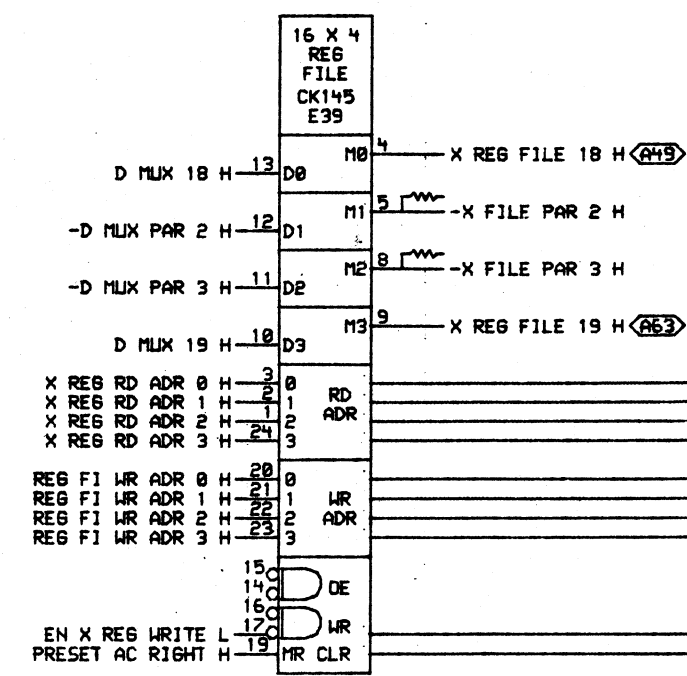
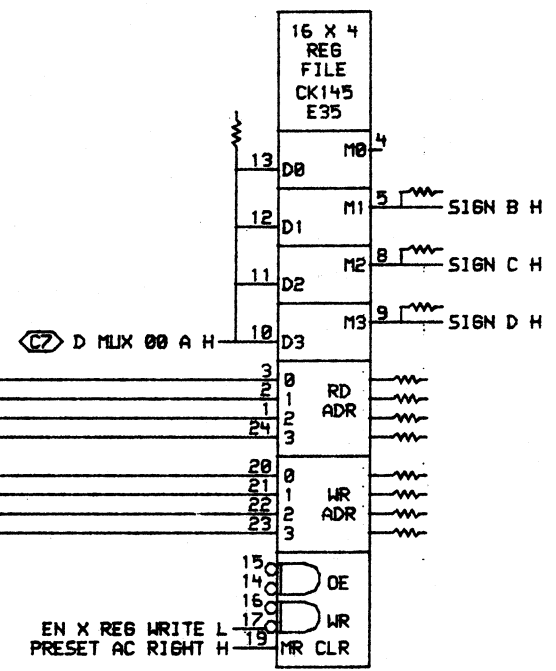
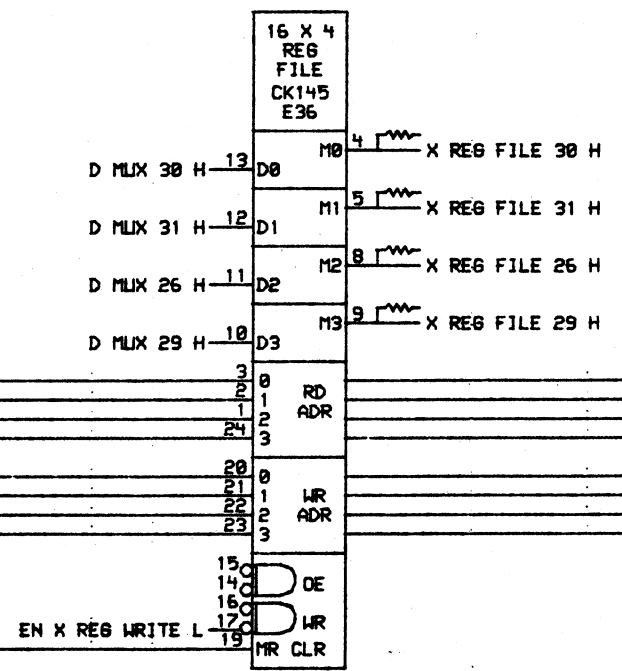
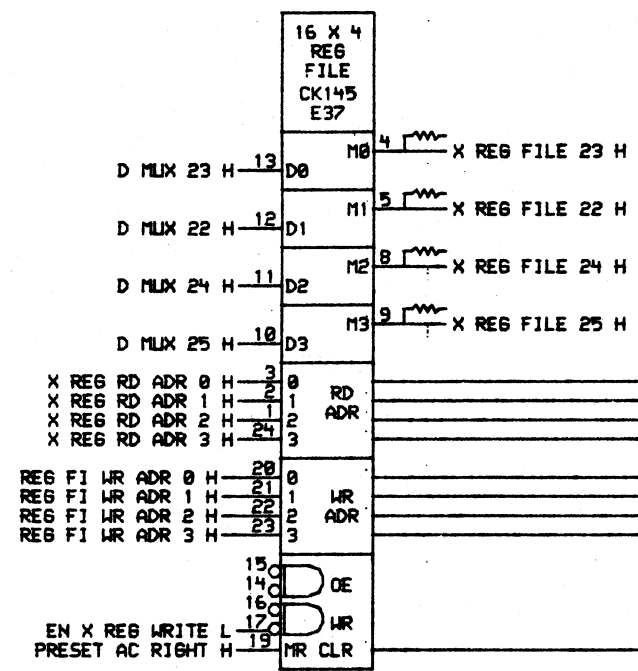
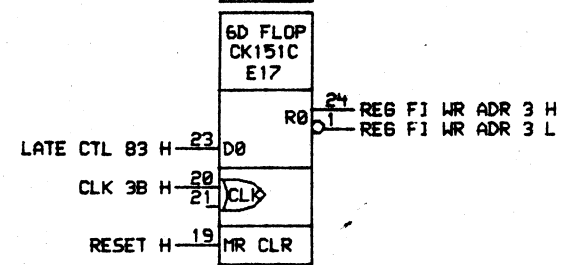
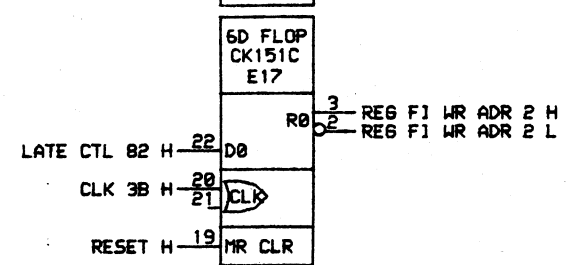
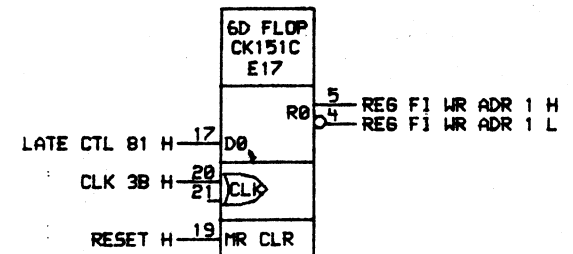
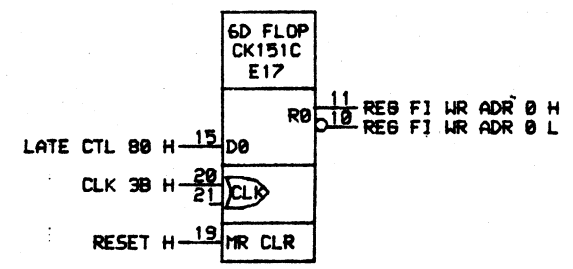
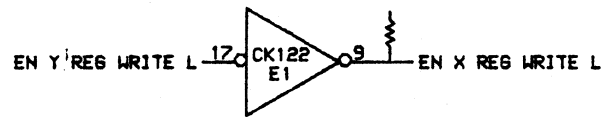
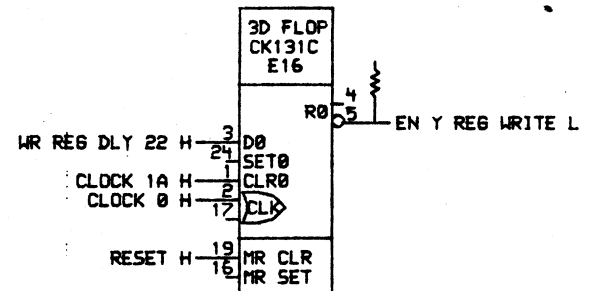
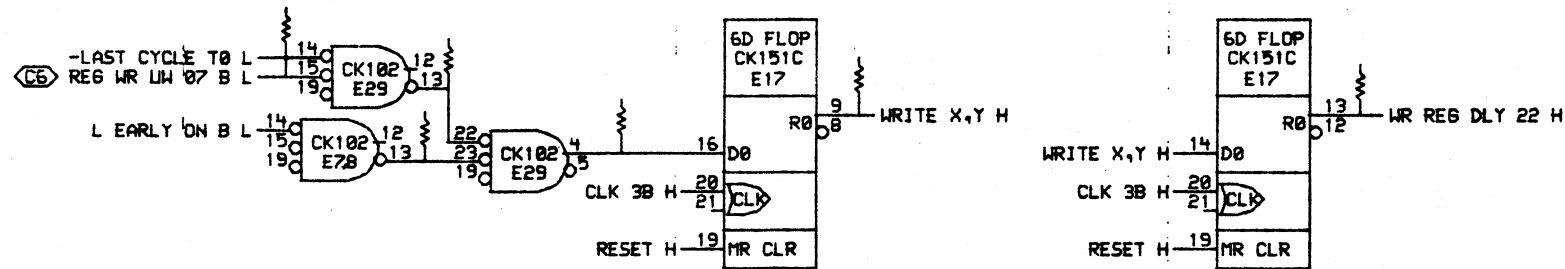
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		0-7 BIT SHIFT
		SHEET	OF	Z REG 16,17
PROTOLIN (2000-1-0071-REV) (2000-1-0071-REV) (20-SEP-82 06:53)	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL: 2080	B-DD-L0071-0	D	CS	L0071-0-SHR3
				REV. *



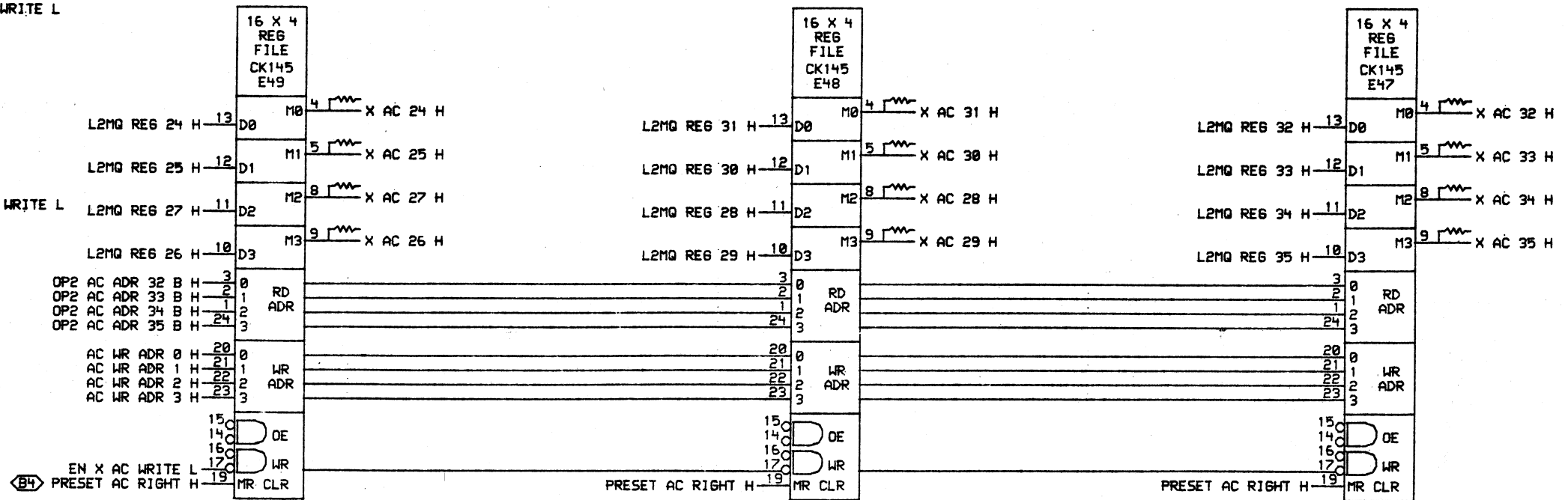
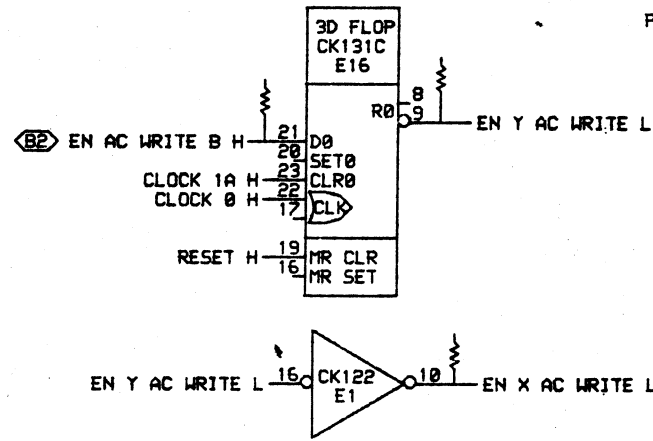
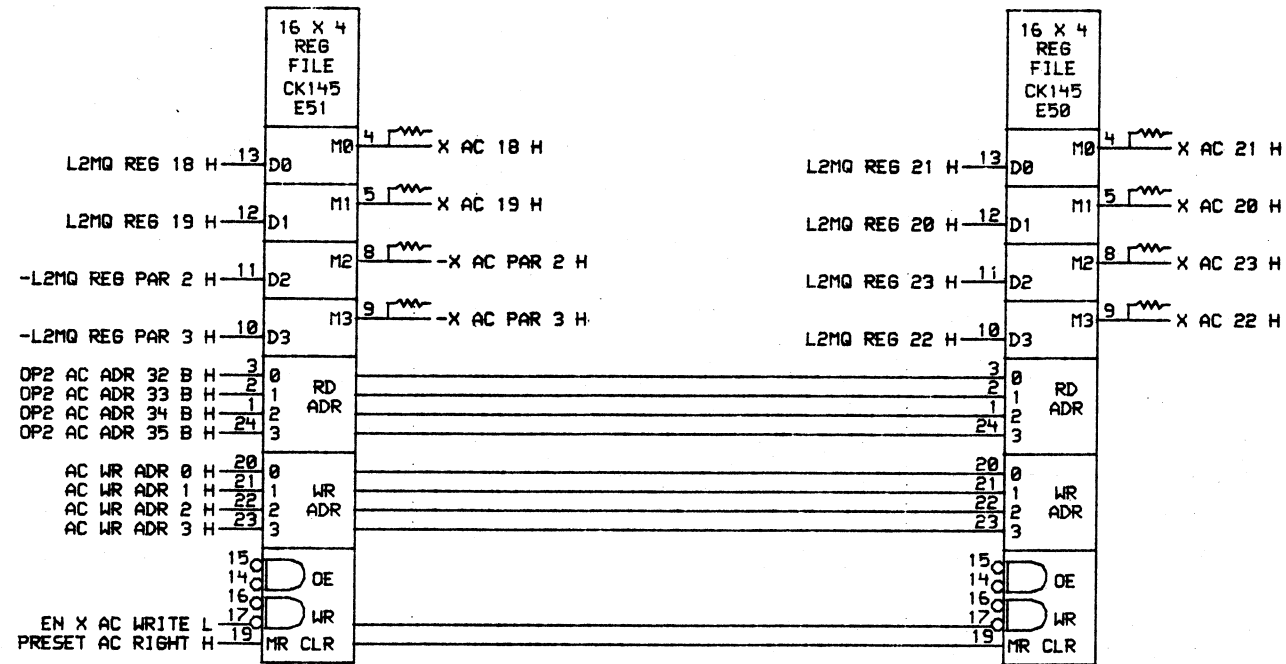


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		X, Y REGS 00-17
		SHEET	OF	X, Y DIAG SCAN
PROTOL (P000) L0071 REV C (S485) DRN	20-SEP-82 06:53	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0071-0	D CS	L0071-0-SHR5
				REV. *

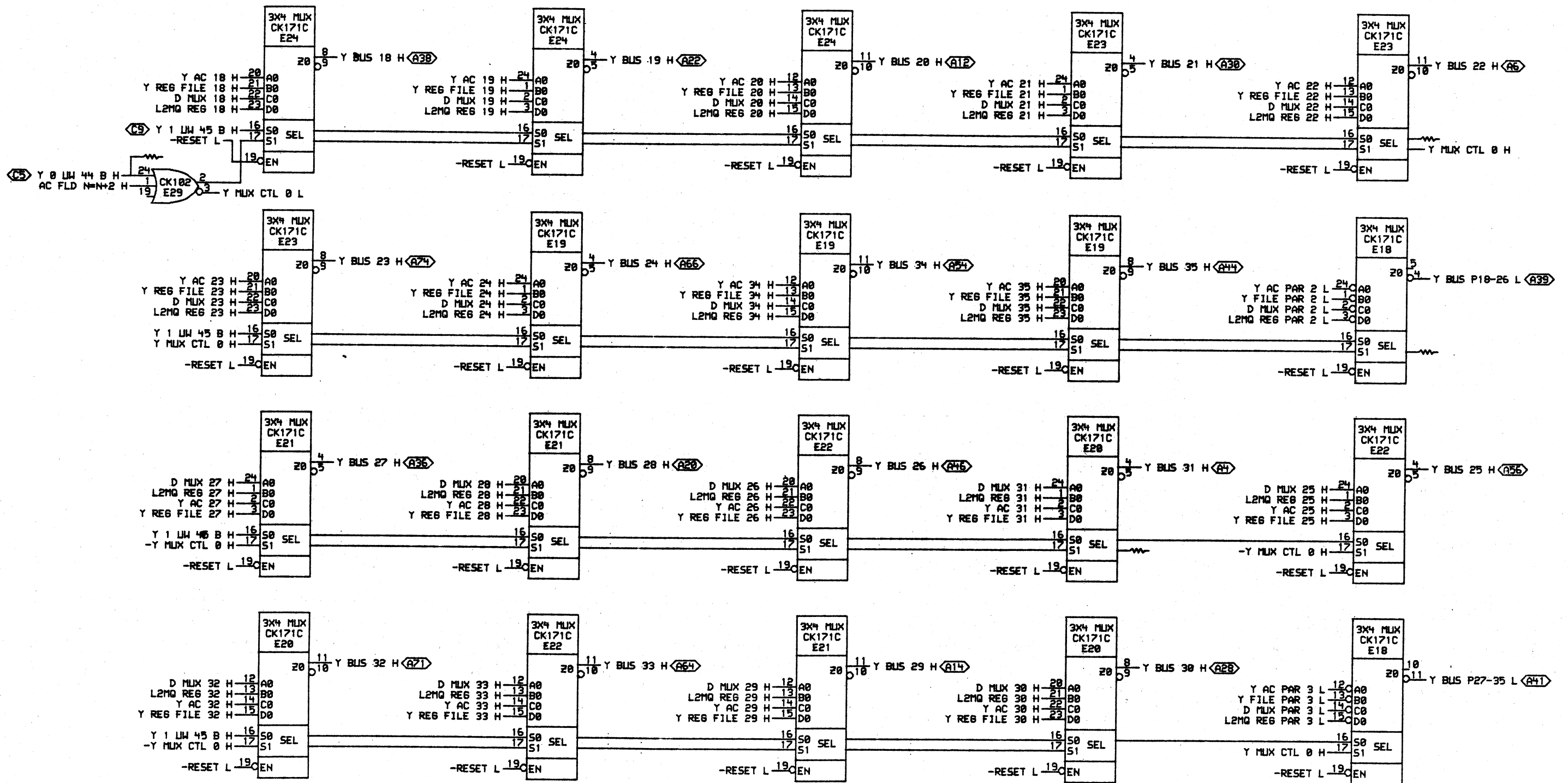




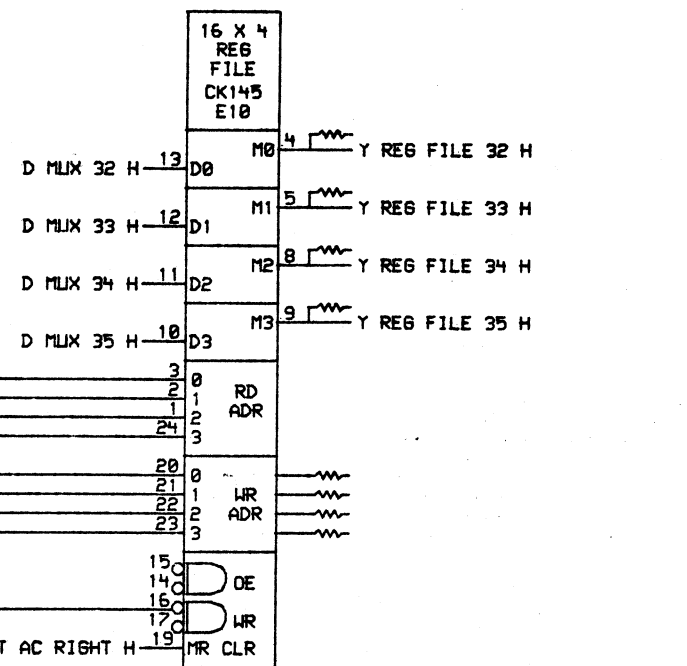
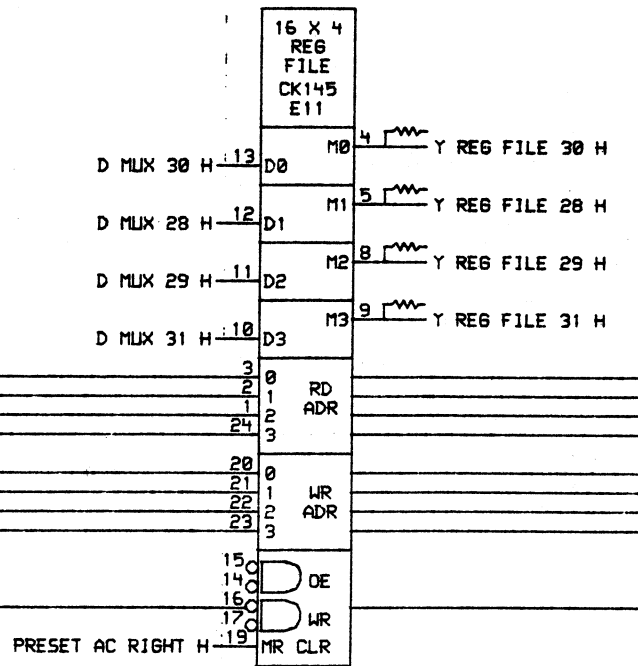
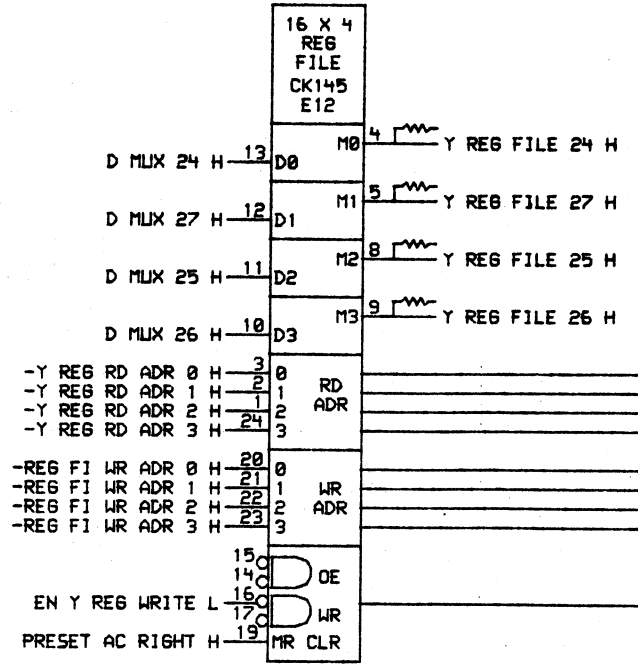
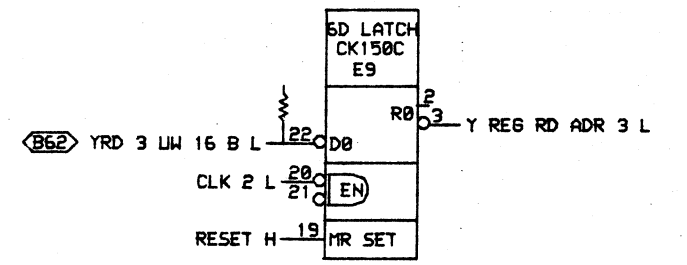
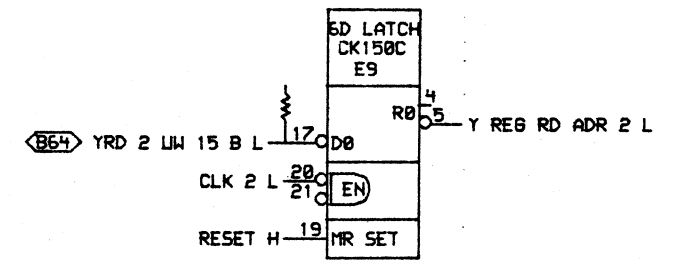
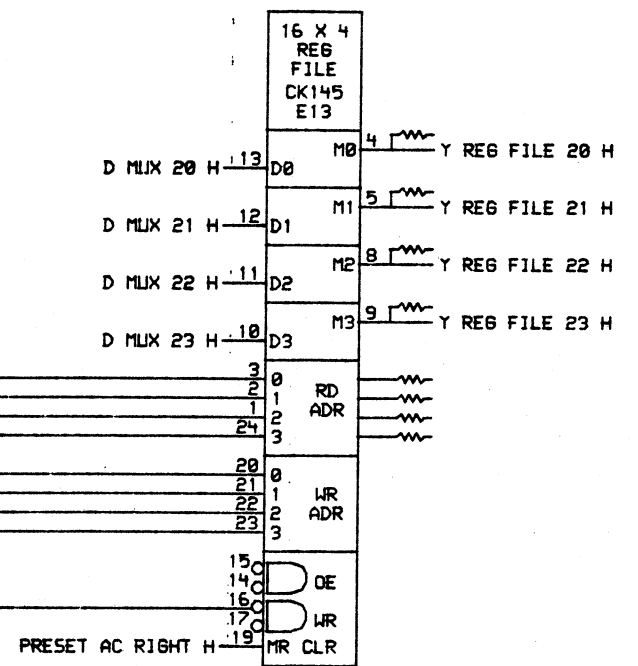
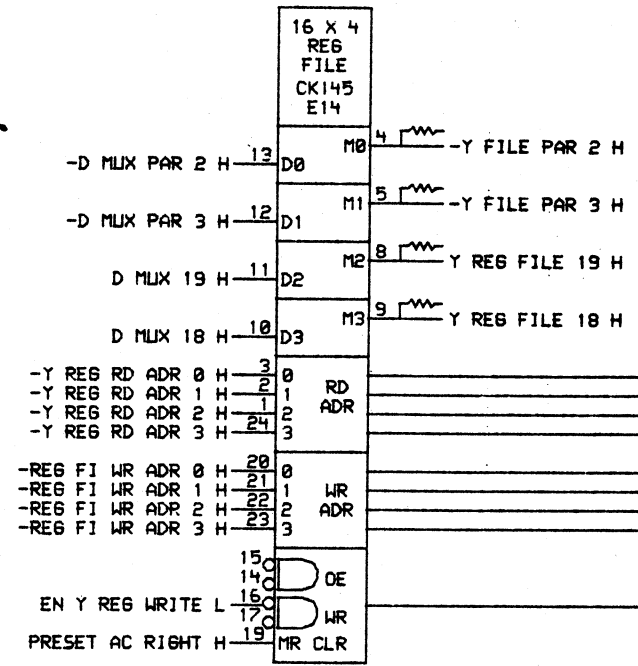
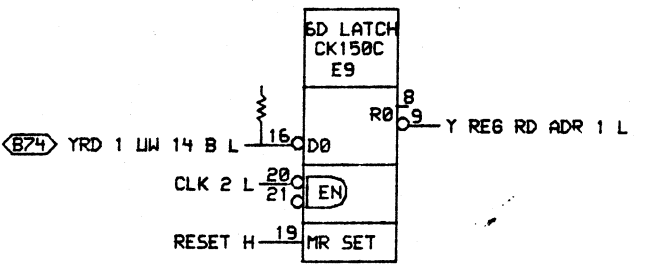
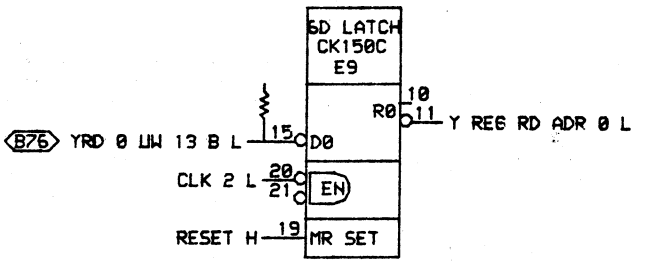
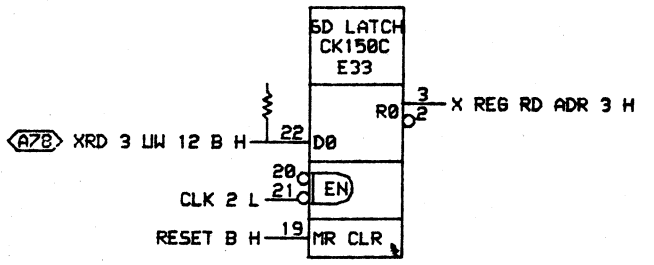
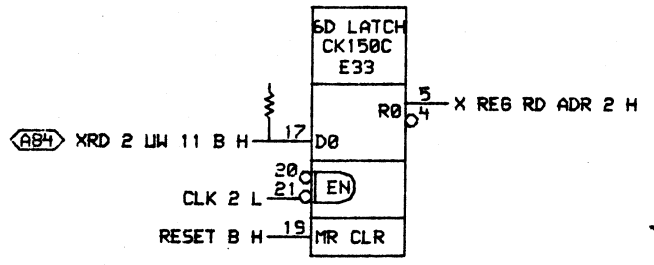
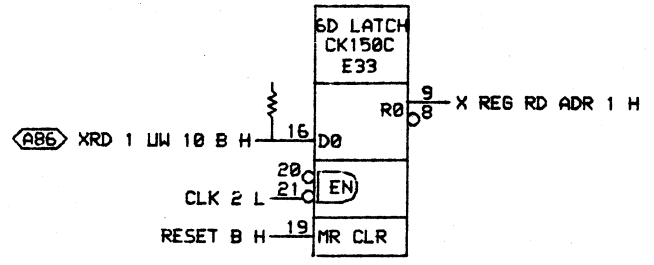
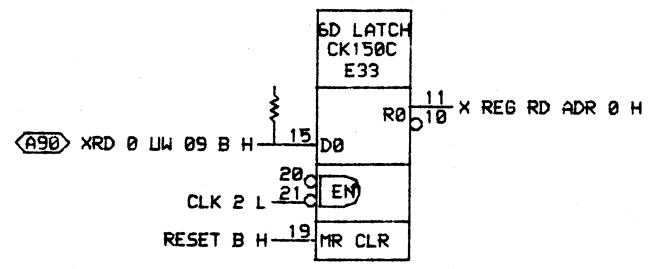
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL: 2080				B-DD-L0071-0
SIZE	CODE	NUMBER	REV.	
D	CS	L0071-0-SHR7	*	



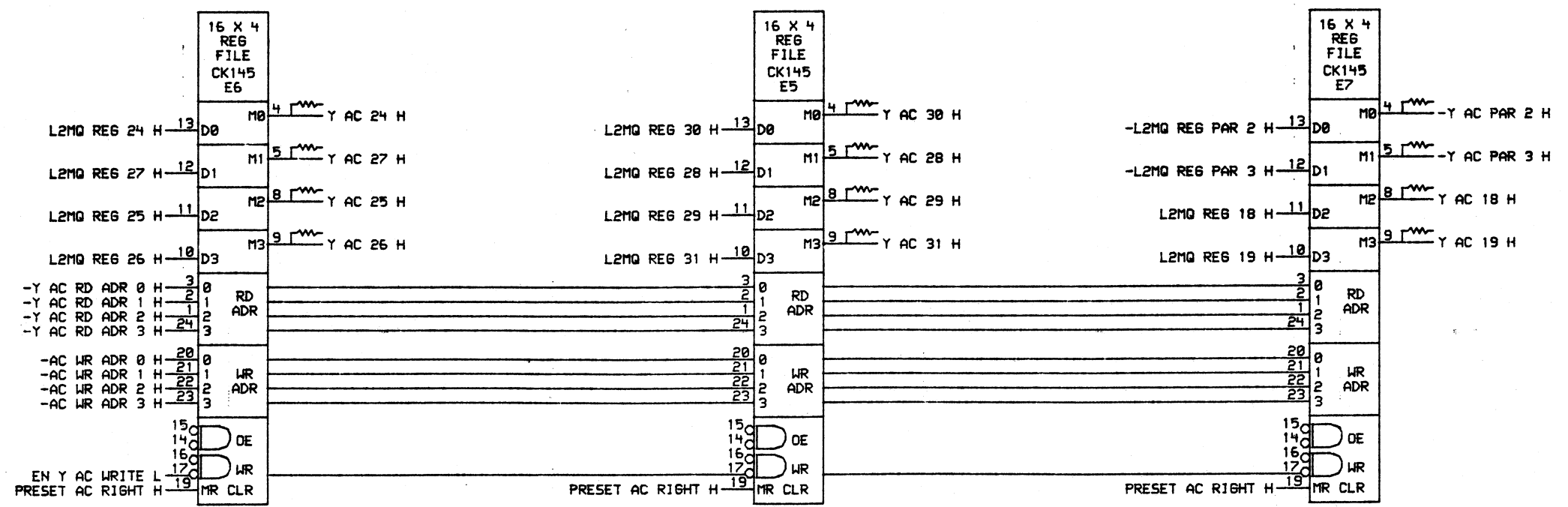
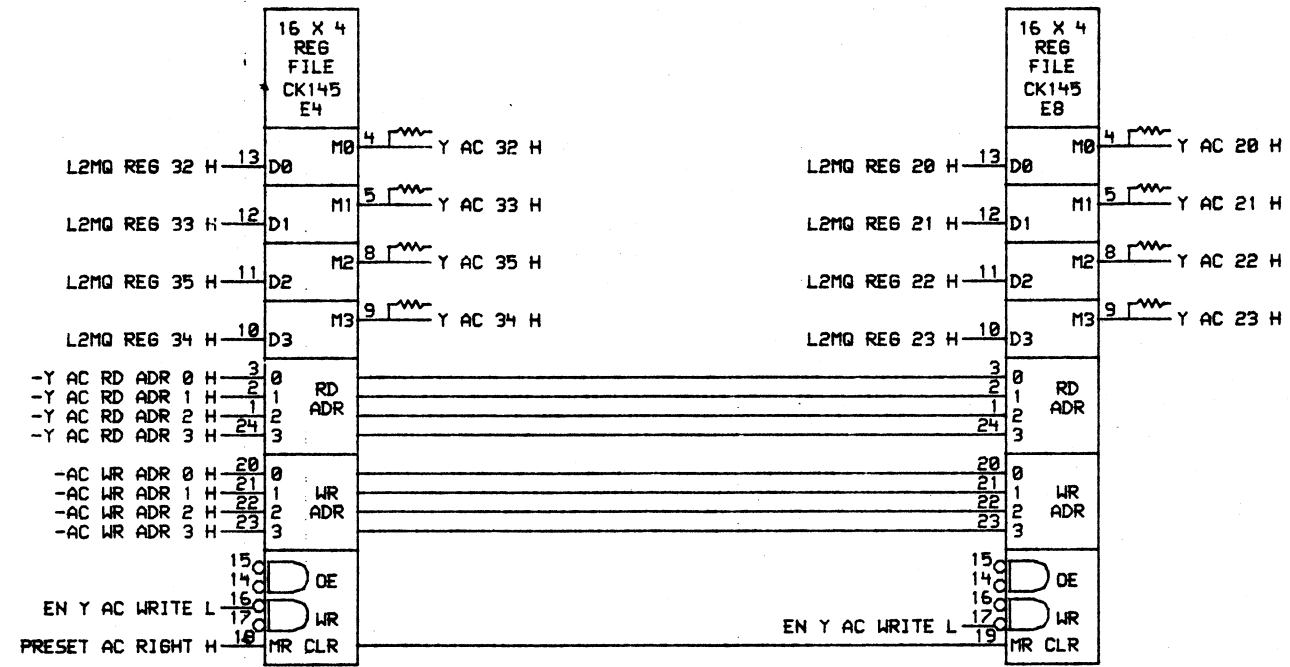
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL: 2080				20-SEP-82 06:54
NEXT HIGHER ASSEMBLY: B-DD-L0071-0				1
SIZE	CODE	NUMBER	REV.	
D	CS	L0071-0-SHR8	*	



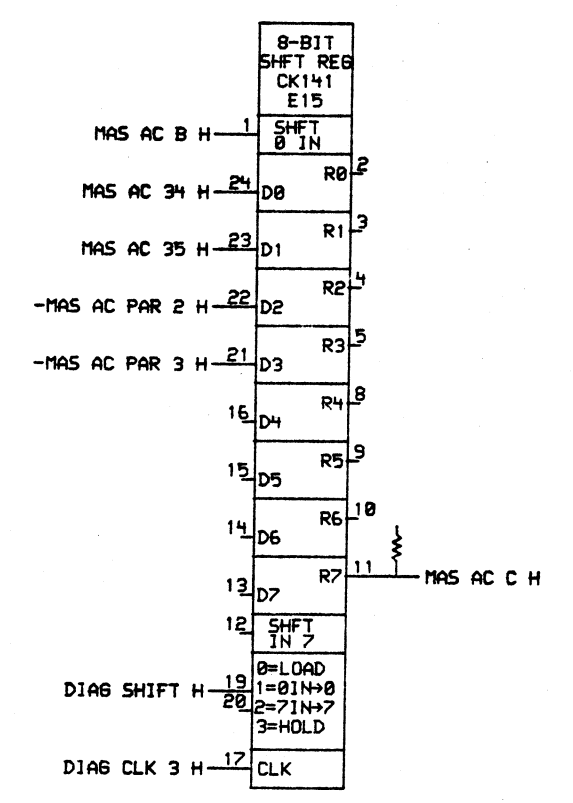
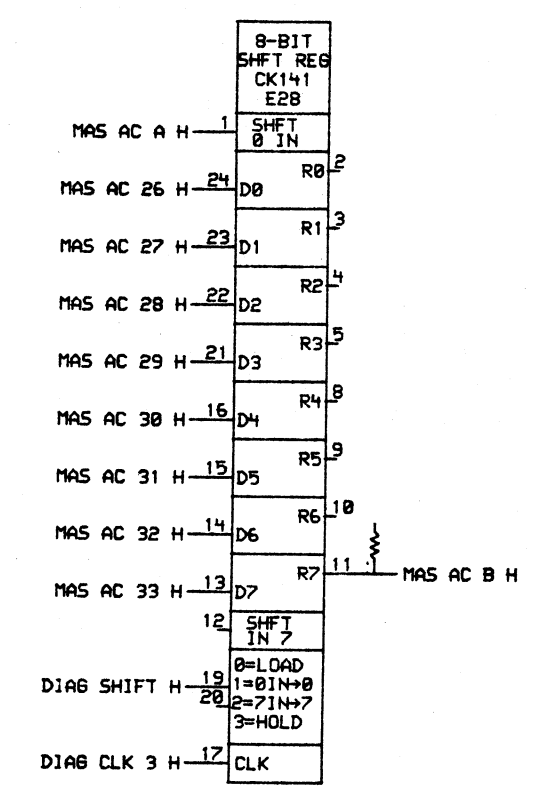
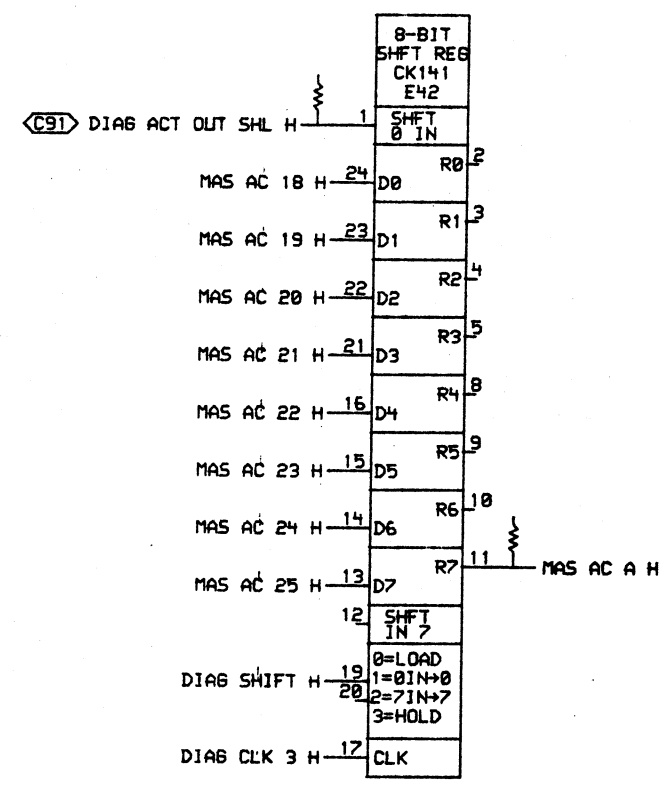
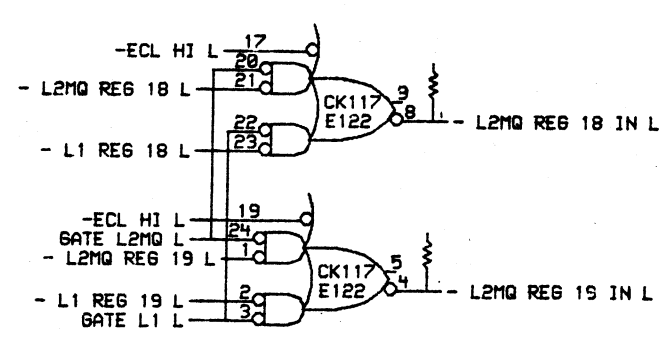
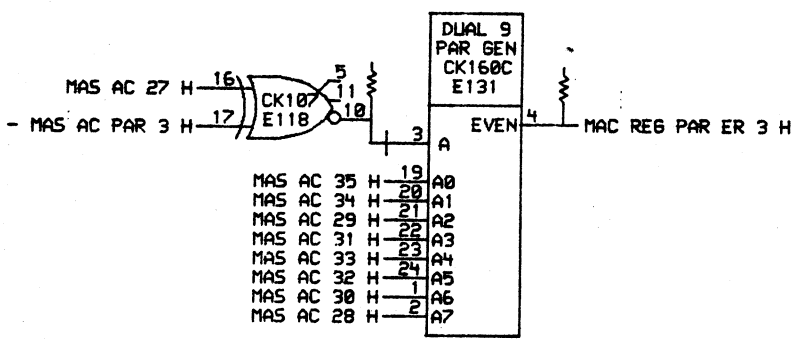
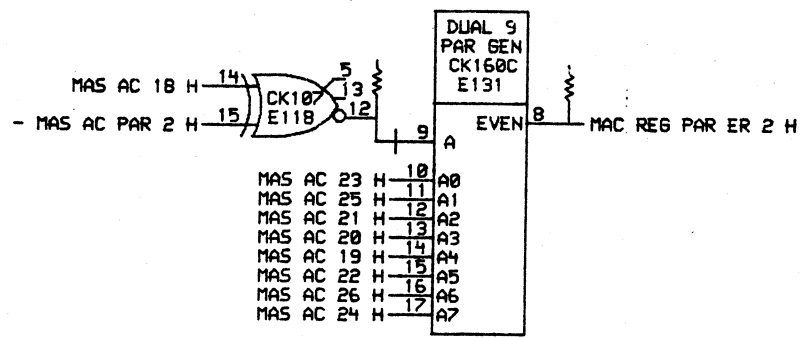
DRN.	DATE	ENB.	DATE	TITLE:
CHK'D.	20-SEP-82			Y MUX 00-17
		BOARD LOCATION:		
		SHEET	OF	
FIRST USED ON OPTION/MODEL:	2080	NEXT HIGHER ASSEMBLY:	B-DD-L0071-0	SIZE CODE
		NUMBER	L0071-0-SHR9	REV. *



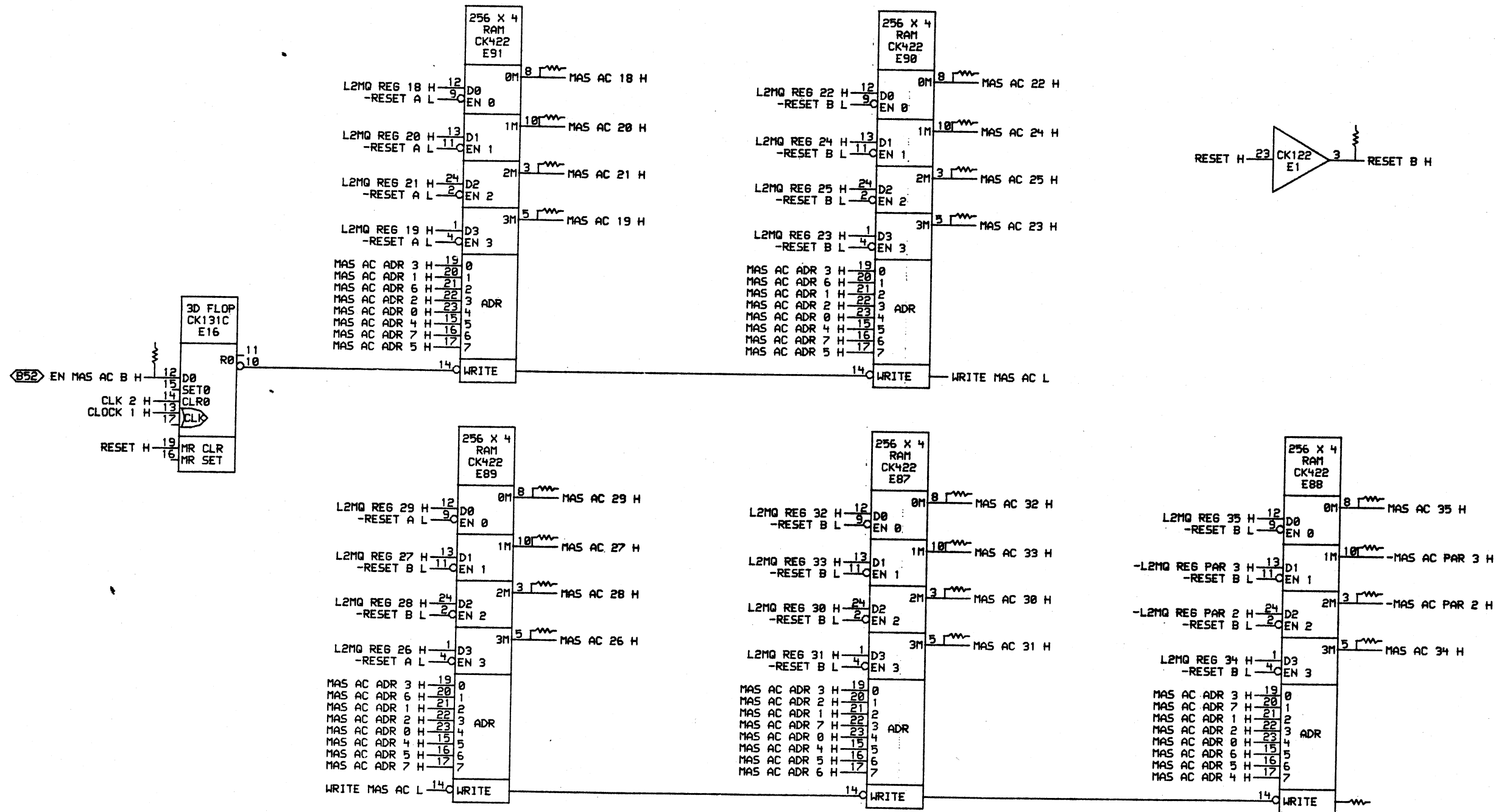
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	Y REG FILE	
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0071-0		BYTES 0,1
SIZE	CODE	NUMBER	REV.	
D	CS	L0071-0-SHRA	*	



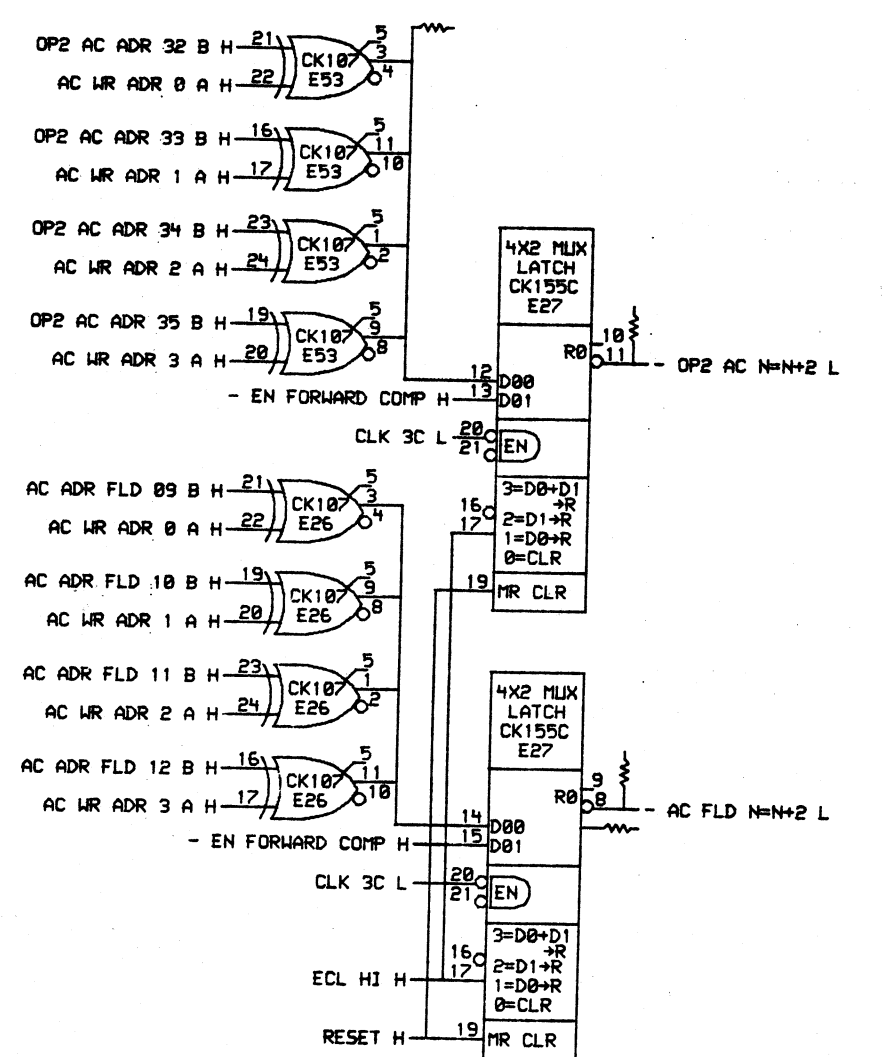
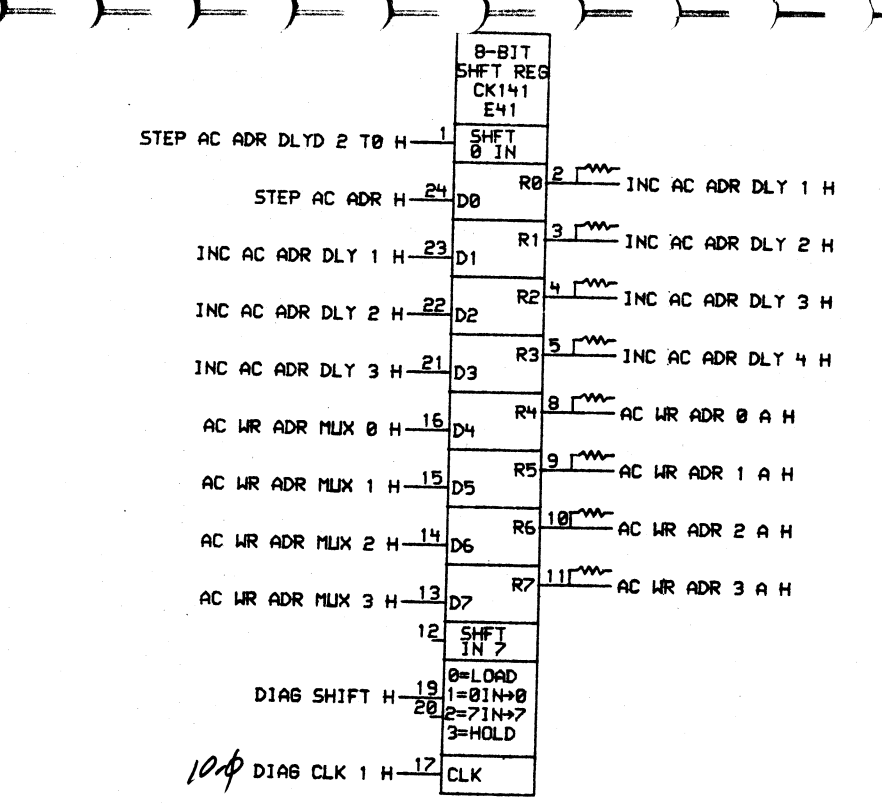
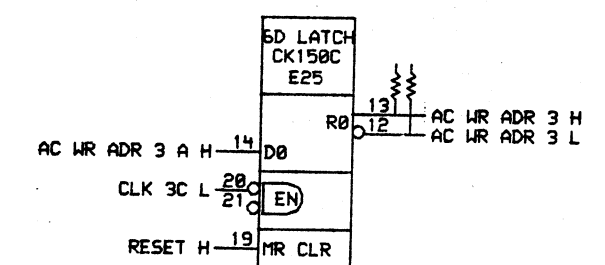
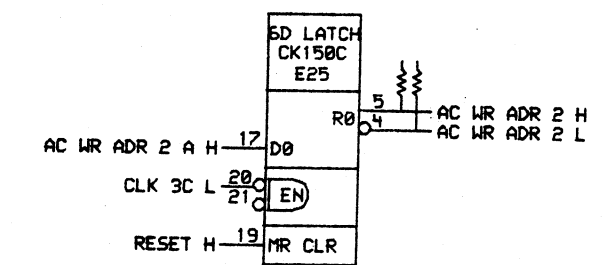
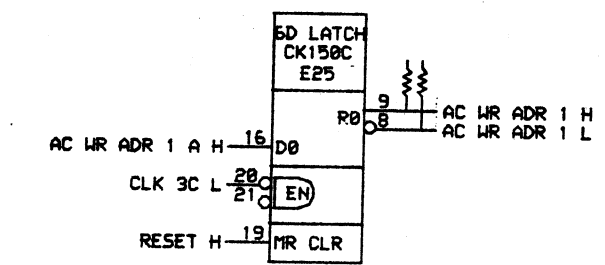
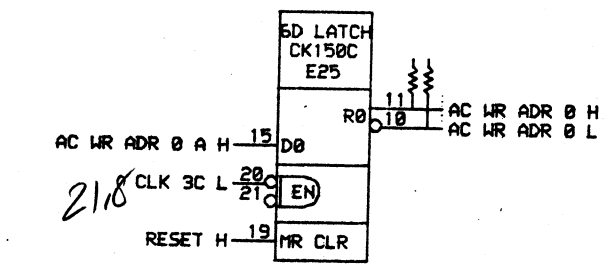
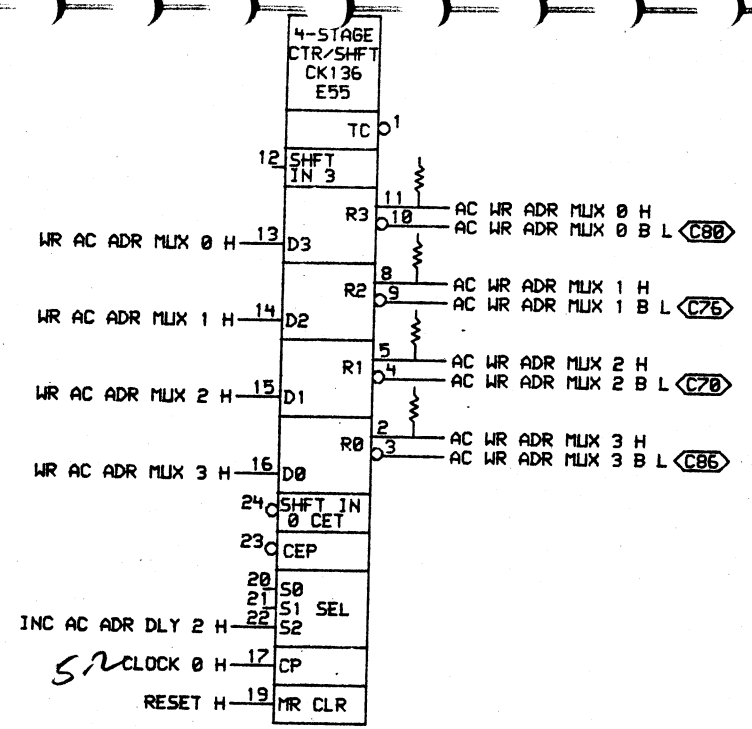
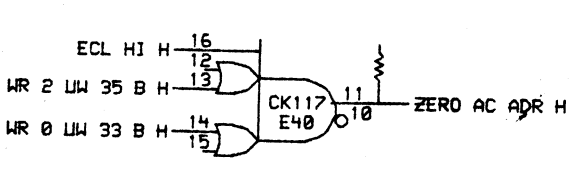
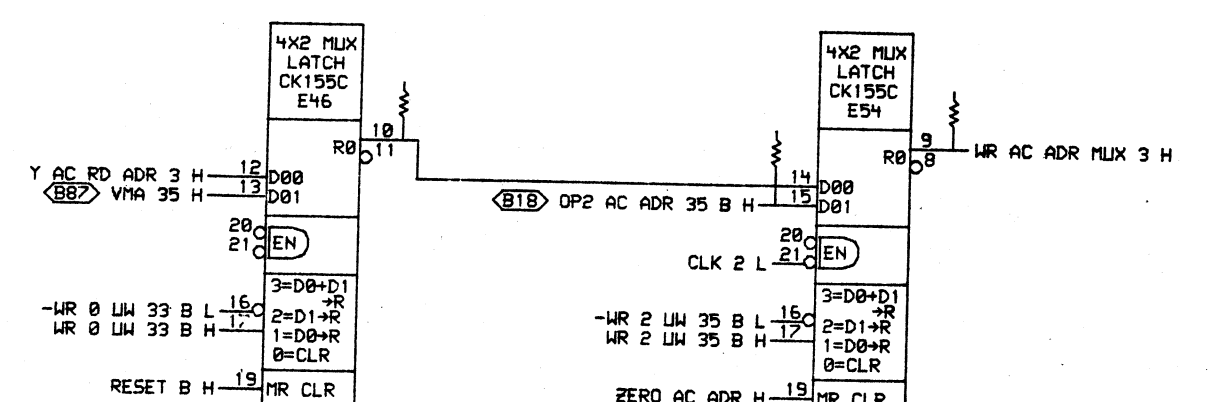
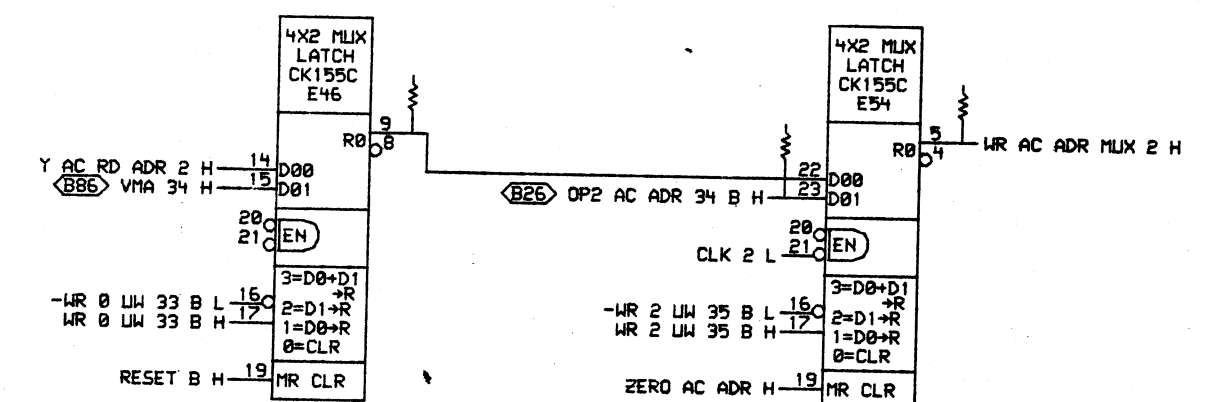
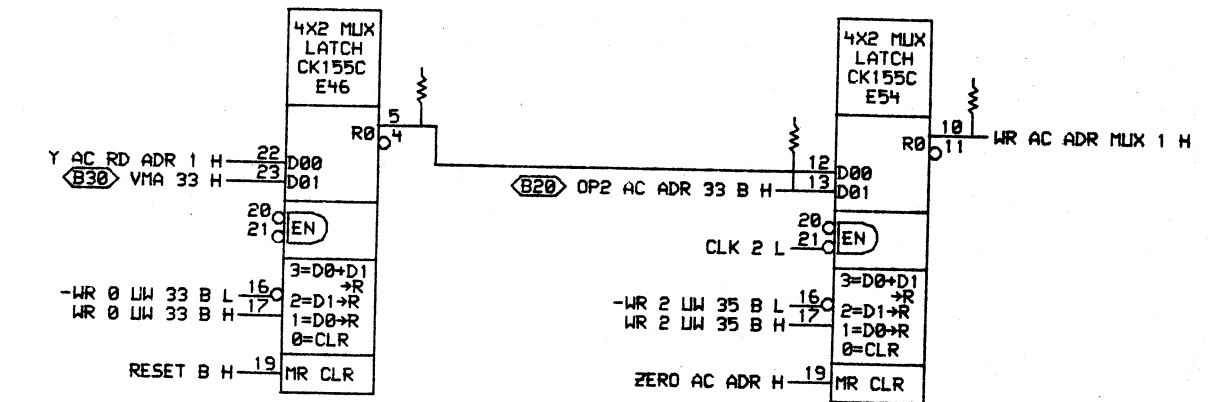
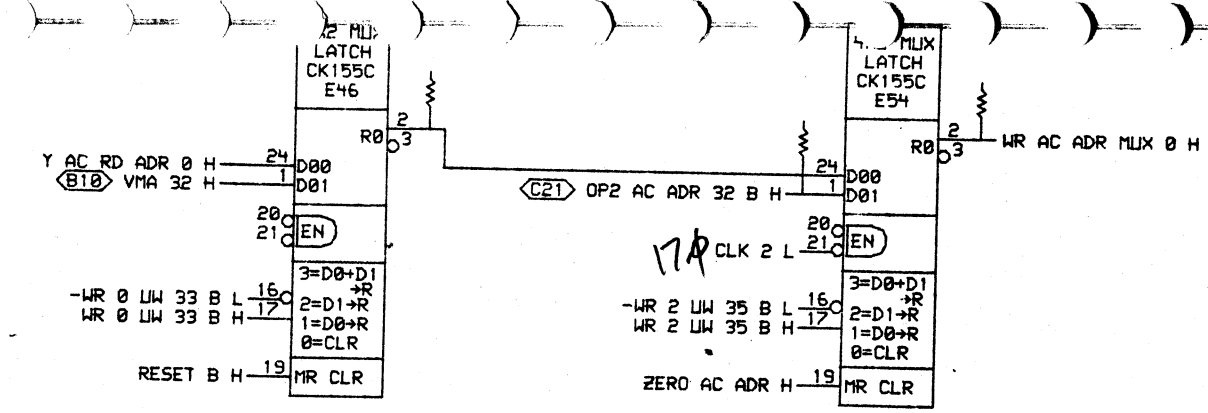
DRN.	DATE	ENG.	DATE	TITLE:	Y AC FILE		
CHK'D.	23-SEP-82	BOARD LOCATION:		BYTES 0,1			
SHEET 1 OF 1				SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: 2080				D	CS	L0071-0-SHRB	*



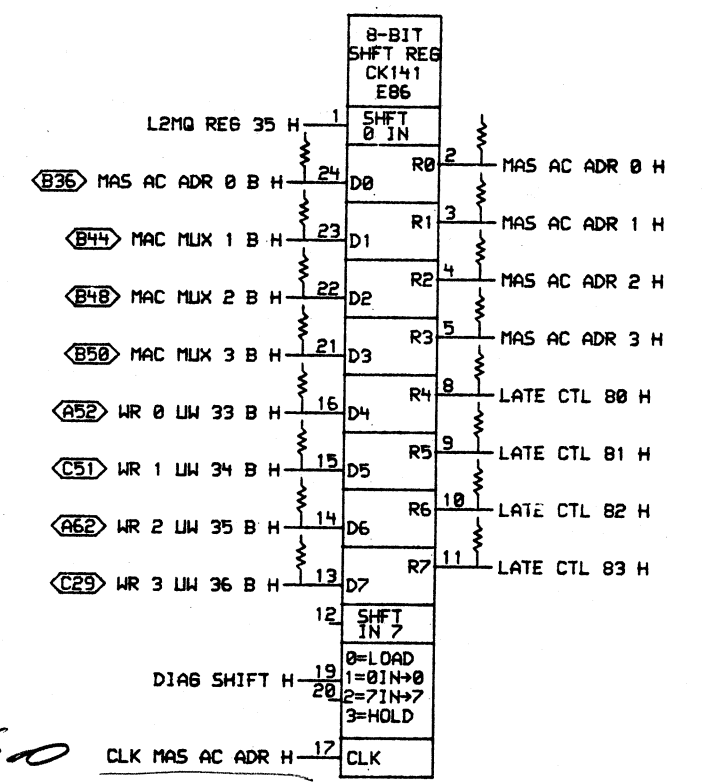
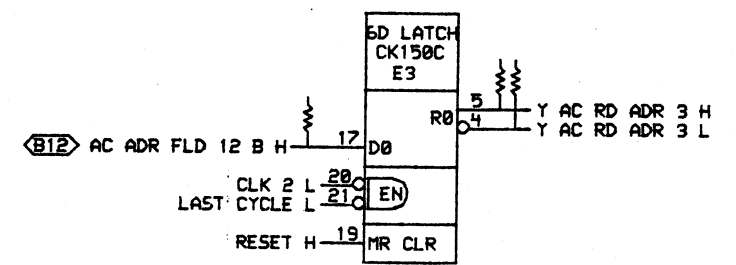
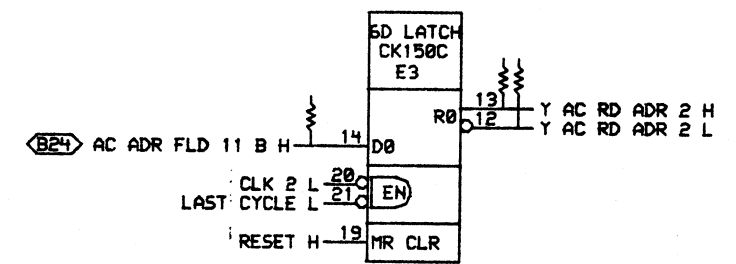
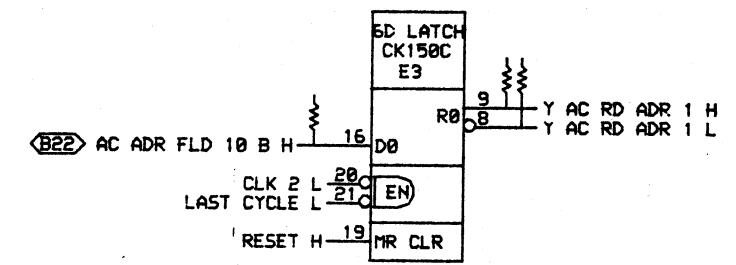
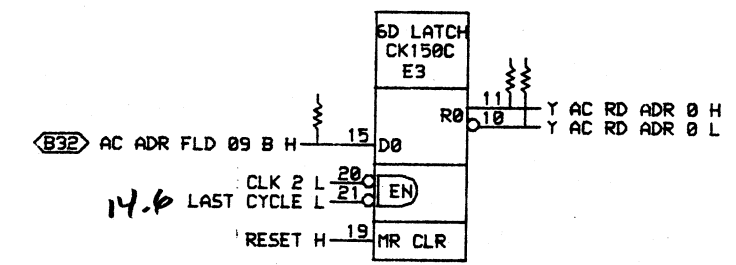
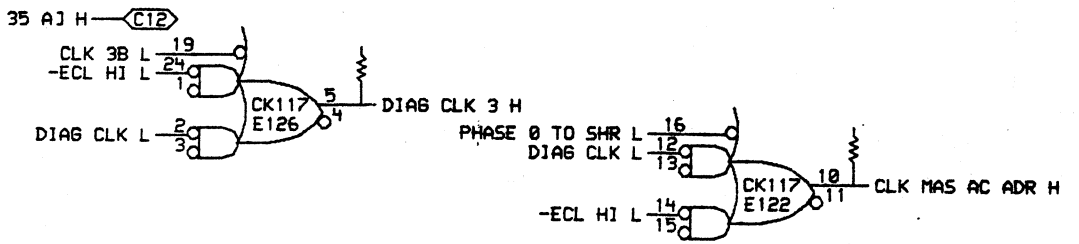
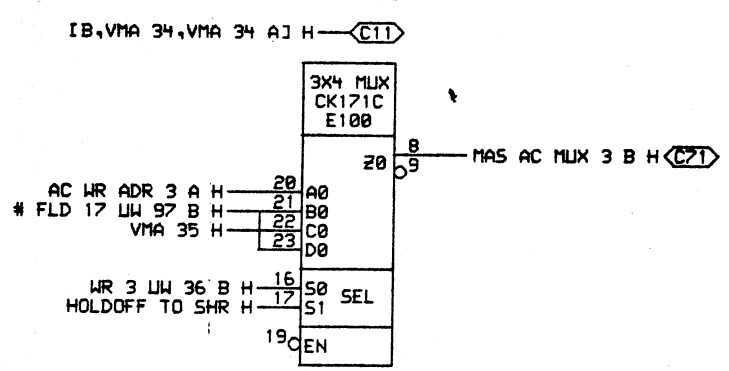
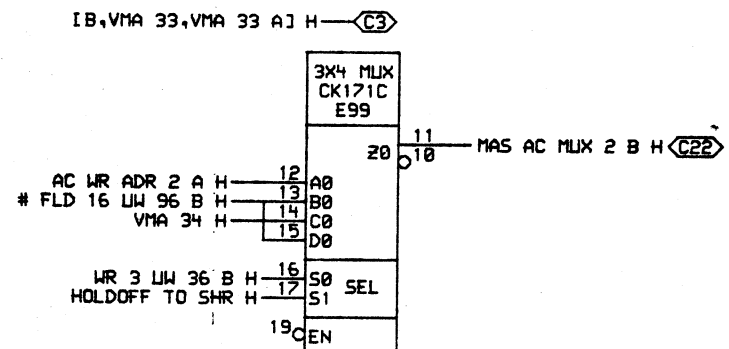
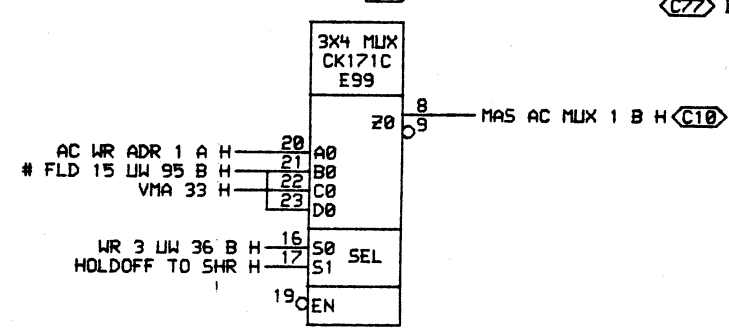
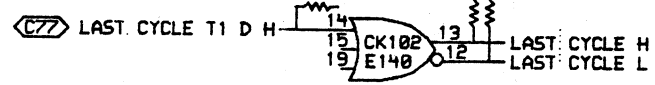
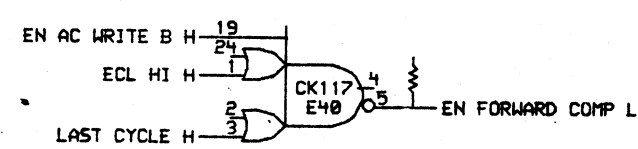
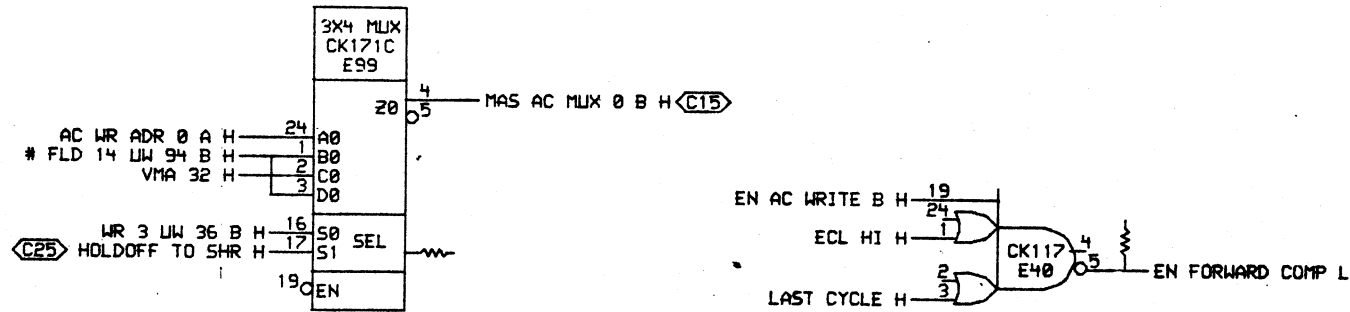
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	23-SEP-82			MAC REG BY 0,1
	DATE	BOARD LOCATION:		PC, L2MQ 0,1 IN
		SHEET	OF	
FIRST USED ON OPTION/MODEL: 2080		NEXT HIGHER ASSEMBLY: B-DD-L0071-0		SIZE CODE
				D CS
				NUMBER
				L0071-0-SHRC
				REV.
				*



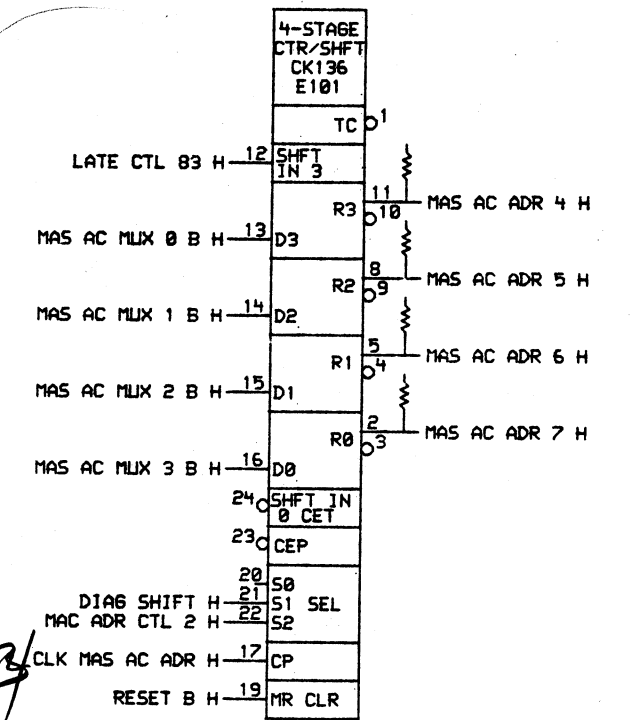
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		MASTER AC
		SHEET	OF	BYTES 0,1
PROJ. NO. (2000) L0071-REV. (S) SHRD. DRN	20-SEP-82 06:54	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 2080	B-DD-L0071-0		D	CS
			NUMBER	REV.
			L0071-0-SHRD	*



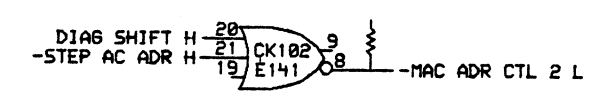
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL: 2080				B-DD-L0071-0
SIZE	CODE	NUMBER	REV.	*
D	CS	L0071-0-SHRE		



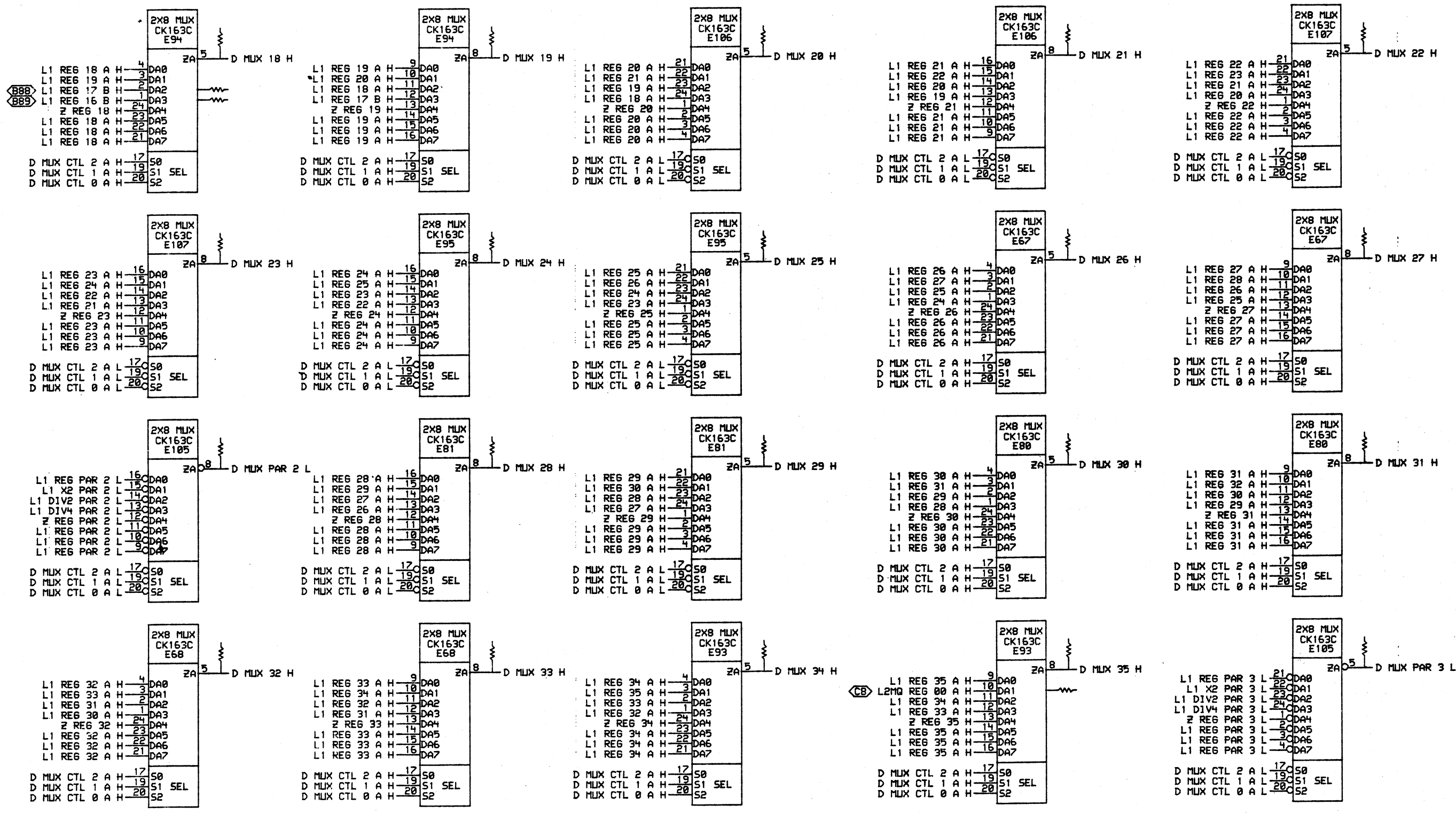
60



5-7



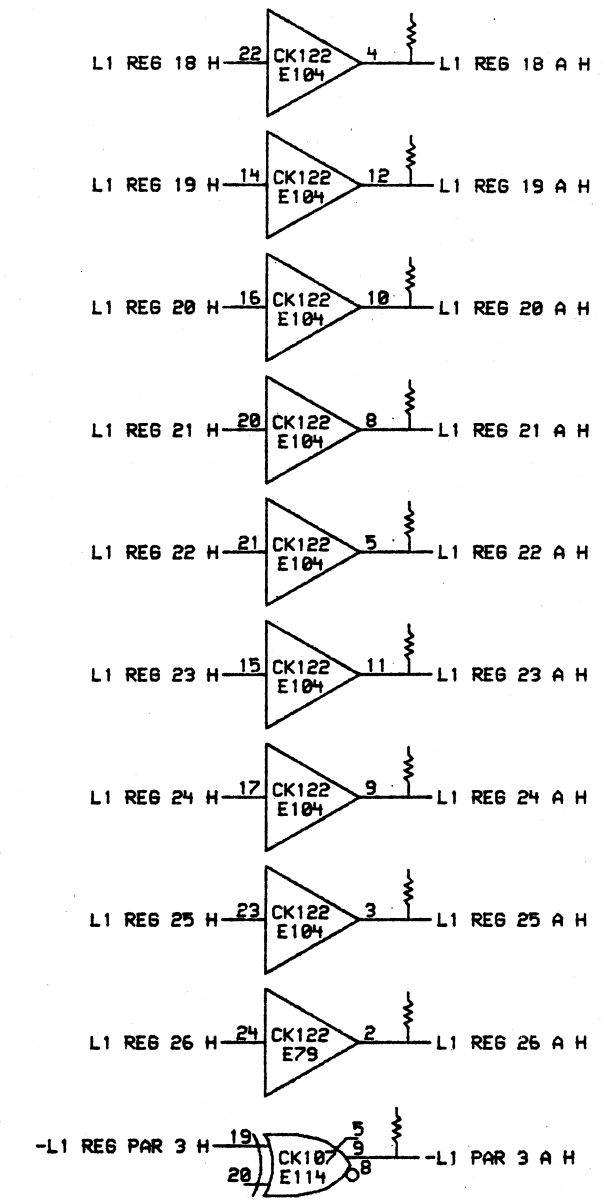
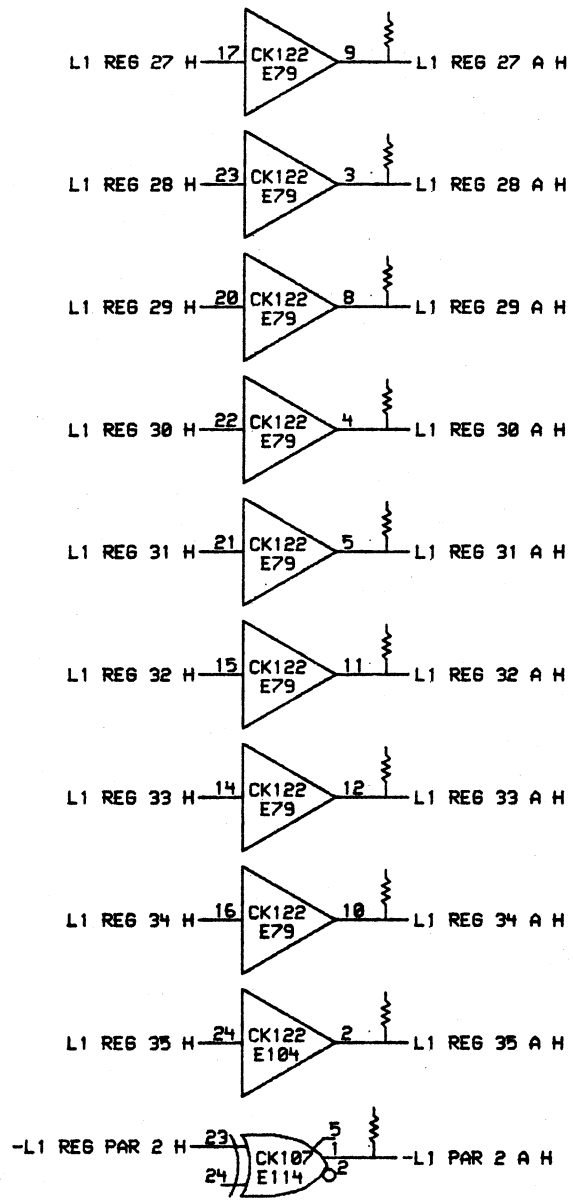
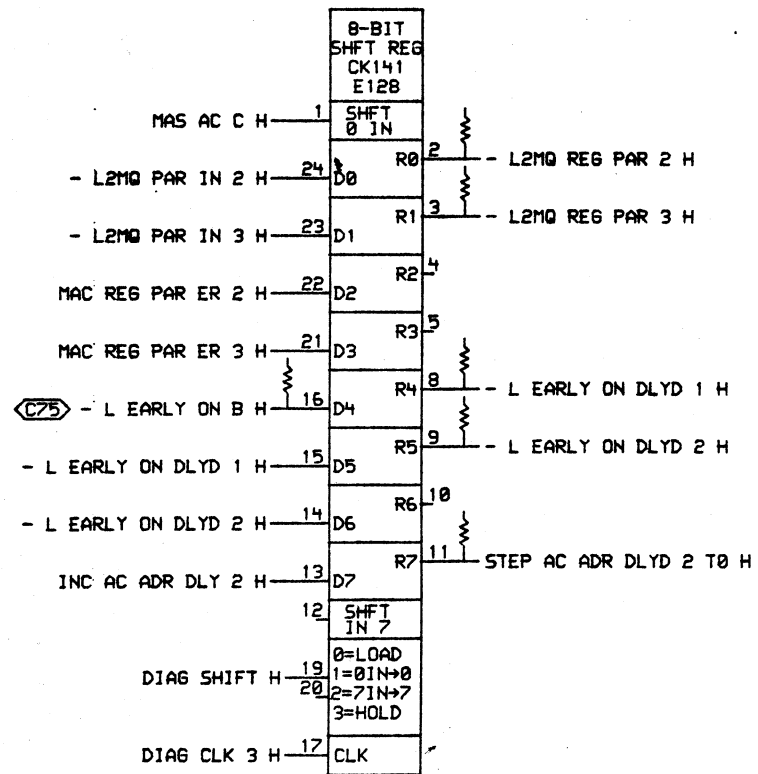
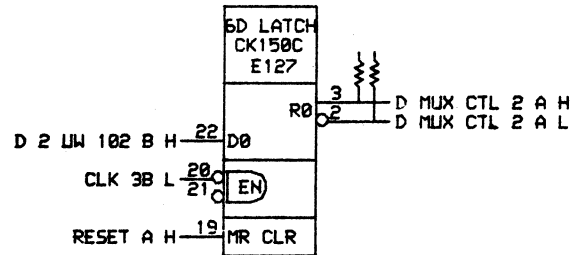
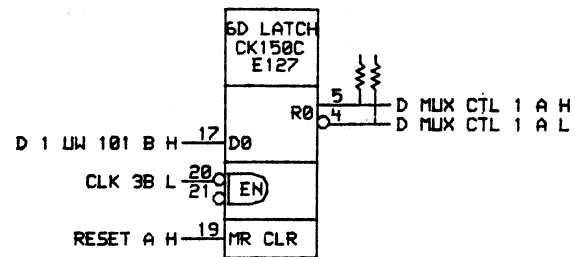
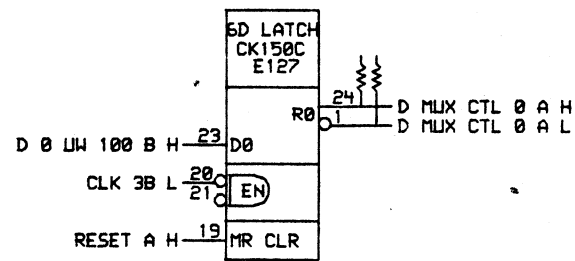
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		Y AC ADDR MUX
		SHEET	OF	MAC WR MUX
PROTOK (2000) L0071.REV.CDCSHR.DRN	20-SEP-82 06:54	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0071-0	D	CS
			NUMBER	REV.
			L0071-0-SHRF	*



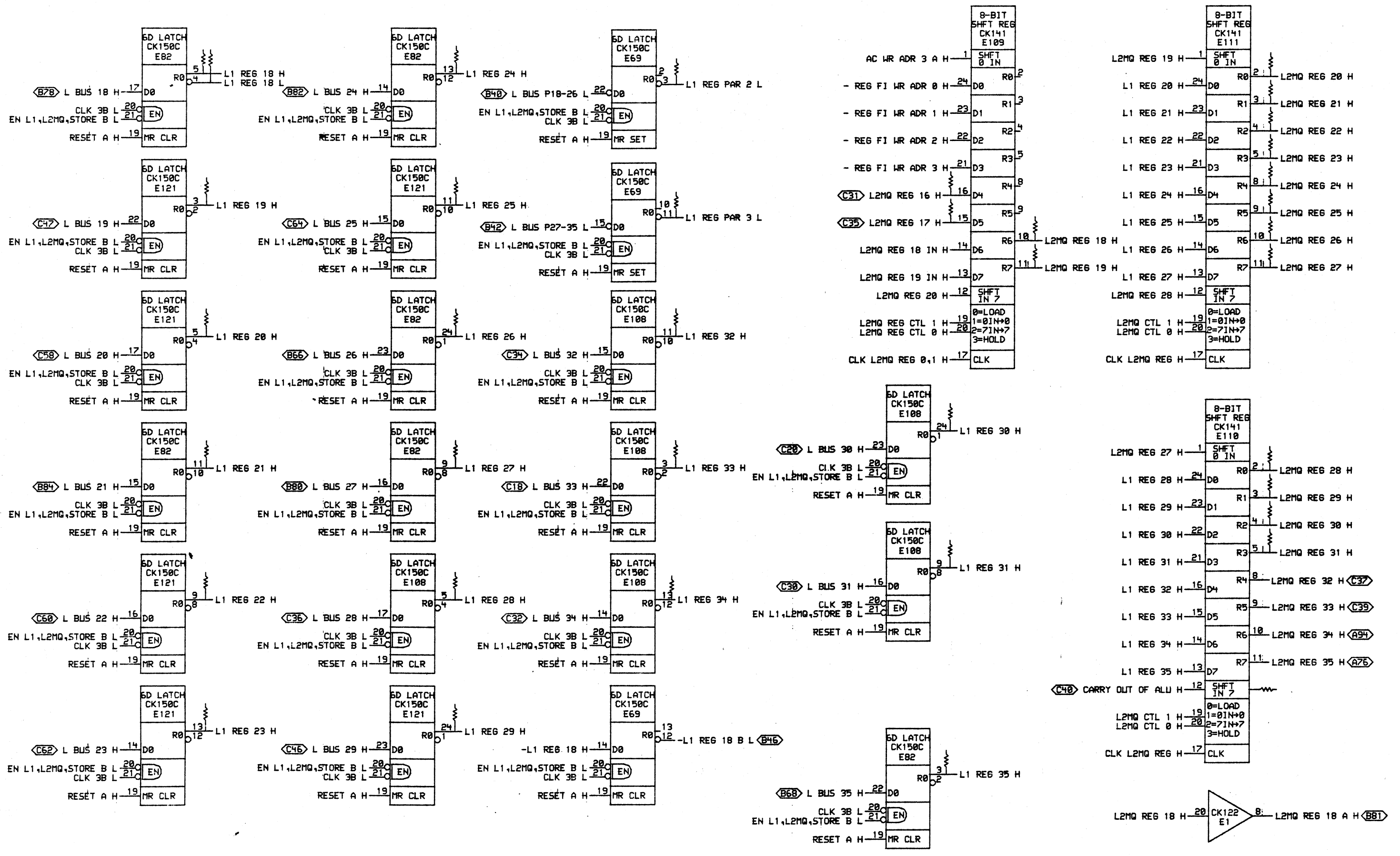
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		D MUX 00-17
		SHEET	OF	
PROTOLIB (C8000.L0071.REV0)PCSHRG.DRN	20-SEP-82 06:55	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL:	2080	B-DD-L0071-0	D	CS
			NUMBER	REV.
			L0071-0-SHRG	*

D
C
V
B
A

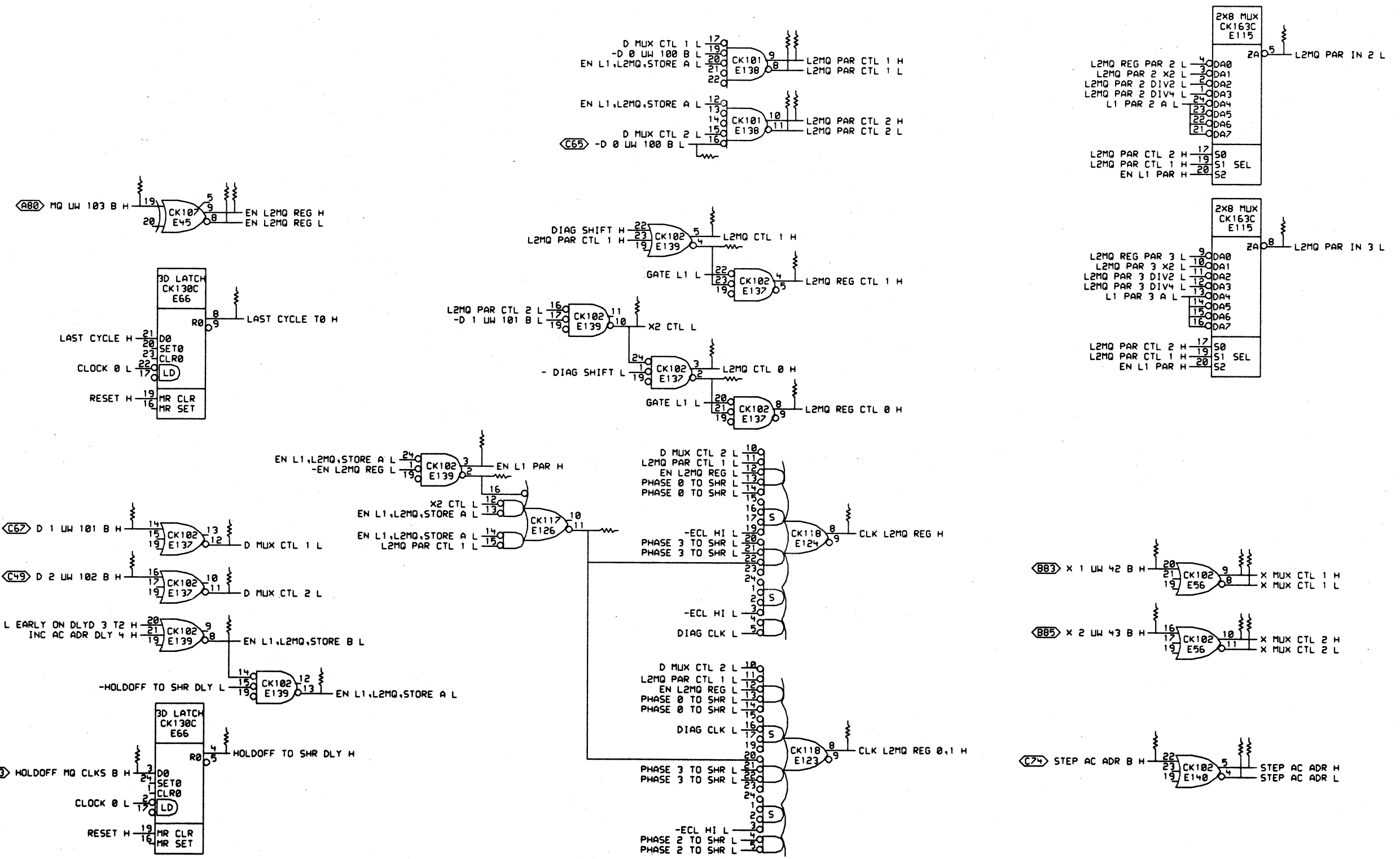
D
C
B
A



DRN.	DATE	ENG.	DATE	TITLE:	SCAD MUX		
CHK'D.	DATE	BOARD LOCATION:					
FIRST USED ON OPTION/MODEL: 2000		NEXT HIGHER ASSEMBLY: B-DD-L0071-0	SIZE	CODE	NUMBER	REV.	
			D	CS	L0071-0-SHRH	*	



DRN.	DATE	ENG.	DATE	TITLE:	L1, L2MQ REGS		
CHK'D.	23-SEP-82	BOARD LOCATION:		BYTES 0,1			
	DATE	SHEET	OF	SIZE	CODE	NUMBER	REV.
	20-SEP-82 06:55	1	1	D	CS	L0071-0-SHRI	*
FIRST USED ON OPTION/MODEL: 2080				NEXT HIGHER ASSEMBLY: B-DD-L0071-0			

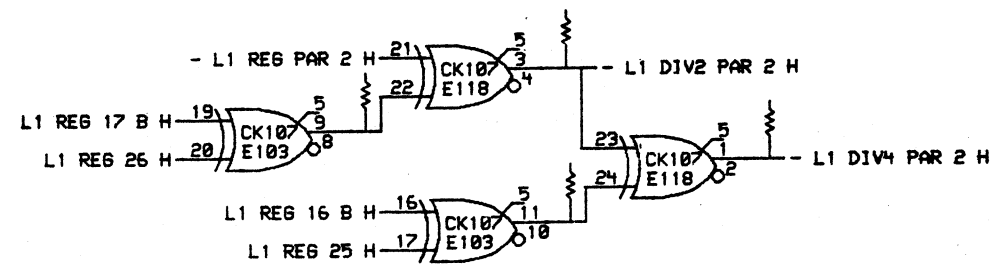
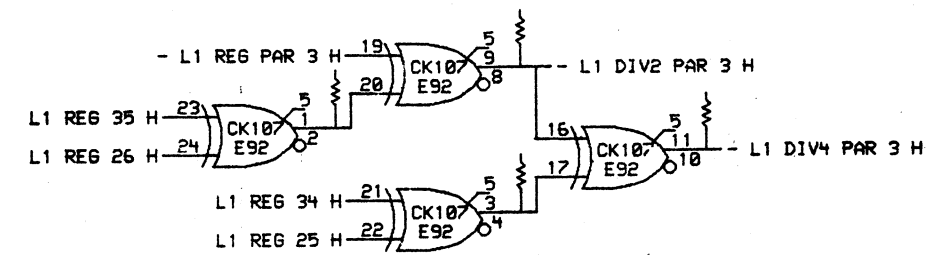
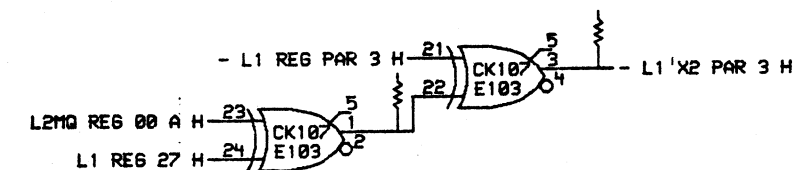
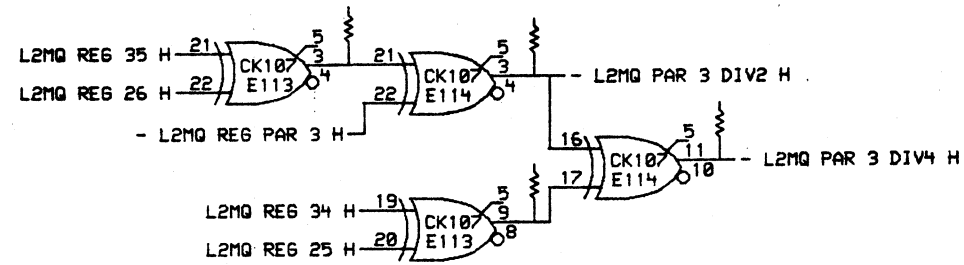
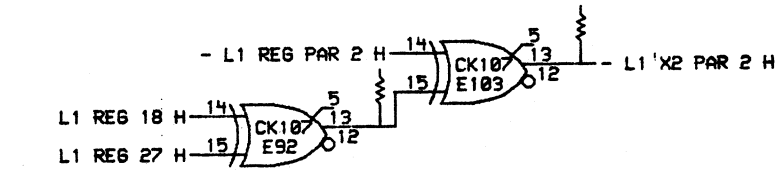
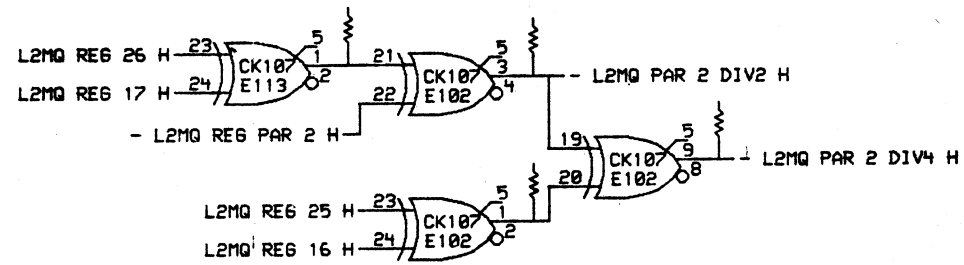
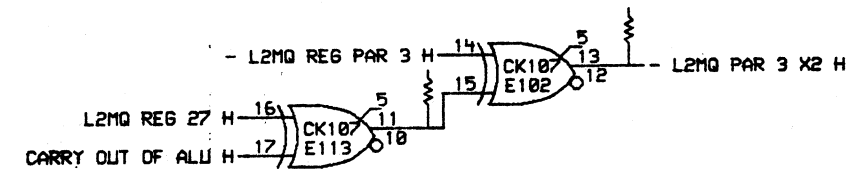
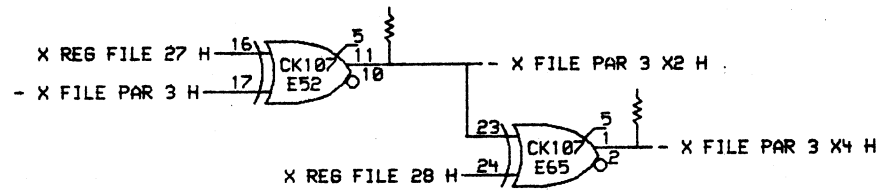
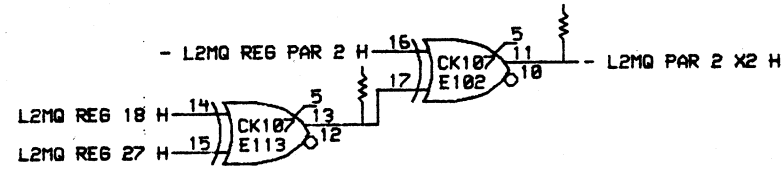
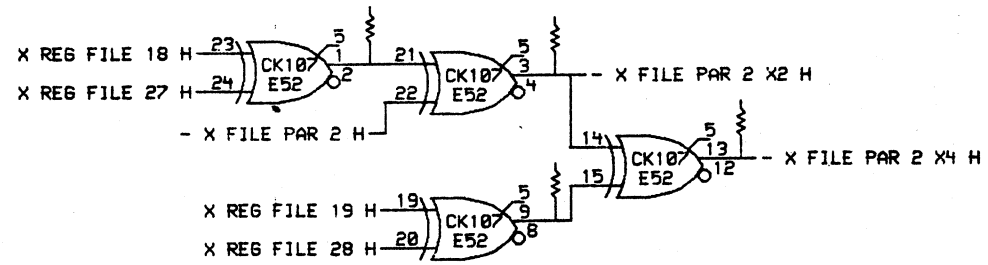


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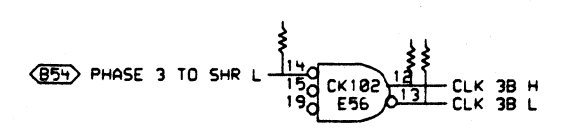
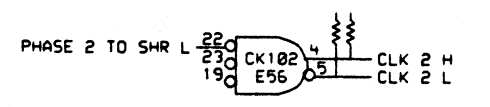
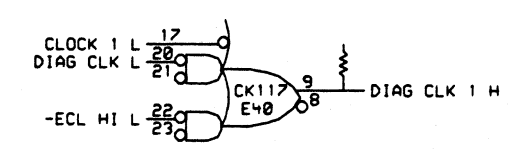
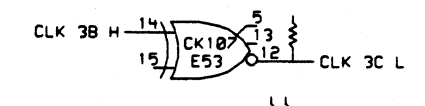
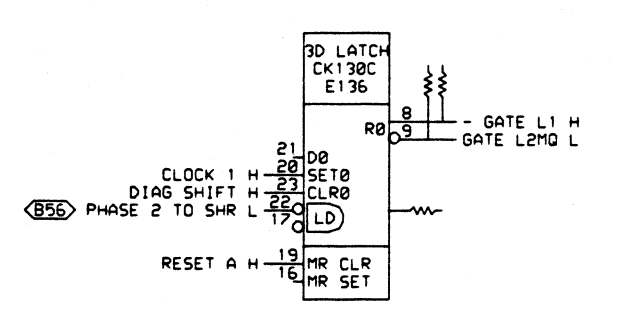
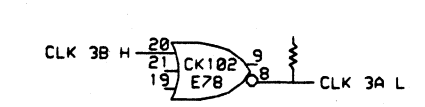
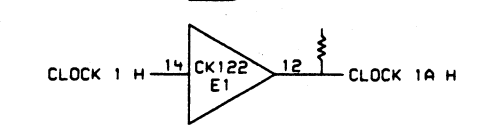
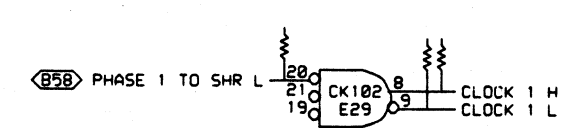
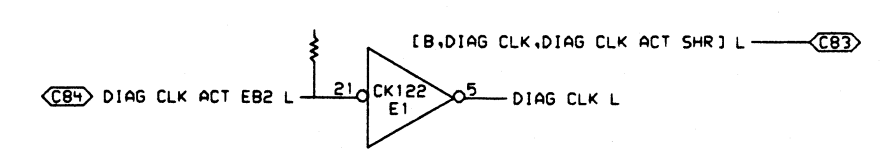
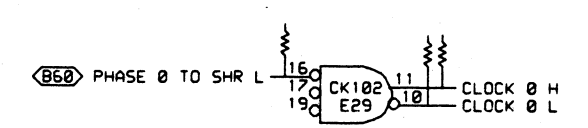
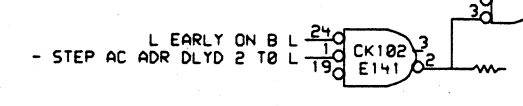
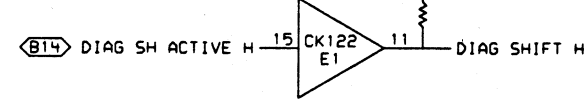
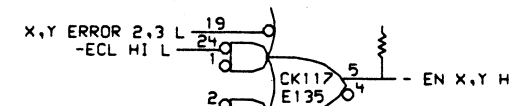
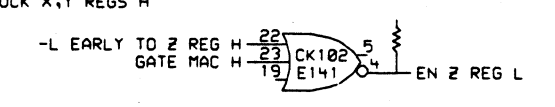
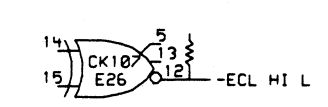
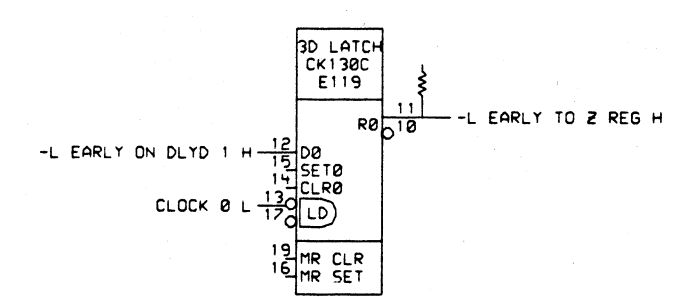
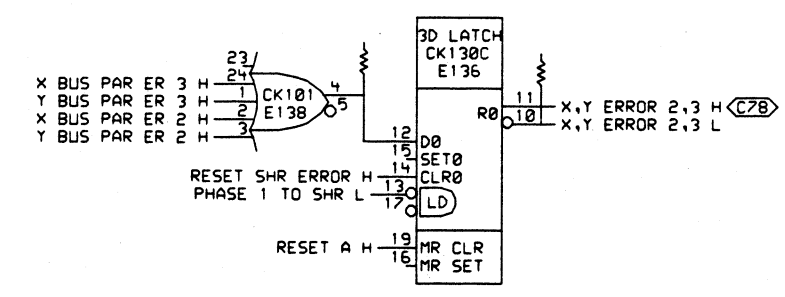
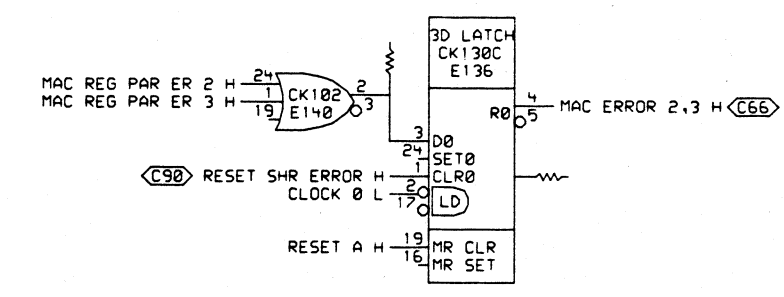
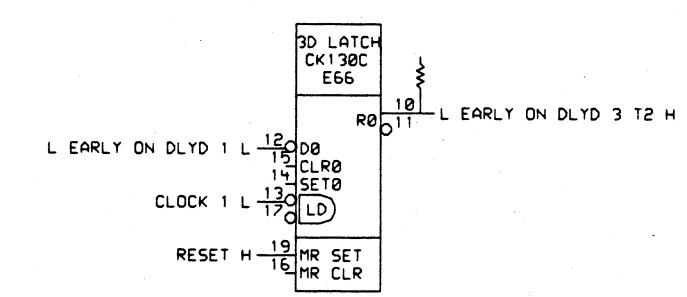
REVISIONS		
CHK	CHANGE NO.	REV.

DATE: 27-DEC-82	ENG.:	DATE:	TITLE: D MUX, L2MQ CTL L2MQ REG PAR
DATE: 22-DEC-82 17:59	CHK'D: J. J. J.	BOARD LOCATION: 1	SIZE CODE: D CS
FIRST USED ON OPTION/MODEL: 2080	NEXT HIGHER ASSEMBLY: B-DD-L0071-0	SHEET: 1	NUMBER: L0071-0-SHRJ

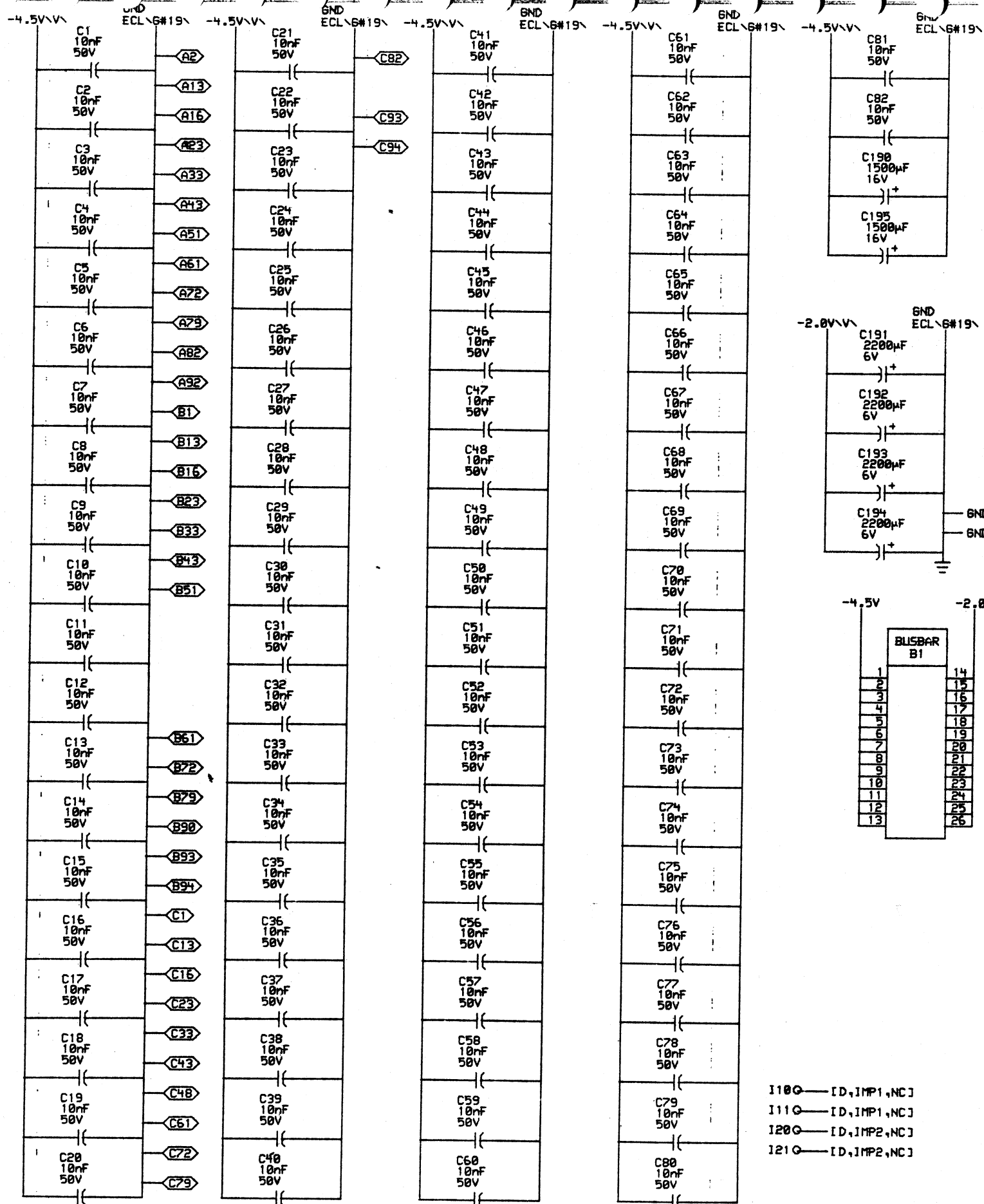
REV. #	REV. NUMBER	SIZE CODE	D CS	L0071-0-SHRJ	REV. #
--------	-------------	-----------	------	--------------	--------



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		SHIFT PARITY ADJUSTMENT
PROT.D. CP000 L0071.REV.C PCSHRK.DRM	20-SEP-82 06:55	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 2080	B-DD-L0071-0	NUMBER	D	CS
		L0071-0-SHRK		REV. *

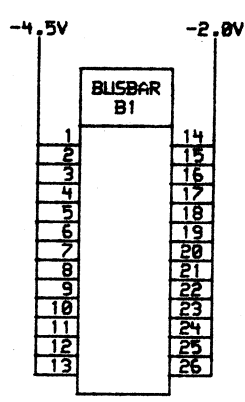
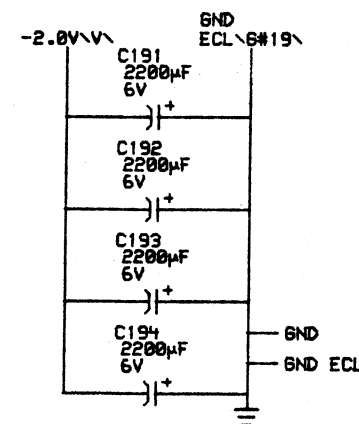
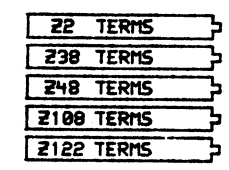
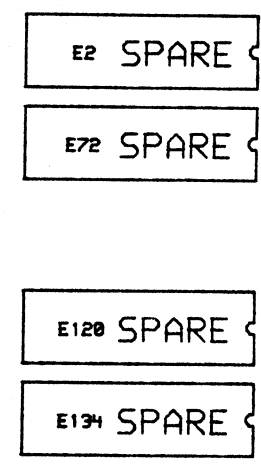


DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	12-JAN-83			ERR CTL
	DATE	BOARD LOCATION:	SHEET	CLK GATING
	11-JAN-83 10:00	11-1	1	
ARCHICUM/PLT/5>19CSH/2L DRW	NEXT HIGHER ASSEMBLY:	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL:	2080	D	CS	L0071-0-SHRL
	B-DD-L0071-0			REV.
				*

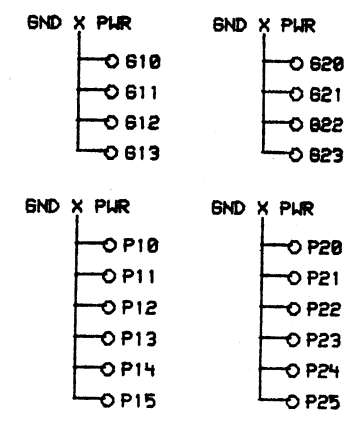


- (B53) [B,GND ECL, GND CLK\G#19]
- (B55) [B,GND ECL, GND CLK\G#19]
- (B57) [B,GND ECL, GND CLK\G#19]
- (B59) [B,GND ECL, GND CLK\G#19]

- (A73) NC A73
- (A88) NC A88
- (B5) NC B5
- (B6) NC B6
- (B7) NC B7
- (B70) NC B70
- (B92) NC B92
- (C2) NC C2
- (C4) NC C4
- (C17) NC C17
- (C19) NC C19
- (C26) NC C26
- (C27) NC C27
- (C28) NC C28
- (C38) NC C38
- (C41) NC C41
- (C42) NC C42
- (C45) NC C45
- (C50) NC C50
- (C52) NC C52
- (C53) NC C53
- (C55) NC C55
- (C57) NC C57
- (C59) NC C59
- (C63) NC C63
- (C81) NC C81
- (C85) NC C85
- (C87) NC C87
- (C88) NC C88
- (C89) NC C89



- I100 ID,IMP1,NC
- I110 ID,IMP1,NC
- I200 ID,IMP2,NC
- I210 ID,IMP2,NC



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		SHR POWER & GROUND
FIRST USED ON OPTION/MODEL: 2080 B-DD-L0071-0				SIZE CODE NUMBER REV.
				D CS L0071-0-SHRM *

VULCAN:<2080.L0051>43MVE1
VULCAN:<2080.L0051>43MVE2
VULCAN:<2080.L0051>43MVE3
VULCAN:<2080.L0051>43MVE4
VULCAN:<2080.L0051>43MVE5
VULCAN:<2080.L0051>43MVE6
VULCAN:<2080.L0051>43MVE7
VULCAN:<2080.L0051>43MVE8
VULCAN:<2080.L0051>43MVE9
VULCAN:<2080.L0051>43MVEA
VULCAN:<2080.L0051>43MVEB
VULCAN:<2080.L0051>43MVEC
VULCAN:<2080.L0051>43MVED
VULCAN:<2080.L0051>43MVEE
VULCAN:<2080.L0051>43MVEF
VULCAN:<2080.L0051>43MVEG
VULCAN:<2080.L0051>43MVEH
VULCAN:<2080.L0051>43MVEI
VULCAN:<2080.L0051>43MVEJ
VULCAN:<2080.L0051>43MVEK
VULCAN:<2080.L0051>44MVEL
VULCAN:<2080.L0051>44MVEM
VULCAN:<2080.L0051>43MVEN

E141 E128 E115 E102 E090 E077 E064 E051 E039 E026 E013

E140 E127 E114 E101 E089 E076 E063 E050 E038 E025 E012

E139 E126 E113 E100 E088 E075 E062 E049 E037 E024 E011

E138 E125 E112 E099 E087 E074 E061 E048 E036 E023 E010

E137 E124 E111 E098 E086 E073 E060 E047 E035 E022 E009

E136 E123 E110 E097 E085 E072 E059 E046 E034 E021 E008

E135 E122 E109 E096 E084 E071 E058 E045 E033 E020 E007

E134 E121 E108 E095 E083 E070 E057 E044 E032 E019 E006

E133 E120 E107 E094 E082 E069 E056 E043 E031 E018 E005

E132 E119 E106 E093 E081 E068 E055 E042 E030 E017 E004

E131 E118 E105 E092 E080 E067 E054 E041 E029 E016 E003

+ C095 + + C094 + + C093 + + C092 + + C091 + + C090 +

E130 E117 E104 E091 E079 E066 E053 E040 E028 E015 E002

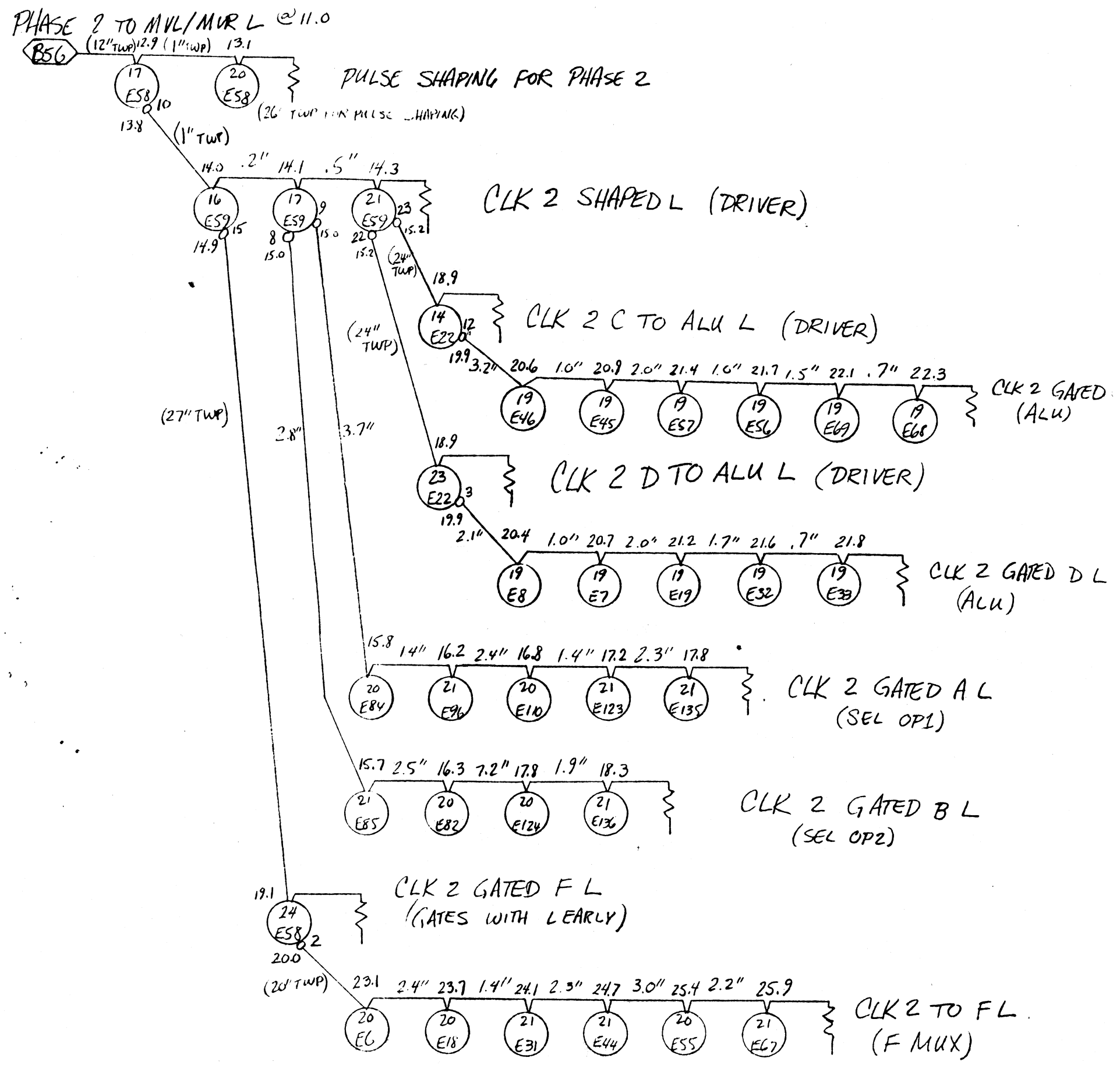
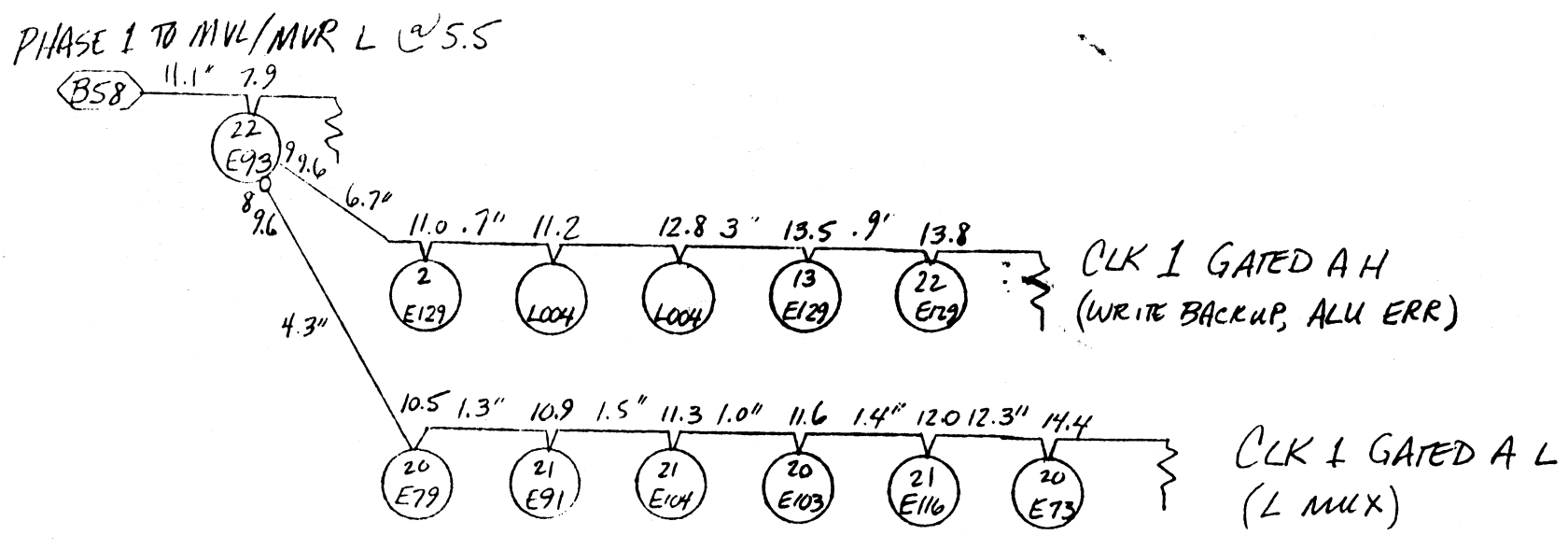
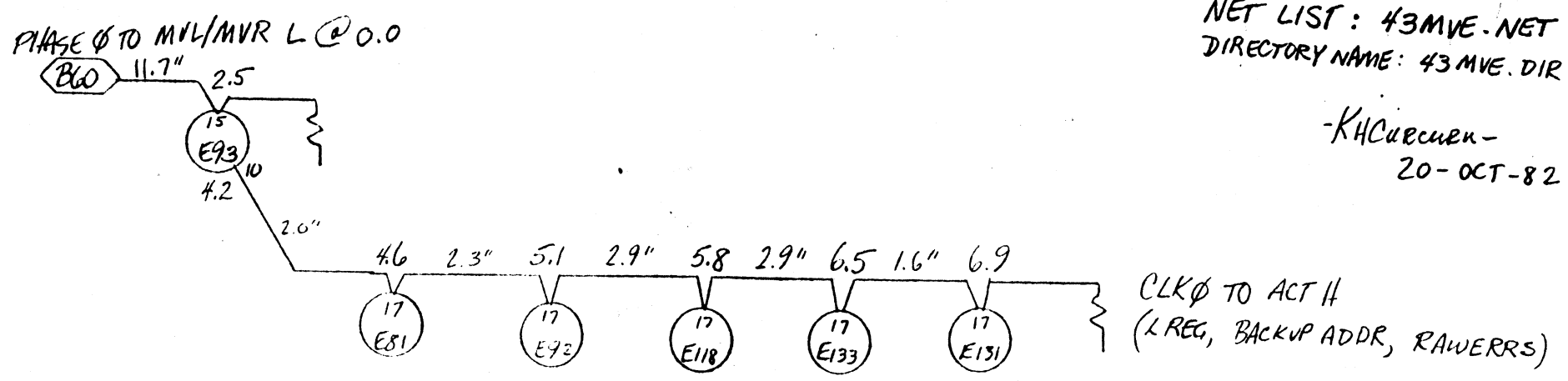
E129 E116 E103 E078 E065 E052

"141"

MVE

MVE CLOCK DIAGRAM
 @ECO LEVEL 43 (6-OCT-82)
 DATE OF PRINTS: ALL PRINTS WERE
 UPDATED TO 43MVE.DRW ON 6-OCT
 NET LIST: 43MVE.NET
 DIRECTORY NAME: 43MVE.DIR

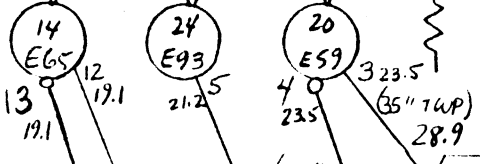
-KHCURCHEN-
 20-OCT-82



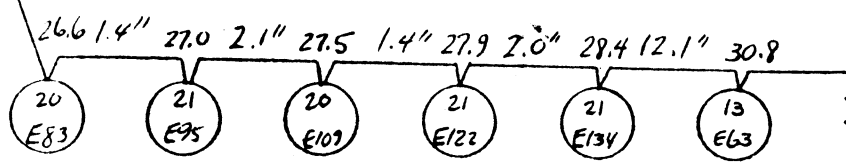
(MVE page 2)

PHASE 3 TO MVL/MVR L @ 16.5

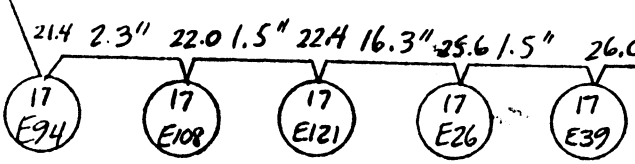
854 8.3" 18.2 6.0" 17.5 (20" TWP) 22.6



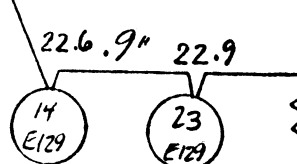
CLK 3 TO PG (ALU GENERATE HOLD REGISTER)



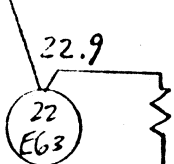
CLK 3 TO RF L (RF MUX, INPUT ERR)



DIAG CLK ACT H (AC BACKUP SCAN, F SCAN)



CLK 3 UNGATED H (CLEAR WRITE PULSE TO BACKUP RAMS)



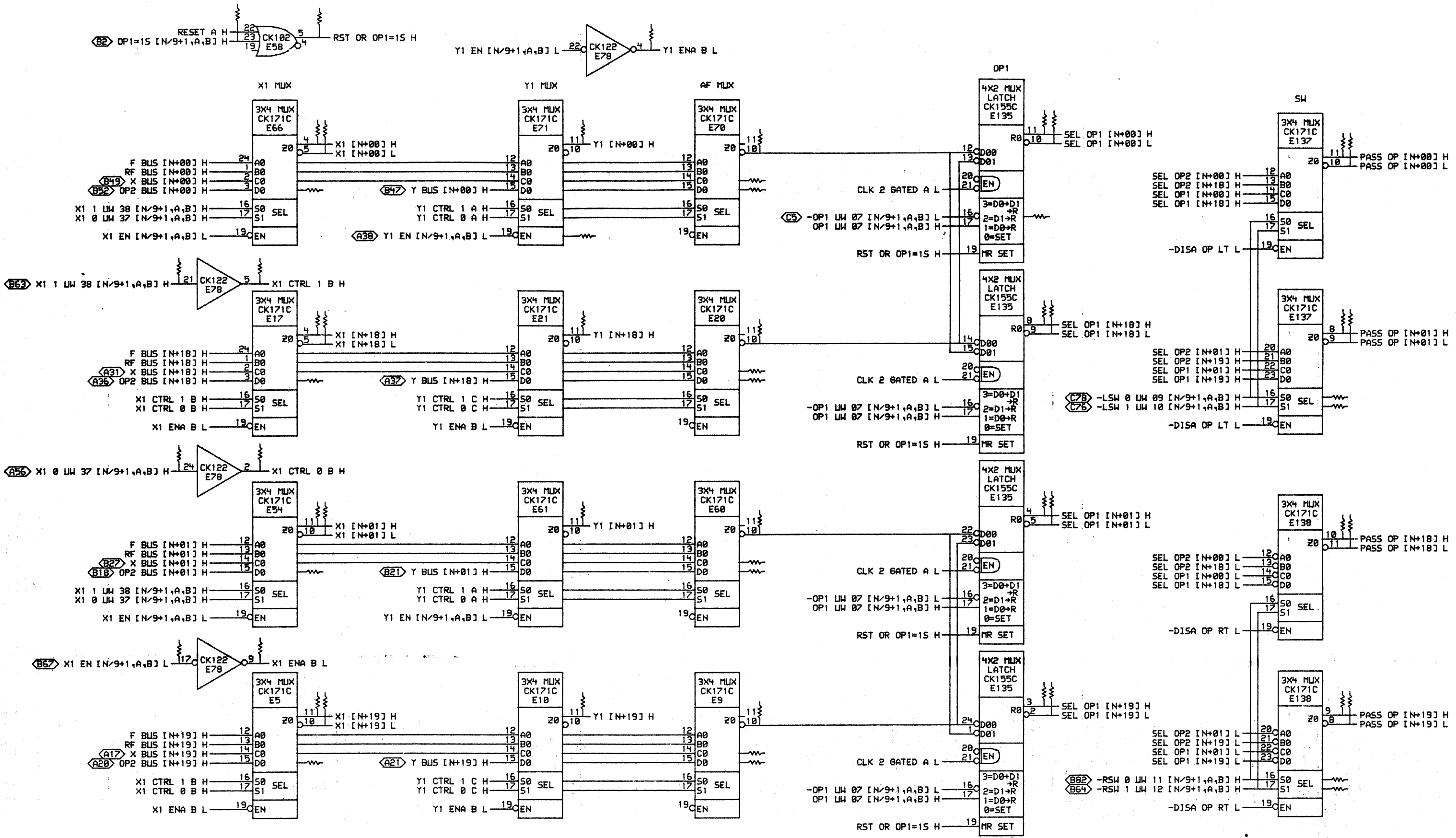
CLK 3 UNGATED L (OUTPUT ERR)

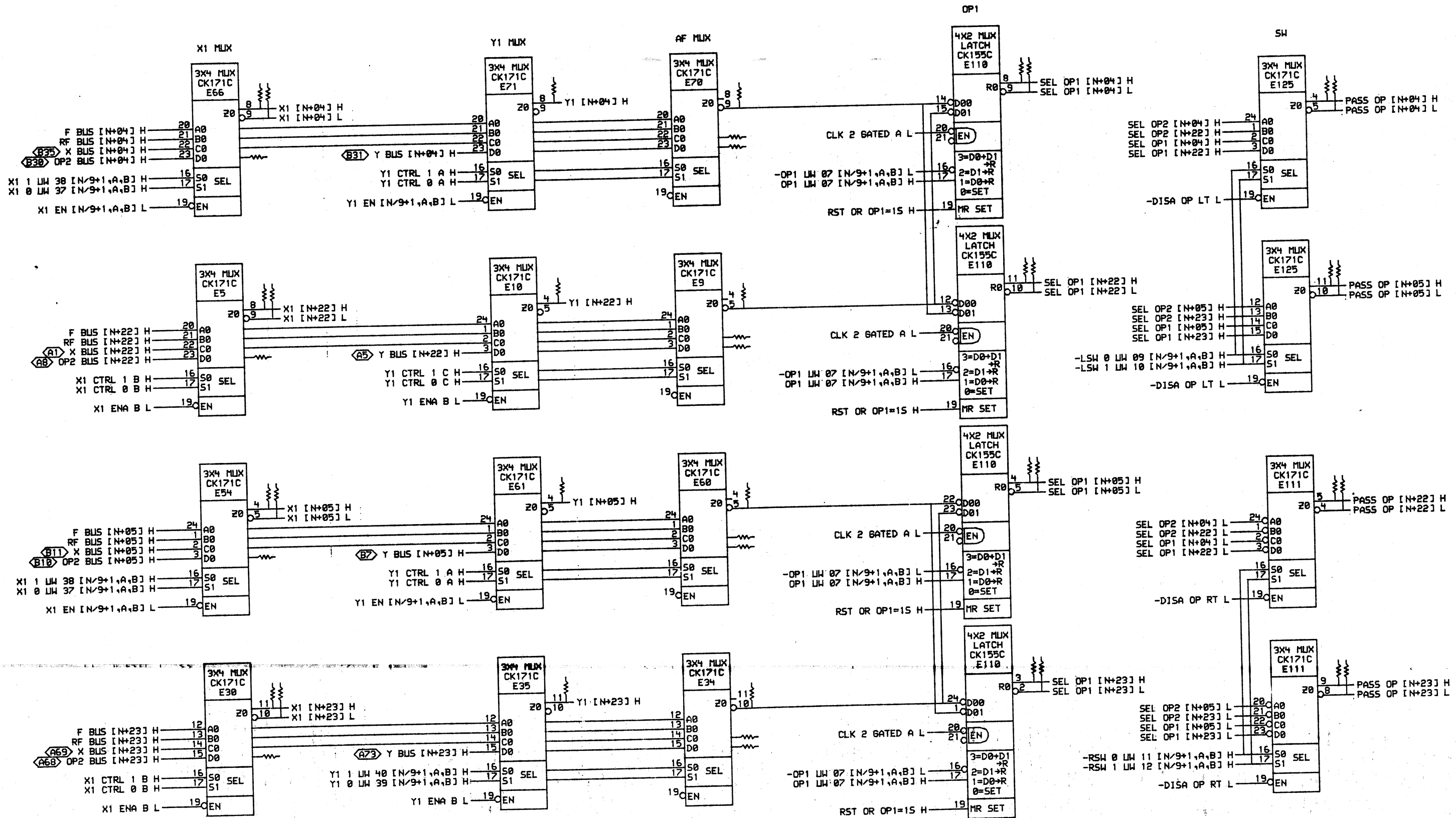
MUST HAVE BEEN A 2-BIT BUS
NO INFO ON THIS FIXED LENGTH!

19.2"

.5"

18"

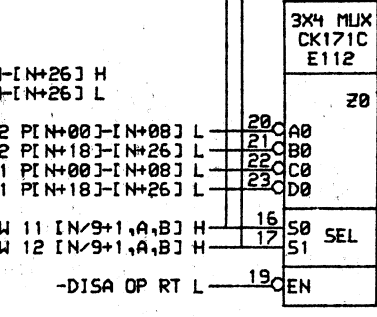
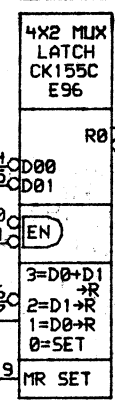
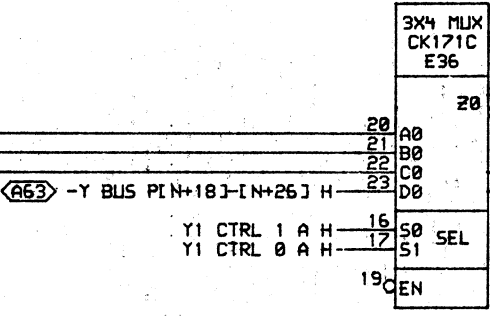
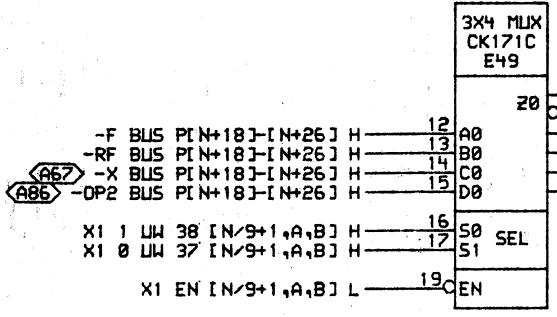
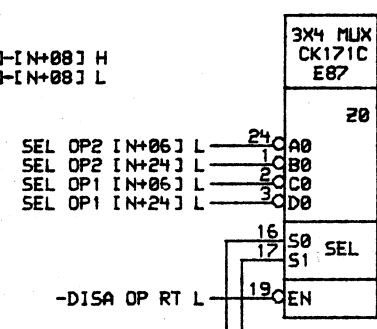
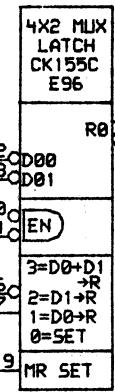
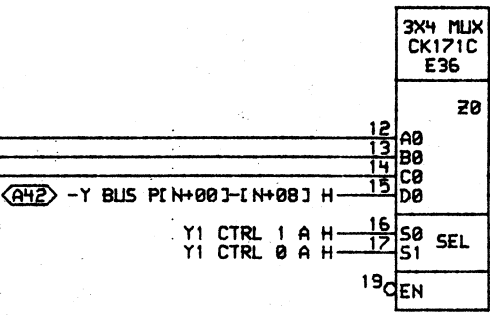
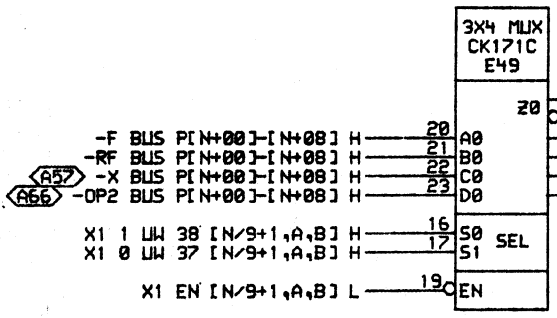
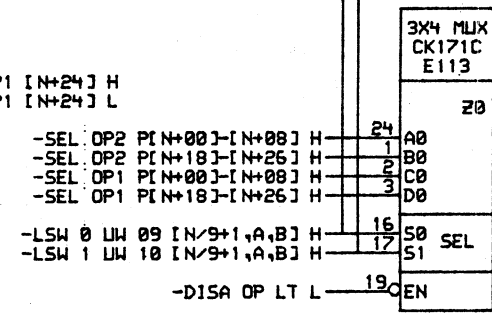
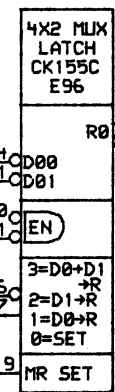
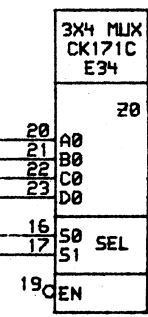
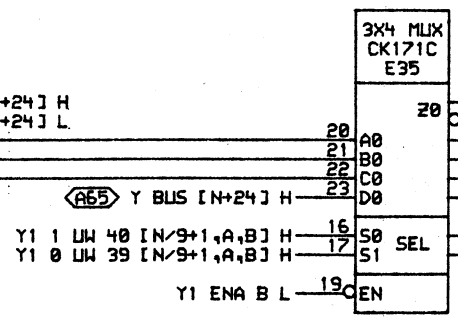
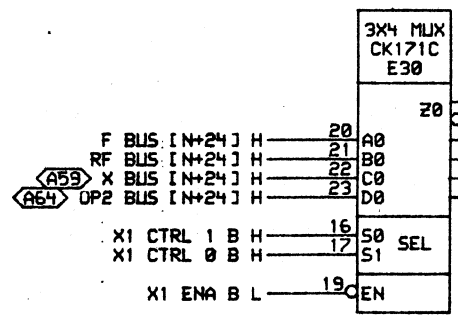
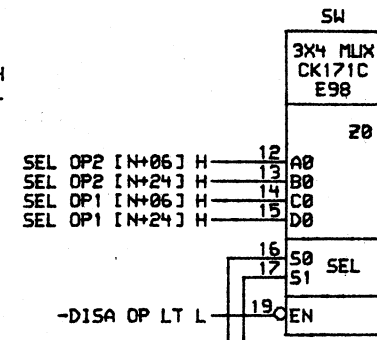
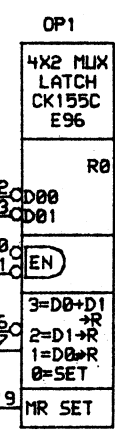
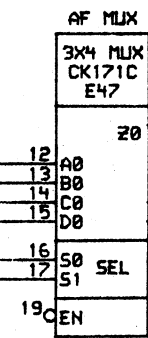
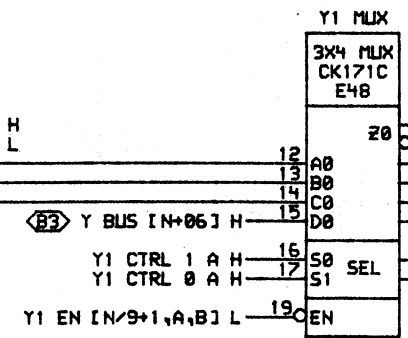
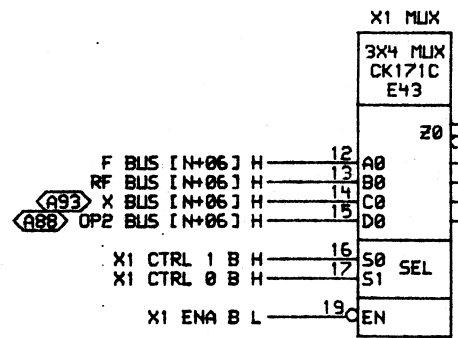
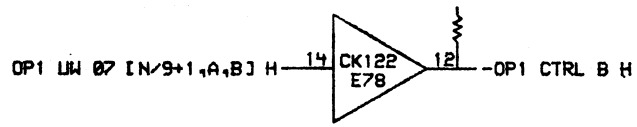




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REVISIONS	
CHK	CHANGE NO. REV

	DRN. <i>J. J. J.</i>	DATE <i>88-OCT-82</i>	ENG.	DATE	TITLE: FWD, EARLY MVE
	CHK'D.	DATE	BOARD LOCATION:	SHEET	NUMBER
FIRST USED ON OPTION/MODEL:			NEXT HIGHER ASSEMBLY:		SIZE CODE D CS
L0051-0-MVE3					REV.



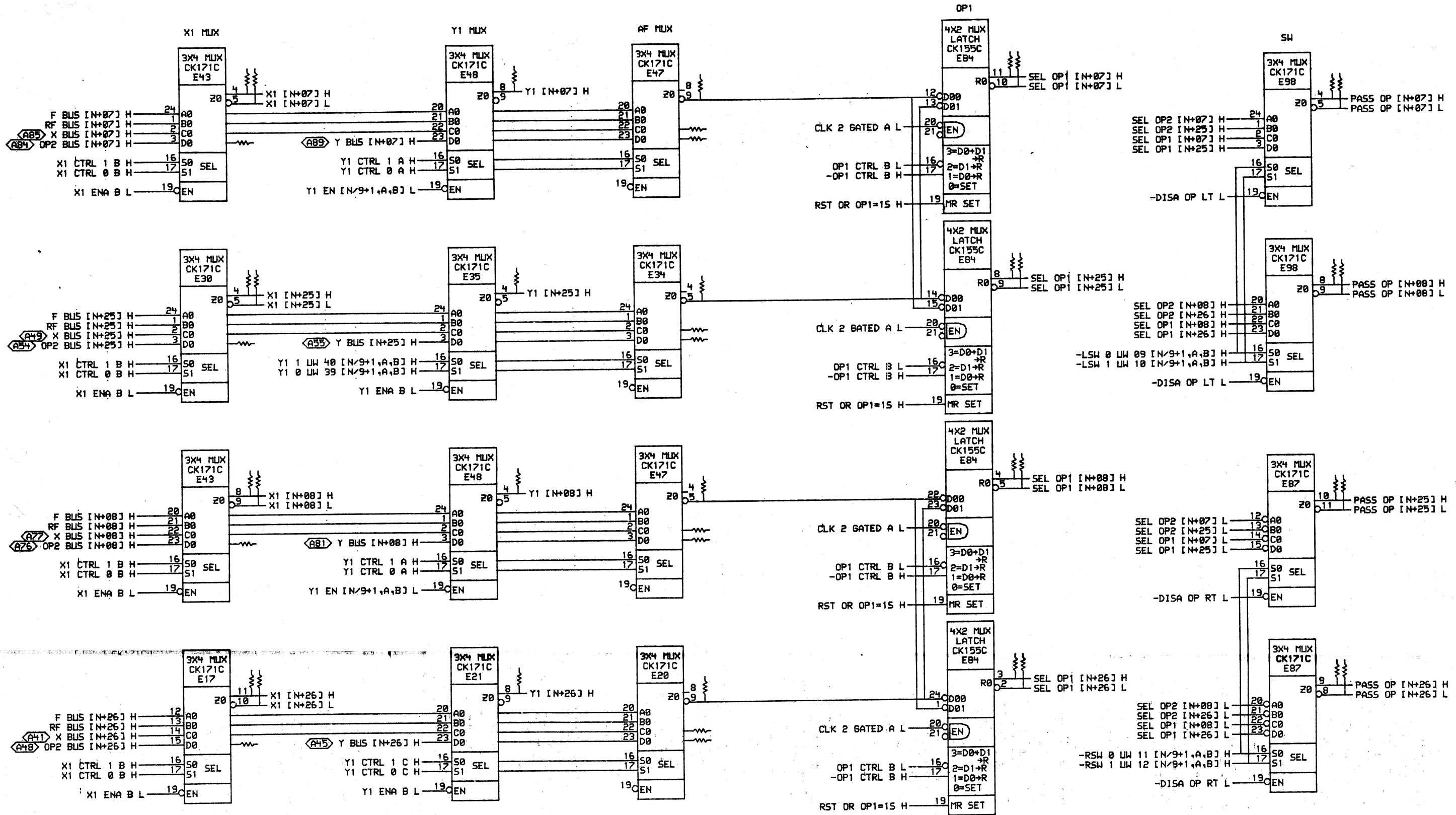
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REVISIONS	
CHK	CHANGE NO. REV

digital *John* DATE ENG. DATE BOARD LOCATION: SHEET OF

VIL CAN (2088) L0051-MVE4.DRW 07-OCT-82 12:04 NEXT HIGHER ASSEMBLY: FIRST USED ON OPTION/MODEL:

TITLE:	FWD, EARLY MVE P, N+6, 24
SIZE CODE	D CS
NUMBER	L0051-0-MVE4
REV.	

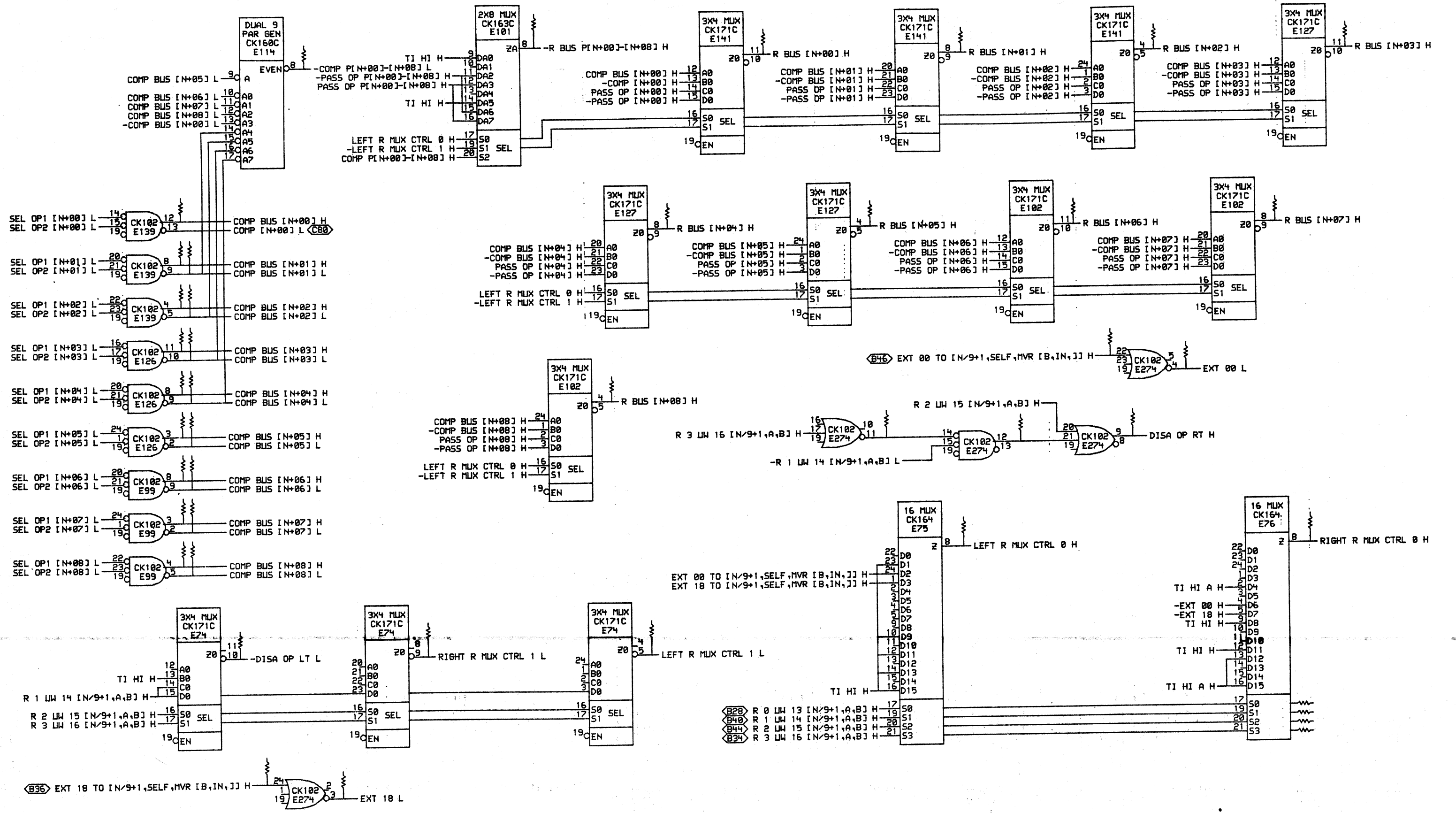


D
 C
 B
 A
 REV. NUMBER CS L0051-0-MVE5
 SIZE D
 B

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REVISIONS	
CHK	CHANGE NO. REV

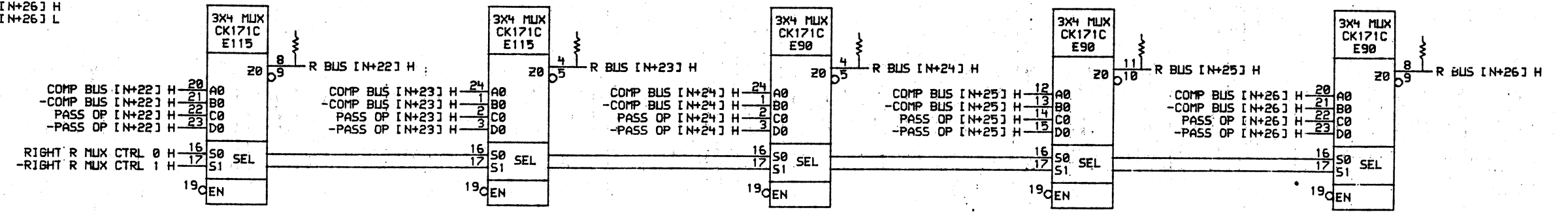
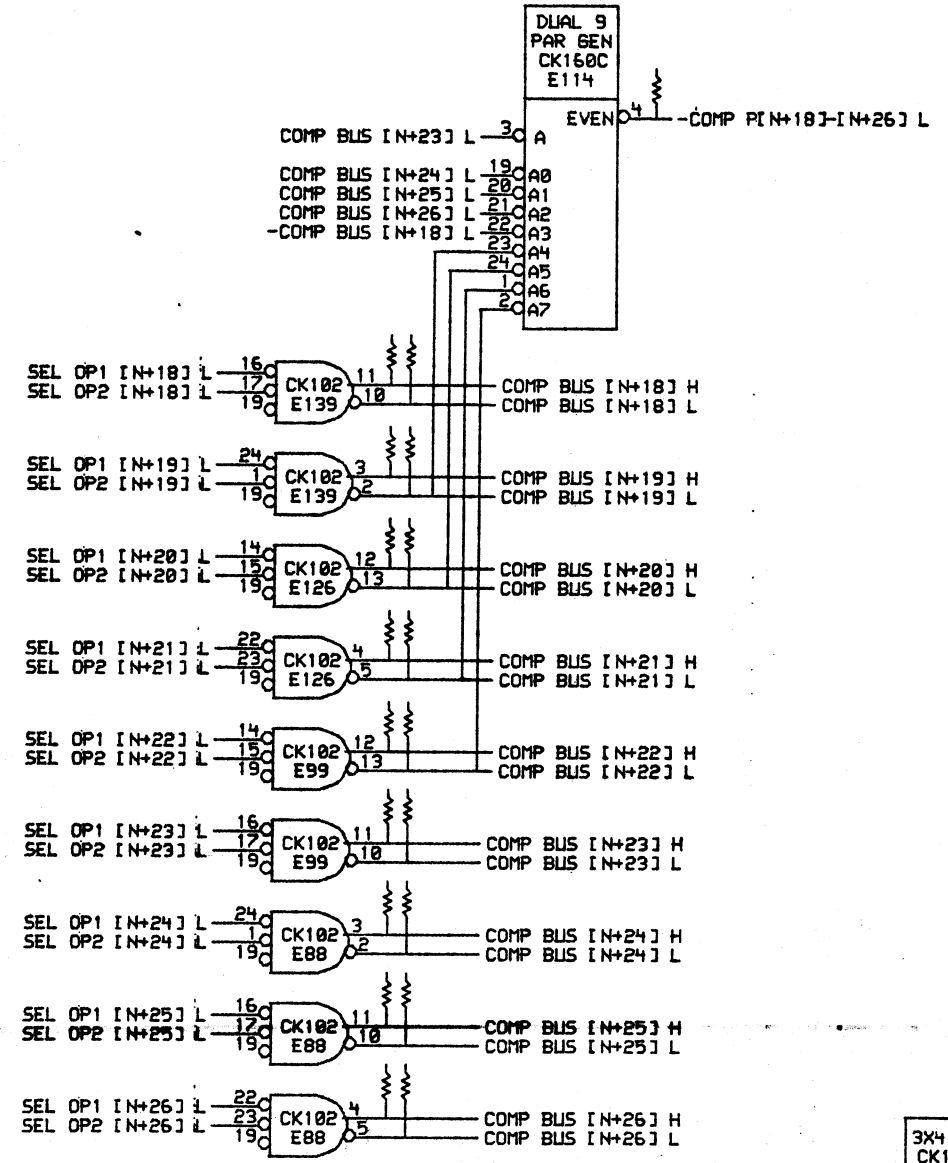
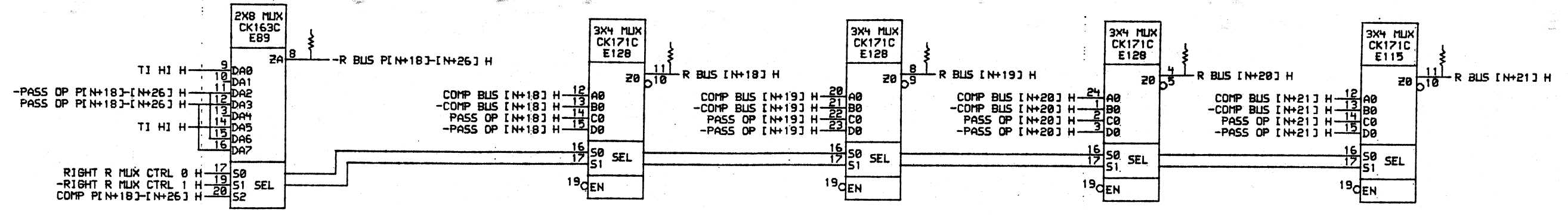
	DATE	ENG.	DATE	TITLE:
	08-OCT-82	J. J. J.	07-OCT-82 12:05	FWD, EARLY MVE N+ 7, 8, 25, 26
BOARD LOCATIONS		SHEET		SIZE
NEXT HIGHER ASSEMBLY		NUMBER		REV.
FIRST USED ON OPTION/MODEL:		D		CS L0051-0-MVE5

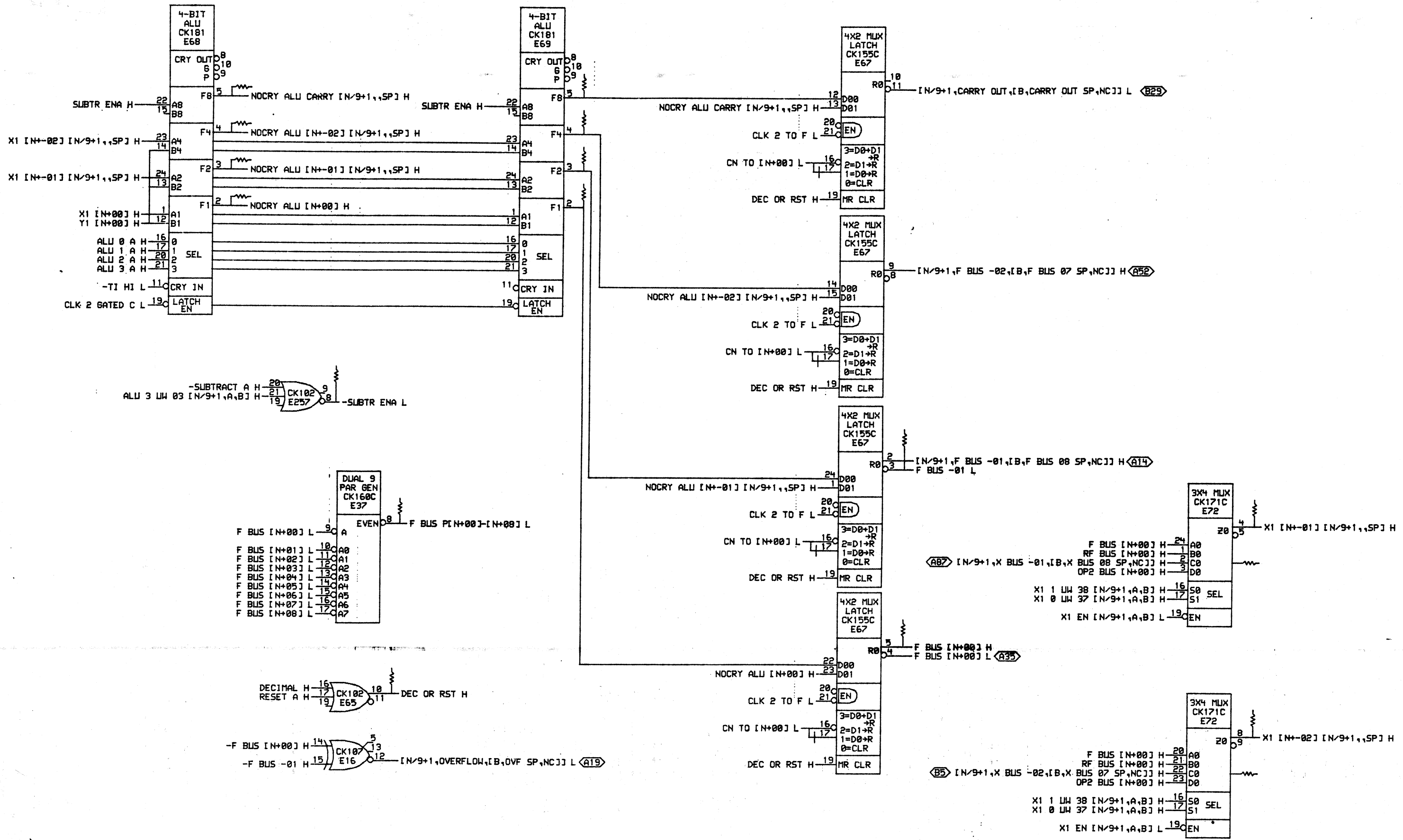


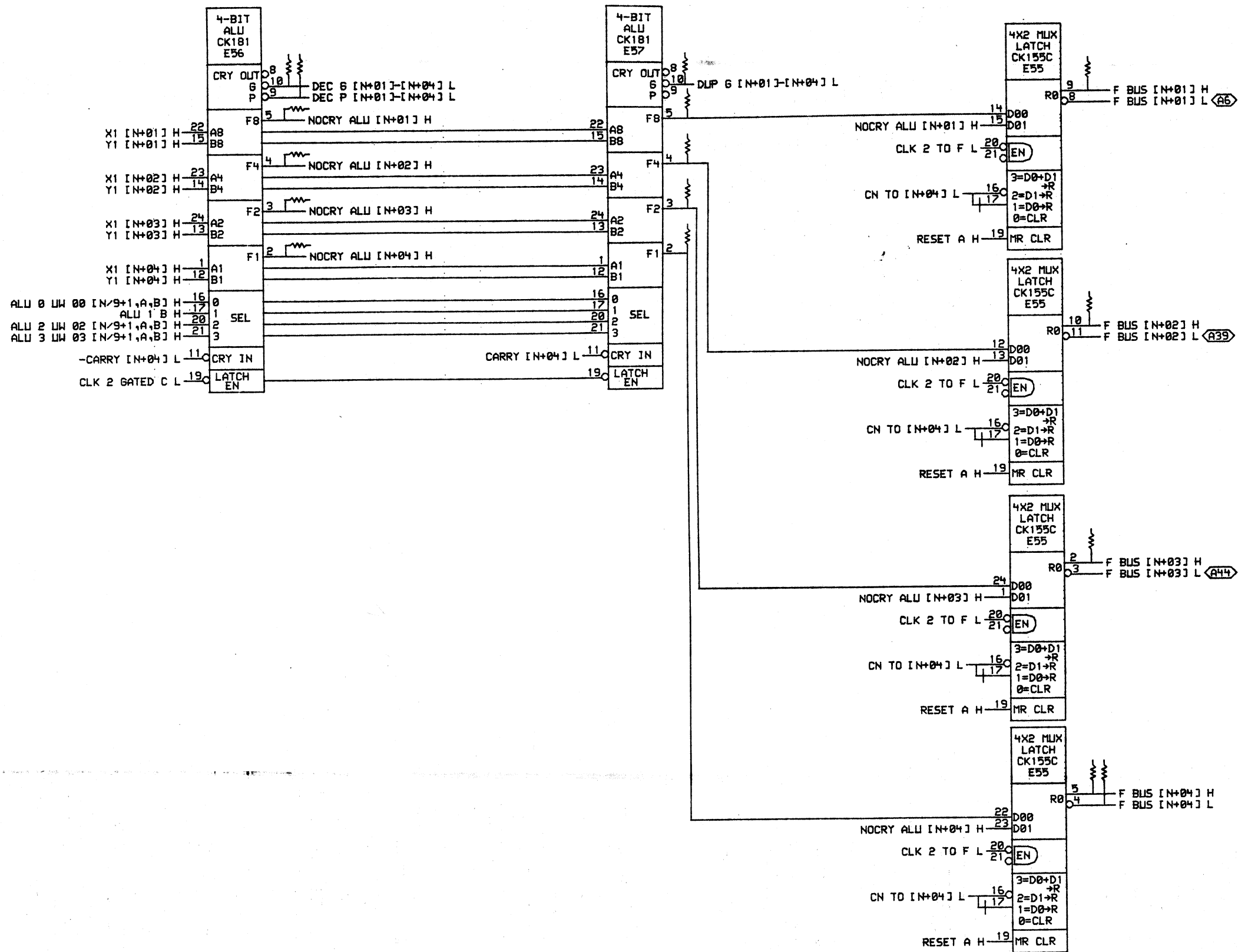
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OR USED IN WHOLE OR IN PART AS
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REVISIONS		
CHK	CHANGE NO.	REV

	DATE: 08-OCT-82 DATE: 07-OCT-82 12:07 FIRST USED ON OPTION/MODEL:	ENG. BOARD LOCATION: SHEET OF:	TITLE: COMP BUS N+ 0-8 LEFT R BUS
	SIZE: D CS CODE: L0051-0-MVE6	NUMBER:	REV.:

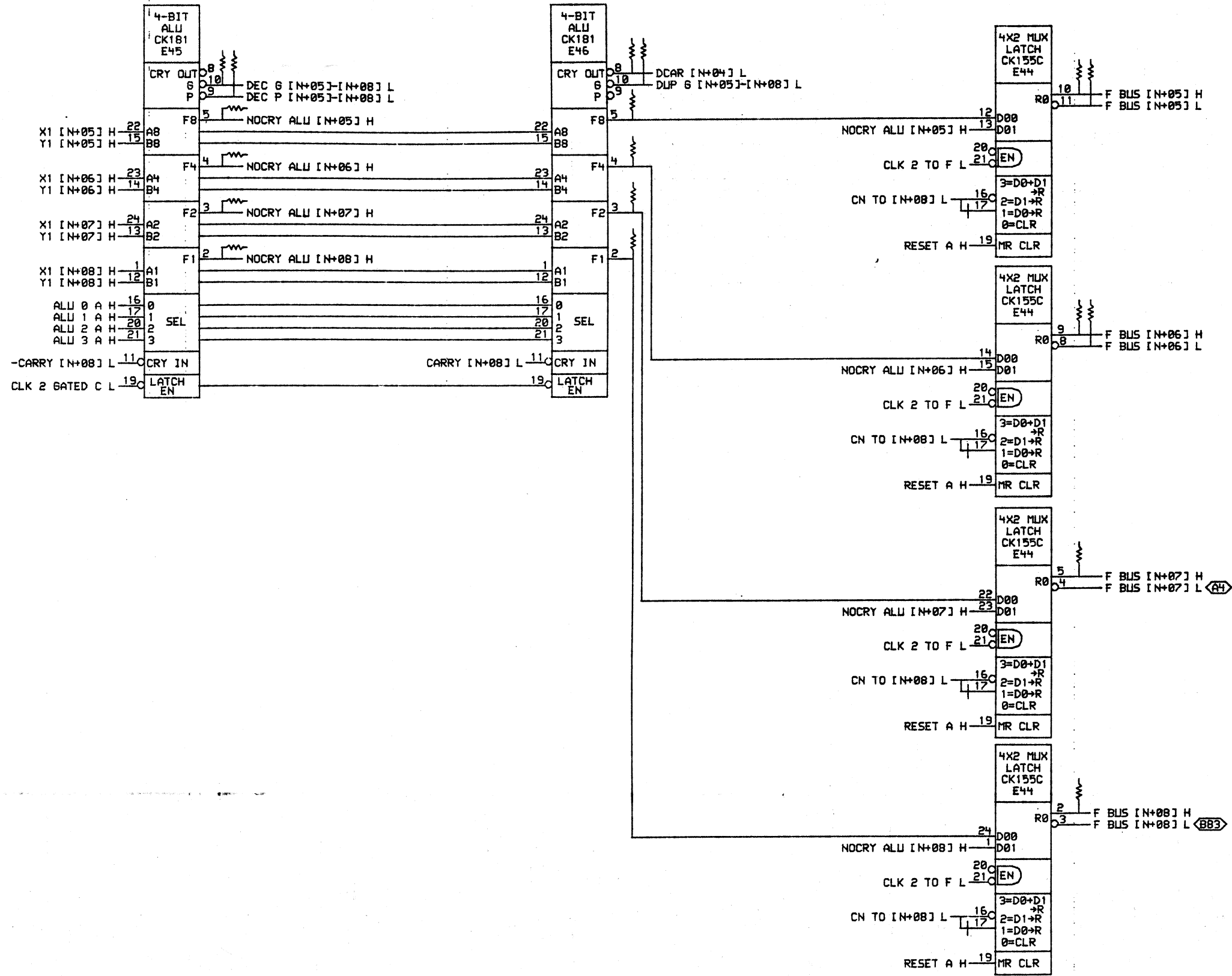






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 NUMBER
 L0051-0-MVE9
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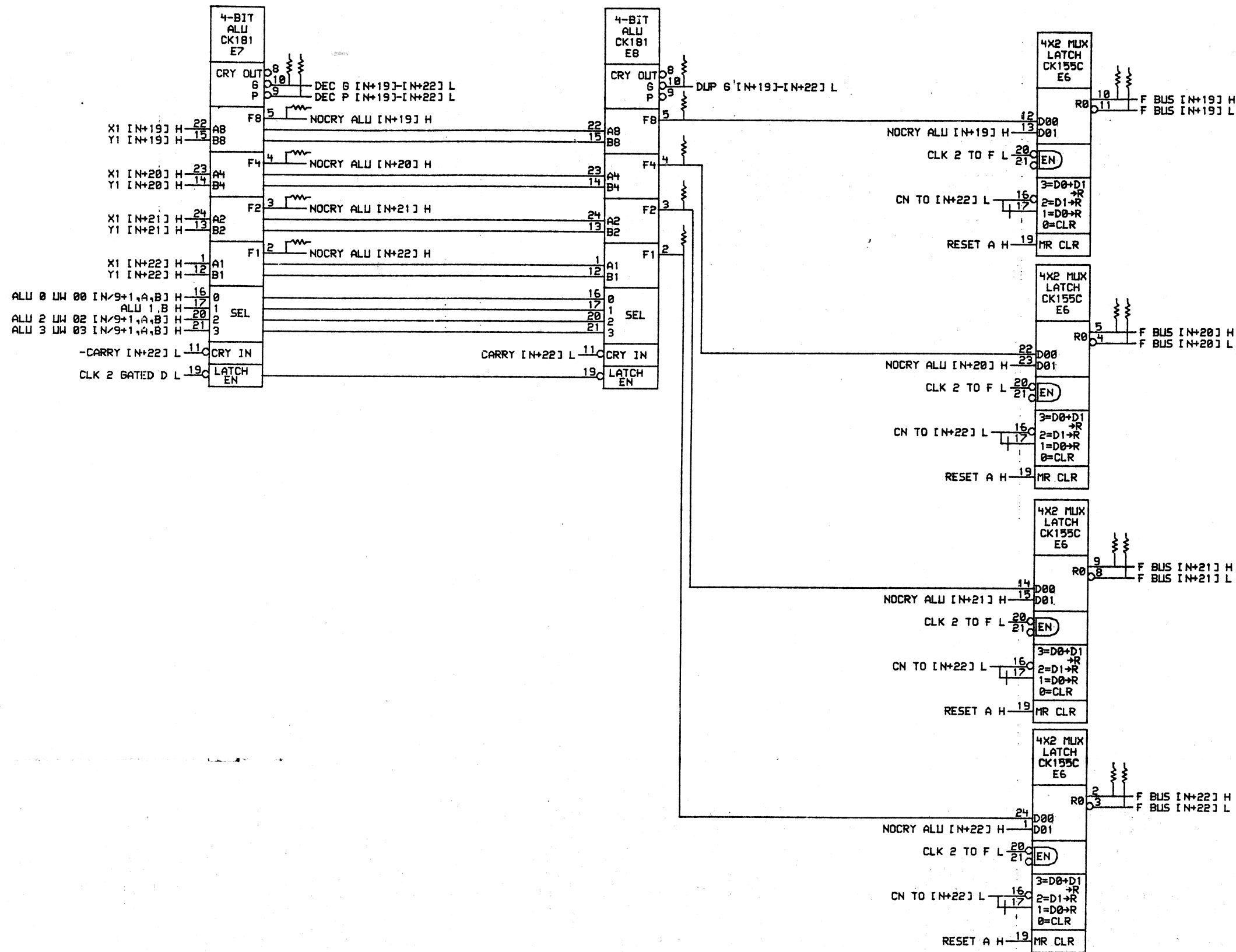
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				NEXT HIGHER ASSEMBLY:			



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. <i>J. J. J.</i>	DATE 88-OCT-82	ENG.	DATE	TITLE: ALU, F MUX
	CHK'D.	DATE	BOARD LOCATION:	SHEET OF	BITS N+05 - N+08
VULCAN: (2000.L0051)MVEA.DRW 06-OCT-82 21:01			NEXT HIGHER ASSEMBLY:		SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL:					D CS L0051-0-MVEA



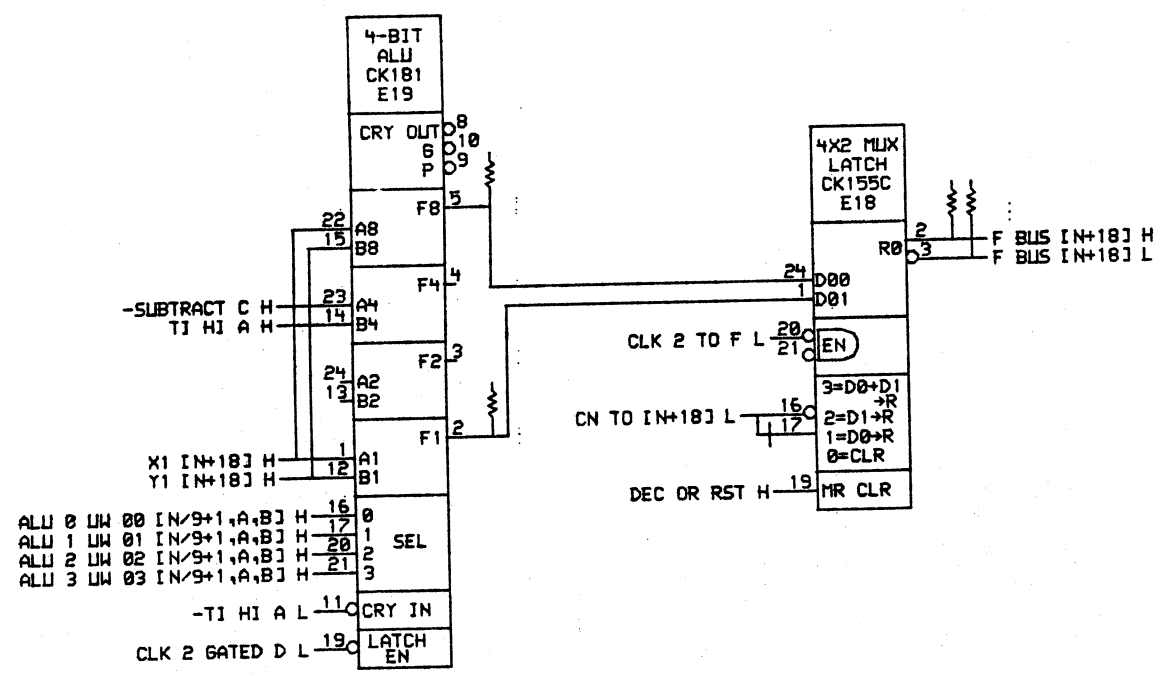
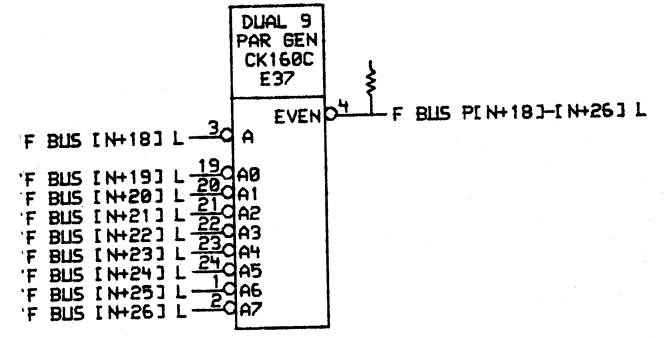
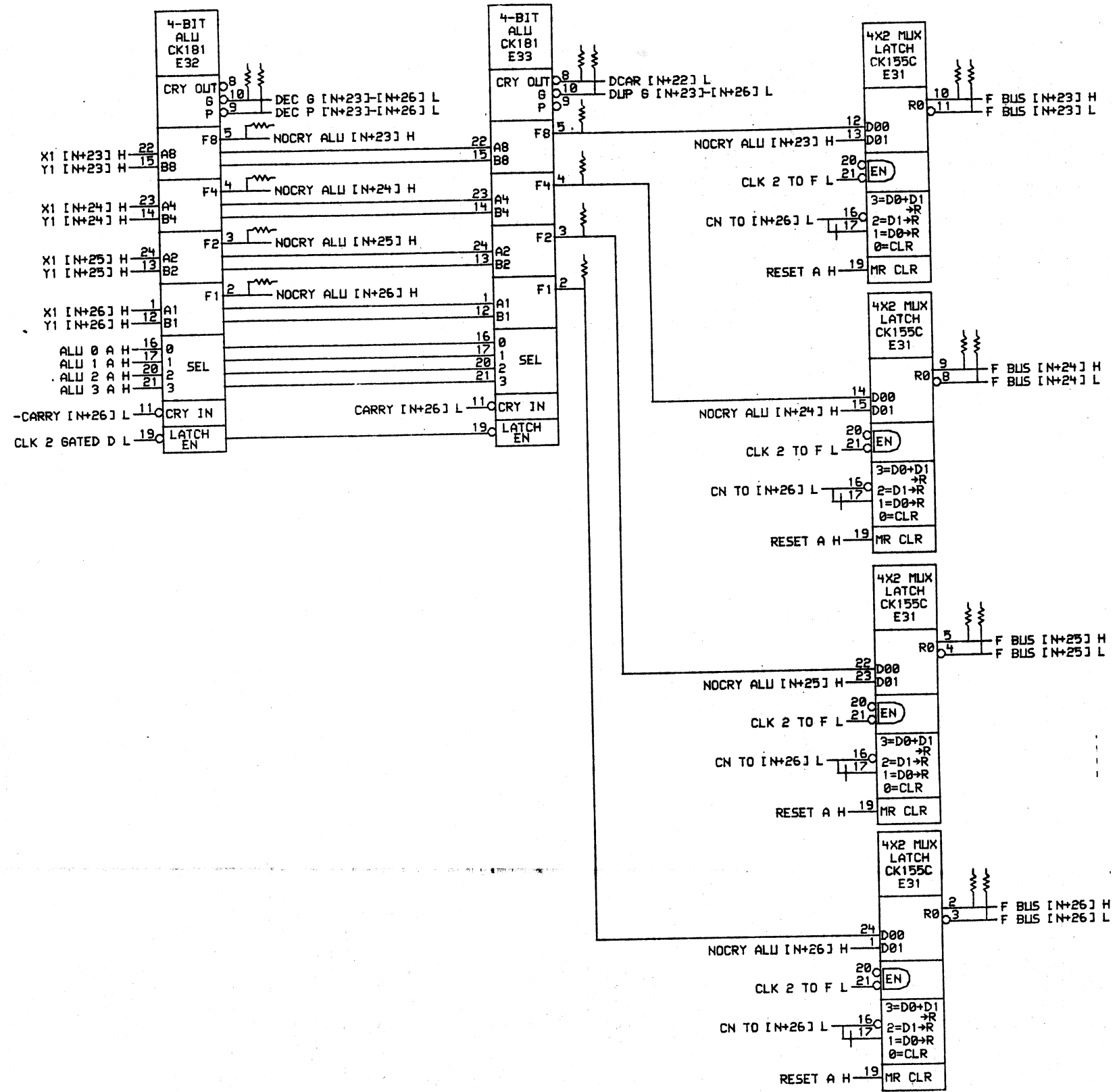
REV. NUMBER L0051-0-MVEB

REV.

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REVISIONS		
CHK	CHANGE NO.	REV

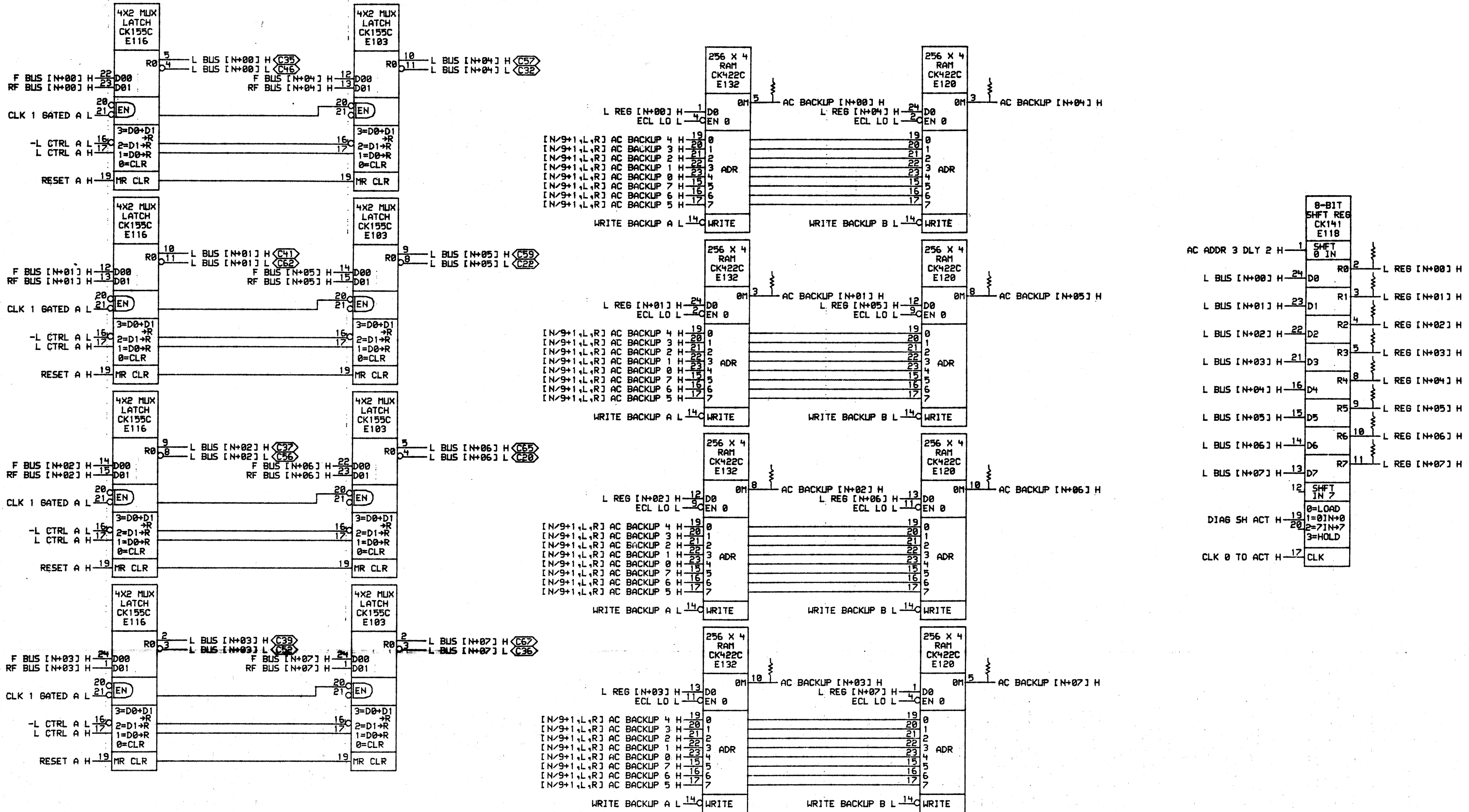
digital	DRW. <i>J. J. J.</i>	DATE 88-OCT-82	ENG.	DATE	TITLE: ALU, F MUX
	CHK'D.	DATE	BOARD LOCATION:	SHEET OF	BITS N+19 - N+22
VIL CAN: <2000> L0051-0-MVEB.DRW 106-OCT-82 21:01 NEXT HIGHER ASSEMBLY:					SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL:					D CS L0051-0-MVEB

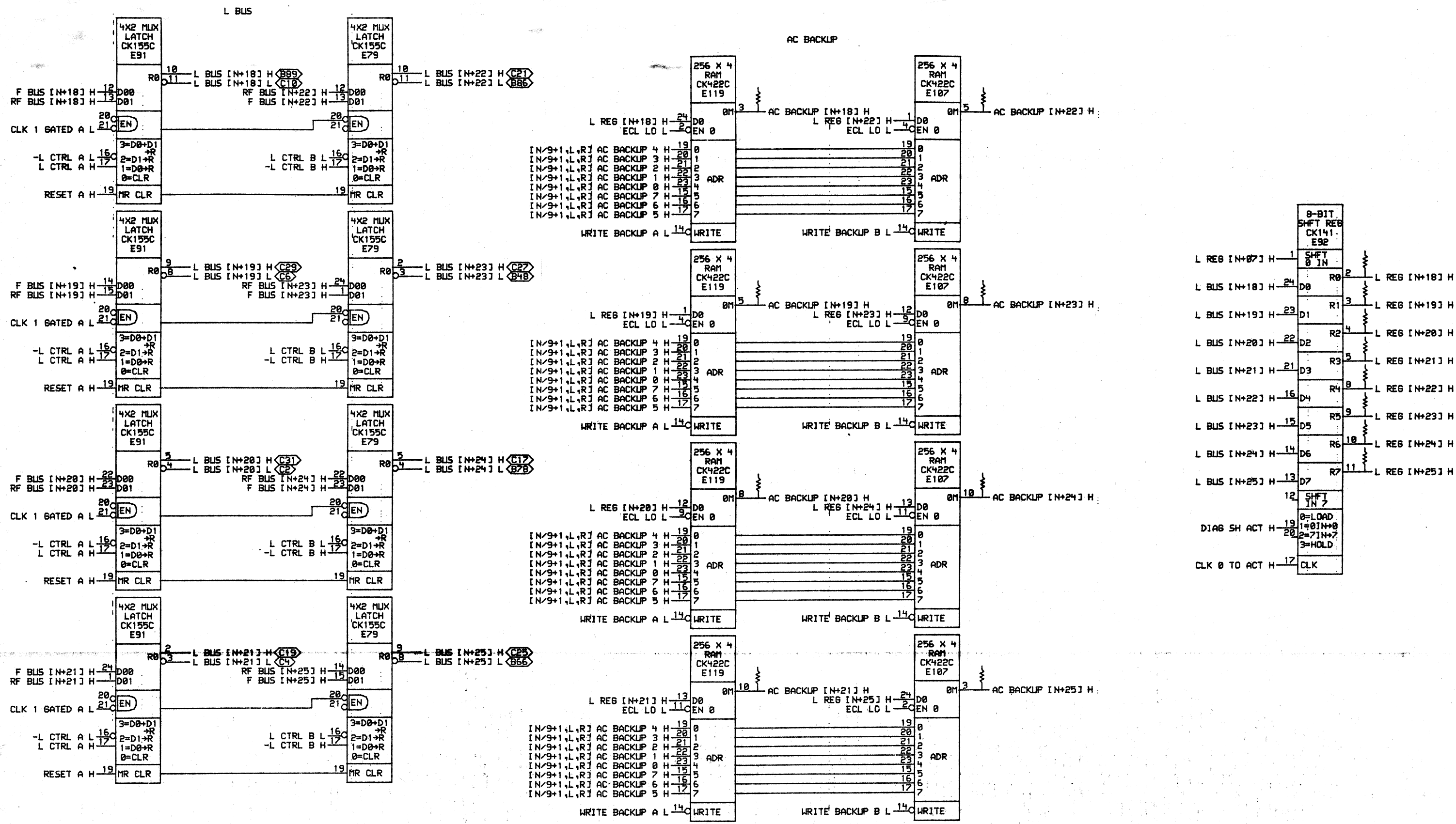


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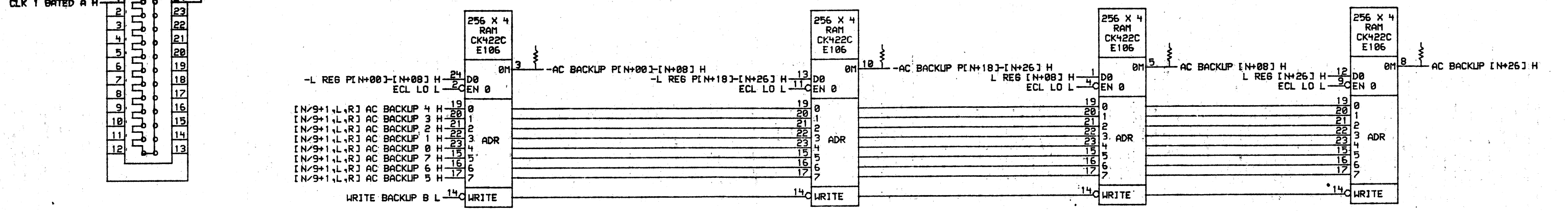
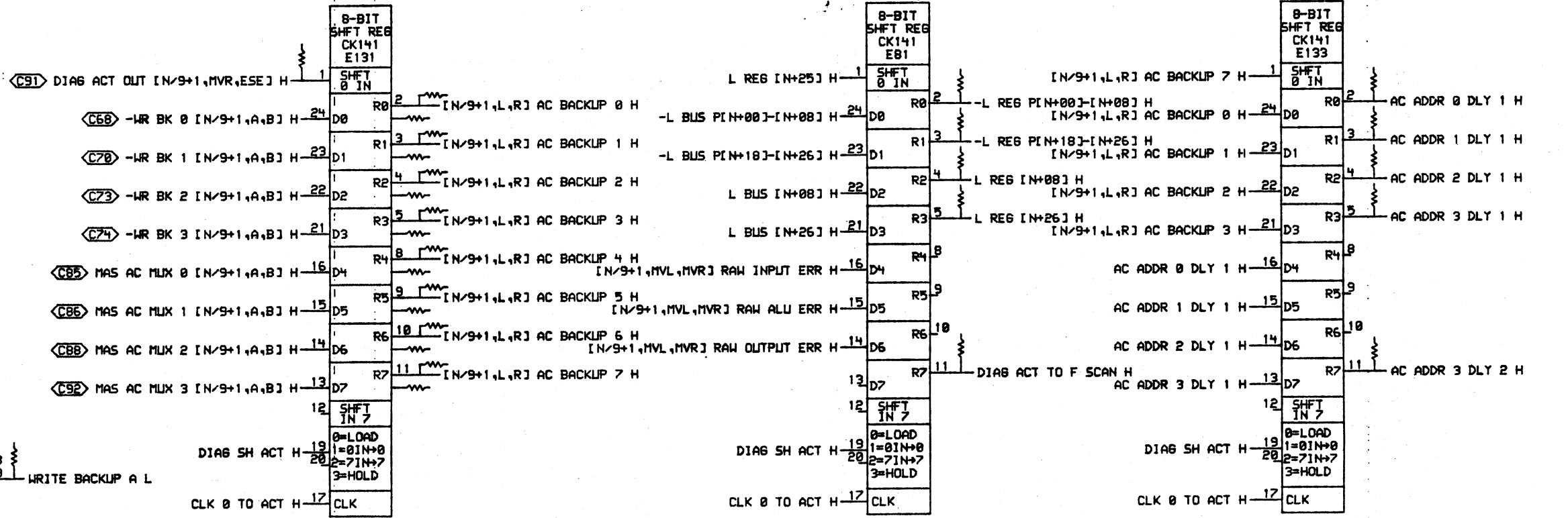
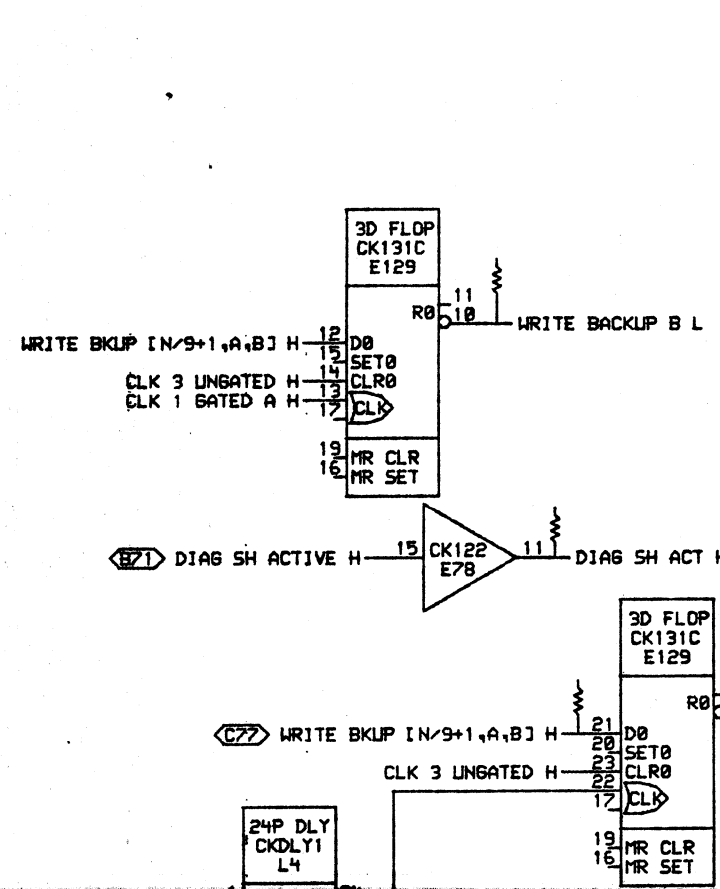
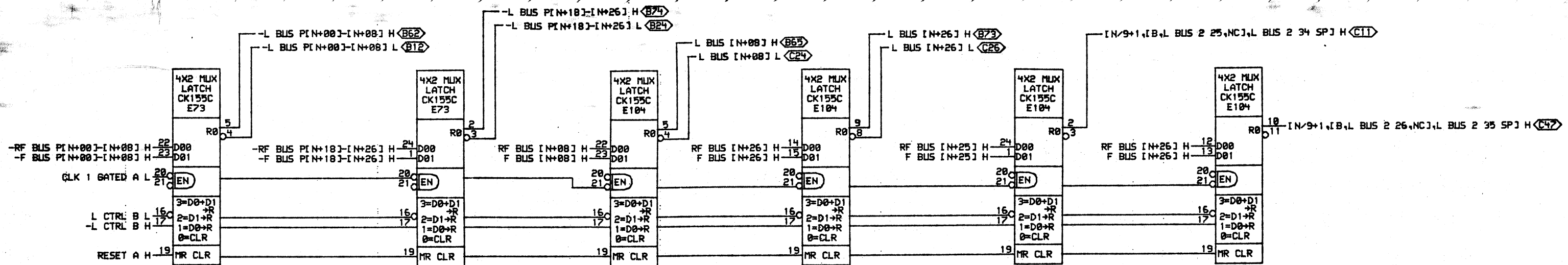
REVISIONS		
CHK	CHANGE NO.	REV

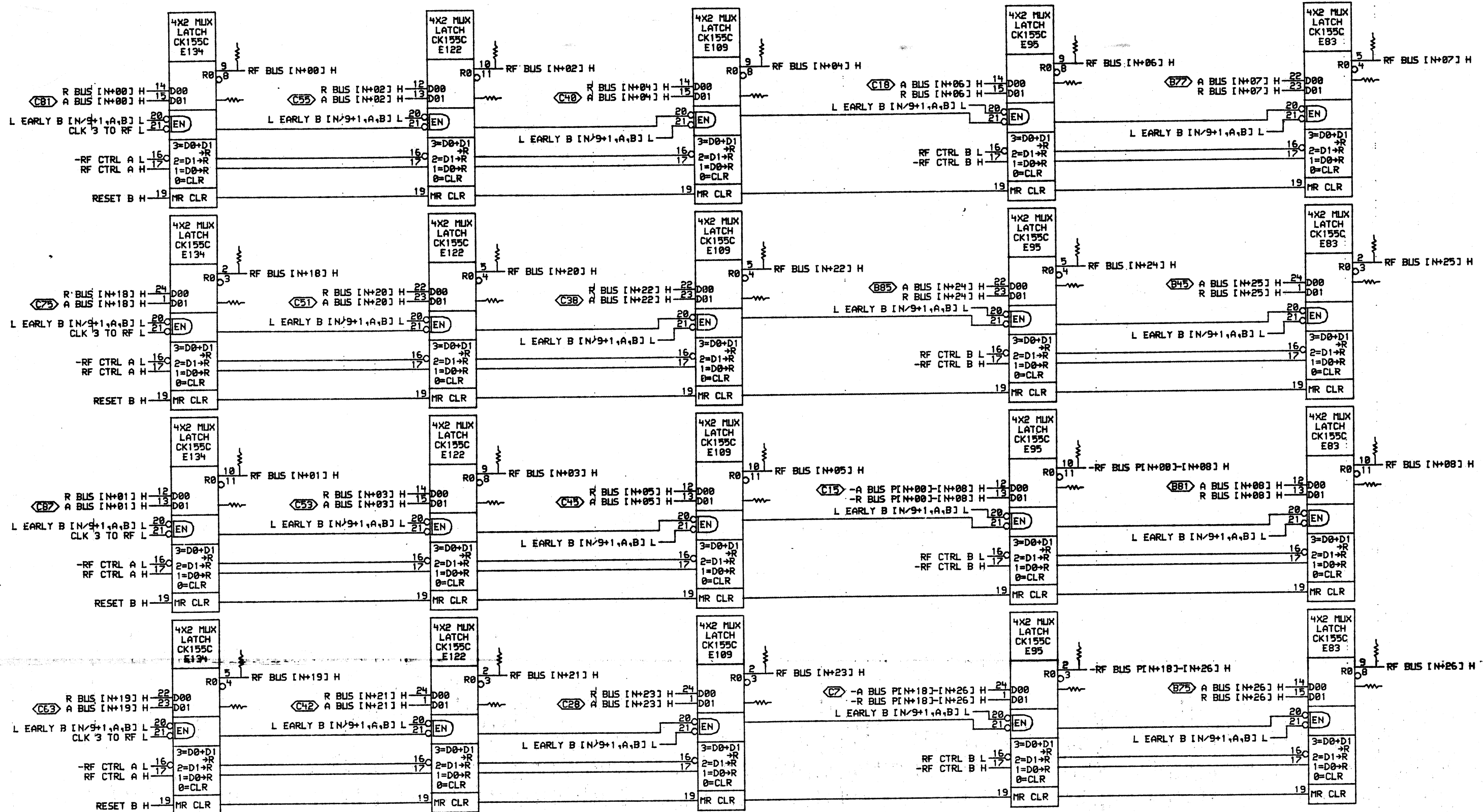
digital	DRN. <i>J. Family</i>	DATE 88-OCT-82	ENG.	DATE	TITLE: ALU, F MUX
	CHK'D.	DATE	BOARD LOCATION:	SHEET OF	BITS N+18, N+23-N+26
VUL CAN: <P888 L 0051 M VEC.DRW 106-OCT-82 21:02 NEXT HIGHER ASSEMBLY:					SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL:					D CS L0051-0-MVEC





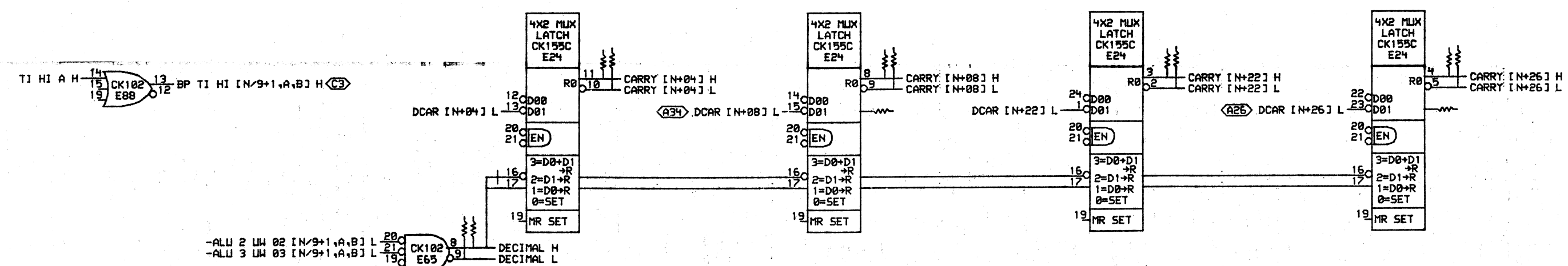
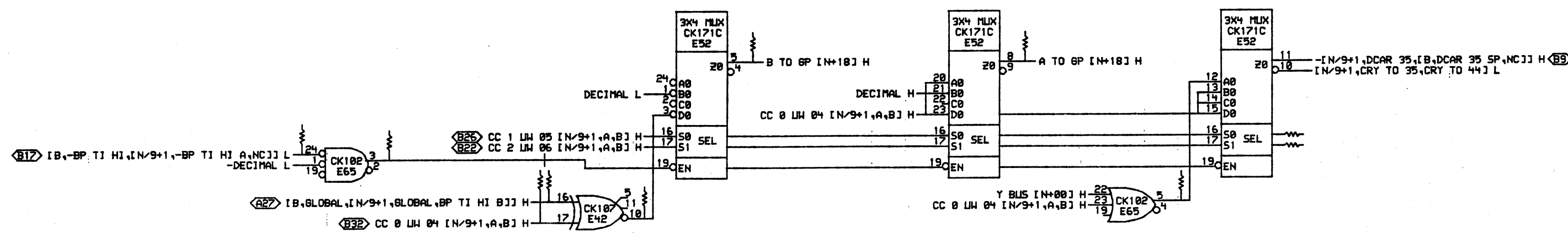
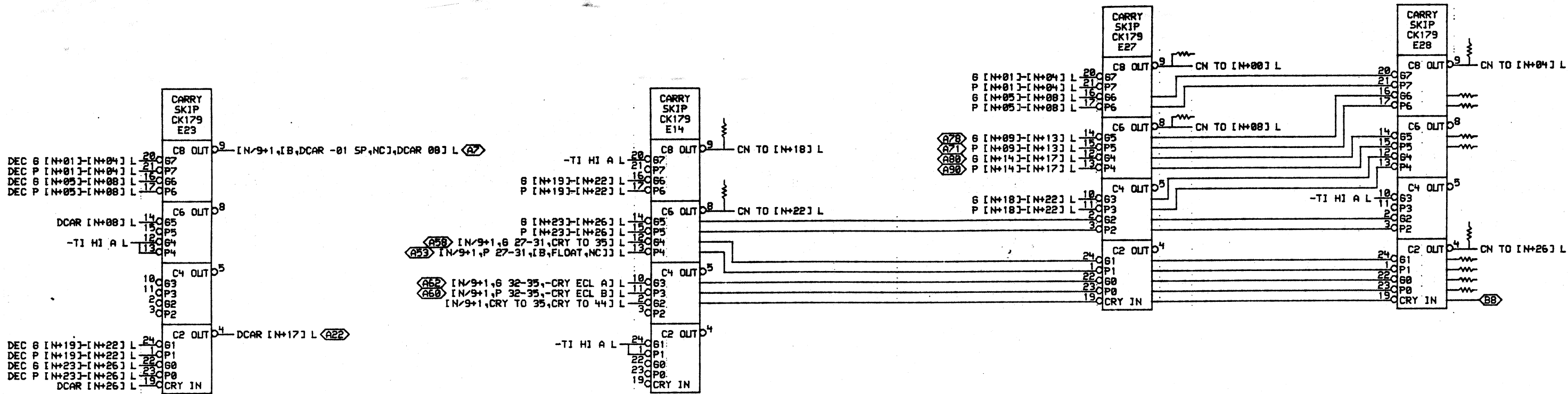
D
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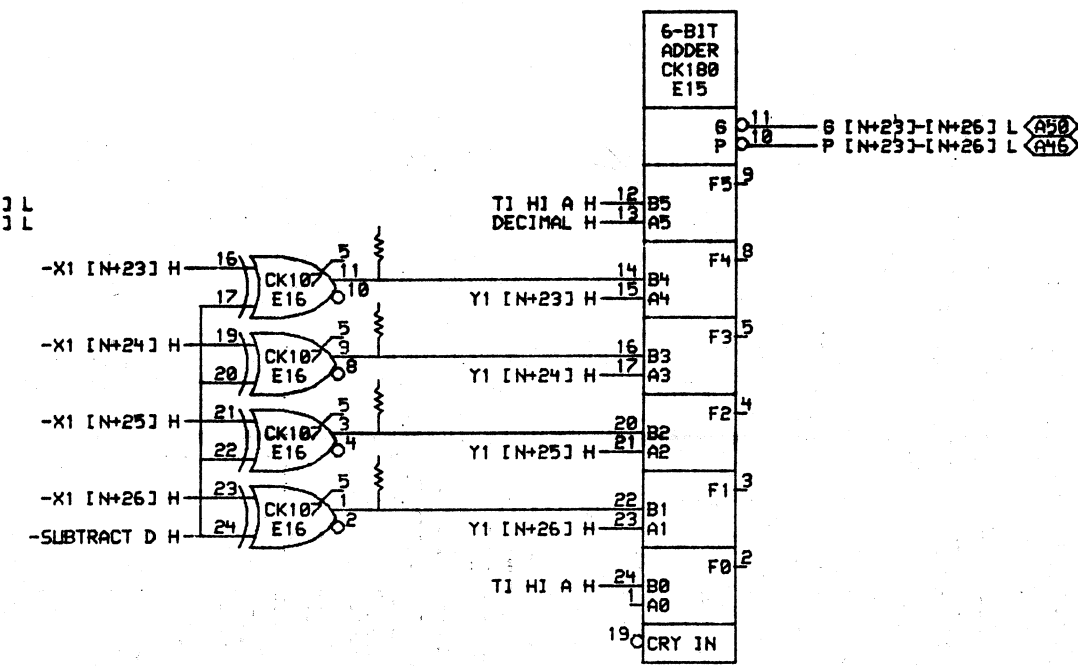
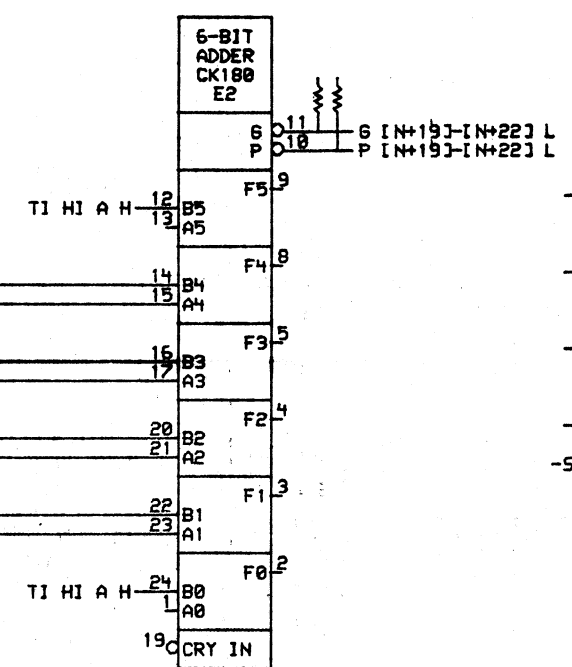
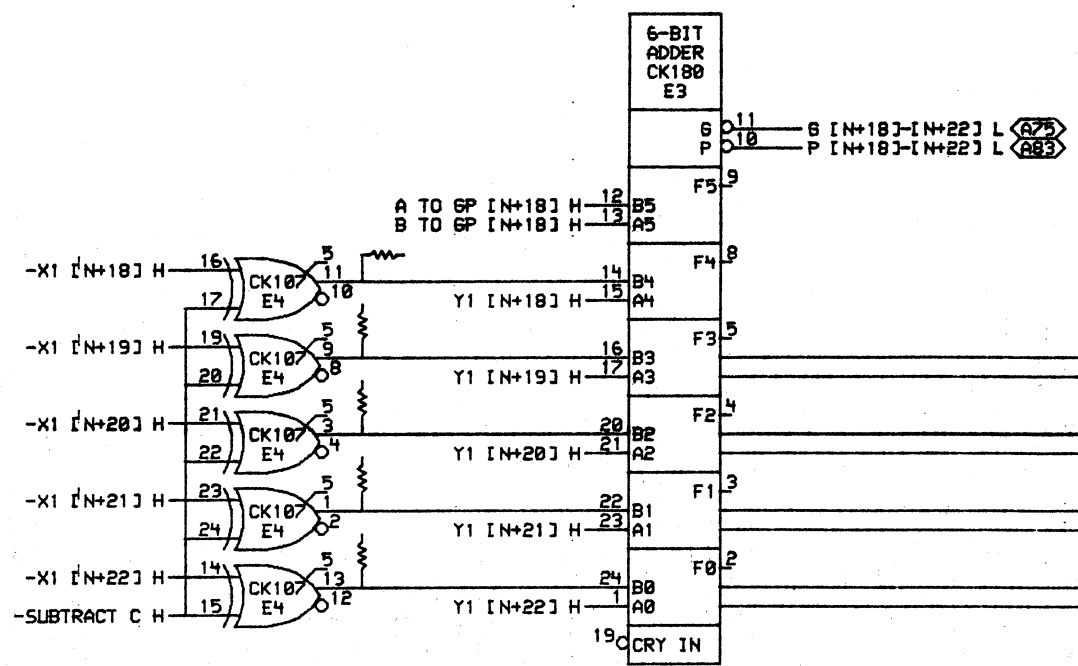
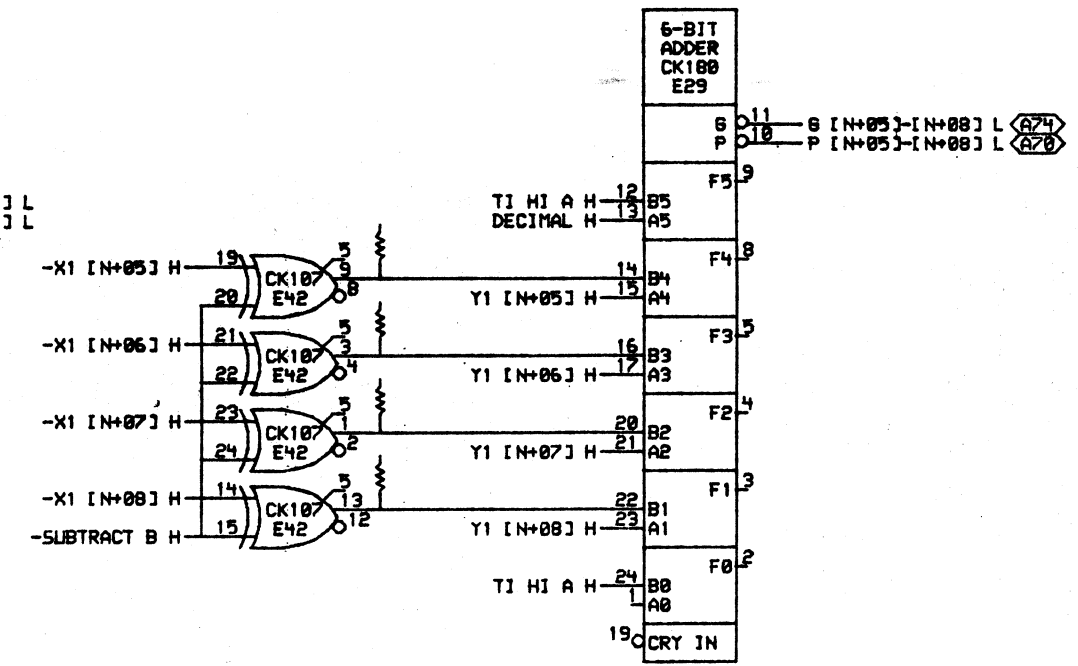
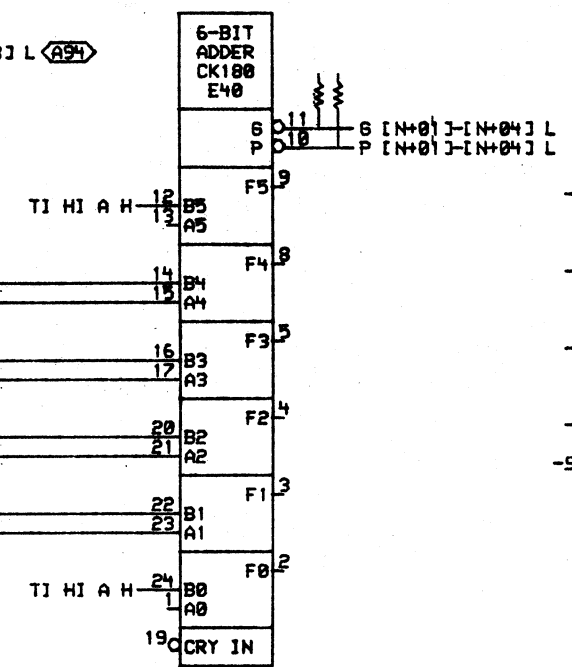
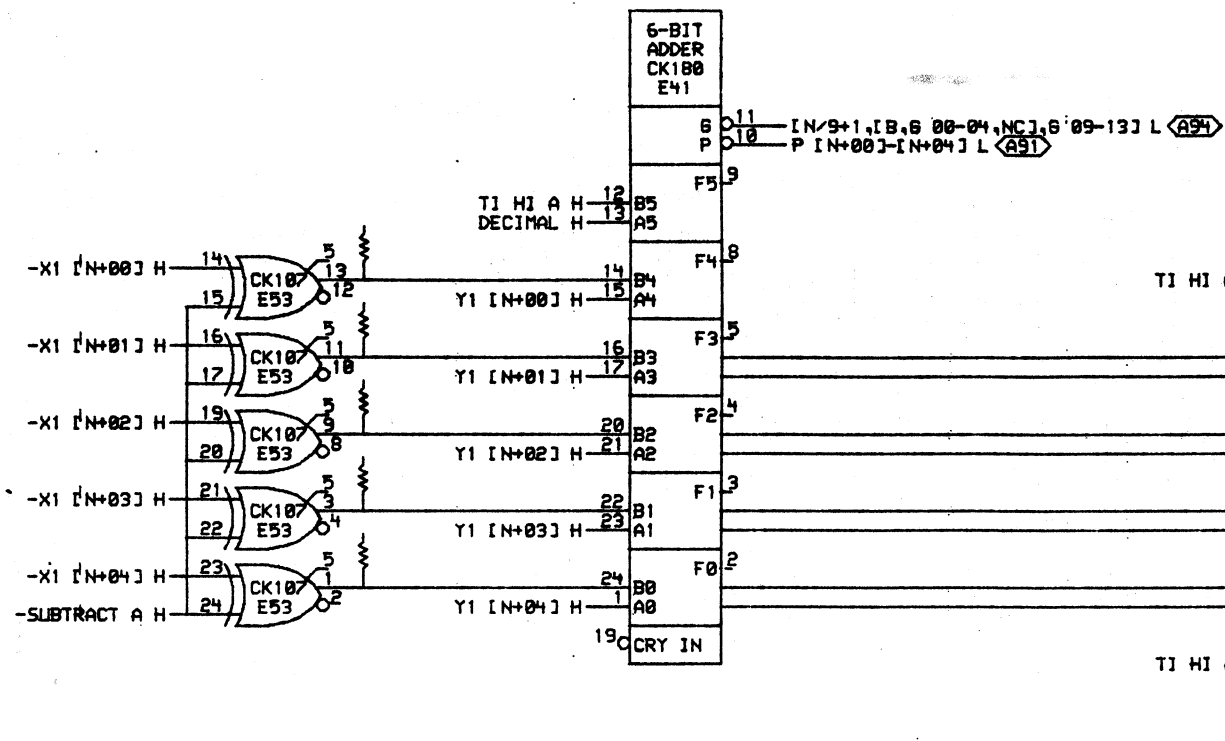


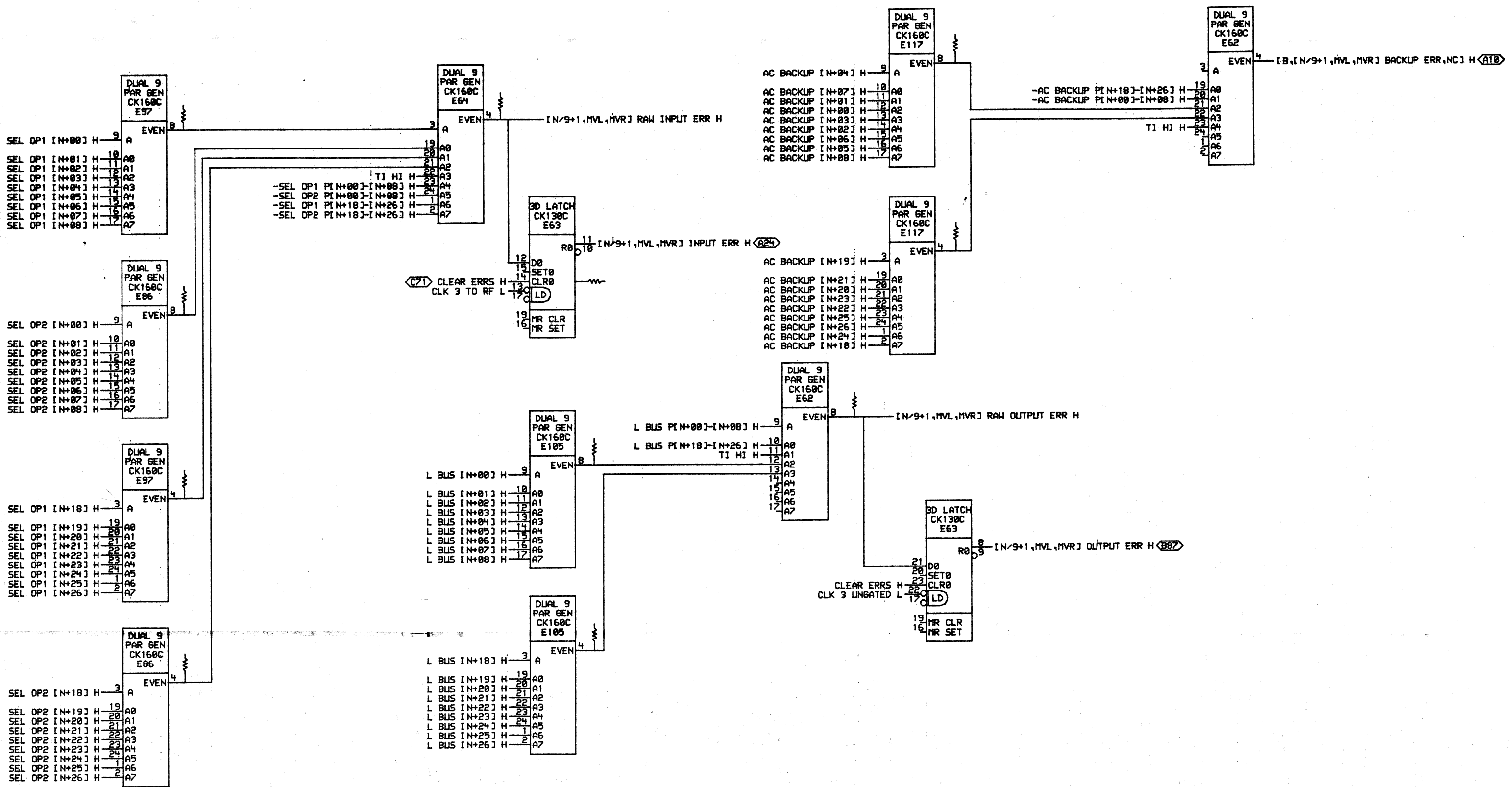


D
 C
 REV.
 NUMBER
 L0051-0-MVEG
 CS
 D
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REVISIONS	
CHK	CHANGE NO. REV

DATE	ENG.	DATE	TITLE:
06-OCT-82			INPUT/OUTPUT ERR

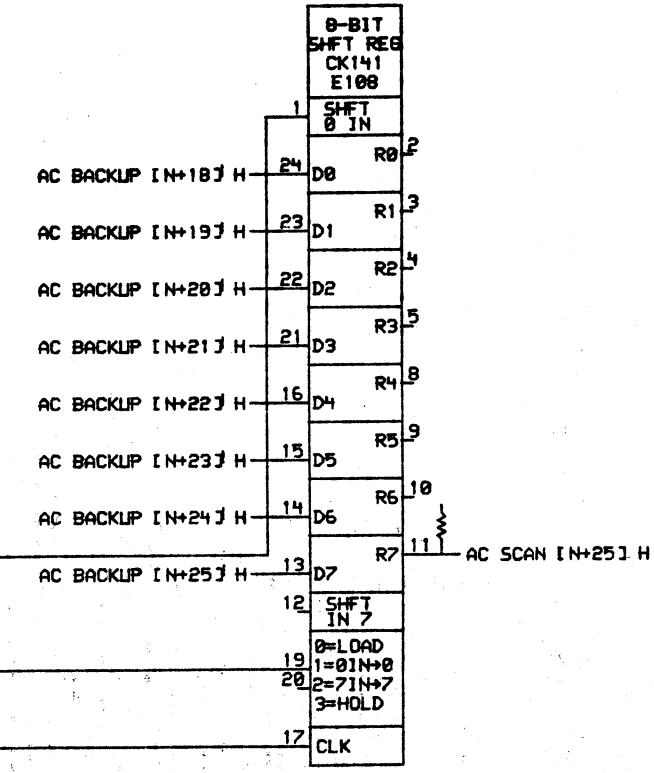
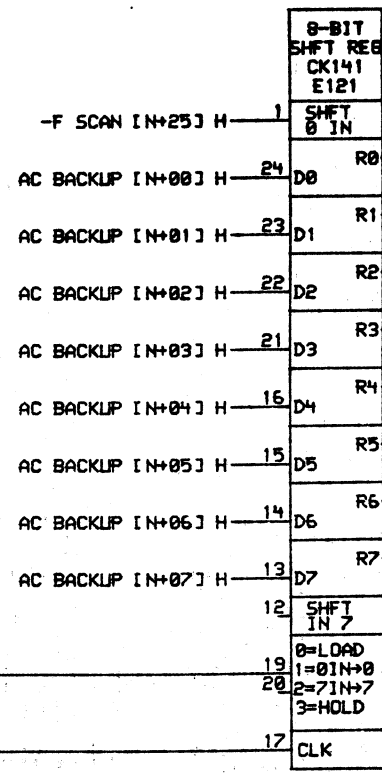
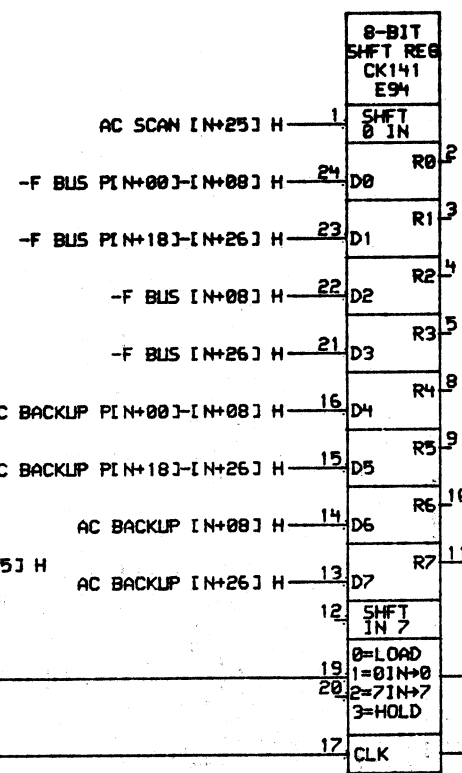
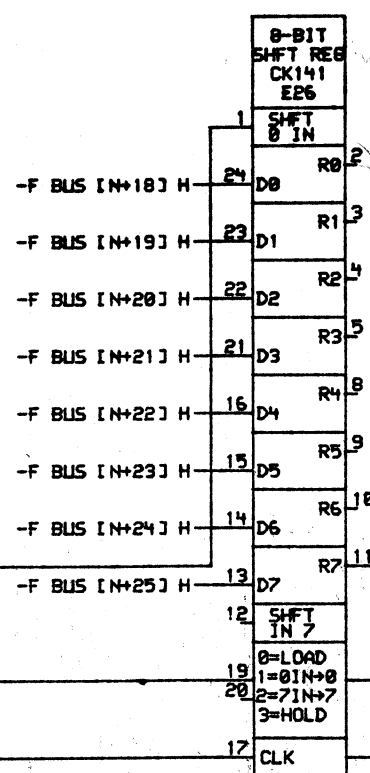
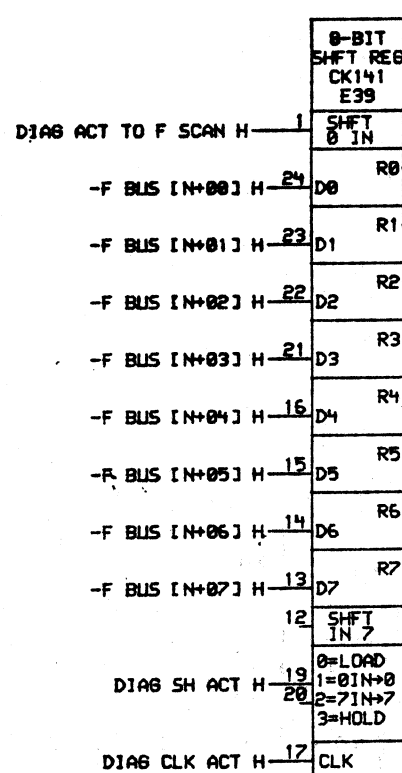
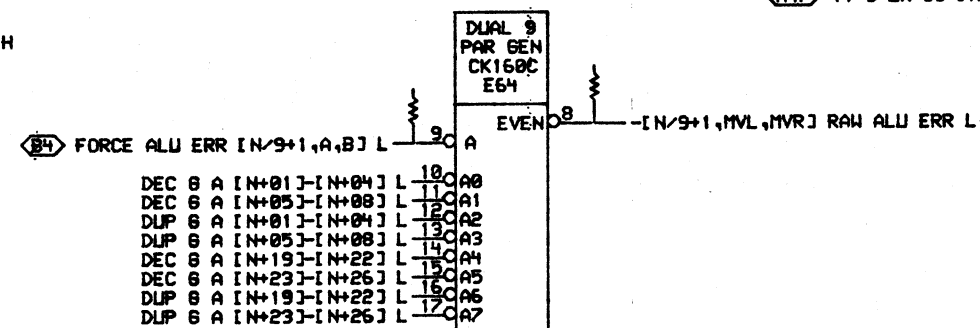
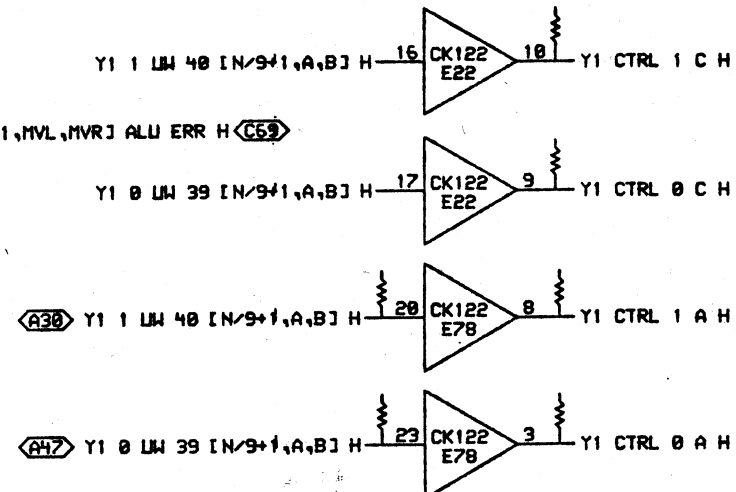
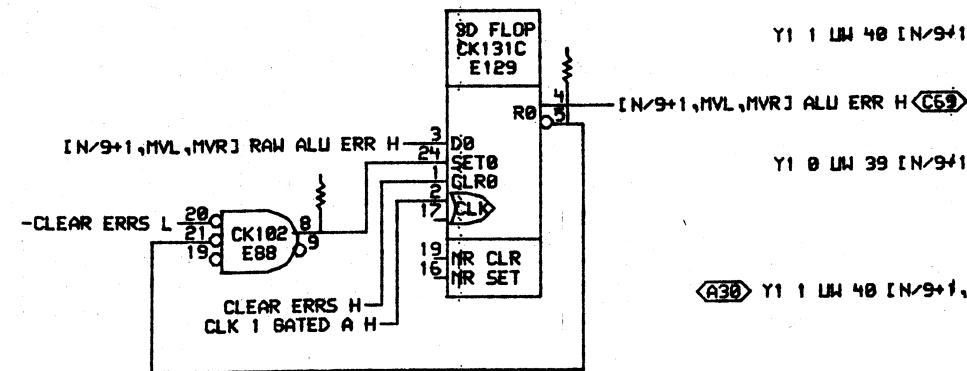
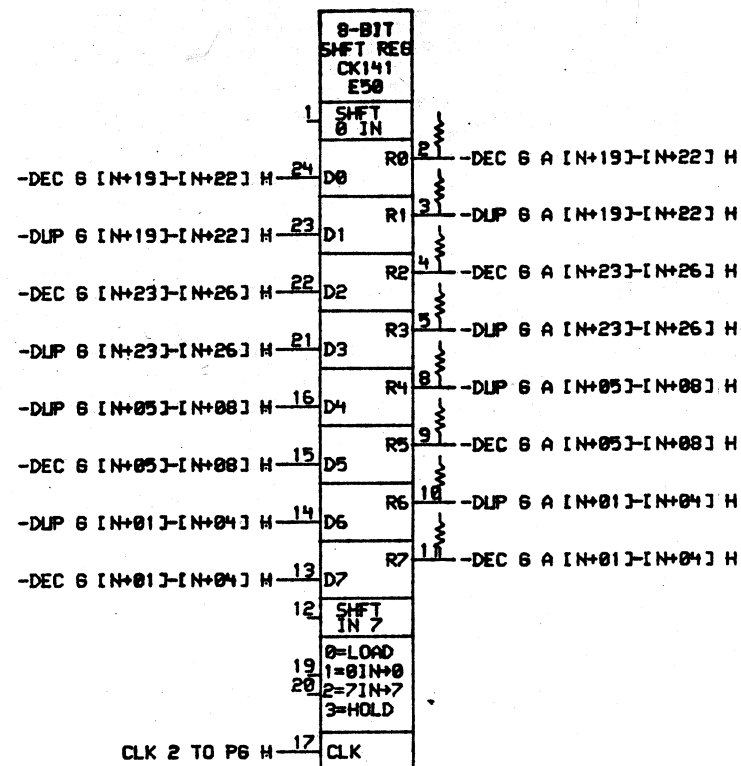
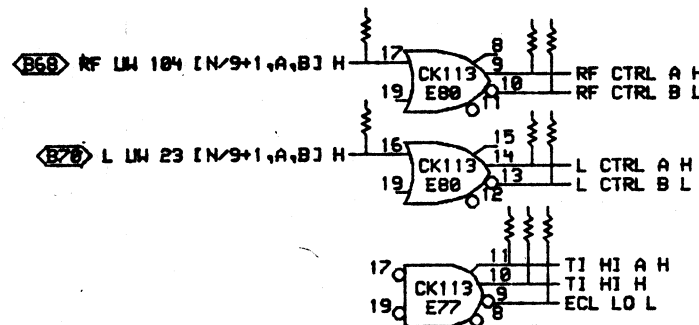
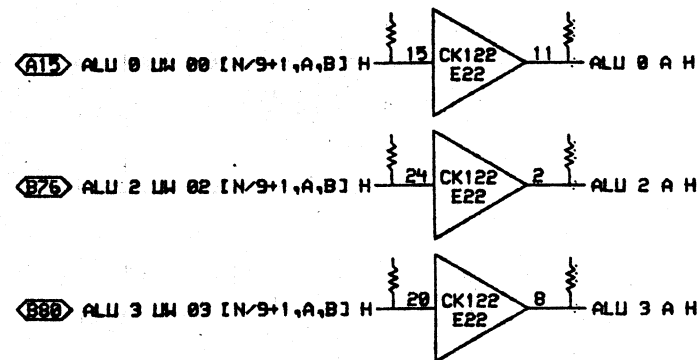
digital
CHK'D. *J. J. J.*

DATE	BOARD LOCATION	SHEET	OF
06-OCT-82 21:07			

SIZE	CODE	NUMBER	REV.
D	CS	L0051-0-MVEK	

VIL CANI < 2000 L0051 > MVEK.DRW 06-OCT-82 21:07 NEXT HIGHER ASSEMBLY:
FIRST USED ON OPTION MODEL:

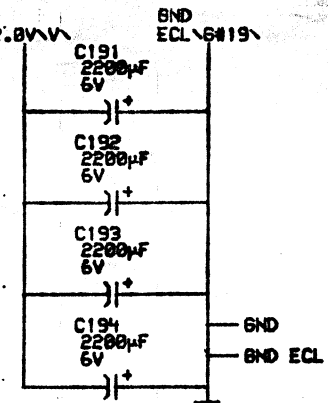
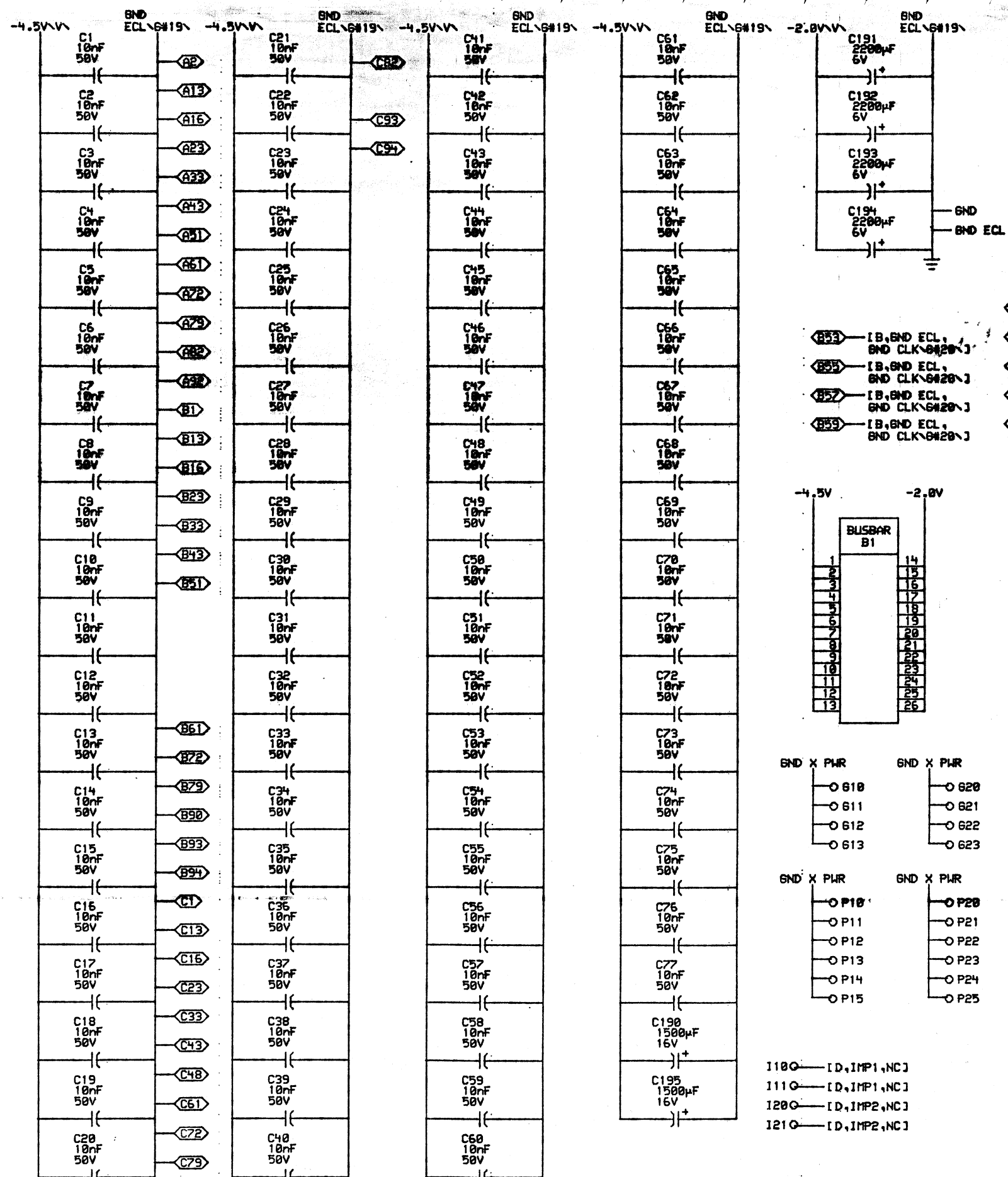
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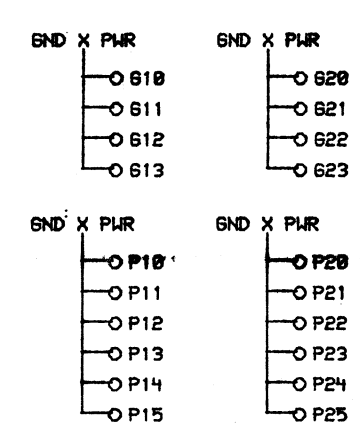
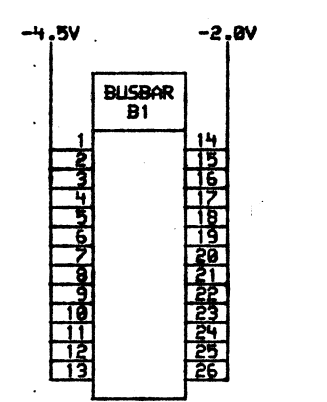
[B,DIAG ACT OUT [N/9+1,MVL,MVR],[N/9+1,EBX SCAN],DIAG ACT OUT MVR] H **C8**

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REV.
NUMBER
L0051-0-MVEL
CS
D
B
A



- ⊖ A1 — NC A3
- ⊖ A16 — NC A32
- ⊖ B1 — [B, GND ECL, GND CLK/GND20]
- ⊖ B13 — [B, GND ECL, GND CLK/GND20]
- ⊖ B16 — [B, GND ECL, GND CLK/GND20]
- ⊖ B23 — [B, GND ECL, GND CLK/GND20]
- ⊖ B29 — [B, GND ECL, GND CLK/GND20]
- ⊖ B33 — [B, GND ECL, GND CLK/GND20]
- ⊖ B43 — [B, GND ECL, GND CLK/GND20]
- ⊖ B51 — [B, GND ECL, GND CLK/GND20]
- ⊖ B61 — [B, GND ECL, GND CLK/GND20]
- ⊖ B72 — [B, GND ECL, GND CLK/GND20]
- ⊖ B79 — [B, GND ECL, GND CLK/GND20]
- ⊖ B90 — [B, GND ECL, GND CLK/GND20]
- ⊖ B93 — [B, GND ECL, GND CLK/GND20]
- ⊖ B94 — [B, GND ECL, GND CLK/GND20]
- ⊖ C1 — [B, GND ECL, GND CLK/GND20]
- ⊖ C13 — [B, GND ECL, GND CLK/GND20]
- ⊖ C16 — [B, GND ECL, GND CLK/GND20]
- ⊖ C23 — [B, GND ECL, GND CLK/GND20]
- ⊖ C33 — [B, GND ECL, GND CLK/GND20]
- ⊖ C43 — [B, GND ECL, GND CLK/GND20]
- ⊖ C48 — [B, GND ECL, GND CLK/GND20]
- ⊖ C51 — [B, GND ECL, GND CLK/GND20]
- ⊖ C59 — [B, GND ECL, GND CLK/GND20]
- ⊖ C62 — [B, GND ECL, GND CLK/GND20]
- ⊖ C63 — [B, GND ECL, GND CLK/GND20]
- ⊖ C64 — [B, GND ECL, GND CLK/GND20]
- ⊖ C65 — [B, GND ECL, GND CLK/GND20]
- ⊖ C66 — [B, GND ECL, GND CLK/GND20]
- ⊖ C67 — [B, GND ECL, GND CLK/GND20]
- ⊖ C68 — [B, GND ECL, GND CLK/GND20]
- ⊖ C69 — [B, GND ECL, GND CLK/GND20]
- ⊖ C70 — [B, GND ECL, GND CLK/GND20]
- ⊖ C71 — [B, GND ECL, GND CLK/GND20]
- ⊖ C72 — [B, GND ECL, GND CLK/GND20]
- ⊖ C73 — [B, GND ECL, GND CLK/GND20]
- ⊖ C74 — [B, GND ECL, GND CLK/GND20]
- ⊖ C75 — [B, GND ECL, GND CLK/GND20]
- ⊖ C76 — [B, GND ECL, GND CLK/GND20]
- ⊖ C77 — [B, GND ECL, GND CLK/GND20]
- ⊖ C78 — [B, GND ECL, GND CLK/GND20]
- ⊖ C79 — [B, GND ECL, GND CLK/GND20]
- ⊖ C82 — [B, GND ECL, GND CLK/GND20]
- ⊖ C83 — [B, GND ECL, GND CLK/GND20]
- ⊖ C84 — [B, GND ECL, GND CLK/GND20]
- ⊖ C85 — [B, GND ECL, GND CLK/GND20]
- ⊖ C86 — [B, GND ECL, GND CLK/GND20]
- ⊖ C89 — GND
- ⊖ C90 — OPEN H



- 1180 — [D, IMP1, NC]
- 1110 — [D, IMP1, NC]
- 1200 — [D, IMP2, NC]
- 1210 — [D, IMP2, NC]

SPARE E1

SPARE E31

- TERMS 211
- TERMS 230
- TERMS 253
- TERMS 278

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REVISIONS	
CHK	CHANGE NO. REV

	DRN. <i>J. J. J.</i>	DATE 80-OCT-82	ENG.	DATE	TITLE: 141 MVE POWER & GROUND
	CHK'D.	DATE	BOARD LOCATION: SHEET 1 OF 1		
VULCAN (2000) L0051-MVEN.DRW 106-OCT-82 21:09		NEXT HIGHER ASSEMBLY: B-DD-L0051-0		SIZE CODE D CS	NUMBER L0051-0-MVEN
FIRST USED ON OPTION/MODEL: 2080				REV. *	

8 7 6 5 4 3 2 1

REV. NUMBER SIZE CODE D

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 VULCAN:<2080.L0048.REVB>13BCLK2
 VULCAN:<2080.L0048.REVB>11BCLK3
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 VULCAN:<2080.L0048.REVB>3BCLK9
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 VULCAN:<2080.L0048.REVB>14BCLKG
 VULCAN:<2080.L0048.REVB>15BCLKH

D

D

C

C

B

REV.

NUMBER

SIZE CODE SP

B

D

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REVISIONS		
CHK	CHANGE NO.	REV

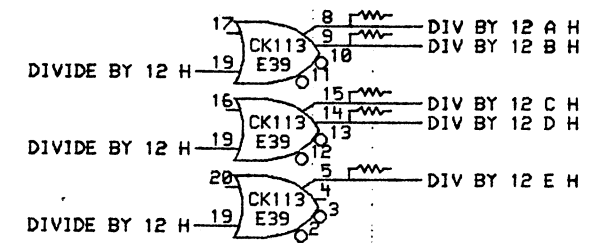
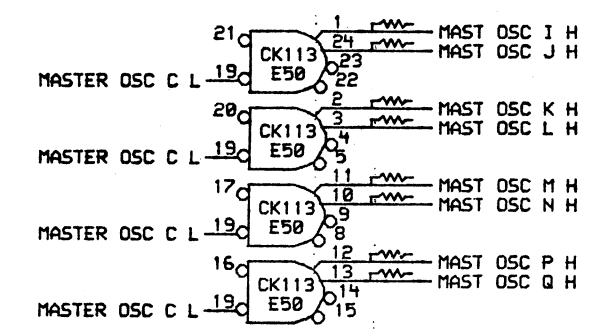
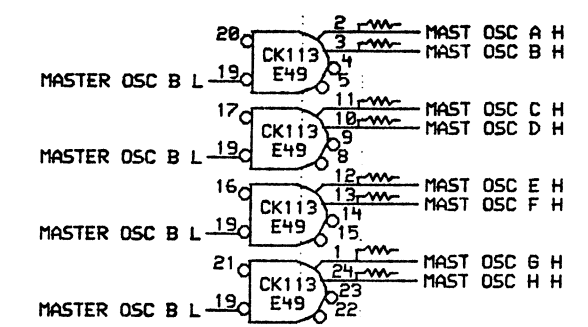
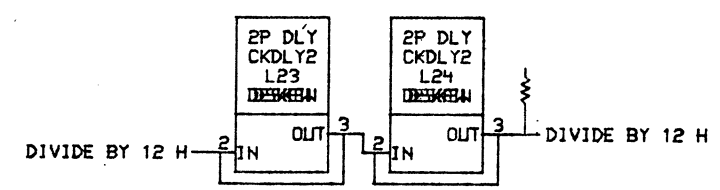
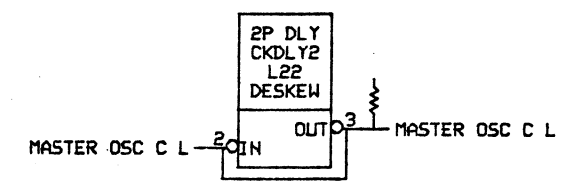
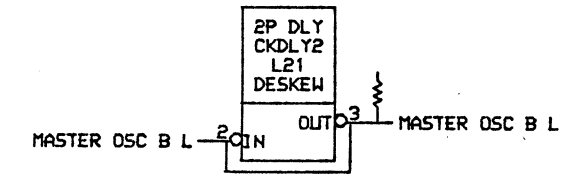
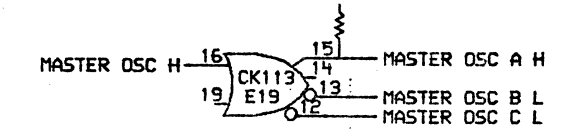
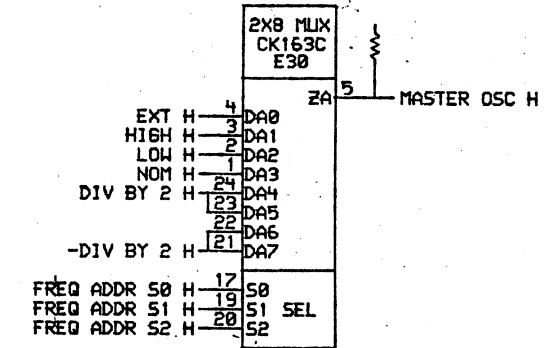
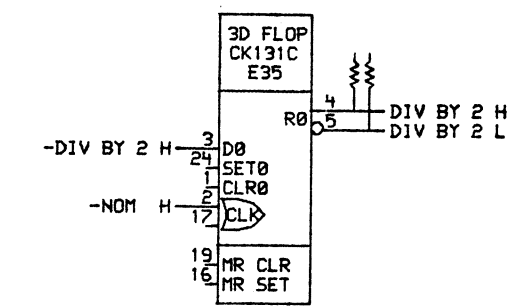
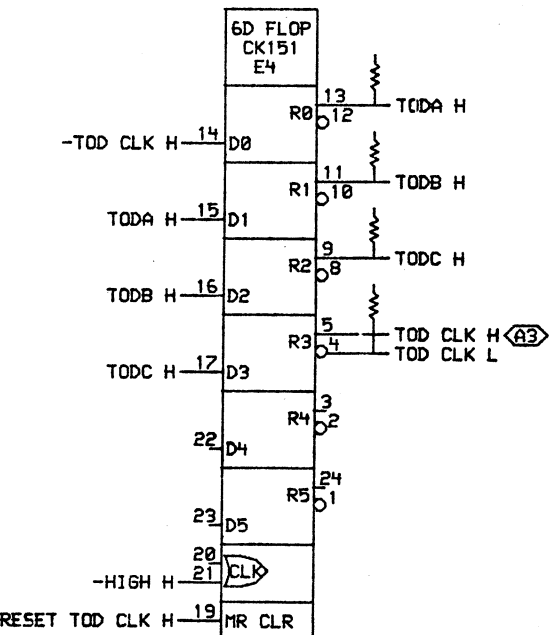
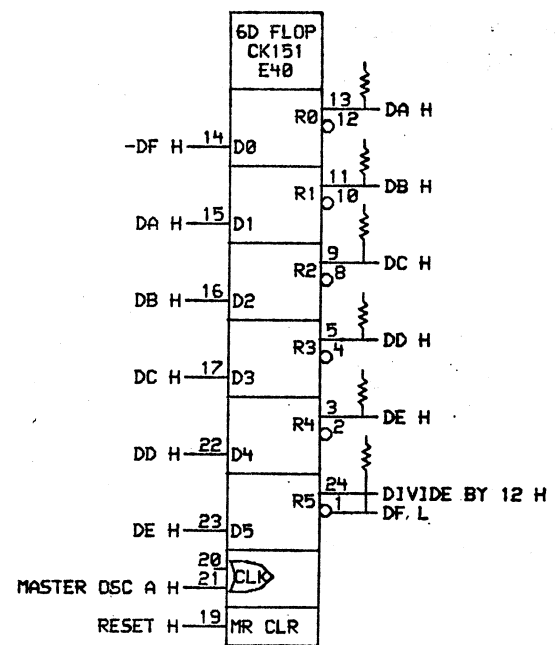
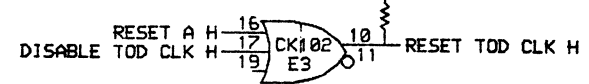
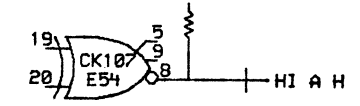
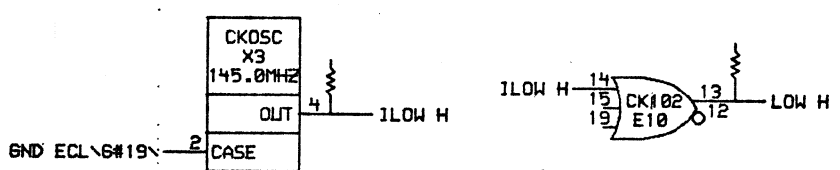
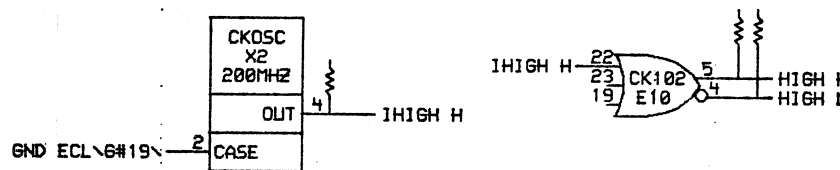
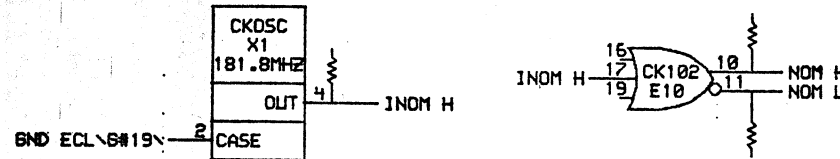
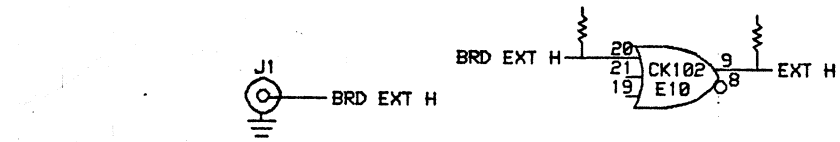
digital
 ARCH:KUMLER.PLOT5\LCLK.DPL.1 10-FEB-83 03:05
 FIRST USED ON OPTION/MODEL:

DRN. *J. J. J.*
 CHK'D.

DATE	ENG.	DATE	TITLE:
10-FEB-83			
DATE	BOARD LOCATION:		
	SHEET	OF	

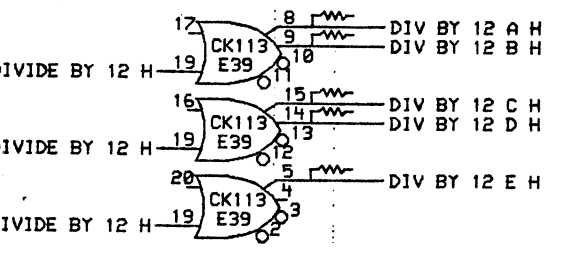
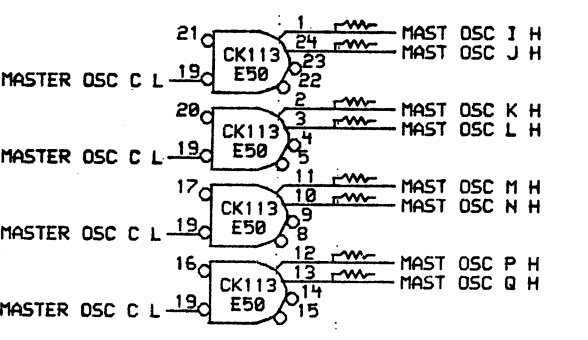
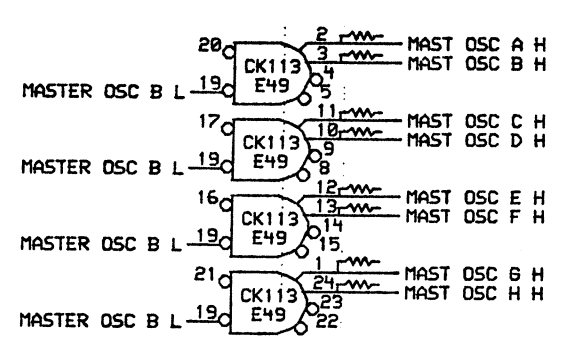
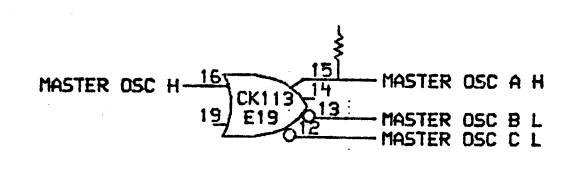
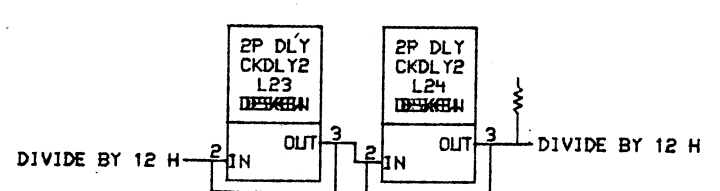
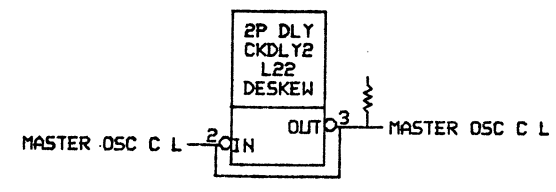
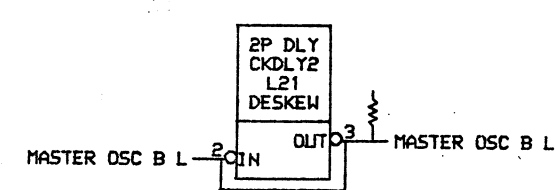
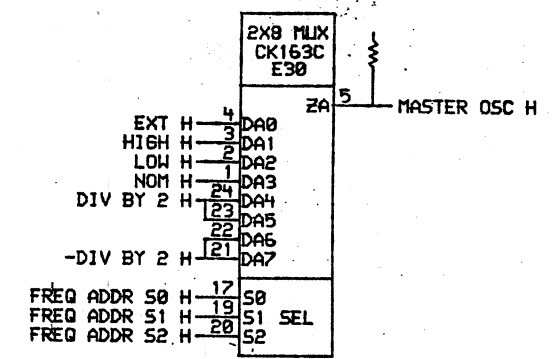
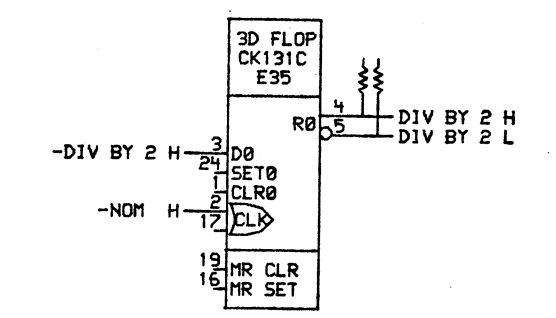
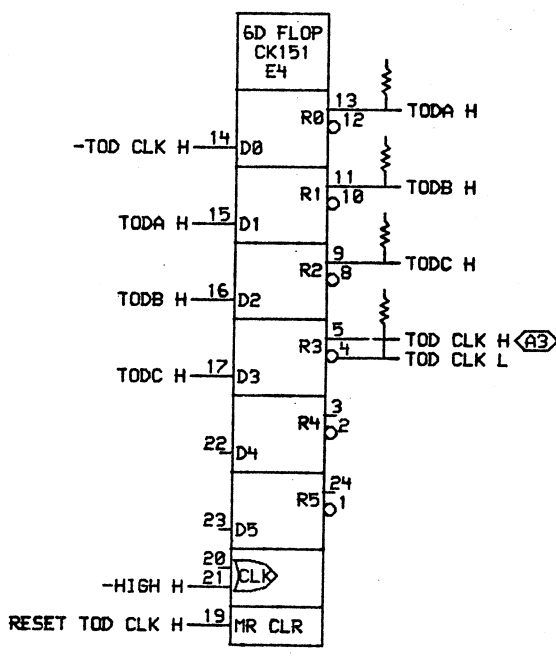
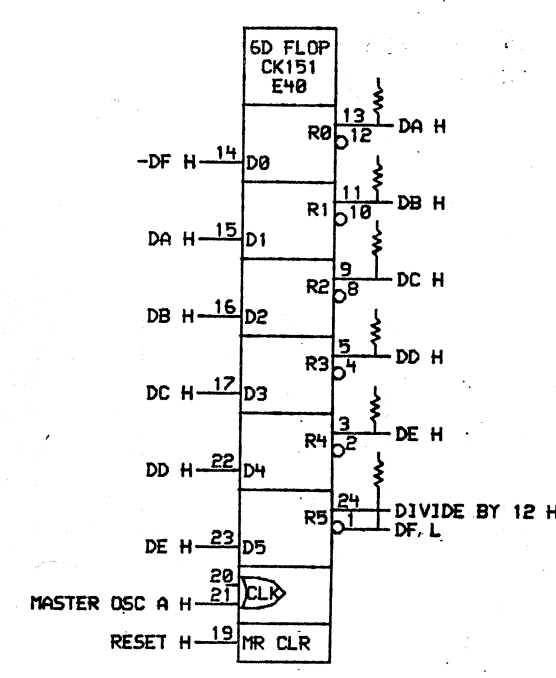
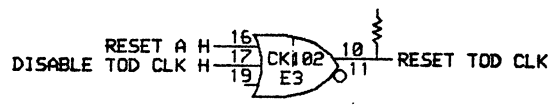
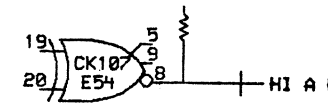
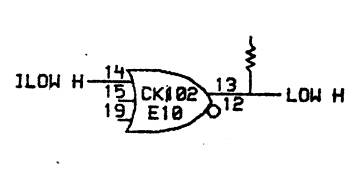
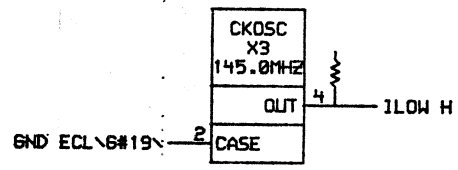
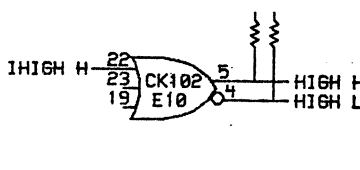
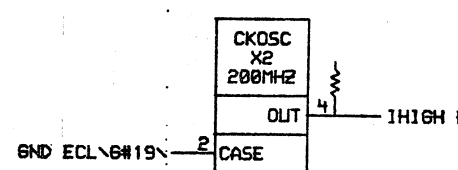
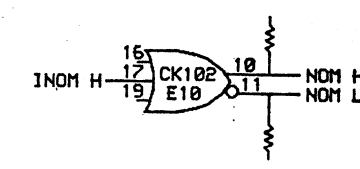
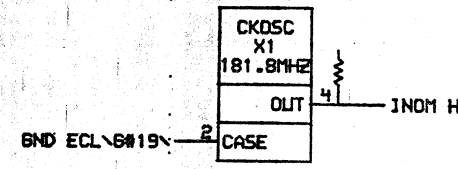
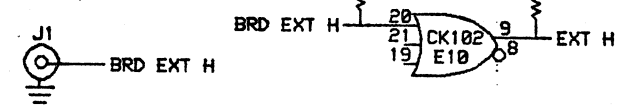
SIZE	CODE	NUMBER	REV.
D	SP		

8 7 6 5 4 3 2 1



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		MAST OSC AND PHASE GENERATOR
FIRST USED ON OPTION/MODEL: 2080				SIZE CODE NUMBER REV.
				D C5 L0048-1-CLK1

D

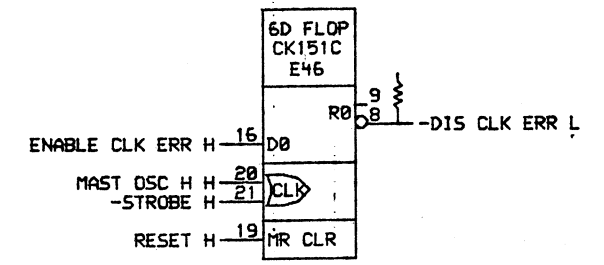
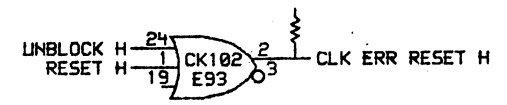
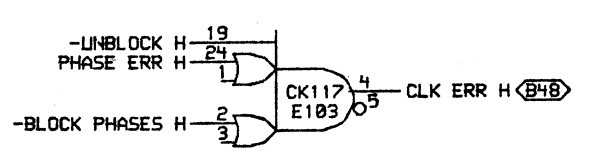
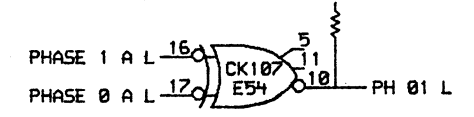
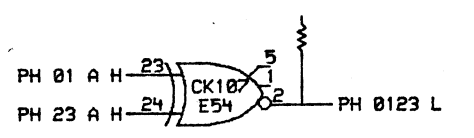
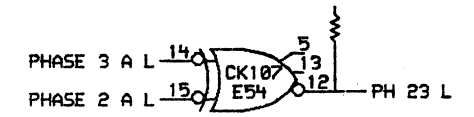
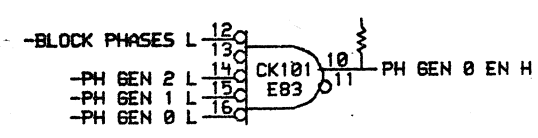
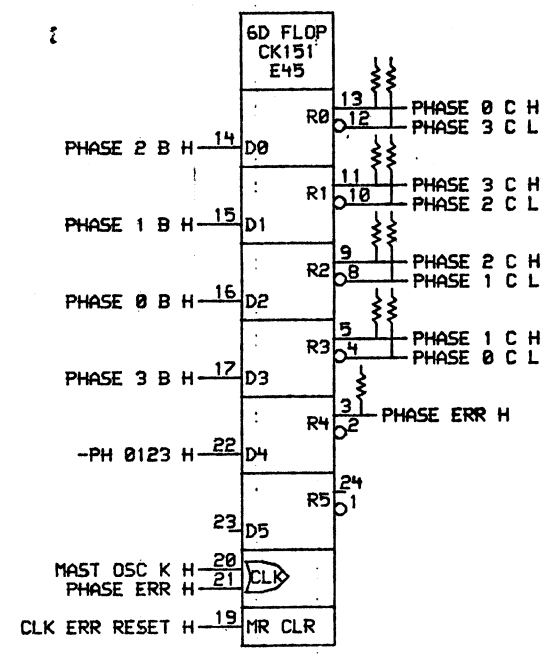
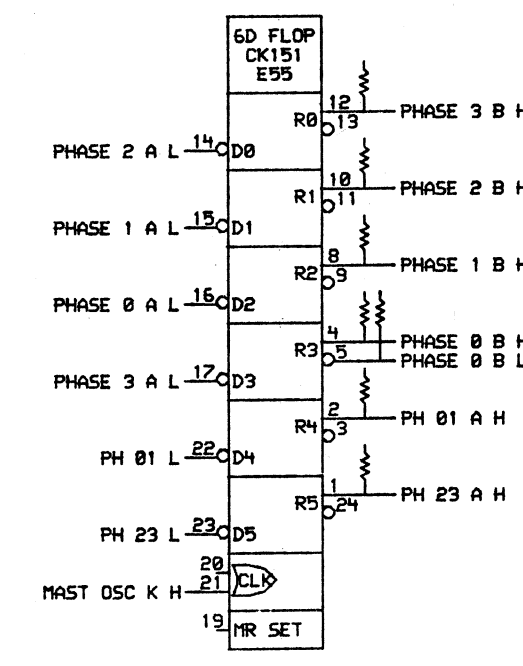
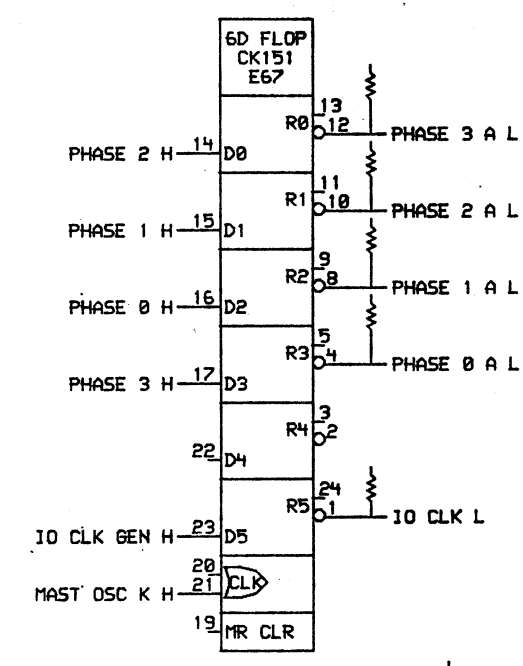
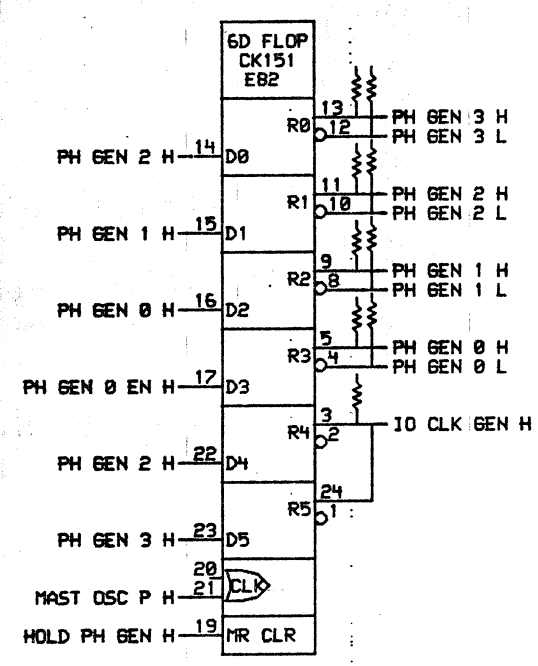
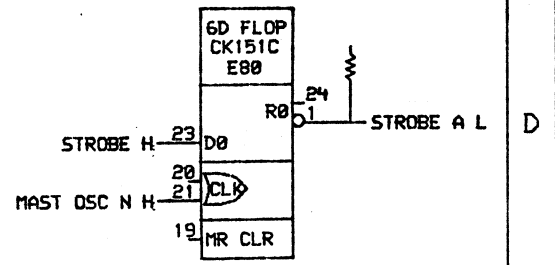
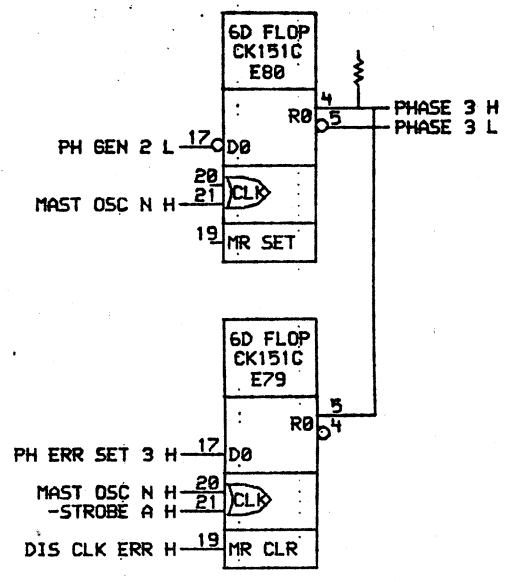
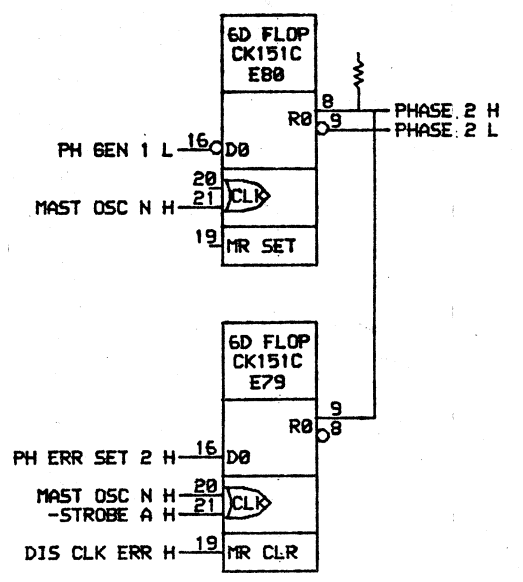
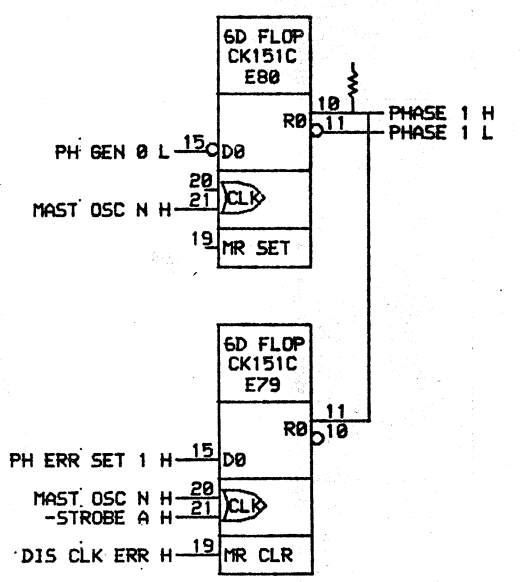
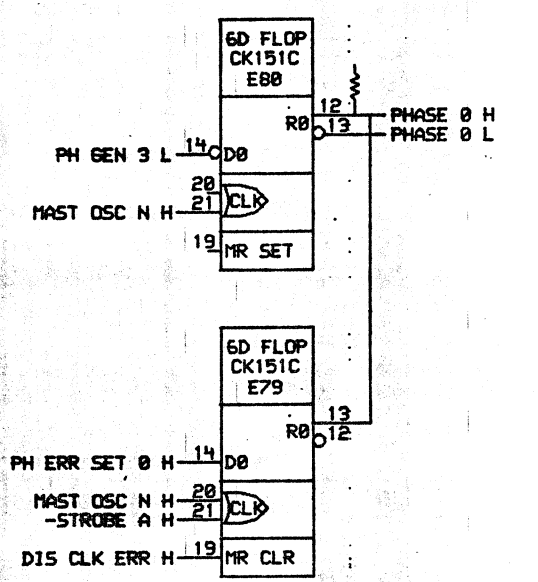


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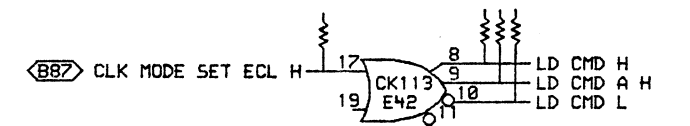
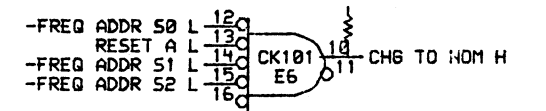
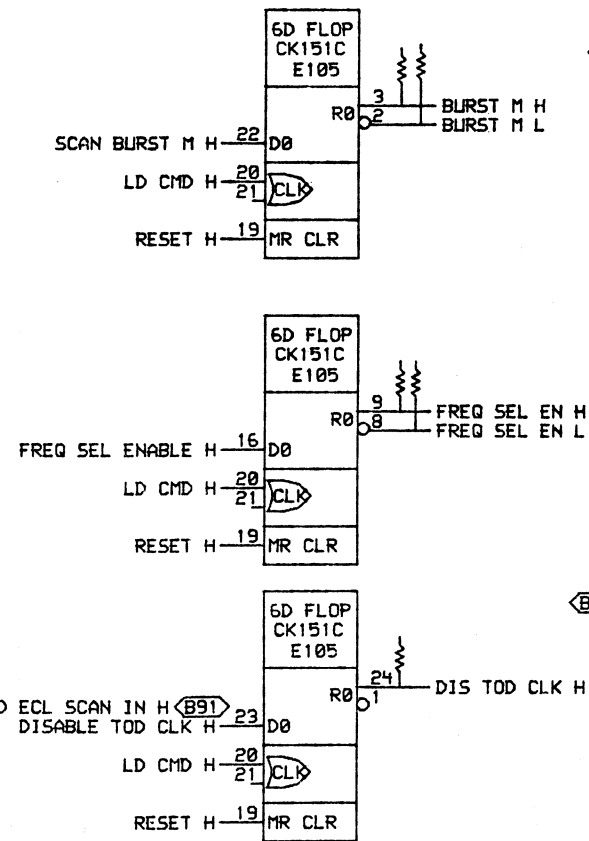
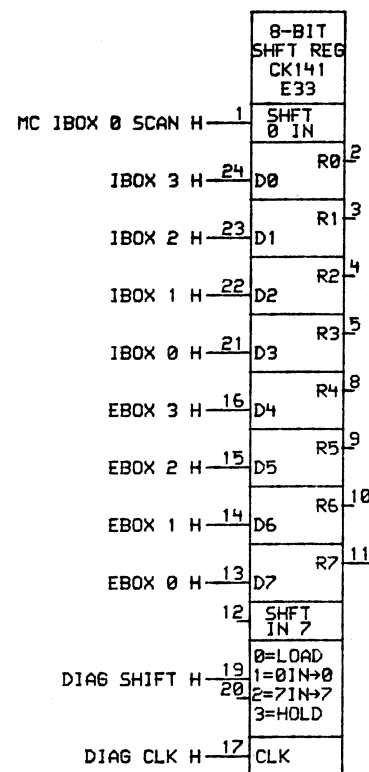
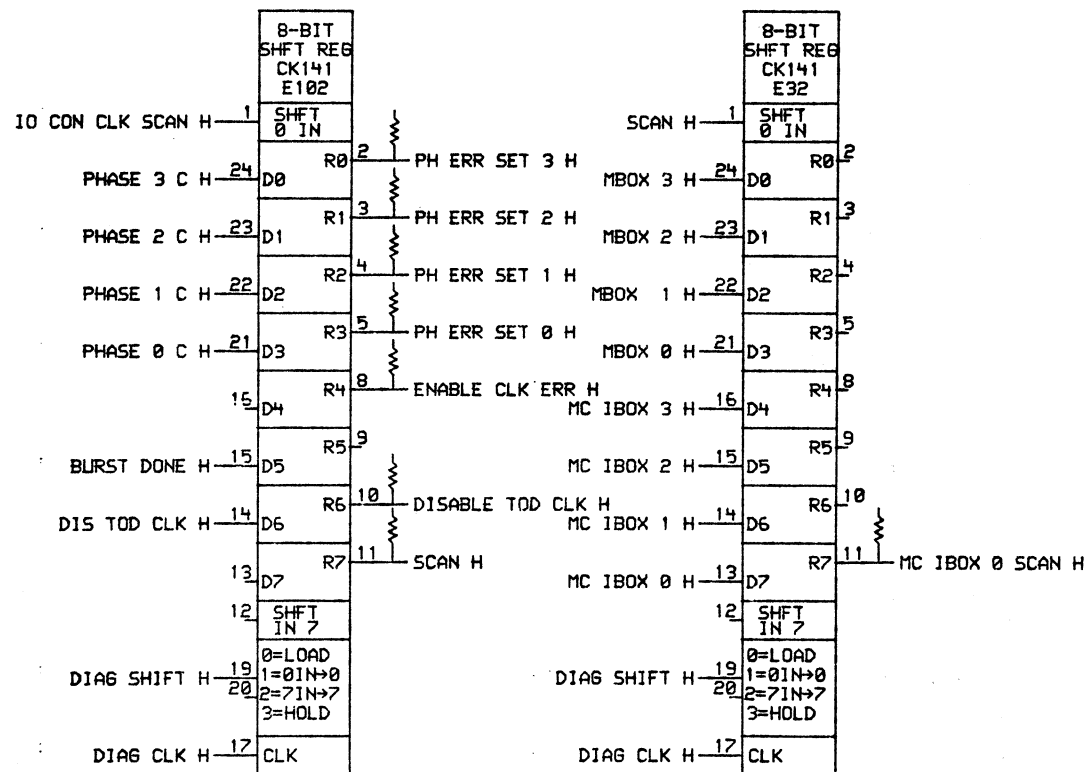
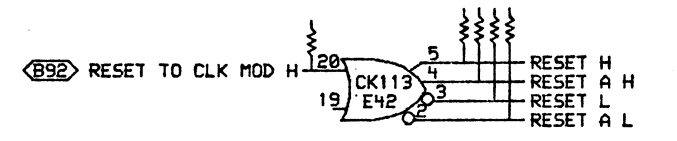
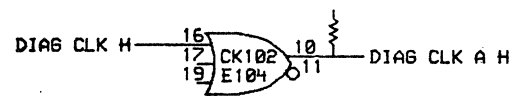
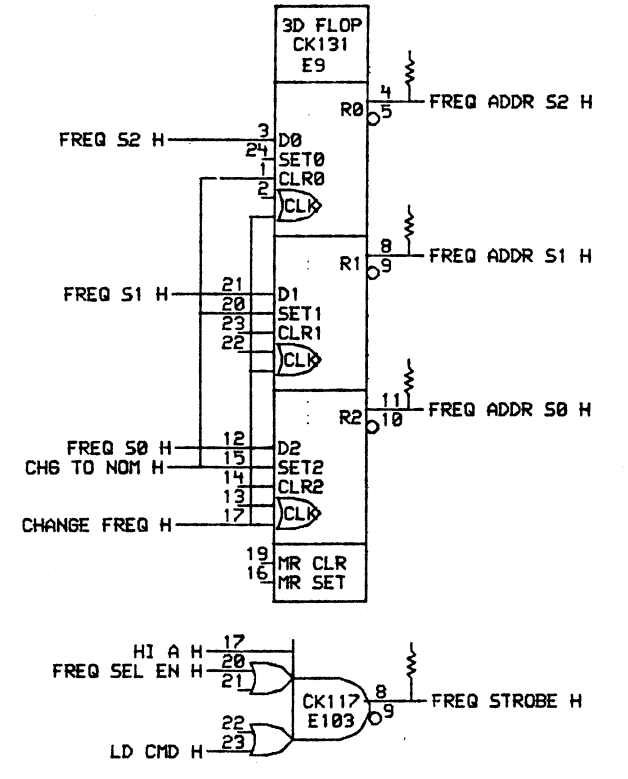
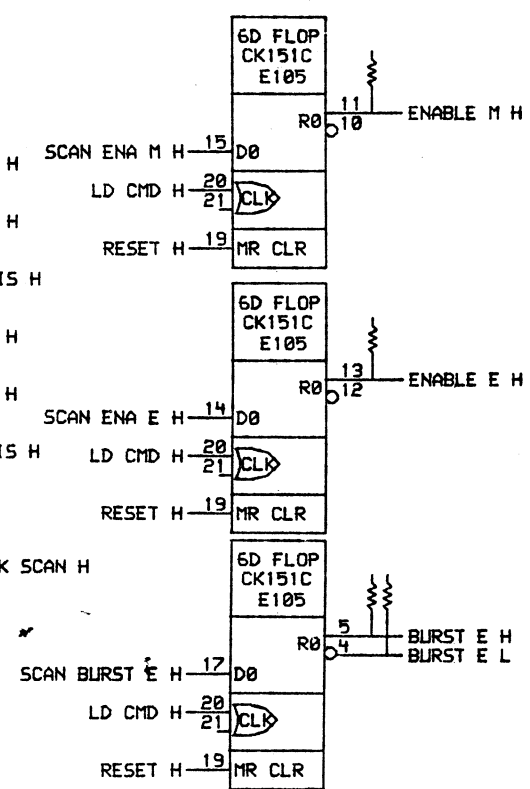
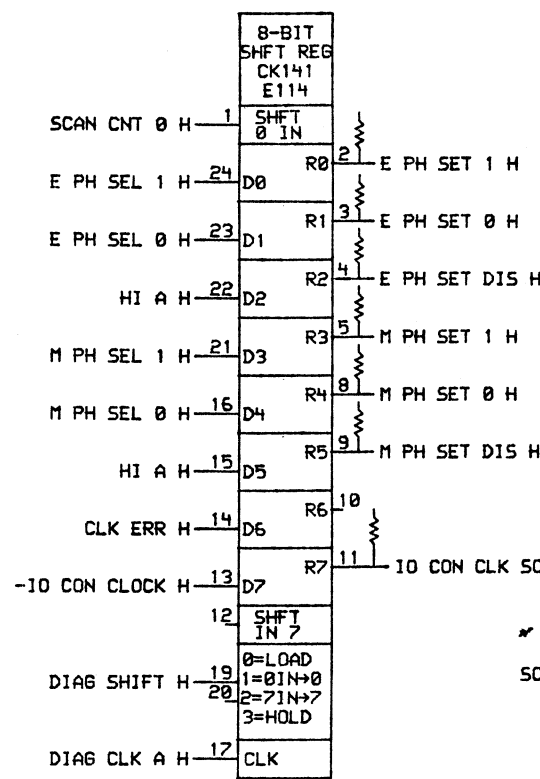
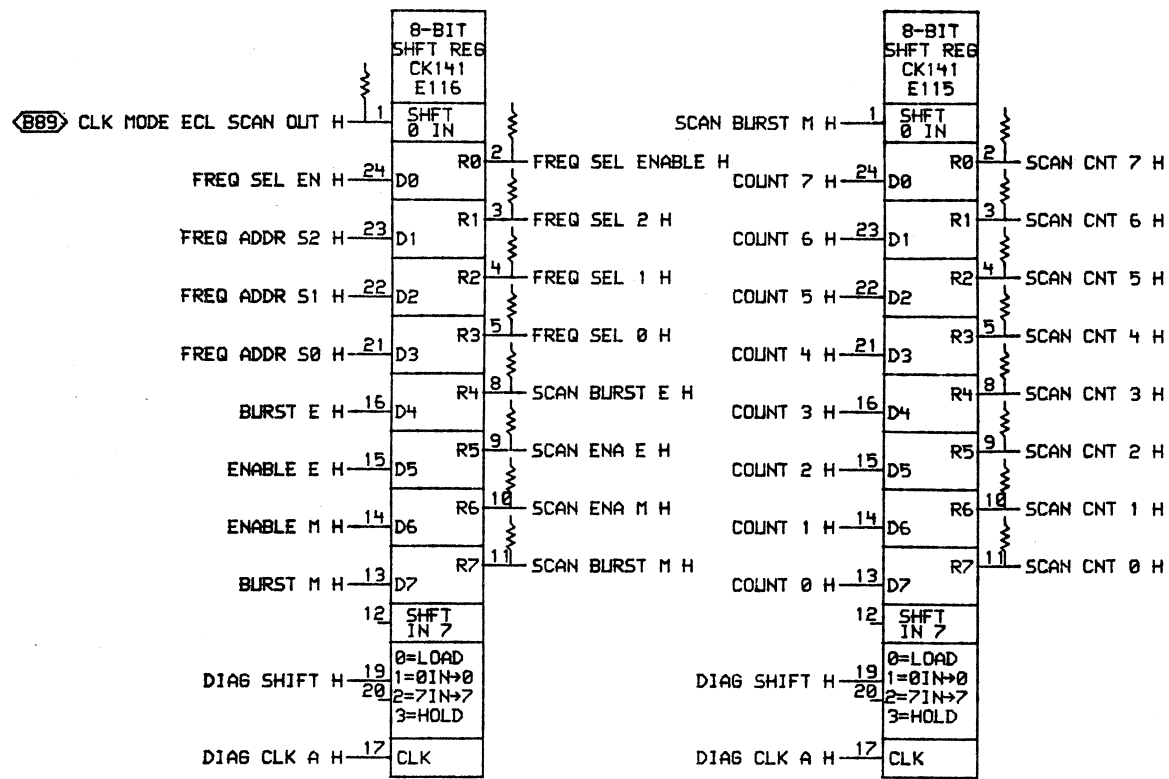
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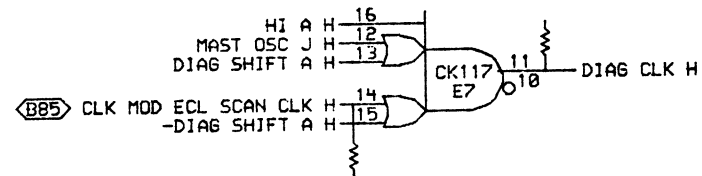
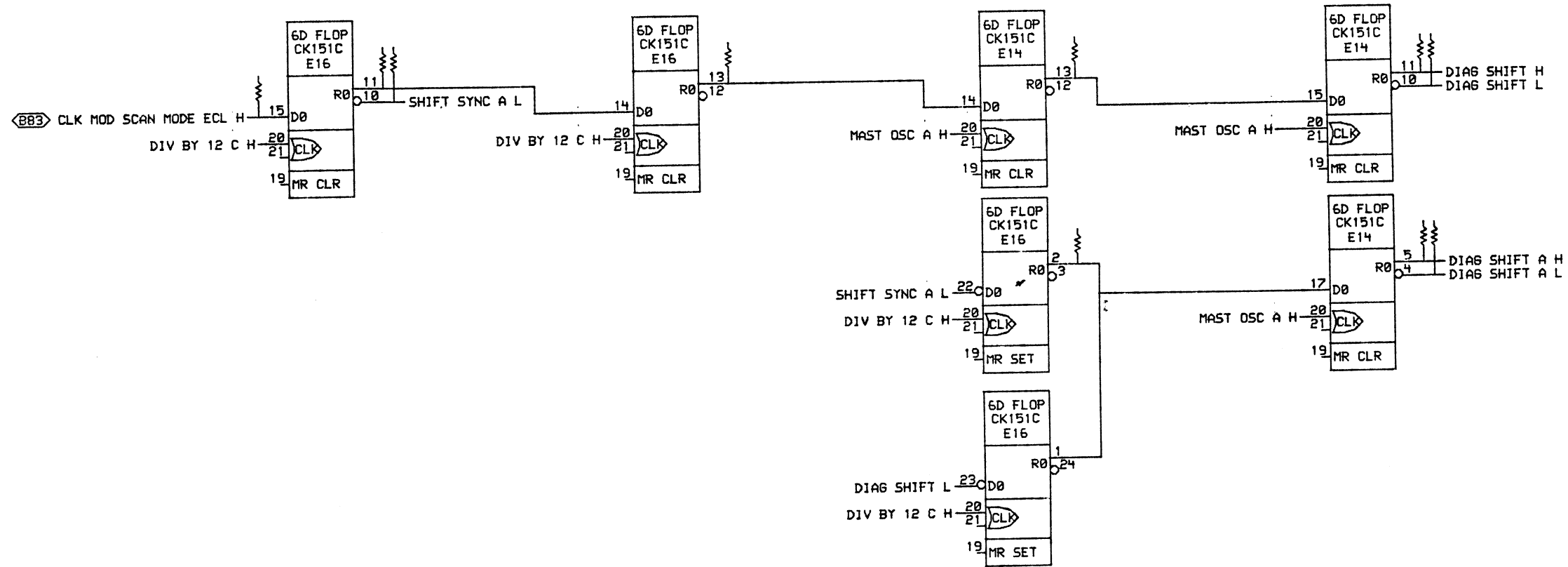
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CHK'D.	22-SEP-82			MAST OSC AND PHASE GENERATOR
	DATE	BOARD LOCATION:		
		SHEET	OF	
FIRST USED ON OPTION/MODEL: 2080				SIZE CODE NUMBER REV.
				D CS L0048-1-CLK1



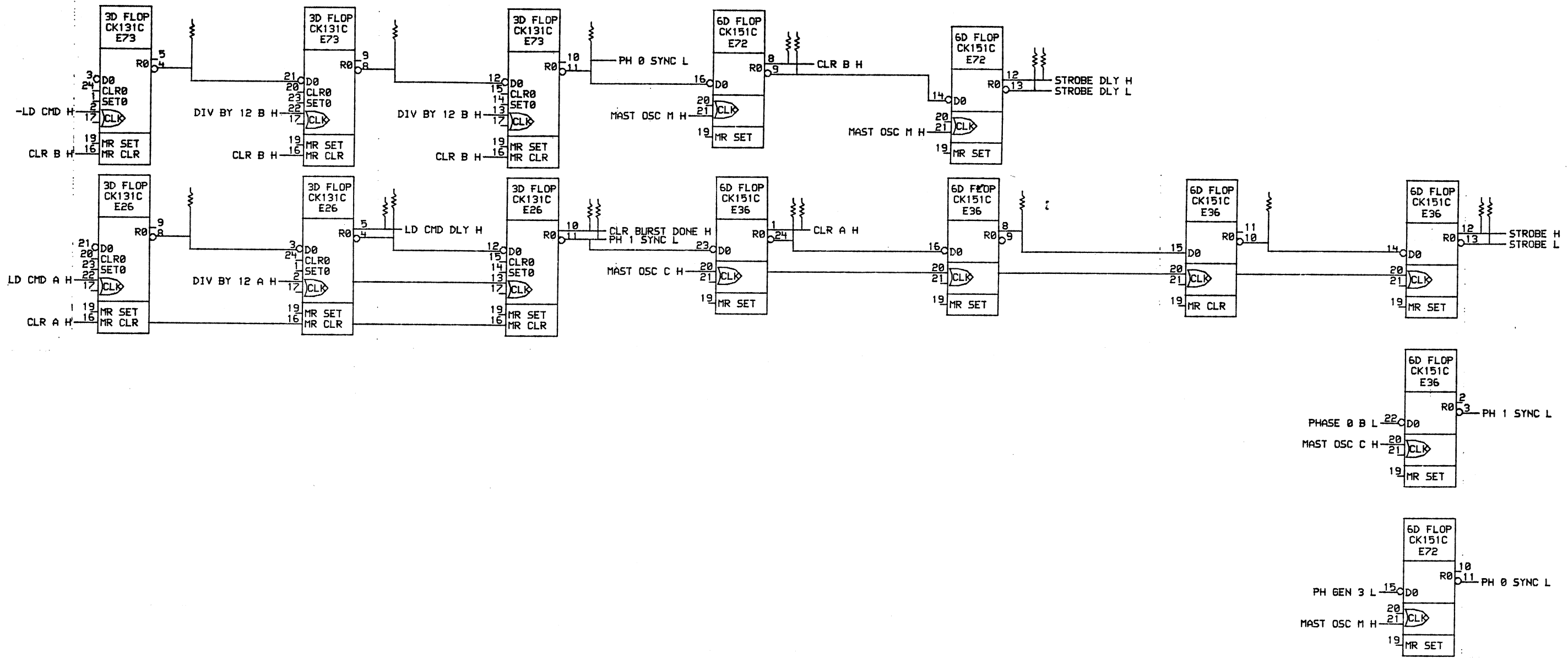
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CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL: 2080				SIZE
VULCAN (2000) L0048 REV B 11 BCLK3.DRW 18-SEP-82 14:11				CODE
NEXT HIGHER ASSEMBLY:				NUMBER
REV.				REV.



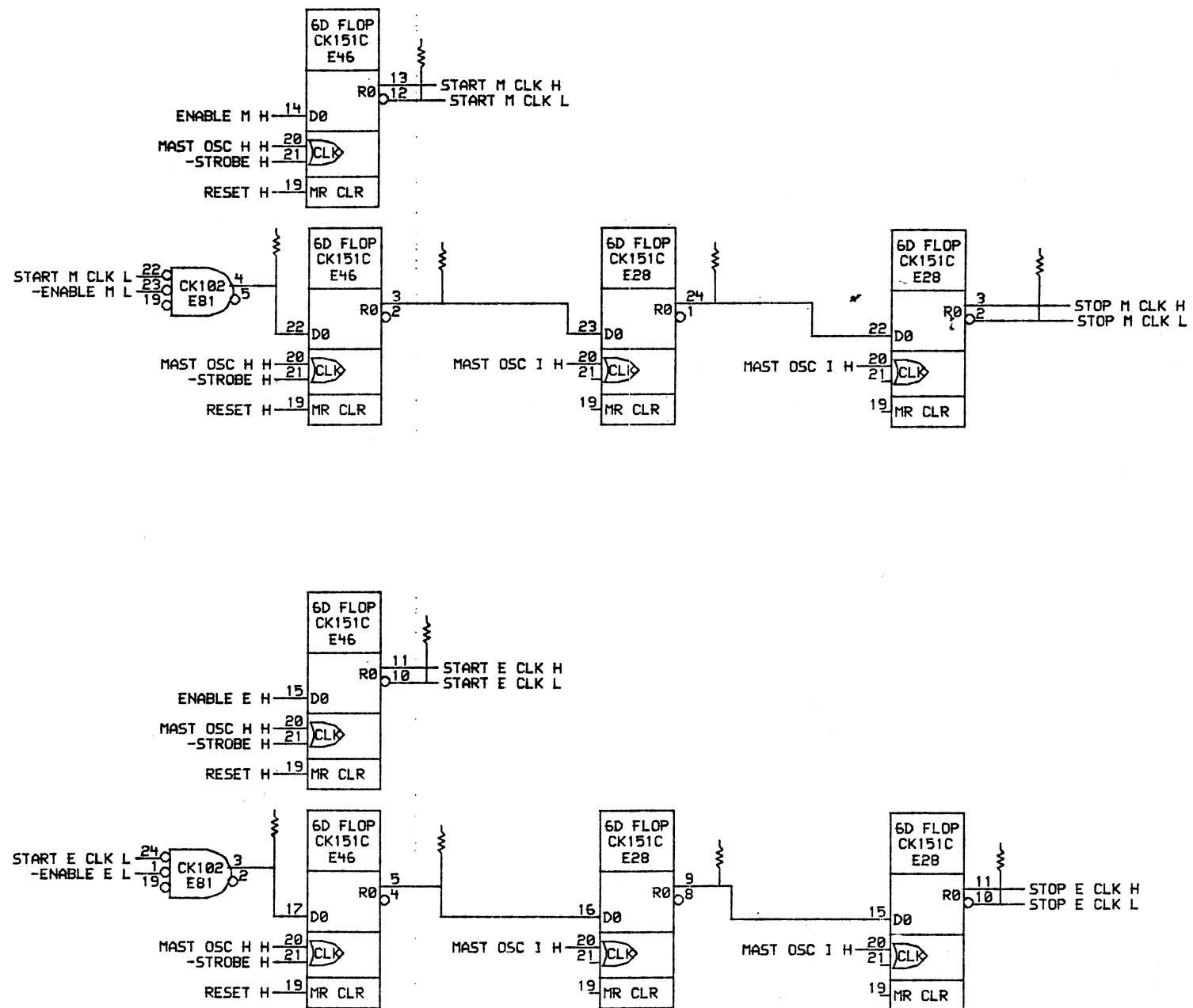
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CHK'D.	DATE	BOARD LOCATION:		CLK SCAN PATH AND FREQ ADDR
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FIRST USED ON OPTION/MODEL: 2080				D C5 L0048-1-CLK4



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		DIAG SHIFT CONTROL
FIRST USED ON OPTION/MODEL:				SIZE CODE NUMBER REV.
21-SEP-82 18:57 NEXT HIGHER ASSEMBLY:				D CS L0048-1-CLK5 1



DRN.	DATE	ENG.	DATE	TITLE:	SCAN CONTROL LOGIC		
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER
FIRST USED ON OPTION/MODEL:				NEXT HIGHER ASSEMBLY:	D	CS	L0048-1-CLK6
VULCAN (2000-1) REV B BELK6.DRW 17-SEP-82 07:30							REV.



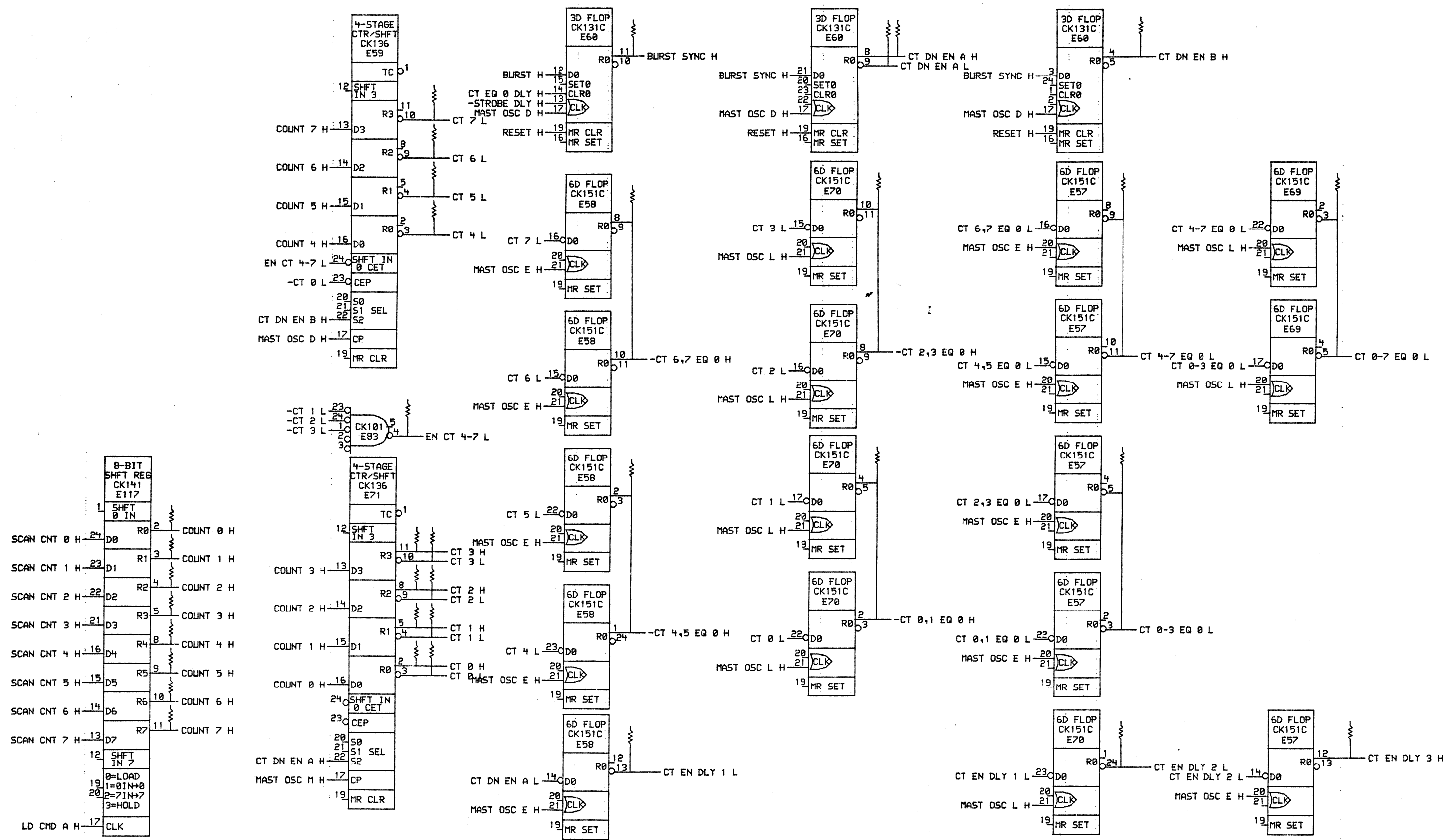
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		START/STOP CLOCK LOGIC
VILCANN<P888.L0048.REV0>CLK7.DRN	17-SEP-82 07:30	SHEET	OF	NUMBER
FIRST USED ON OPTION/MODEL:	NEXT HIGHER ASSEMBLY:	SIZE	CODE	L0048-1-CLK7
		D	CS	REV.

D

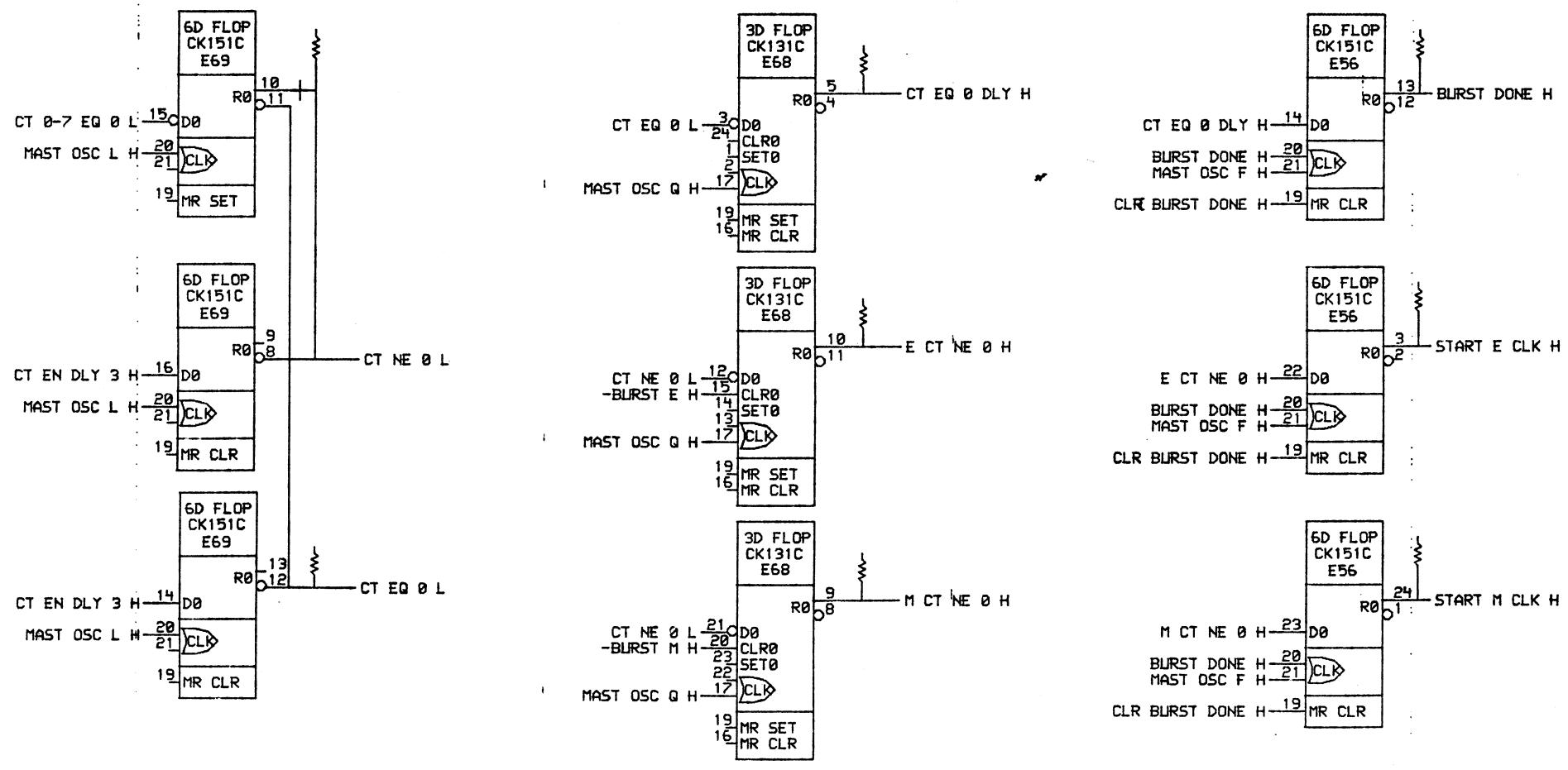
C

B

A

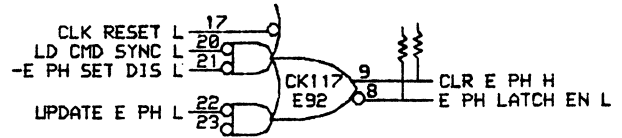
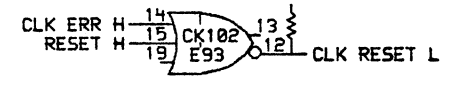
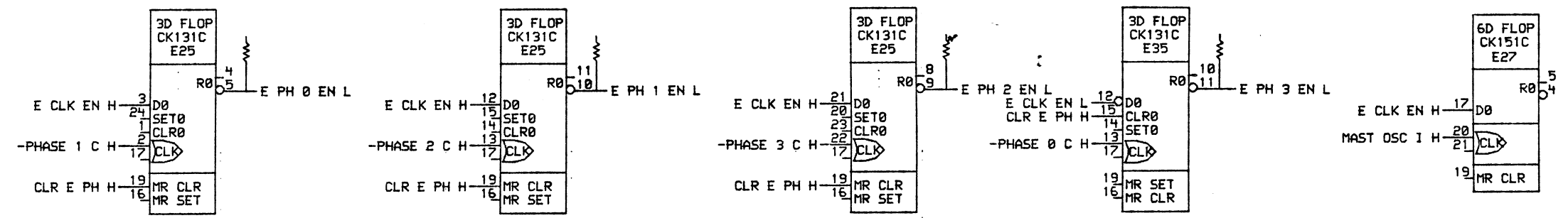
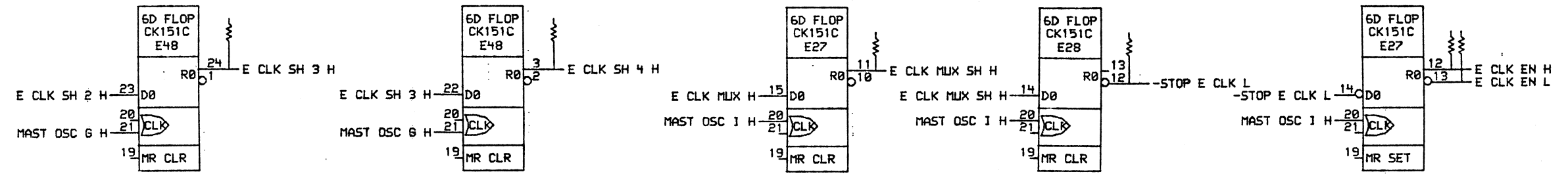
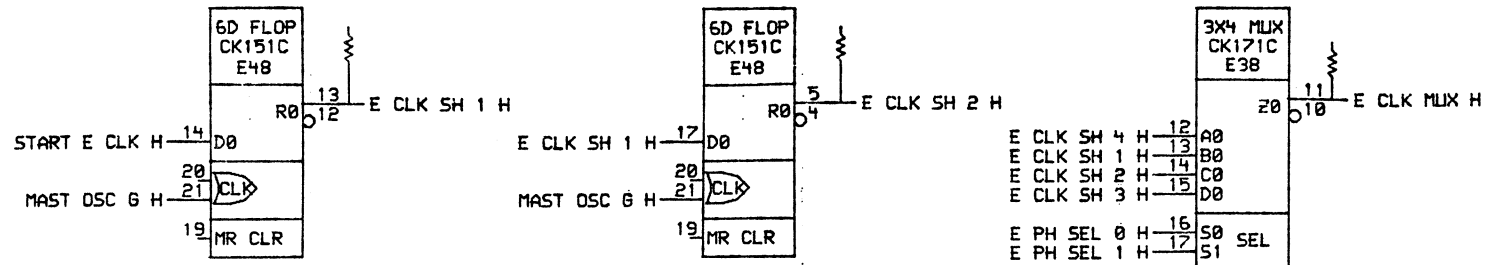


DRN.	DATE	ENG.	DATE	TITLE: BURST E/M CONTROL LOGIC
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
VIL CAM (2000.L0048.REV0) BCLB.DRW 17-SEP-82 07:31 NEXT HIGHER ASSEMBLY:				SIZE CODE
FIRST USED ON OPTION/MODEL:				NUMBER
				REV.



DRN.	DATE	ENG.	DATE	TITLE: BURST E/M CONTROL LOGIC
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
VILCANT (2888.L0048.REV)30CLK9.DRW 17-SEP-82 07:34 NEXT HIGHER ASSEMBLY!				SIZE CODE NUMBER REV.
FIRST USED ON OPTION/MODEL:				D CS L0048-1-CLK9 1

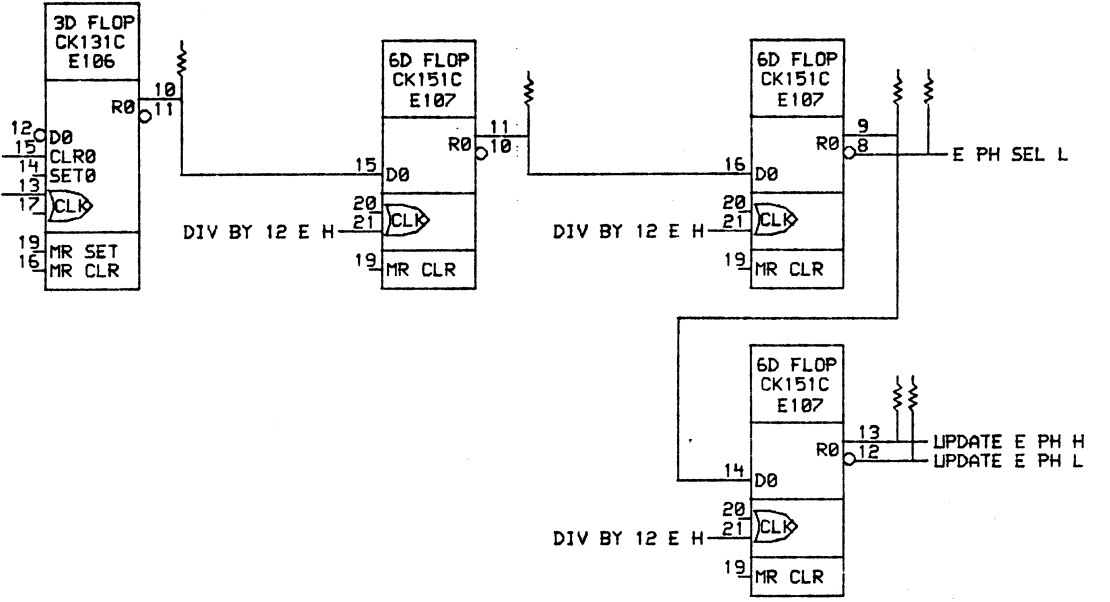
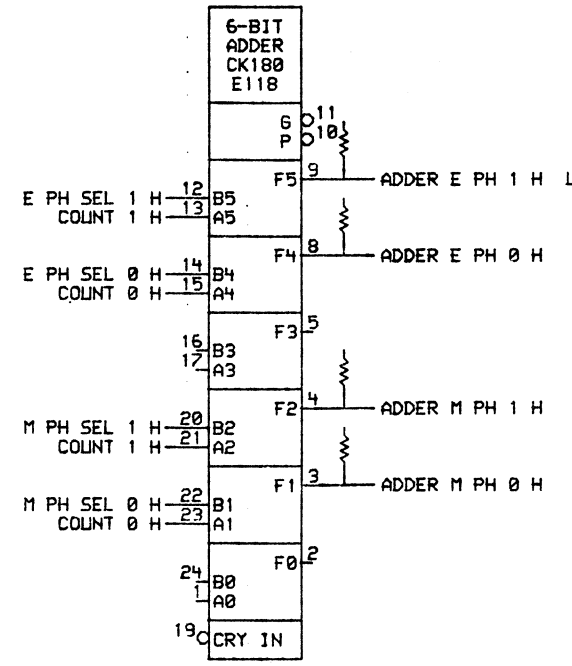
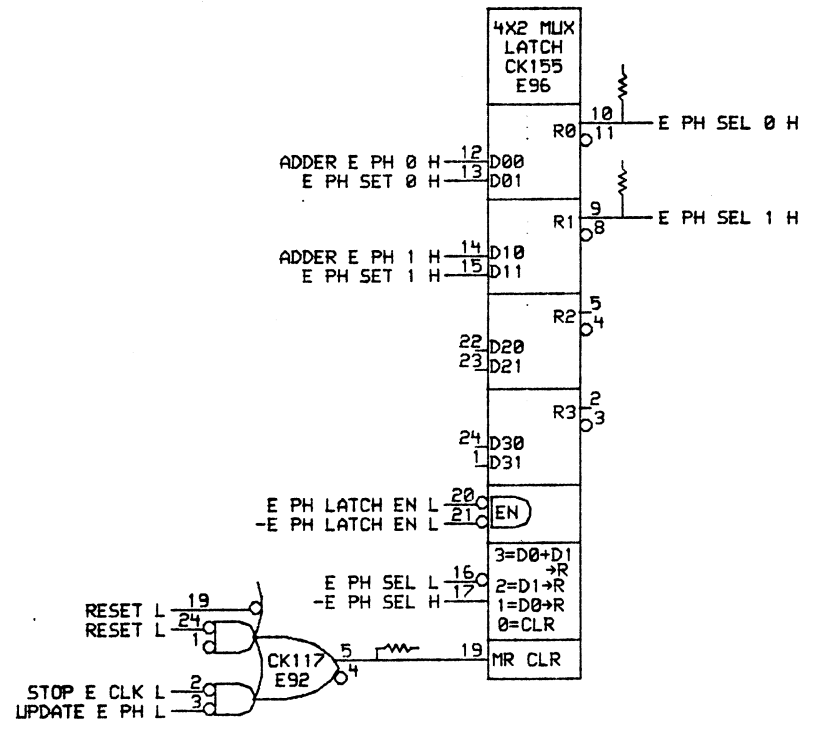
D



C

B

A



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	22-SEP-82			PHASE ALIGNMENT LOGIC
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		SIZE CODE NUMBER REV.
				D CS L0048-1-CLKA

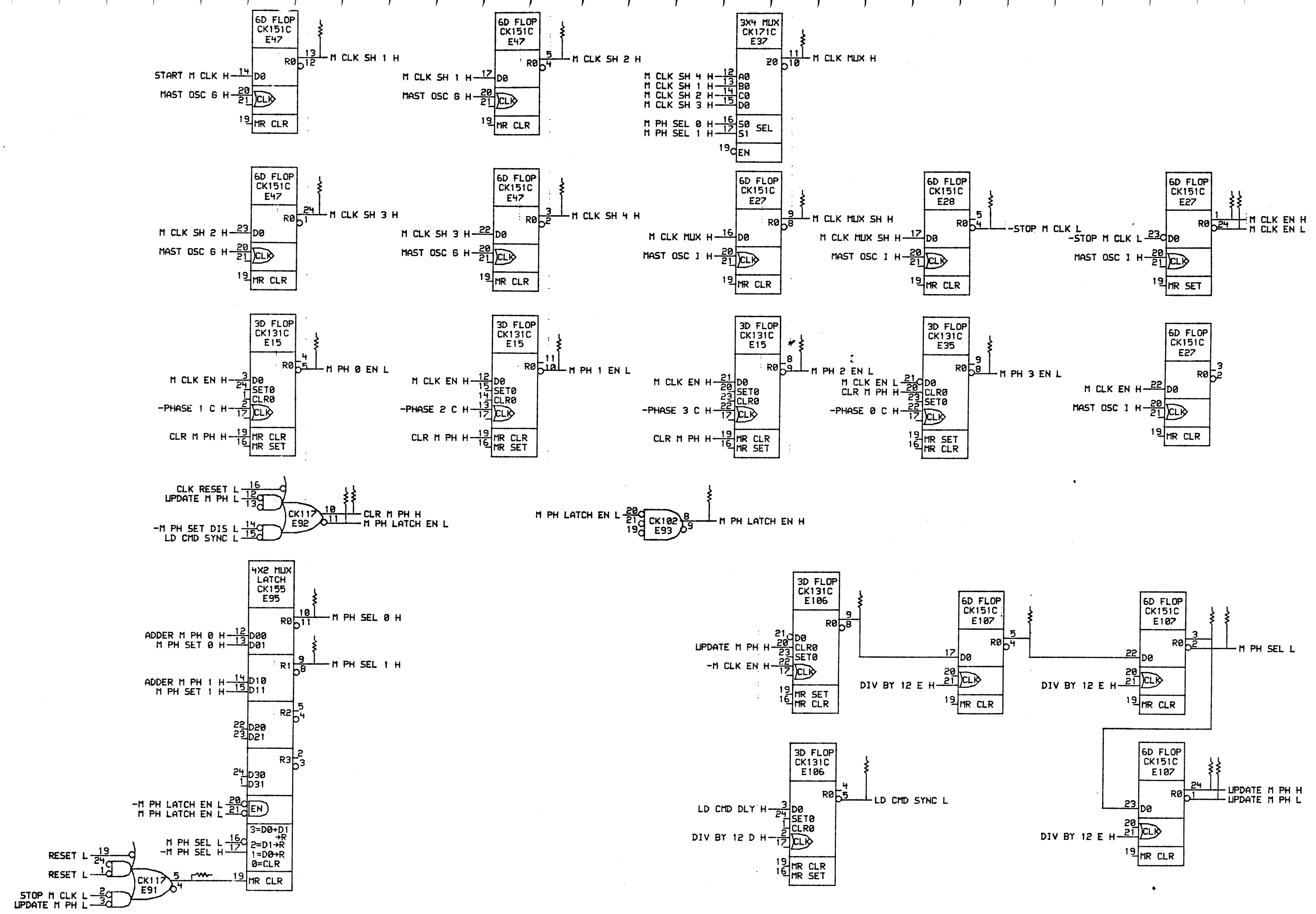
8 7 6 5 4 3 2 1

D

C

B

A



DRN.	DATE: 22-SEP-82	ENG.	DATE	TITLE: PHASE ALIGNMENT LOGIC
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL:				NUMBER: L0048-1-CLKB
NEXT HIGHER ASSEMBLY:				REV.

8

7

6

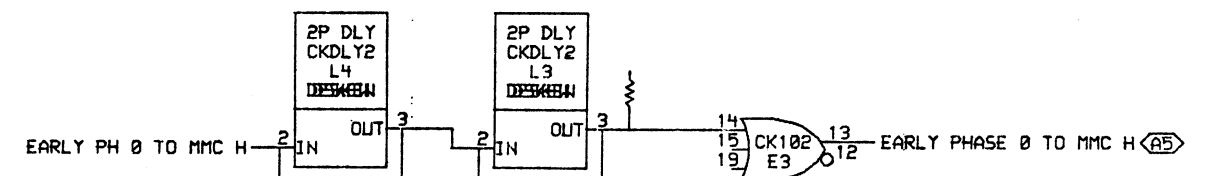
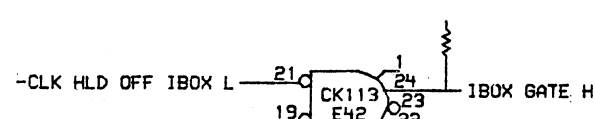
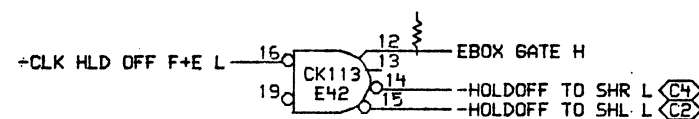
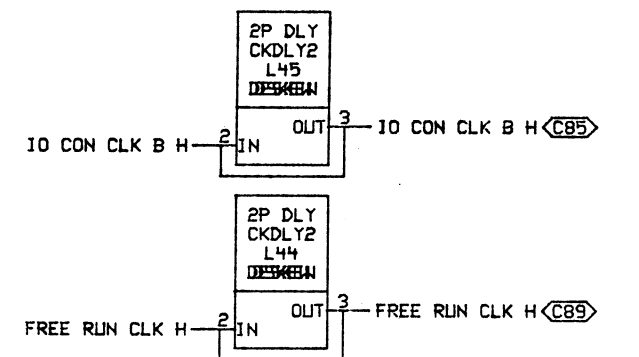
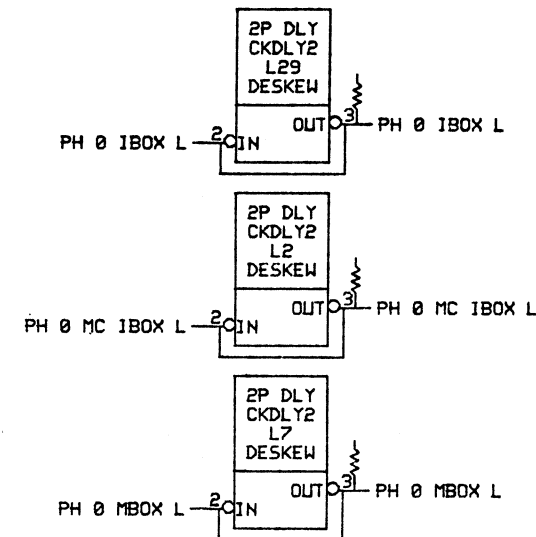
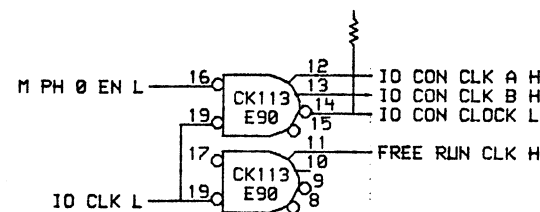
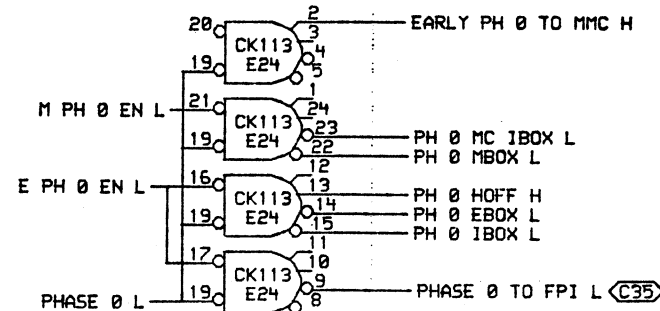
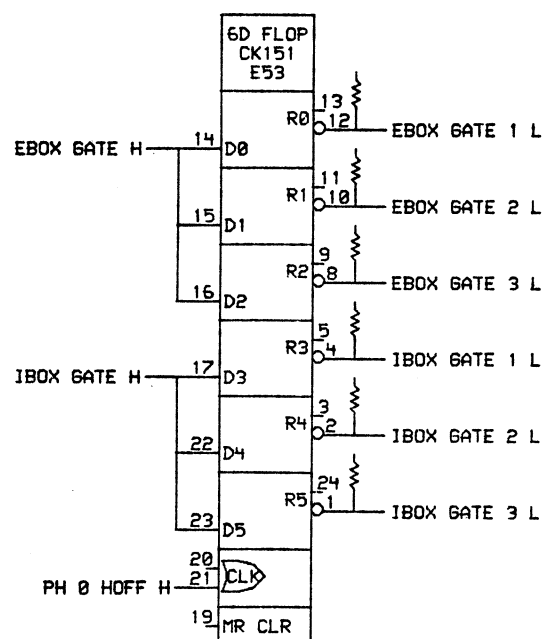
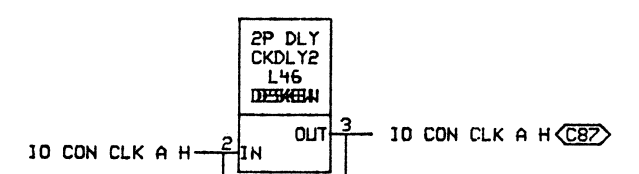
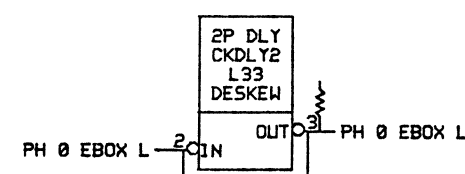
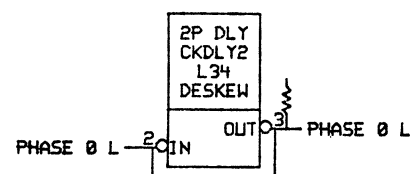
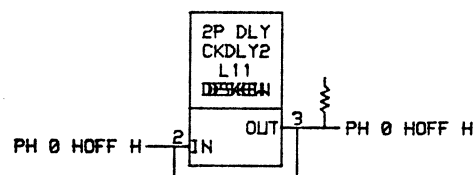
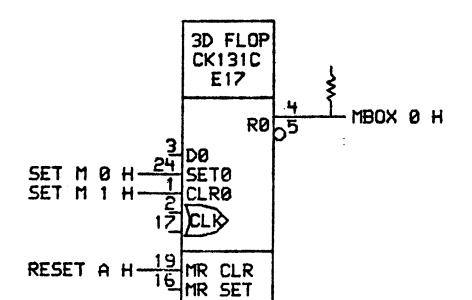
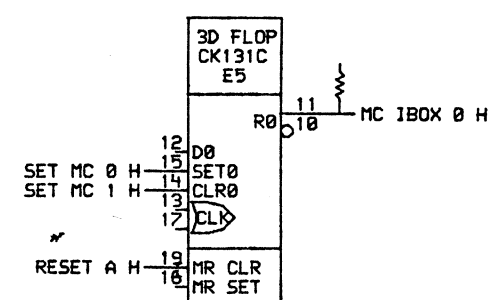
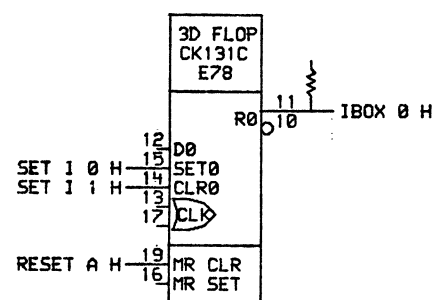
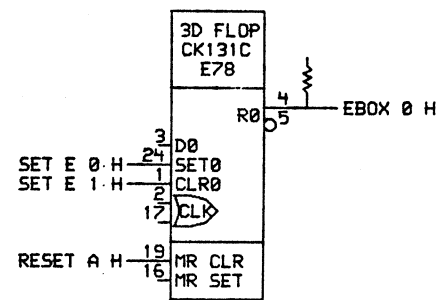
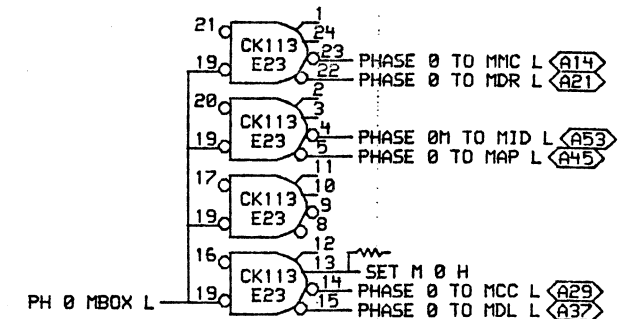
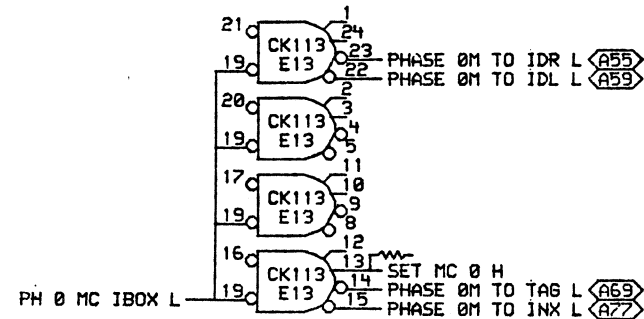
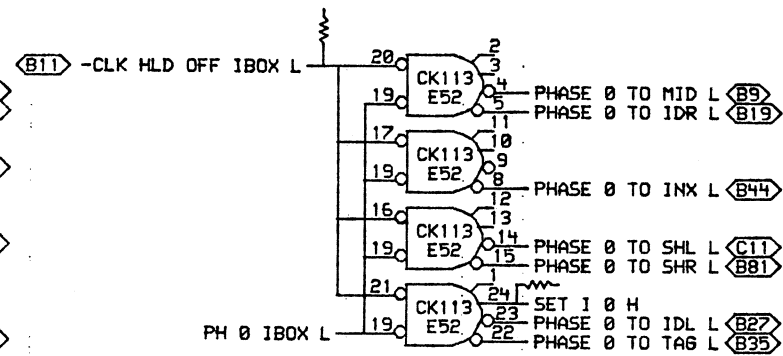
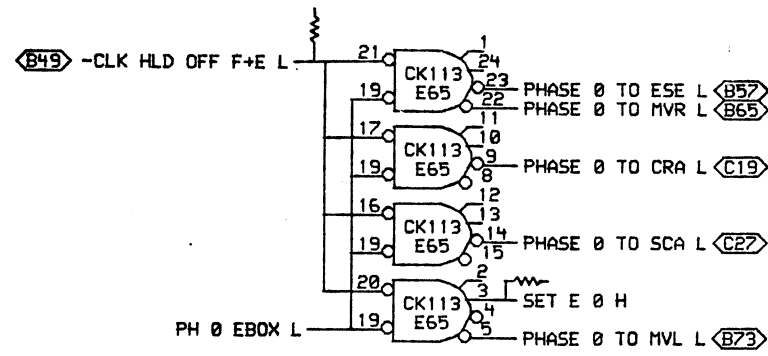
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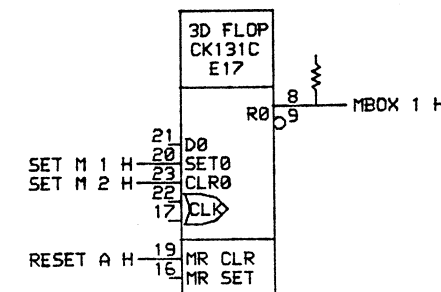
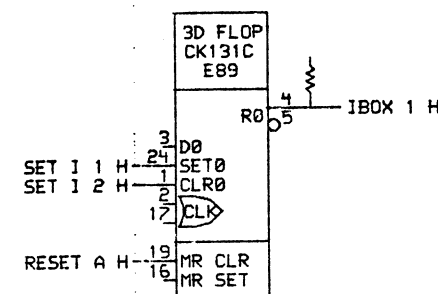
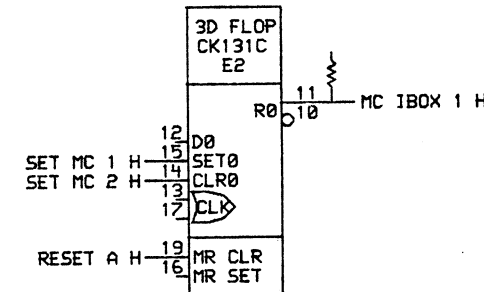
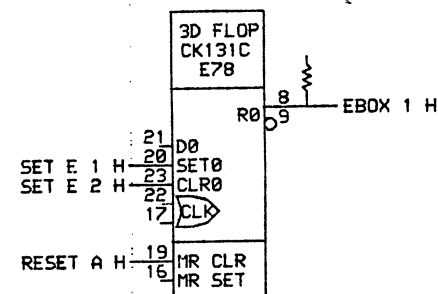
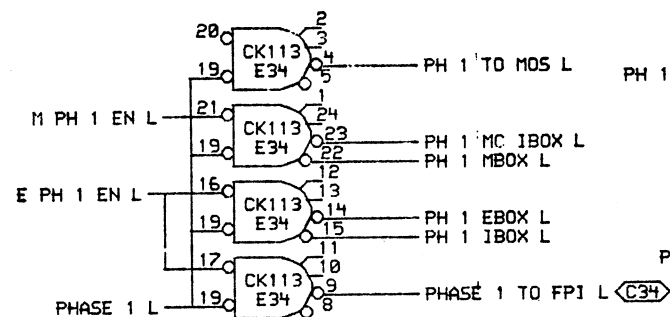
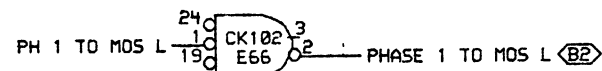
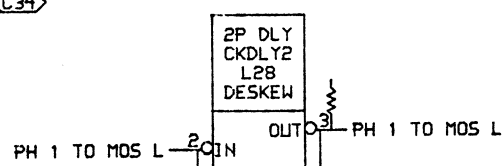
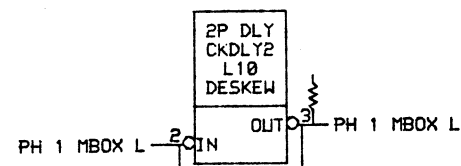
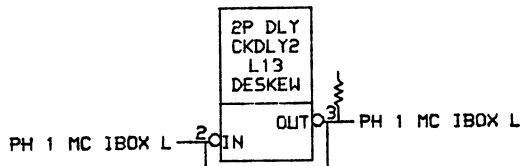
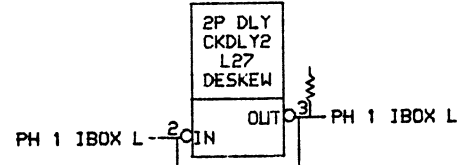
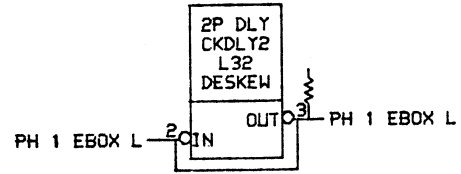
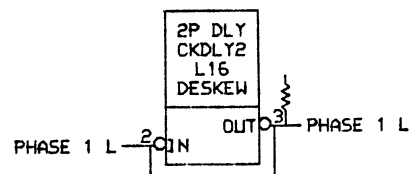
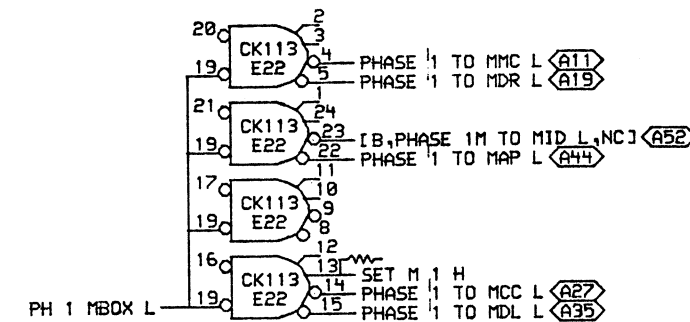
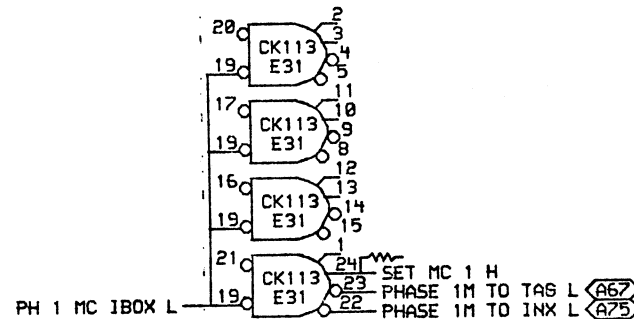
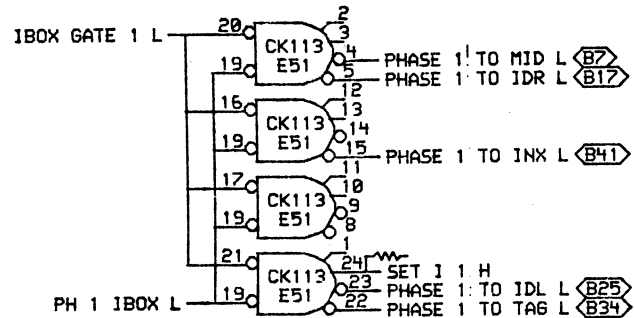
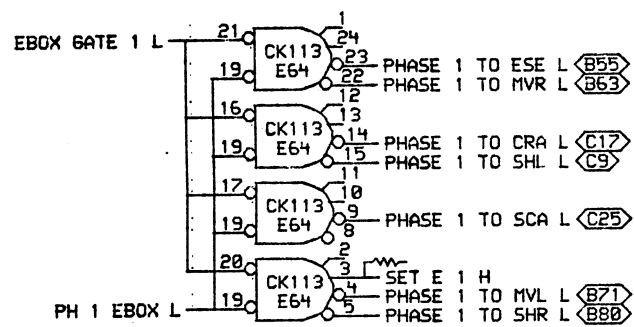
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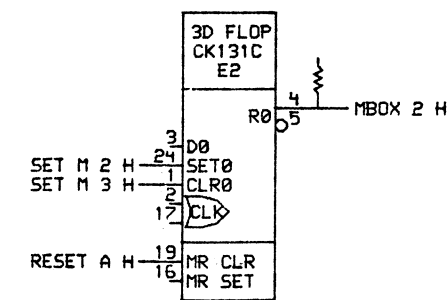
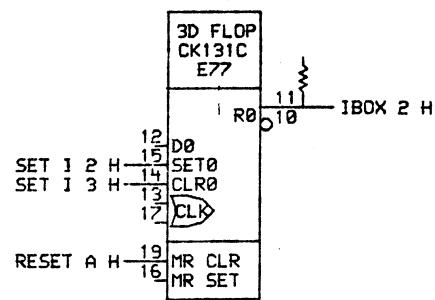
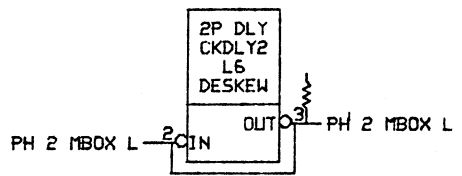
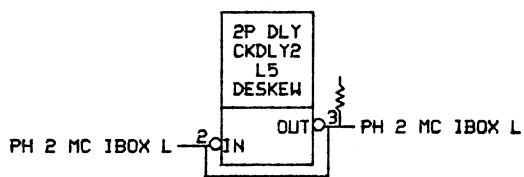
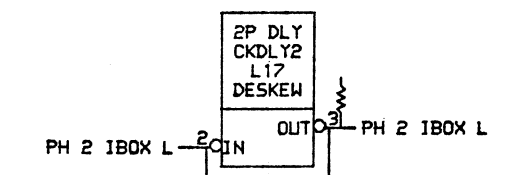
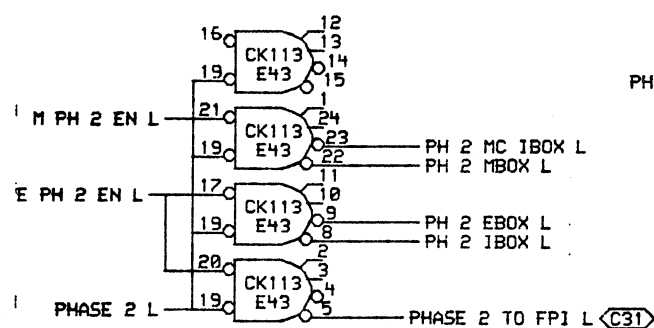
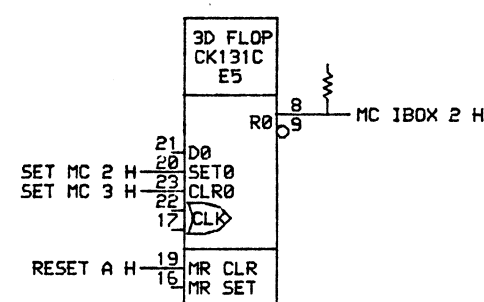
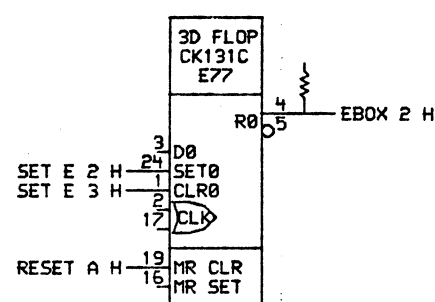
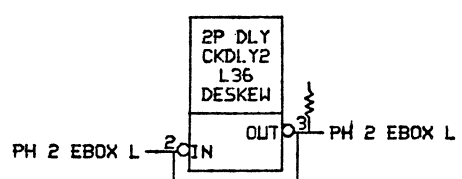
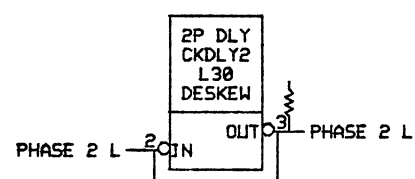
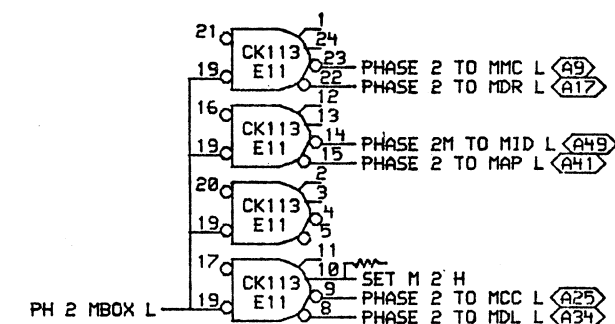
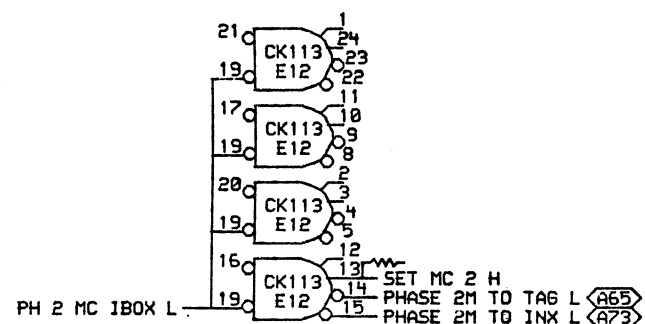
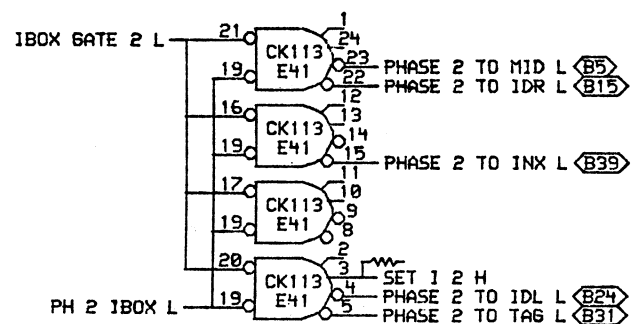
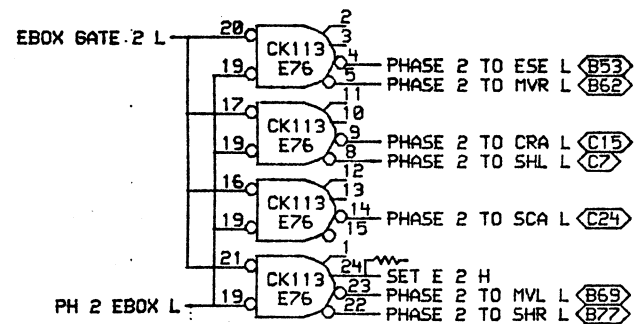
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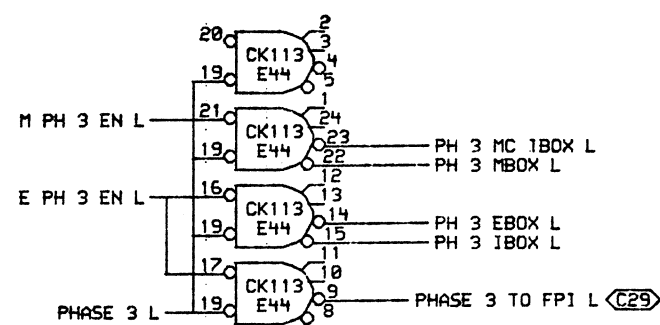
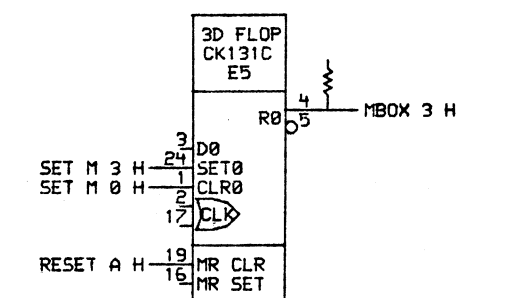
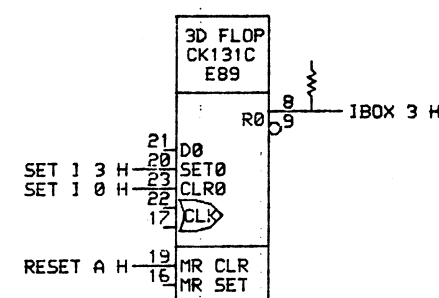
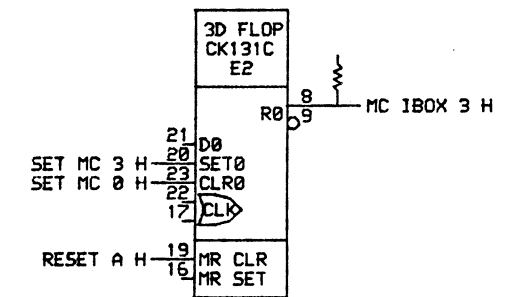
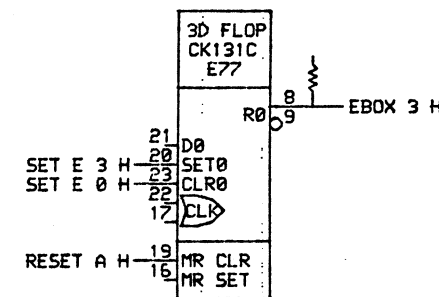
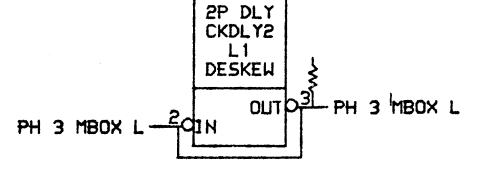
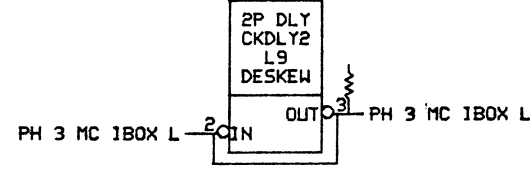
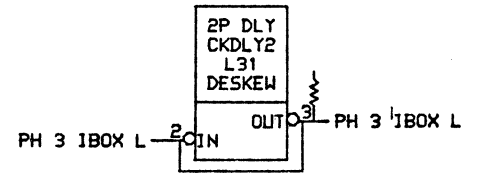
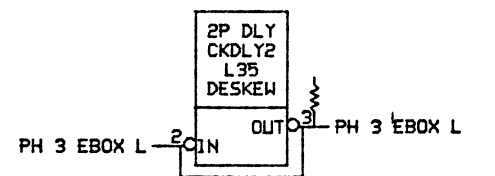
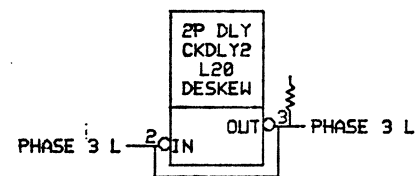
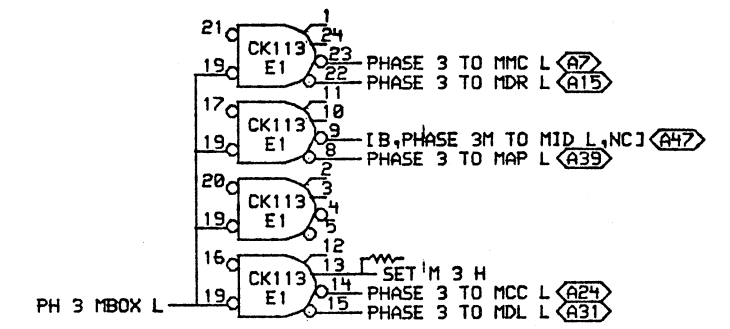
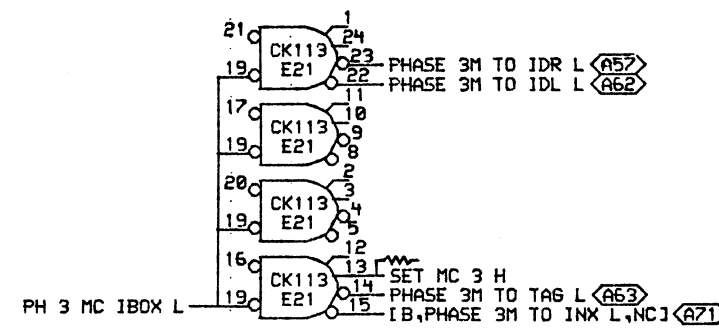
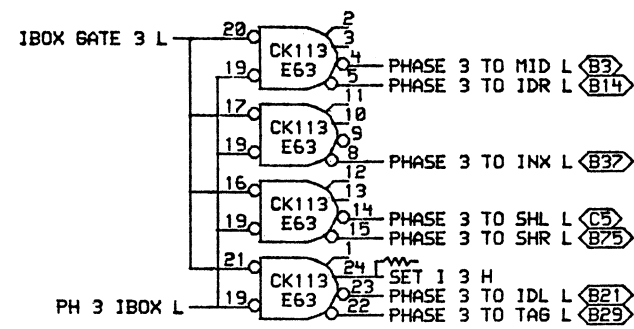
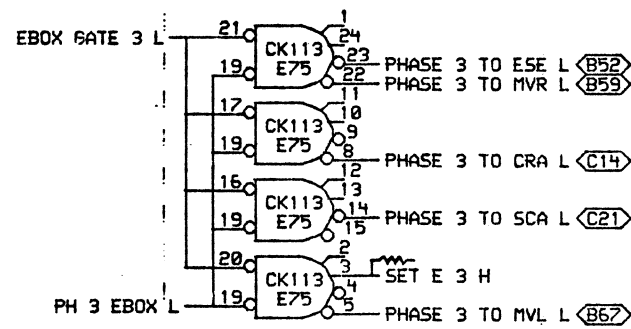
DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		PHASE 0 FANOUT
		SHEET	OF	CLOCK HOLDOFF
FIRST USED ON OPTION/MODEL:				SIZE
NEXT HIGHER ASSEMBLY:				CODE
				NUMBER
				REV.



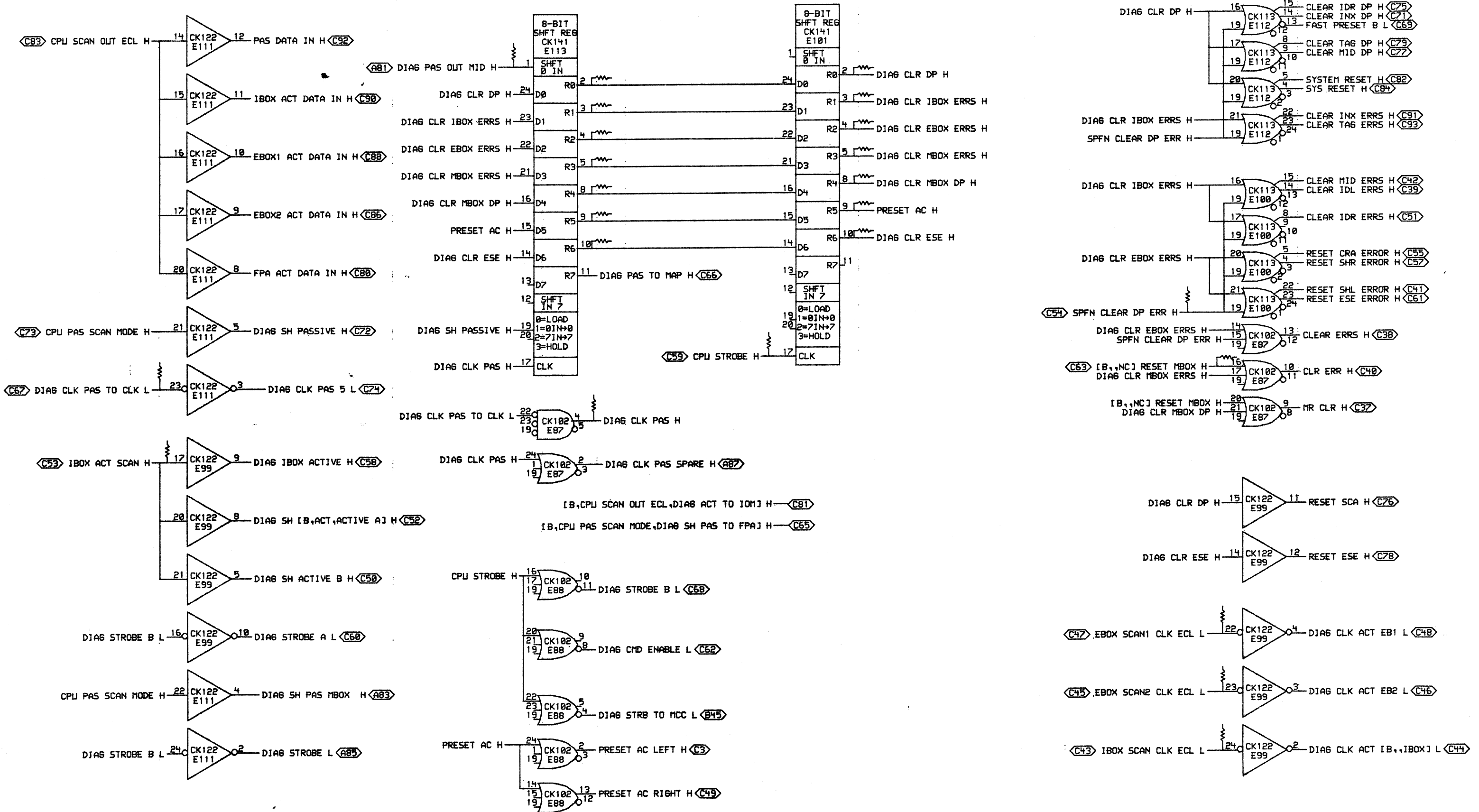
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CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	SIZE	CODE	NUMBER
VILL CANN (2008) L0048 REV D 1 SECLKD.DRW	21-SEP-82 17:49	NEXT HIGHER ASSEMBLY:			D	CS	L0048-1-CLKD
FIRST USED ON OPTION/MODEL:							REV.



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	22-SEP-82			PHASE 2 FANOUT
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		SIZE CODE
VLL CANN (2008.L0048.REV0)130CLKE.DRW [21-SEP-82 19:32				D CS
				NUMBER
				L0048-1-CLKE
				REV.



DRN.	DATE	ENG.	DATE	TITLE:	PHASE 3 FANOU	
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	NUMBER	REV.
FIRST USED ON OPTION/MODEL:				SIZE	CODE	L0048-1-CLKF



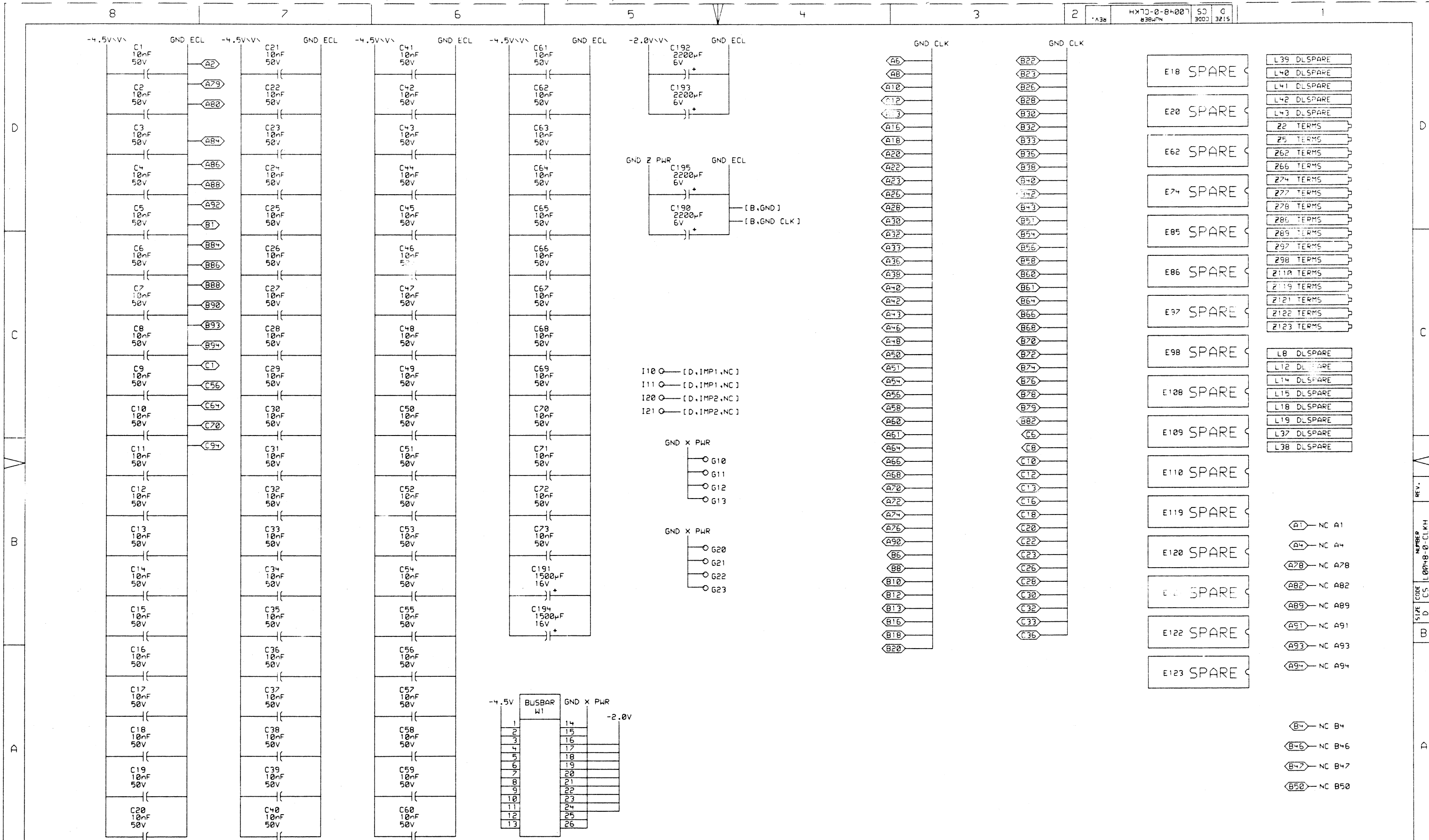
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REVISIONS		
CHK	CHANGE NO.	REV

8	7	6	5	4	3	2	1
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	DATE	ENG.	DATE	TITLE:
	11-OCT-82			CLK PASSIVE SCAN CLEAR ERRORS
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
FIRST USED ON OPTION/MODEL:			NEXT HIGHER ASSEMBLY:	SIZE
				D CS
				NUMBER
				L0048-1-CLKG
				REV.

REV. NUMBER SIZE CODE D CS L0048-1-CLKG



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REVISIONS	
CHK	CHANGE NO. REV

digital *DRW. J. J. J.* DATE ENG. 10-FEB-83 DATE BOARD LOCATION: SHEET 1 OF 1

DATE 10-FEB-83 19:26 NEXT HIGHER ASSEMBLY: B-DD-L0048-0

FIRST USED ON OPTION/MODEL: 2080

TITLE: **CLOCK POWER & GROUND**


SIZE CODE D CS NUMBER L0048-0-CLKH REV.

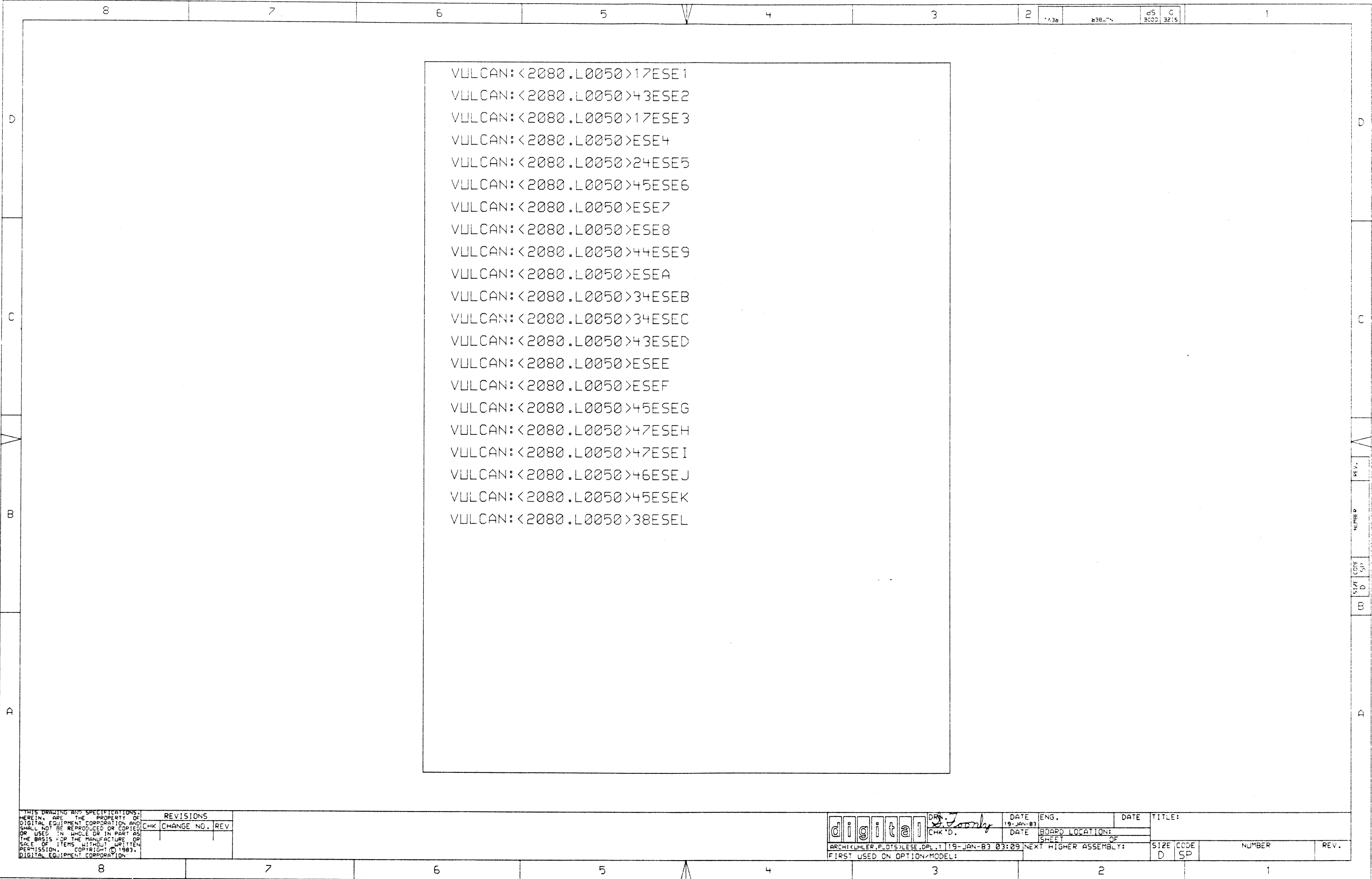
REV. NUMBER L0048-0-CLKH

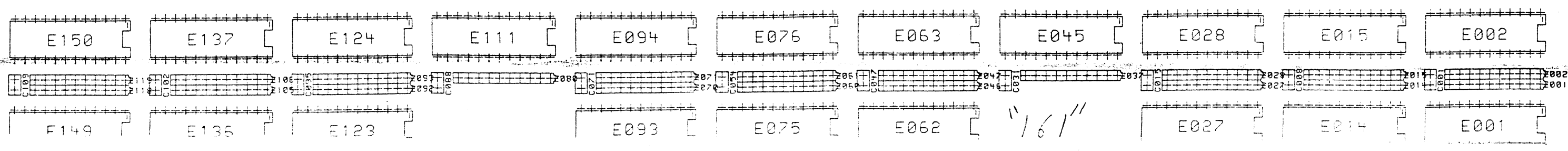
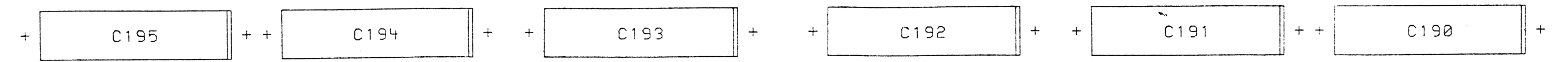
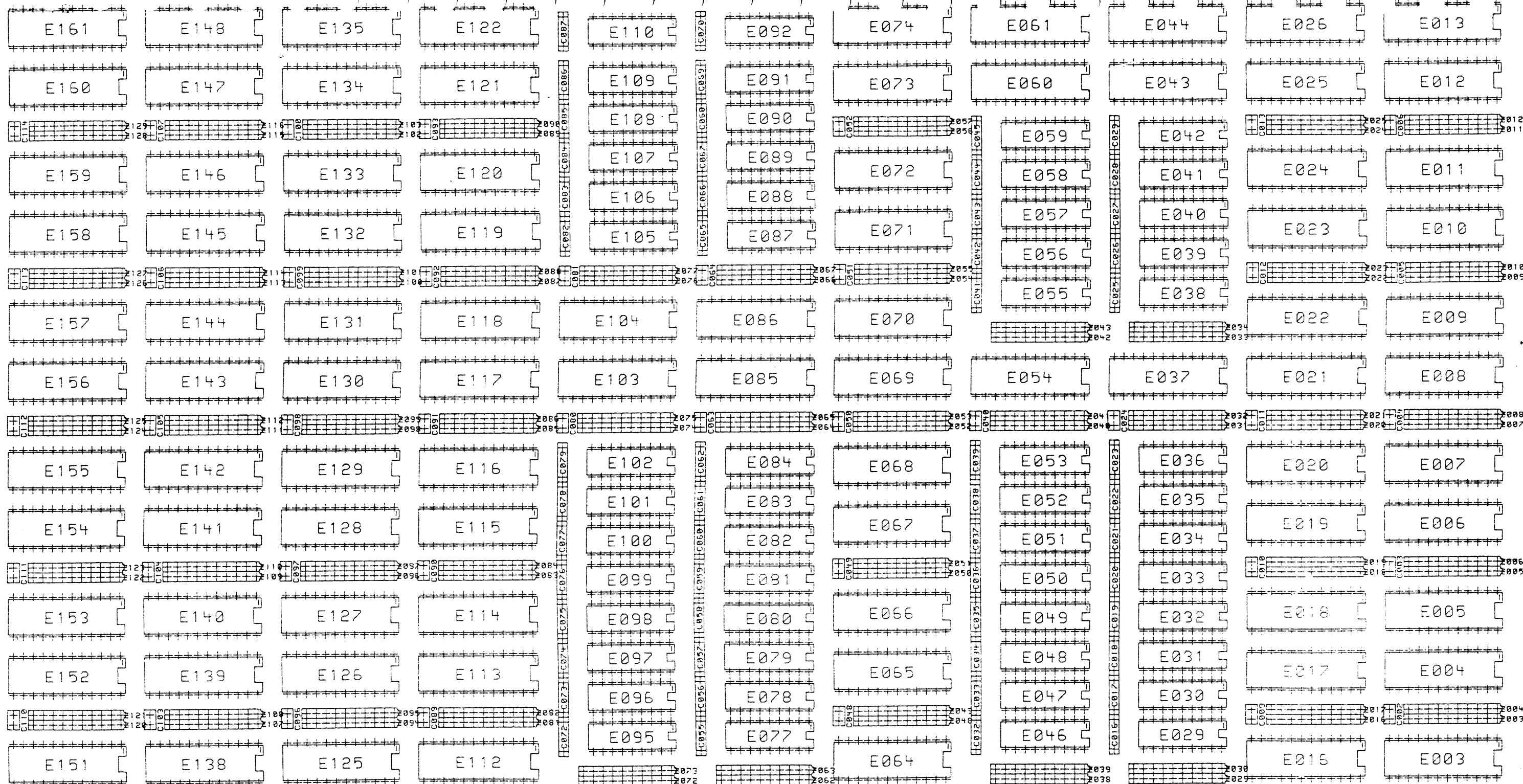
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 VULCAN:<2080.L0050>17ESE3
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 VULCAN:<2080.L0050>24ESE5
 VULCAN:<2080.L0050>45ESE6
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 VULCAN:<2080.L0050>47ESEI
 VULCAN:<2080.L0050>46ESEJ
 VULCAN:<2080.L0050>45ESEK
 VULCAN:<2080.L0050>38ESEL

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REVISIONS		
CHK	CHANGE NO.	REV

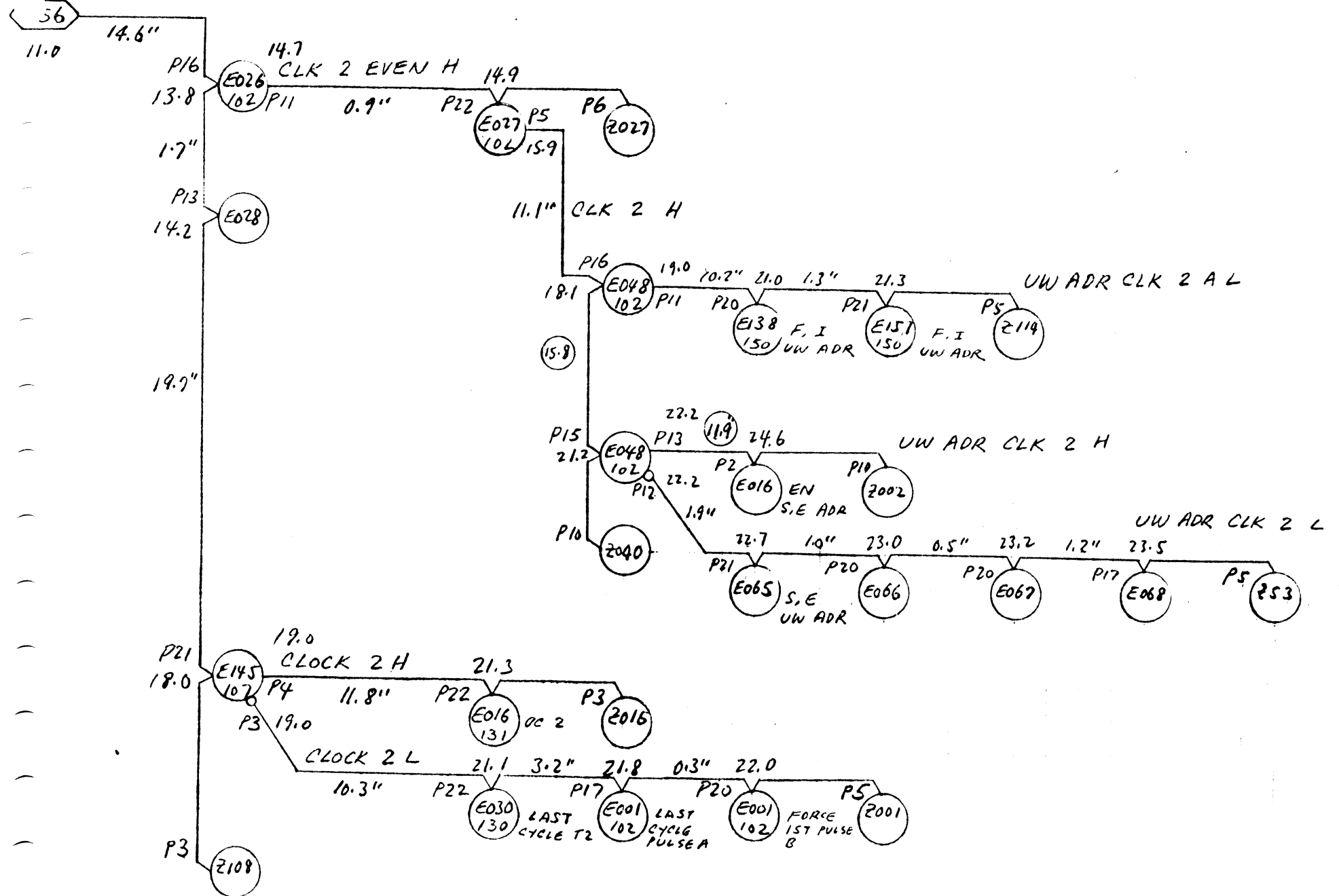
	DRN. <i>J. Jorjky</i> CHK'D.	DATE 19-JAN-83	ENG.	DATE	TITLE:
	ARCH:KUMLER.PLOTS\LESE.DPL.119-JAN-83 03:09 FIRST USED ON OPTION/MODEL:	DATE SHEET	BOARD LOCATION: OF	NEXT HIGHER ASSEMBLY:	SIZE CODE D SP



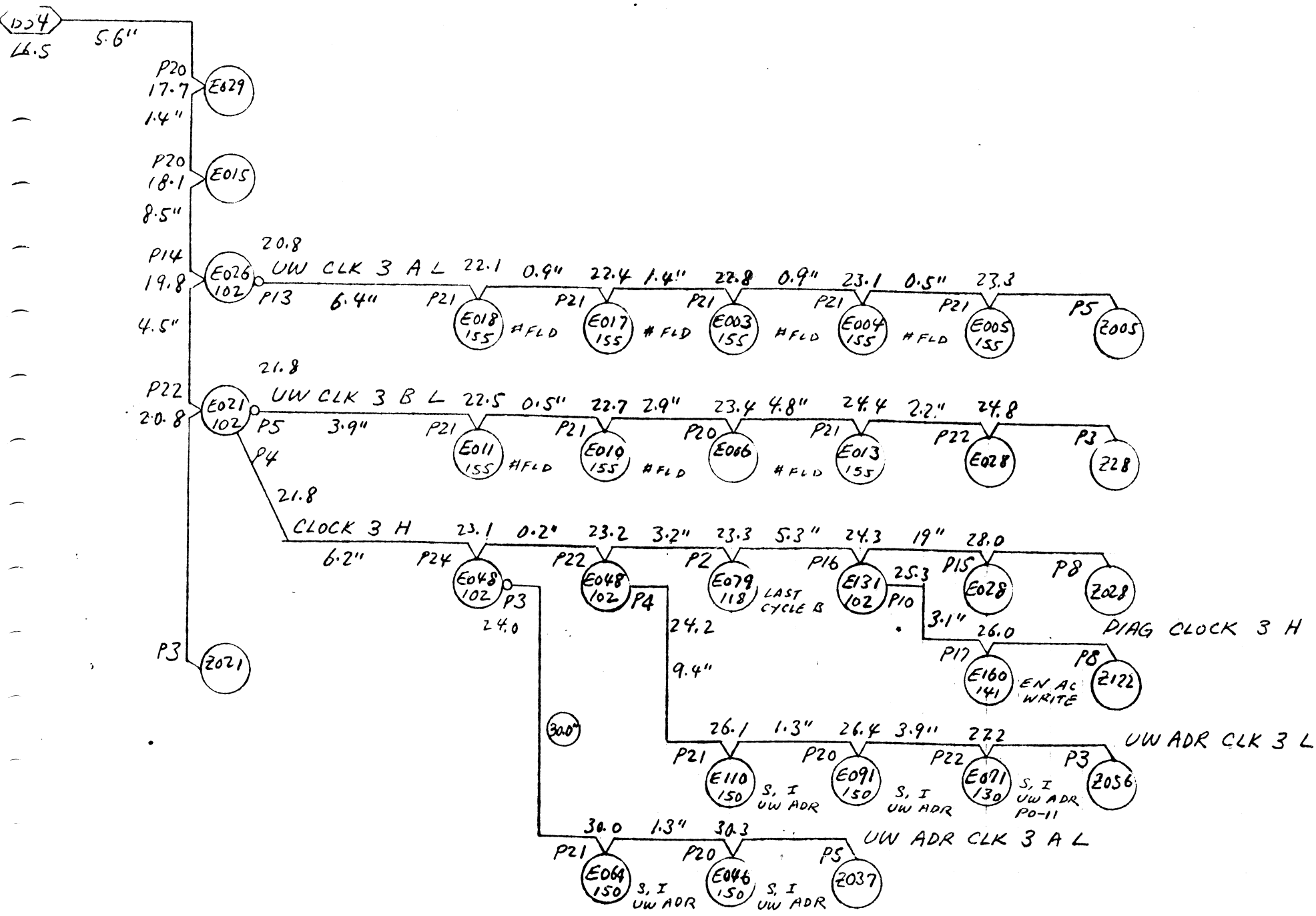


"161"

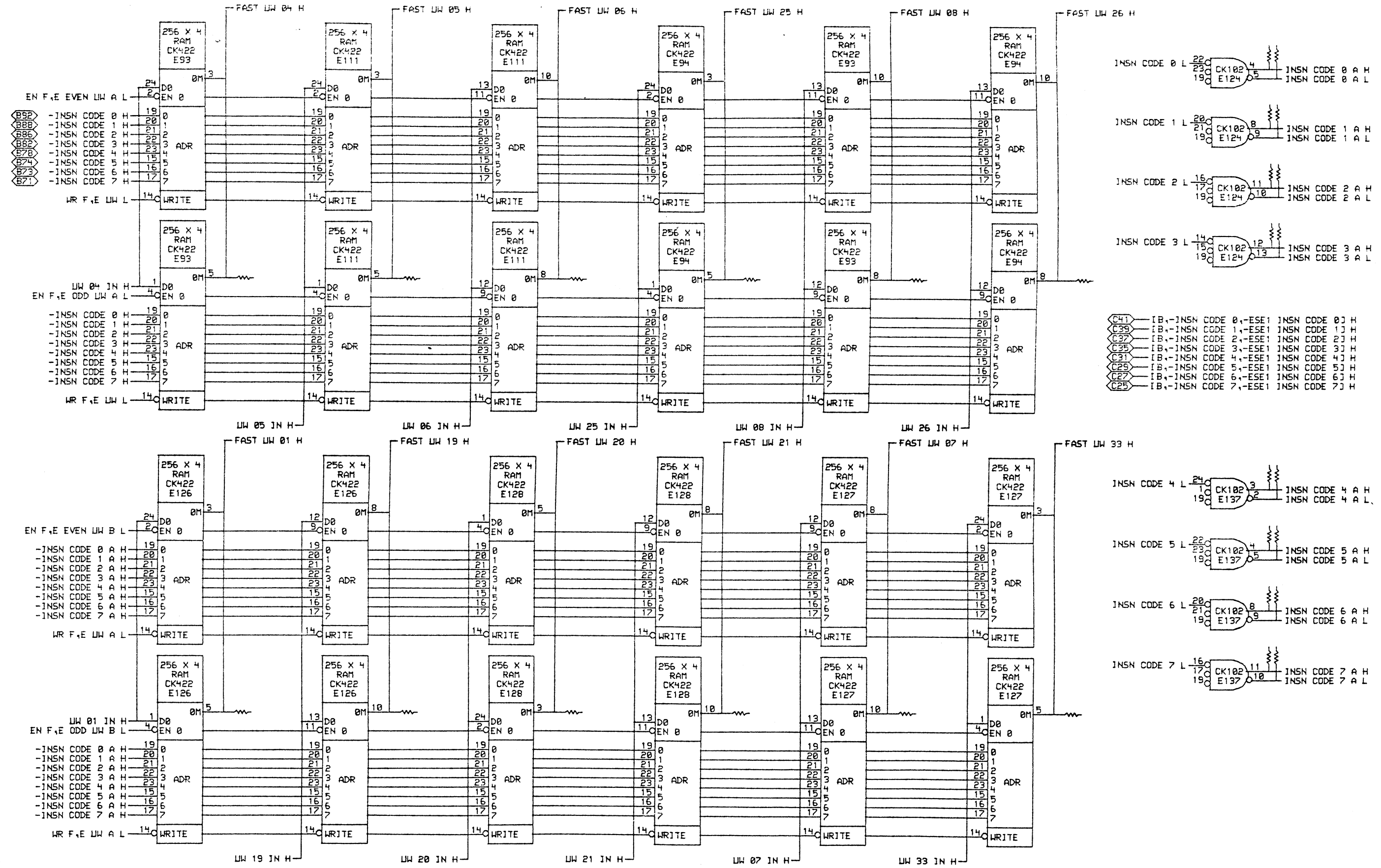
PHASE 2 TO ESE L



PHASE 3 TO ESE L



NOTE: (X) FIXED LENGTH



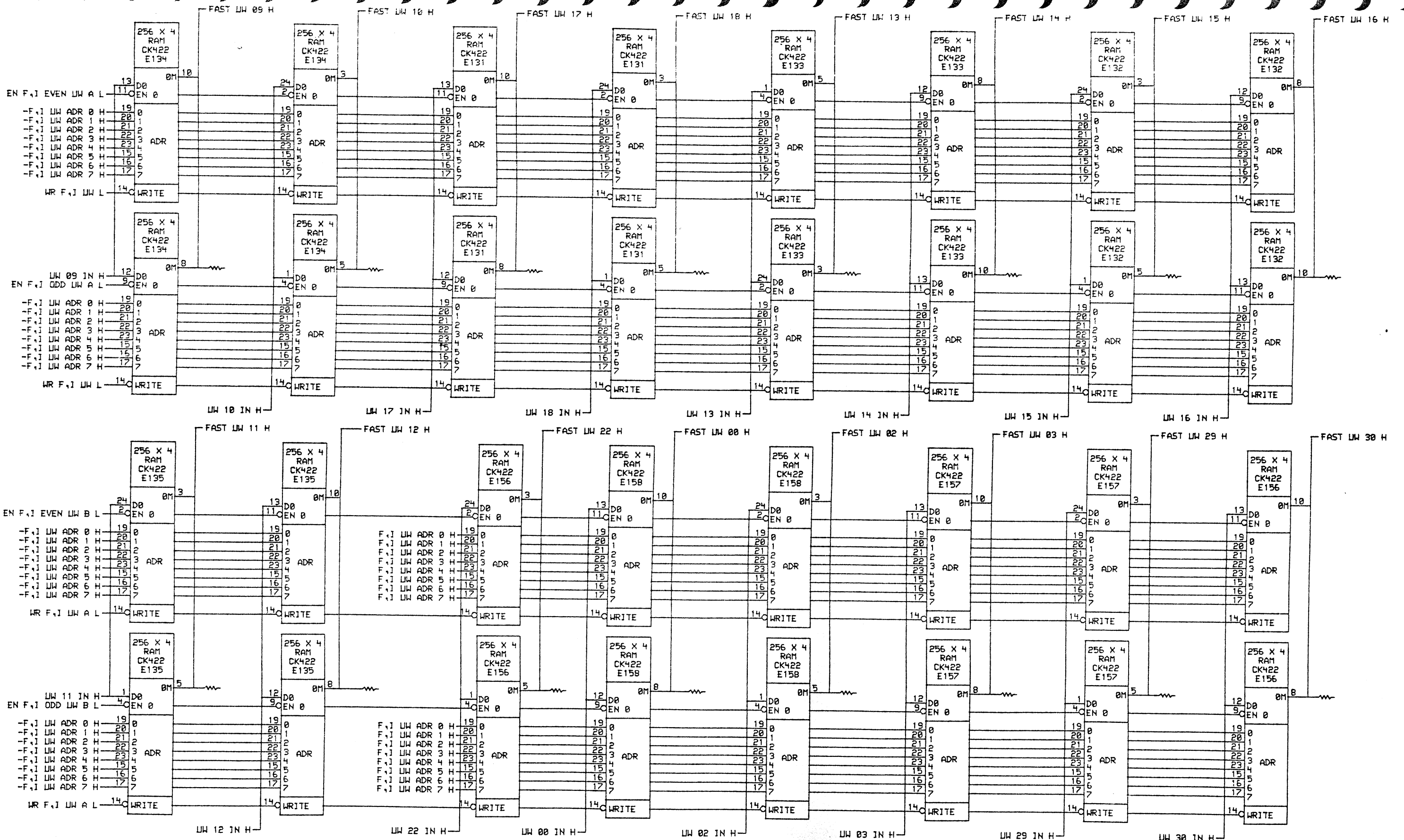
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- INSN CODE 1 L 20, 21, 19 → CK102 E124 → INSN CODE 1 A H, INSN CODE 1 A L
- INSN CODE 2 L 16, 17, 19 → CK102 E124 → INSN CODE 2 A H, INSN CODE 2 A L
- INSN CODE 3 L 14, 15, 19 → CK102 E124 → INSN CODE 3 A H, INSN CODE 3 A L
- C41 IB, -INSN CODE 0, -ESE1 INSN CODE 0 J H
- C39 IB, -INSN CODE 1, -ESE1 INSN CODE 1 J H
- C37 IB, -INSN CODE 2, -ESE1 INSN CODE 2 J H
- C35 IB, -INSN CODE 3, -ESE1 INSN CODE 3 J H
- C31 IB, -INSN CODE 4, -ESE1 INSN CODE 4 J H
- C29 IB, -INSN CODE 5, -ESE1 INSN CODE 5 J H
- C27 IB, -INSN CODE 6, -ESE1 INSN CODE 6 J H
- C25 IB, -INSN CODE 7, -ESE1 INSN CODE 7 J H

- INSN CODE 4 L 24, 19 → CK102 E137 → INSN CODE 4 A H, INSN CODE 4 A L
- INSN CODE 5 L 22, 23, 19 → CK102 E137 → INSN CODE 5 A H, INSN CODE 5 A L
- INSN CODE 6 L 20, 21, 19 → CK102 E137 → INSN CODE 6 A H, INSN CODE 6 A L
- INSN CODE 7 L 16, 17, 19 → CK102 E137 → INSN CODE 7 A H, INSN CODE 7 A L

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:
	02-FEB-82			FAST, EARLY LW
CHK'D.	DATE	BOARD LOCATION:	00-08, 26, 33, 70	
FIRST USED ON OPTION/MODEL:		SHEET	OF	
SIZE	CODE	NUMBER	REV.	
D	CS	L0050-0-ESE1		

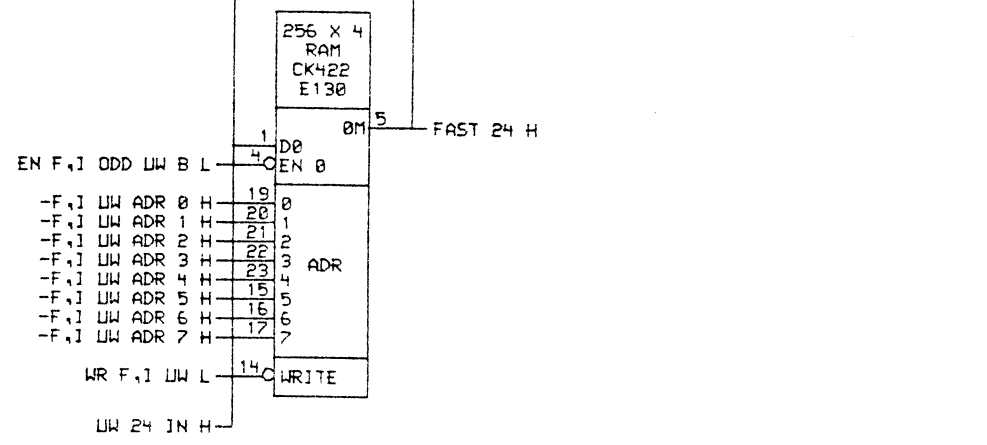
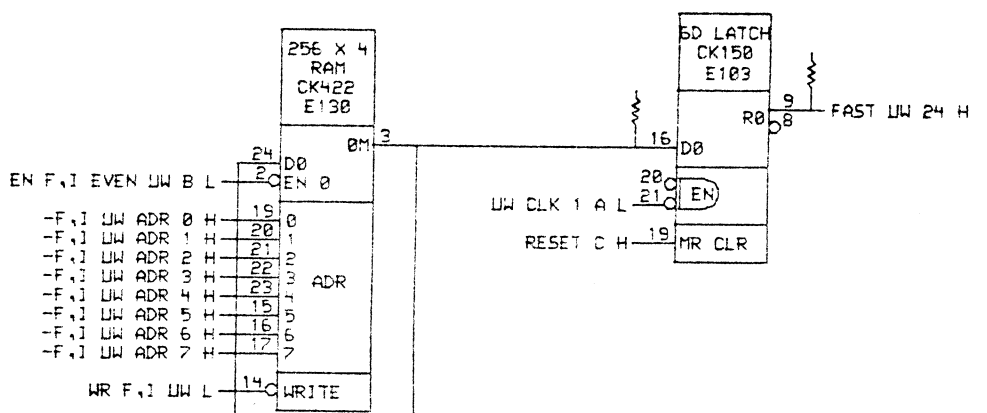
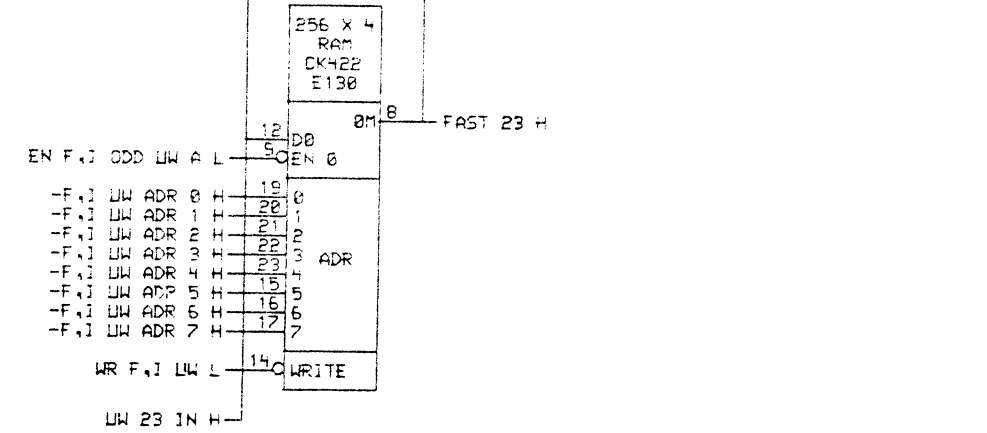
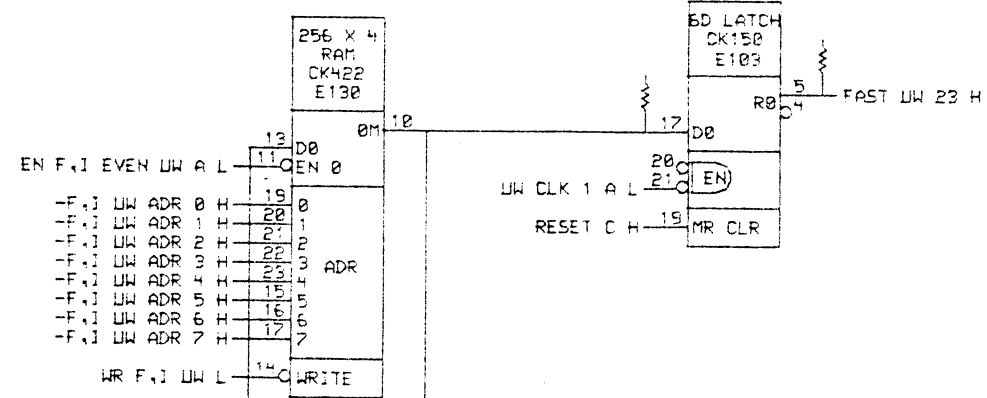


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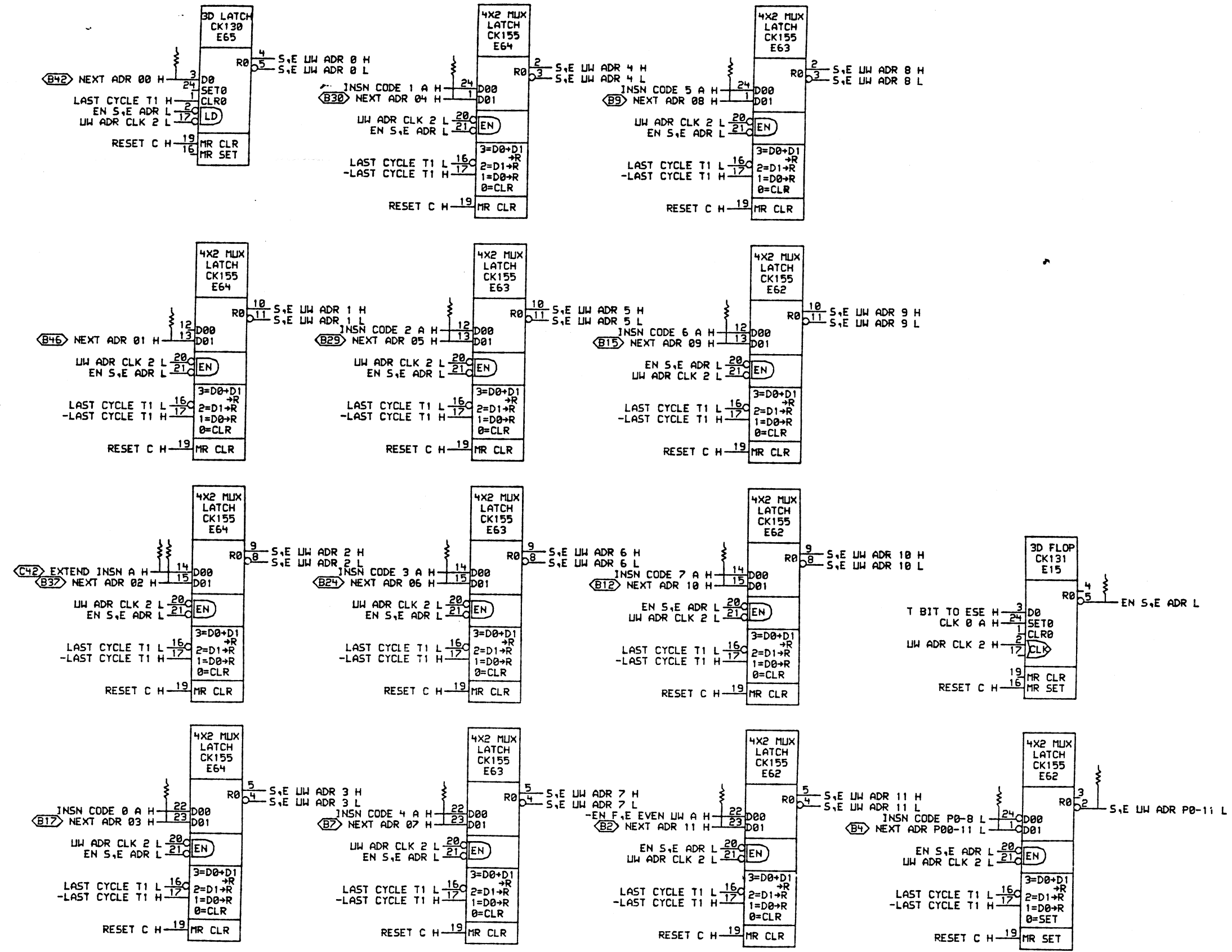
REVISIONS		
CHK	CHANGE NO.	REV

	DRW. <i>J. J. J.</i>	DATE 03-FEB-82	ENG.	DATE	TITLE: FAST, INTER LW 09-22, 29, 30
	CHK'D.		BOARD LOCATION:	SHEET OF	
VUL CAN: (2080) 1.0000 17E5E3.DRW 02-FEB-82 18:41		NEXT HIGHER ASSEMBLY:		SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL:				D CS	L0050-0-ESE3

REV. NUMBER 10050-0-ESE3



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		FAST, LATE LW
VILCANI <2000.L0050> ESE4.DRW 18-JUN-81 20:49 NEXT HIGHER ASSEMBLY:				23, 24
FIRST USED ON OPTION/MODEL:	SIZE	CODE	NUMBER	REV.
	D	CS	10050-0-ESE4	

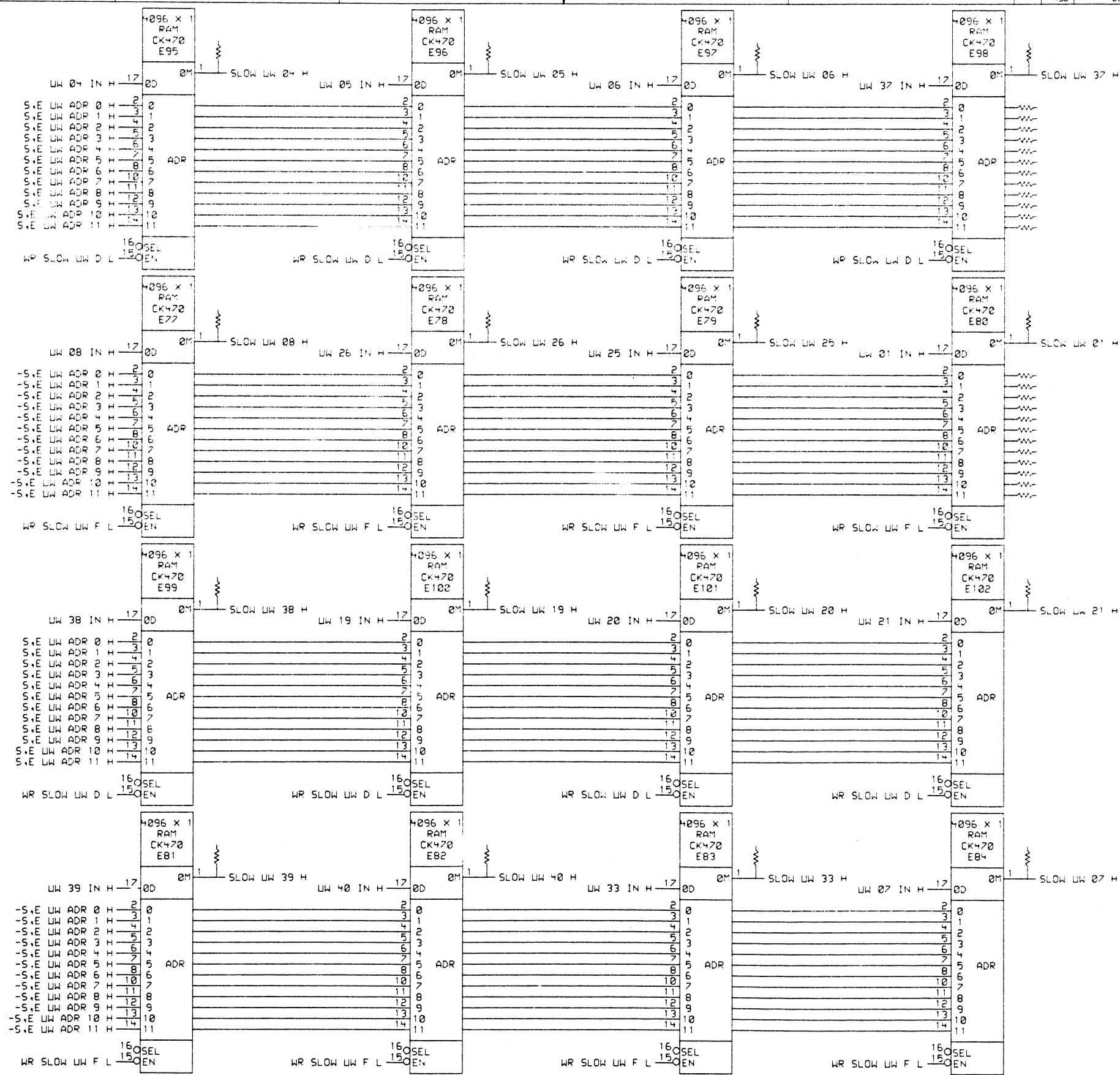


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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	ENG.	DATE	TITLE:
	30-APR-82			SLOW, EARLY CONTROL ADR
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF
VLCAM (2000-L-0050) ESE5.DRW	29-APR-82 23:16	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL:			D	CS
			NUMBER	REV.
			L0050-0-ESE5	

REV. NUMBER BOARD-0-ESE5



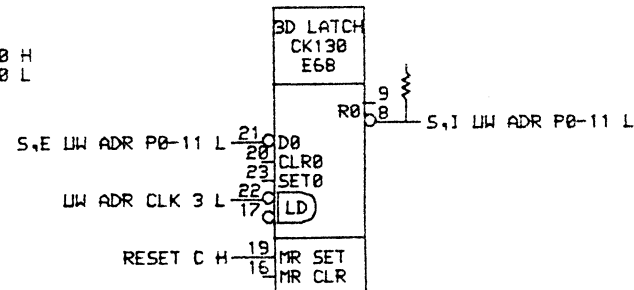
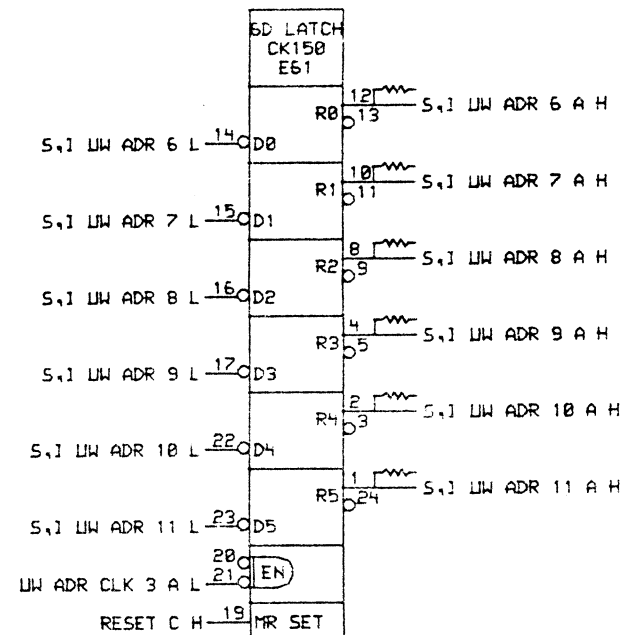
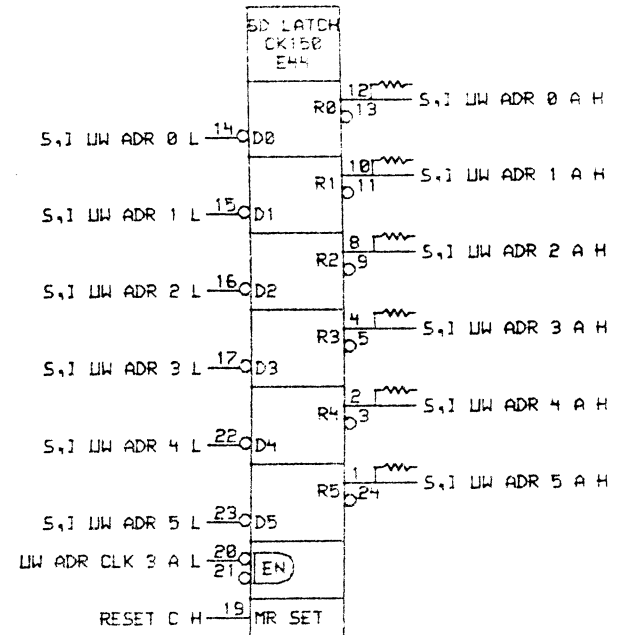
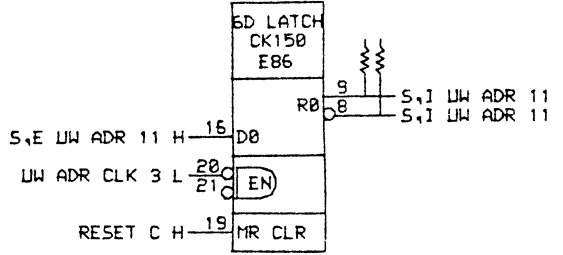
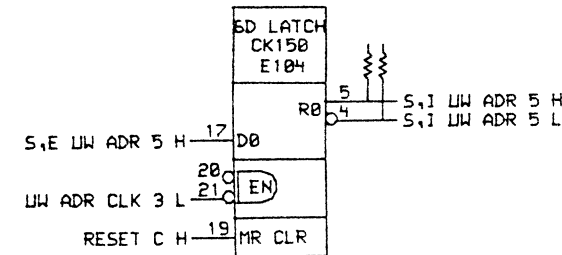
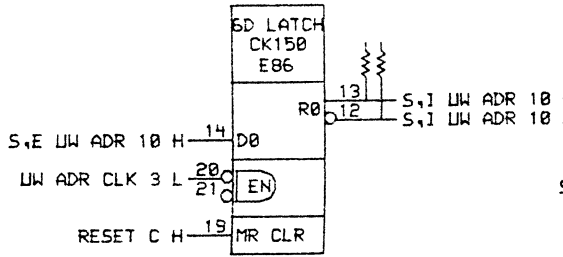
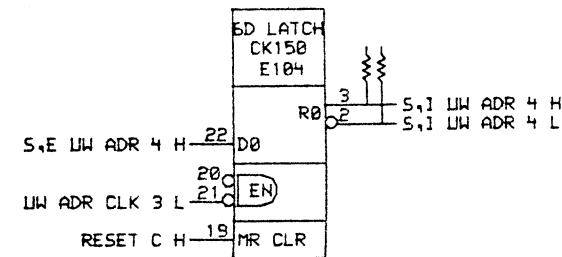
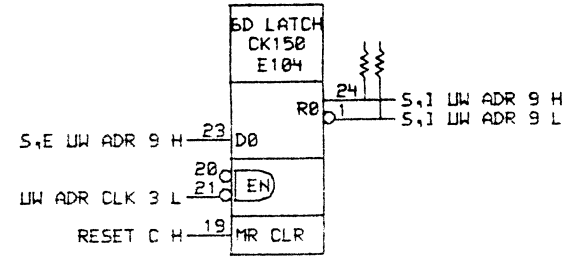
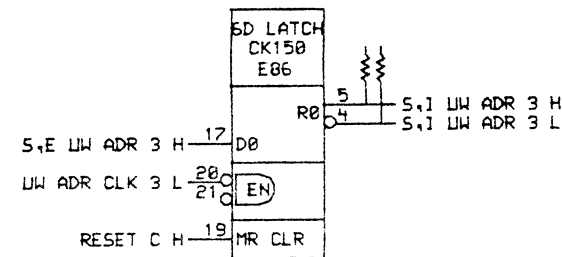
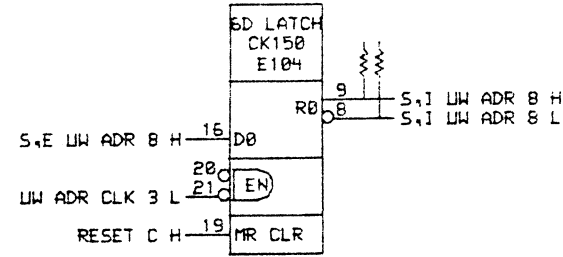
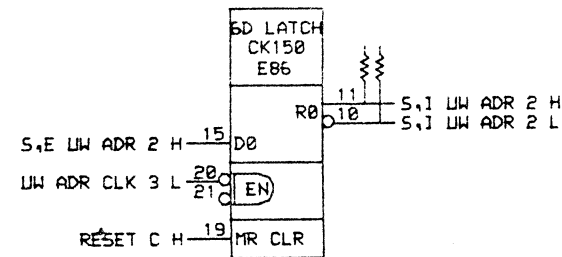
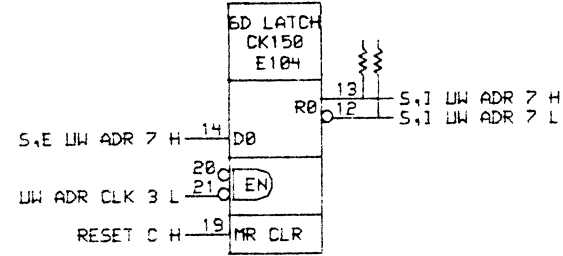
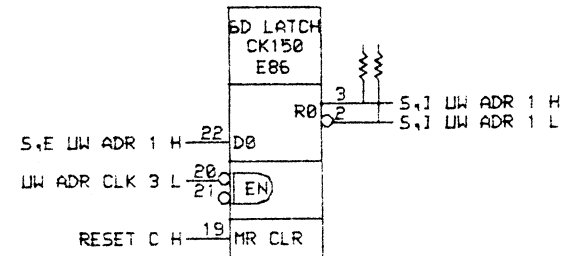
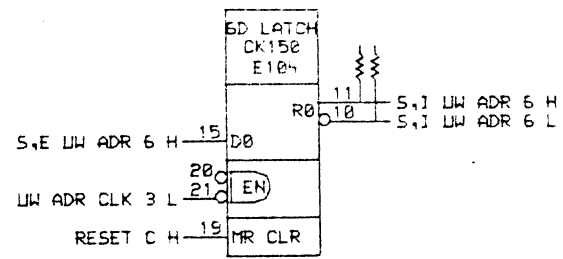
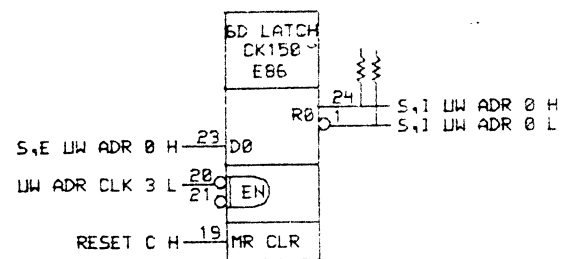
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REVISIONS		
CHK	CHANGE NO.	REV

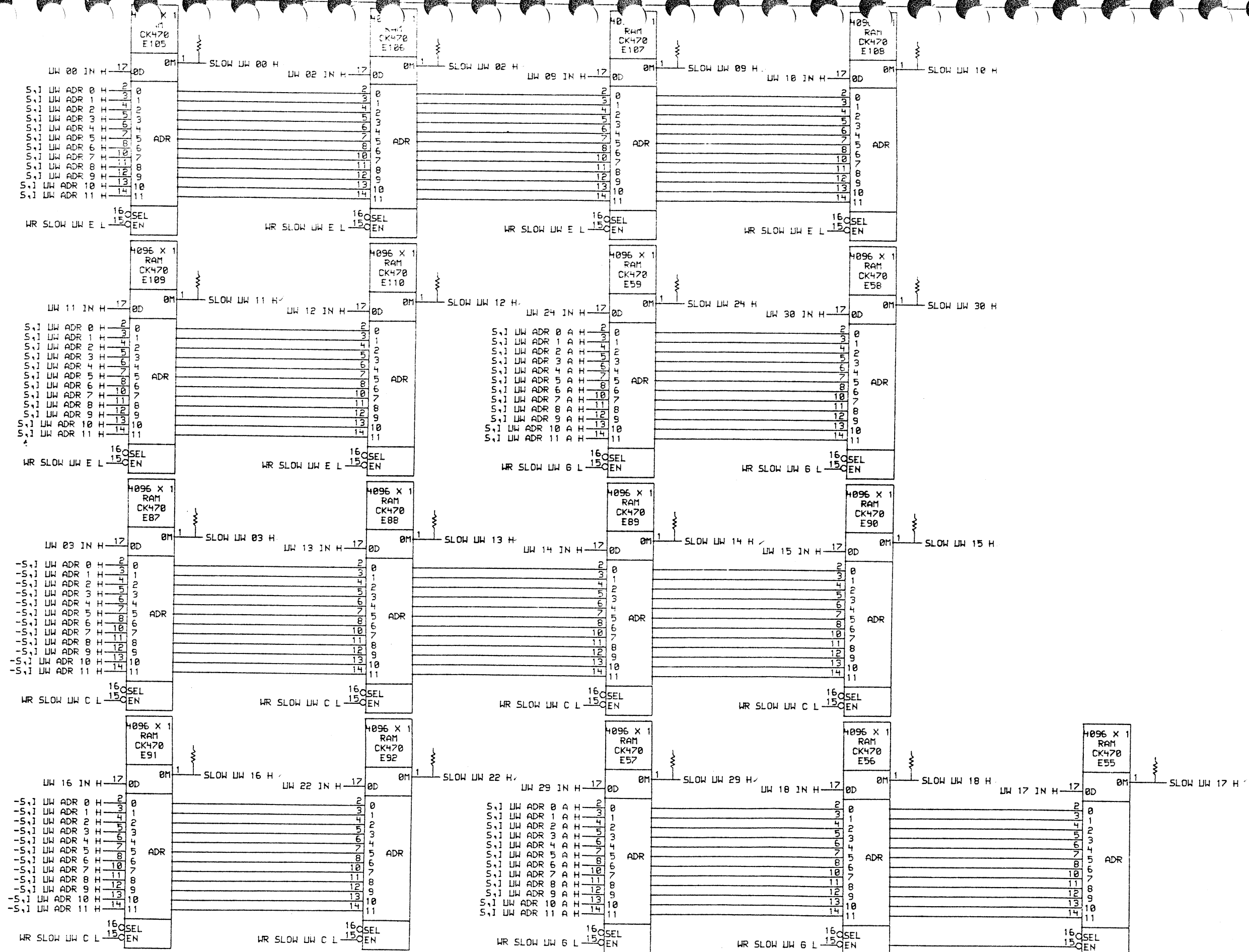
digital DRN. J. J. J. DATE 11-JAN-83
 CHK'D. DATE BOARD LOCATION: SHEET OF
 VULCAN: (2080.L0050)*E5E6.DRW 10-JAN-83 20:59 NEXT HIGHER ASSEMBLY:
 FIRST USED ON OPTION MODEL: SIZE CODE NUMBER REV.
 D CS L0050-0-E5E6

TITLE: SLOW, EARLY LW 00-8
 25, 26, 33, 37-40

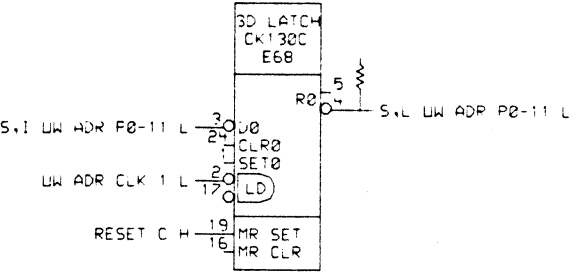
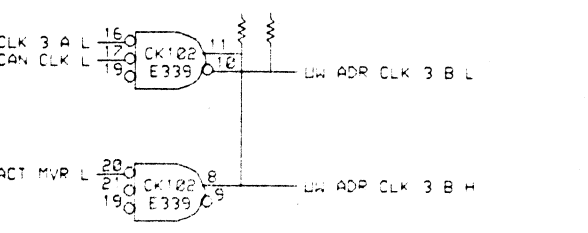
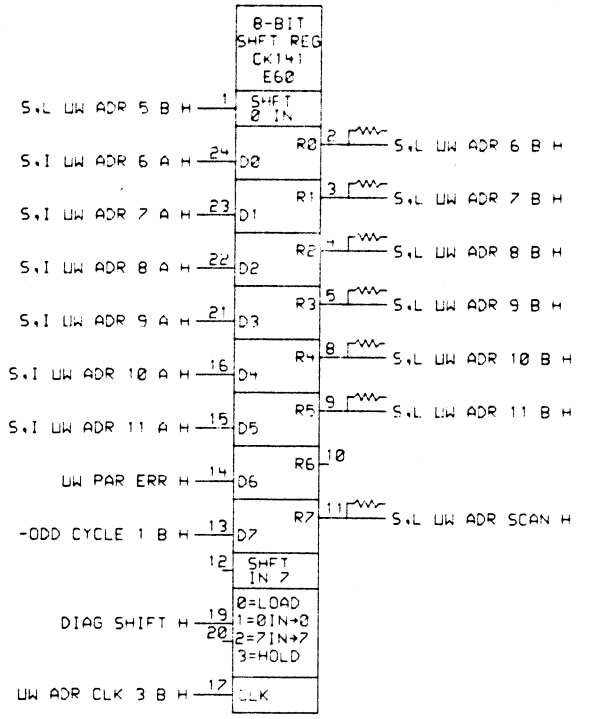
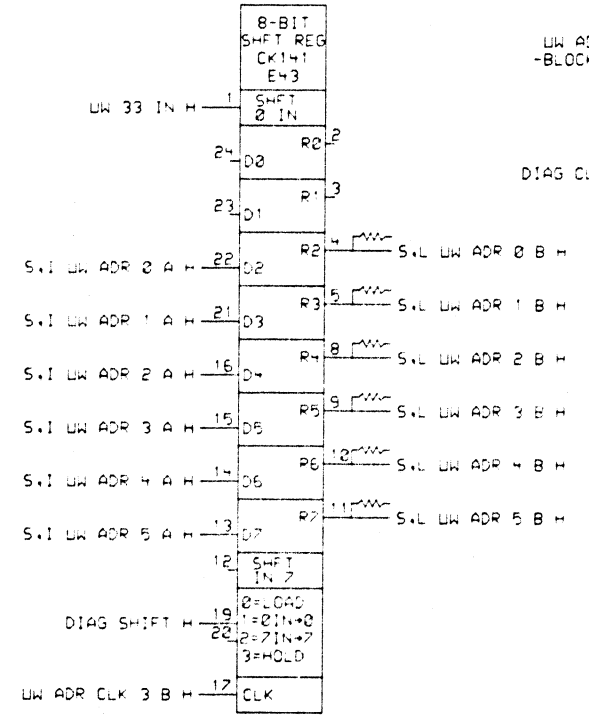
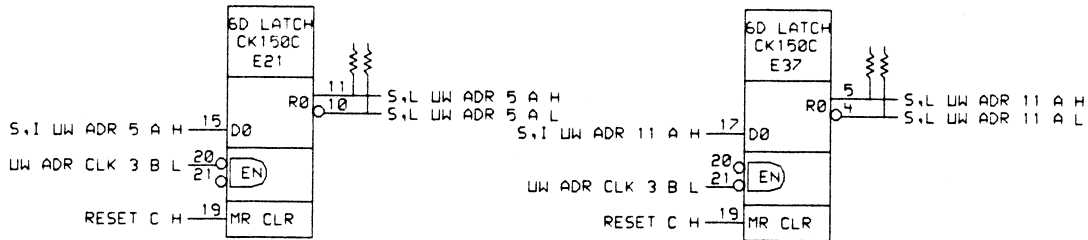
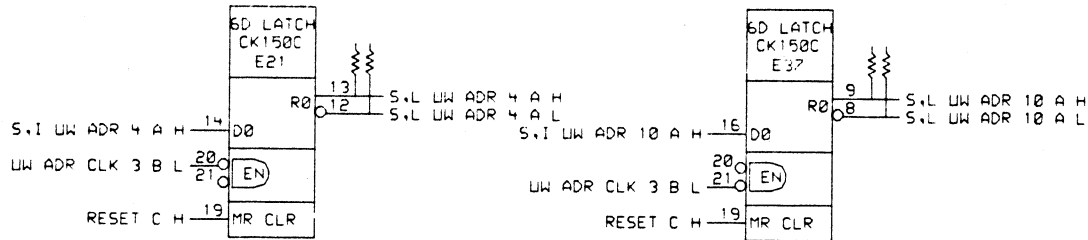
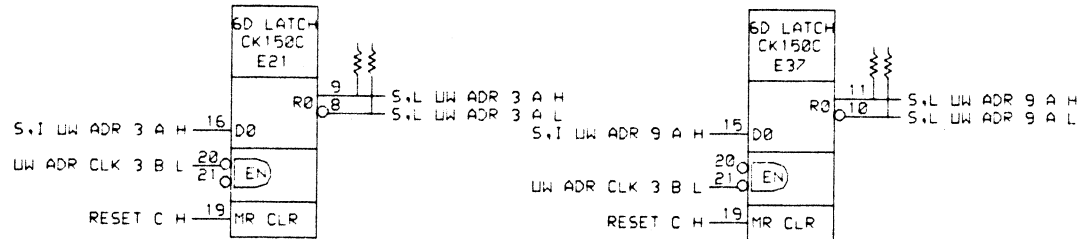
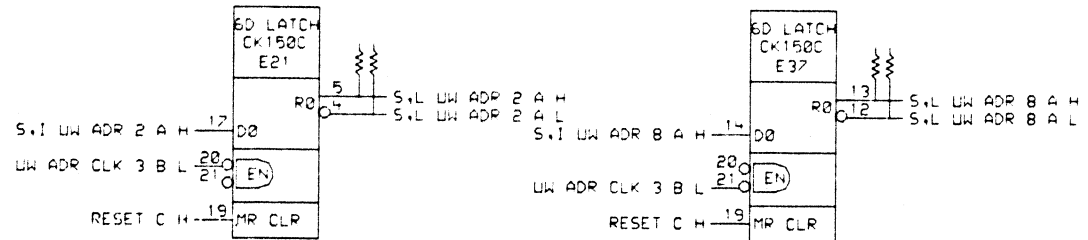
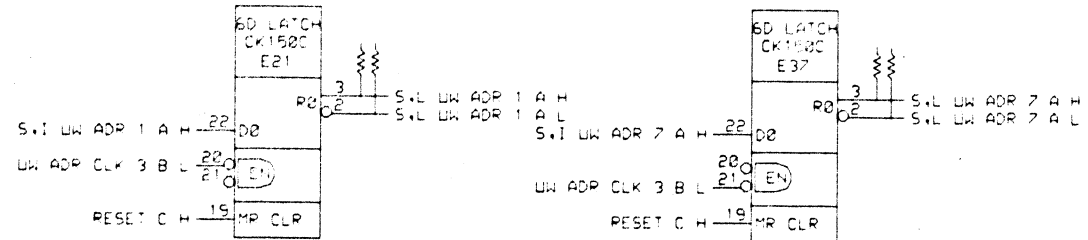
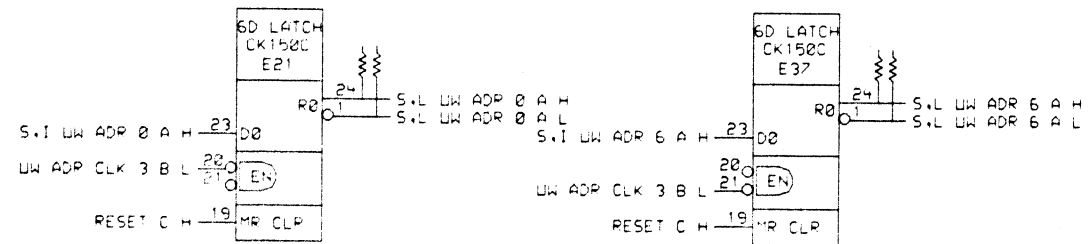
REV. 1
 NUMBER L0050-0-E5E6
 CODE CS
 SIZE D



DRN.	DATE	ENG.	DATE	TITLE:	SLOW, INTER CTL ADR REG
CHK'D.	DATE	BOARD LOCATION:	SHEET	OF	
VILCANI (2000.L0050)E57.DRW 18-JUN-81 20:50				NEXT HIGHER ASSEMBLY:	SIZE CODE
FIRST USED ON OPTION/MODEL:				D	CS
				NUMBER	REV.
				L0050-0-E57	



D
C
B
A



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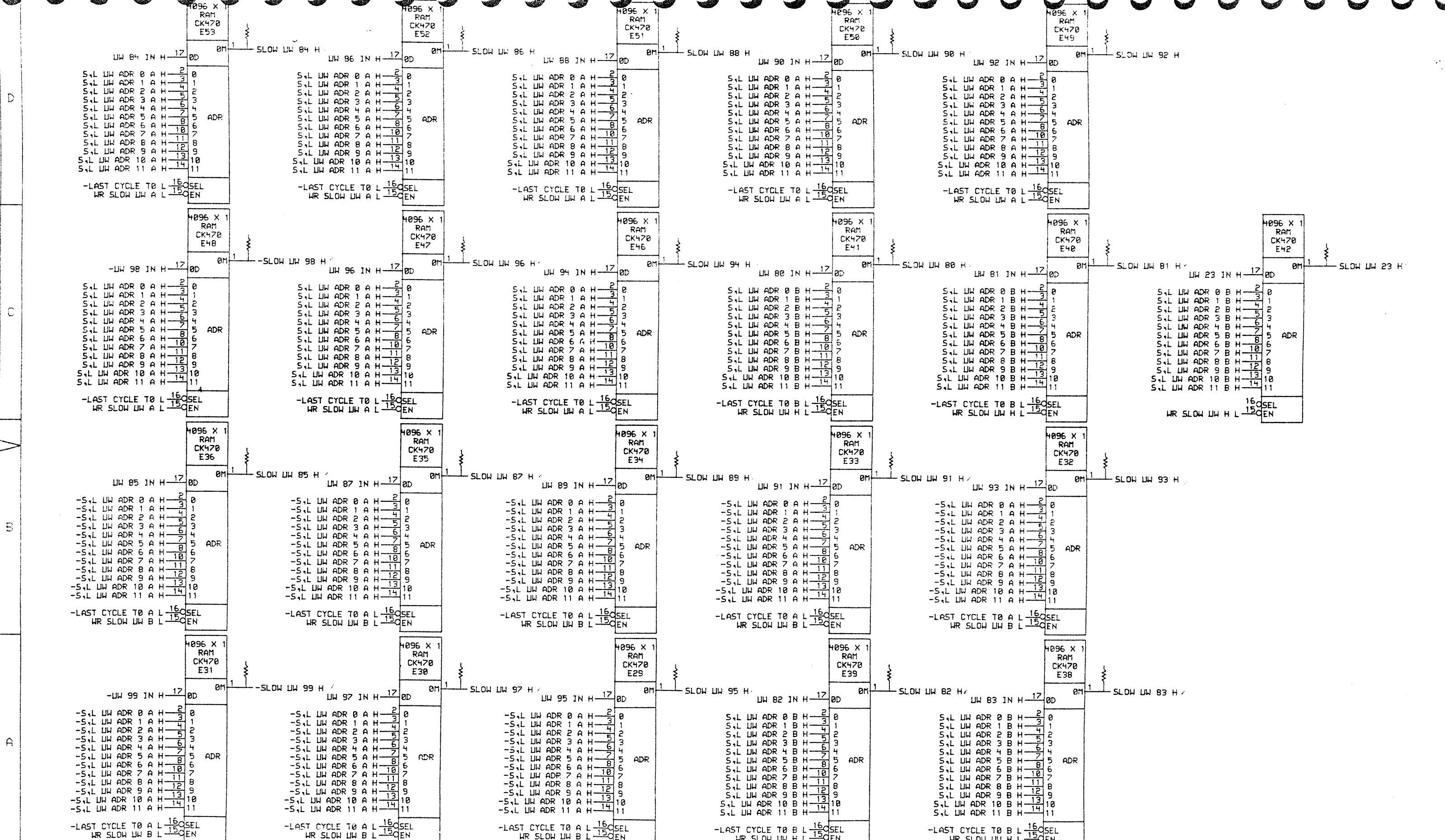
REVISIONS	
CHK	CHANGE NO. REV.

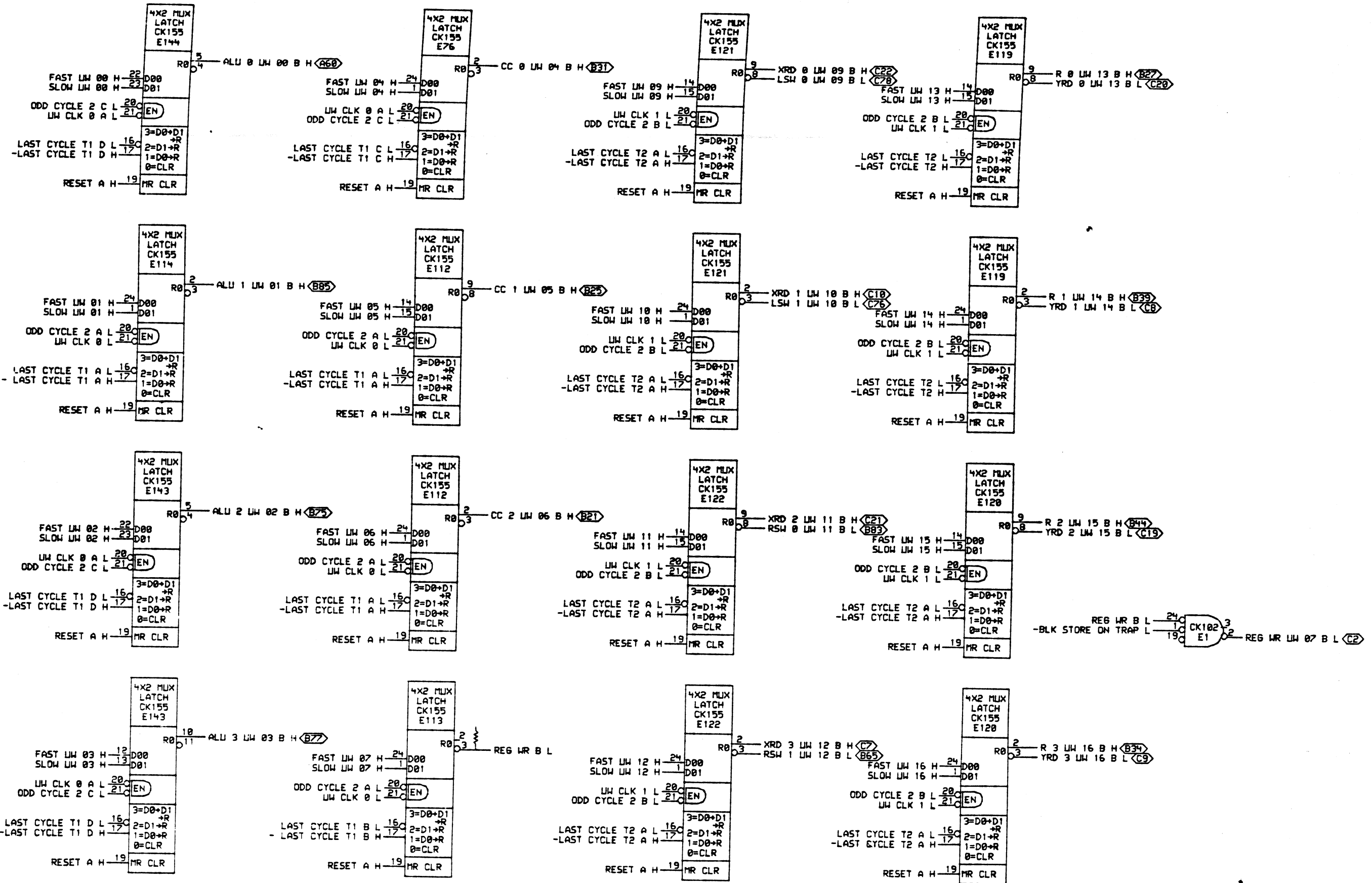
digital
VULCAN: (2000.L0050) + ESE9.DRW 10-JAN-83 16:03
FIRST USED ON OPTION/MODEL:

DATE 11-JAN-83
DATE 10-JAN-83
ENG. BOARD LOCATION: SHEET OF
NEXT HIGHER ASSEMBLY:

TITLE: SLOW, LATE CTL ADR 0-11
SIZE CODE NUMBER REV.
D CS L0050-0-ESE9

REV. 1
STATE CODE CS
NUMBER L0050-0-ESE9



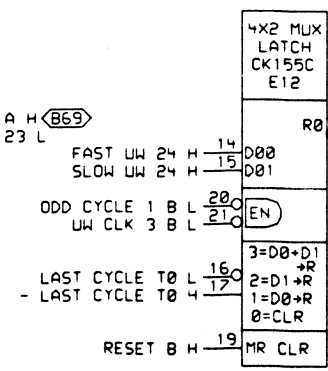
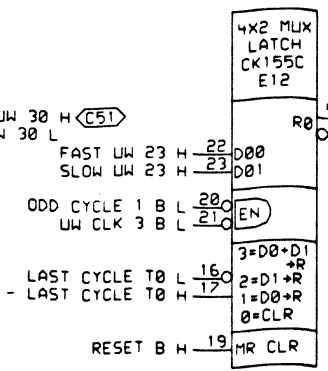
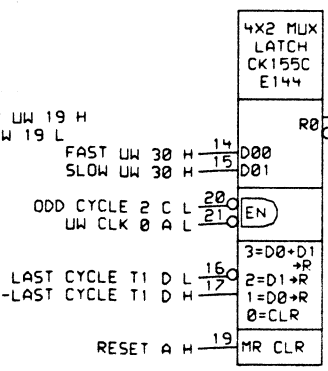
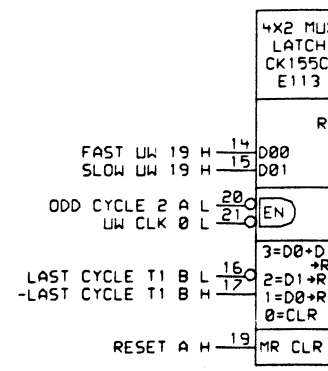
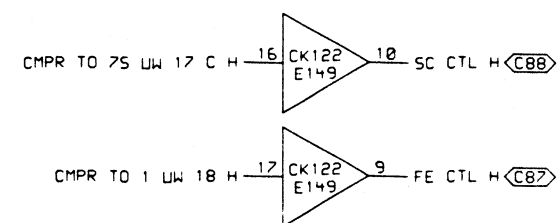
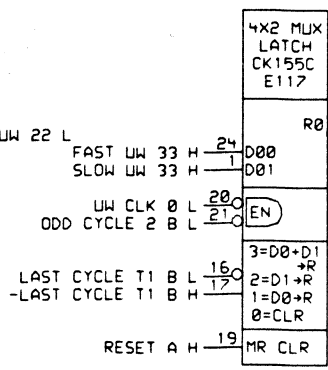
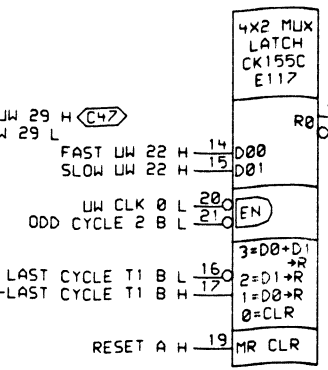
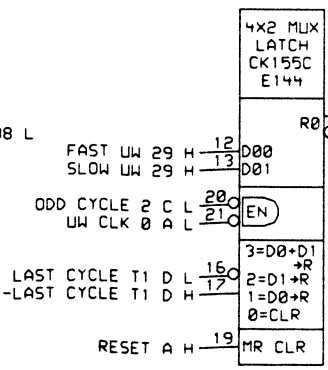
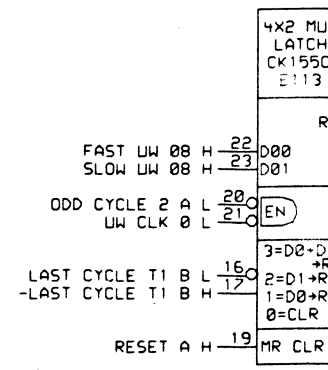
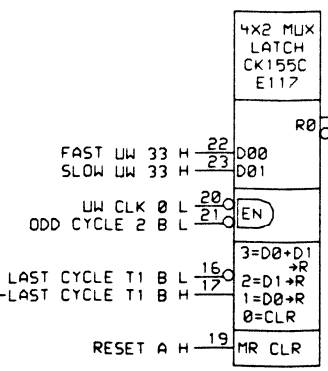
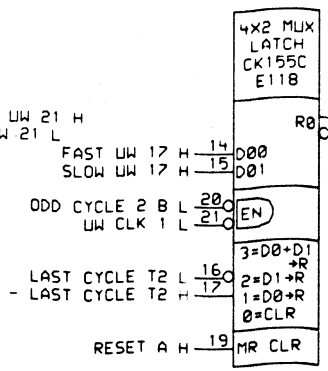
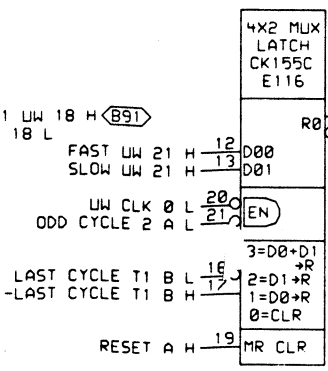
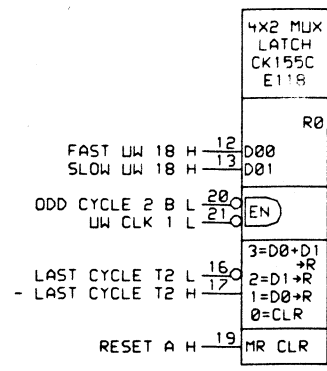
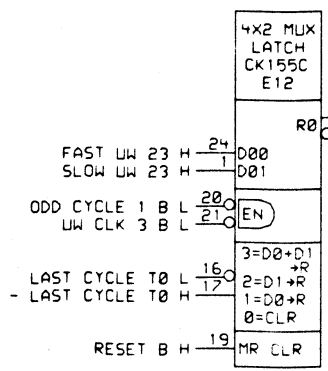
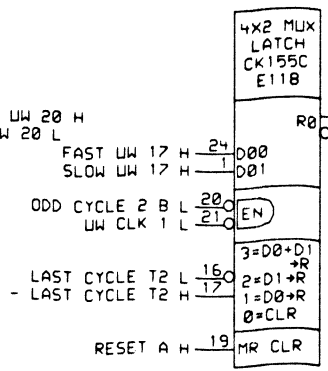
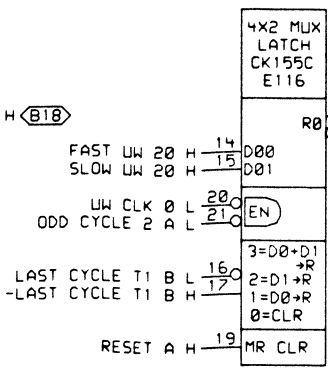
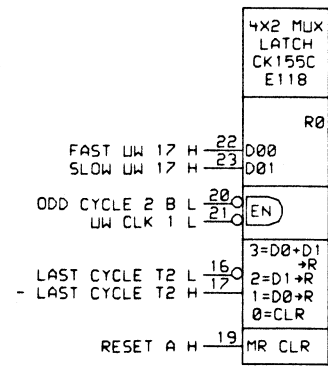


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REVISIONS	
CHK	CHANGE NO. REV.

8	7	6	5	4	3	2	1
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	DATE: 29-JUN-82 ENG. BOARD LOCATION: SHEET OF	DATE:	TITLE: LW 00-07, 09-16
	FIRST USED ON OPTION/MODEL:	NEXT HIGHER ASSEMBLY:	SIZE CODE: D CS
NUMBER: L0050-0-ESEC		REV.:	



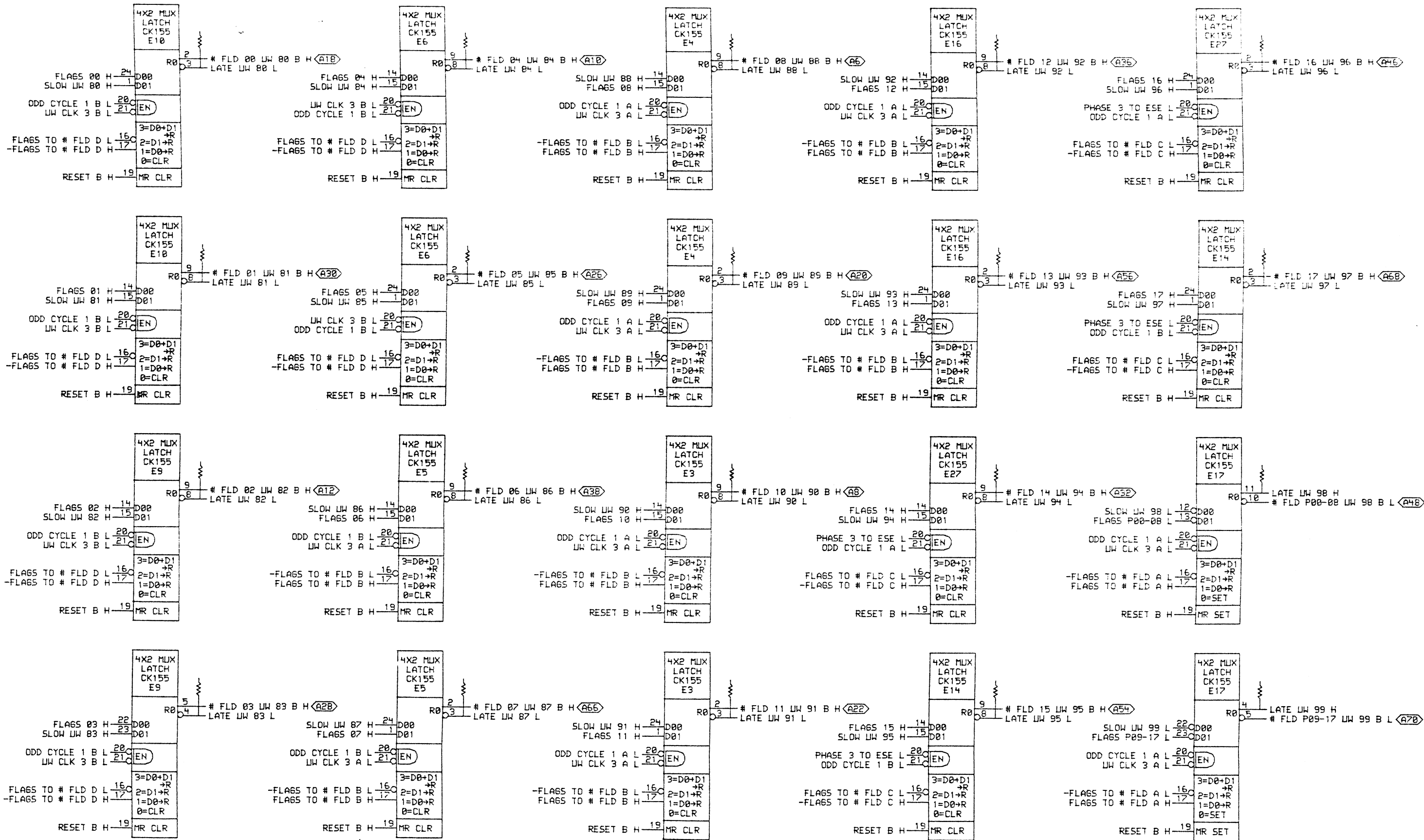
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REVISIONS		
CHK	CHANGE NO.	REV

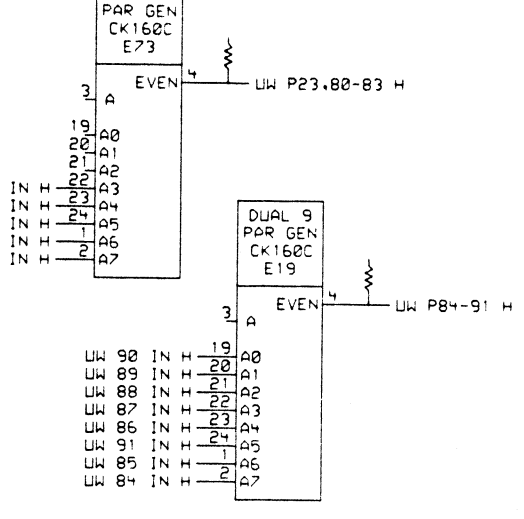
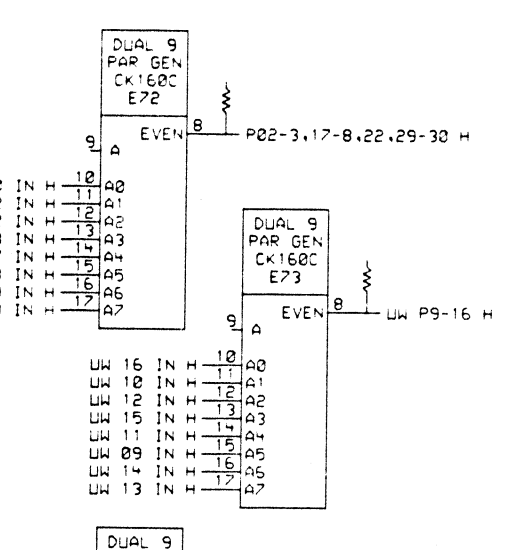
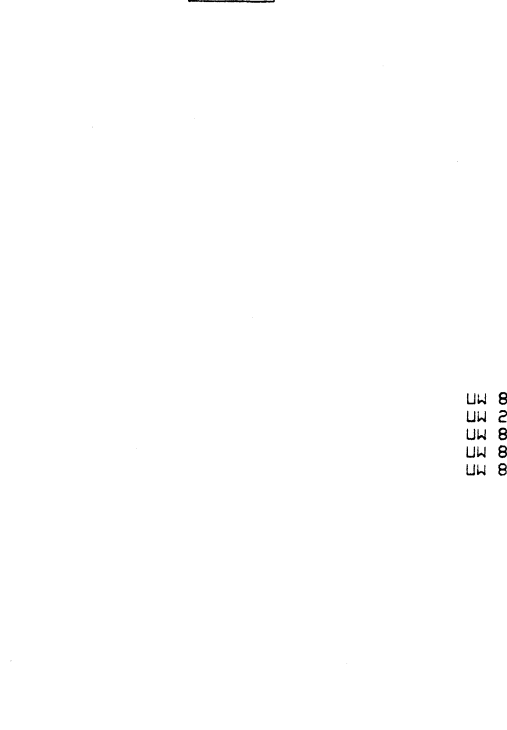
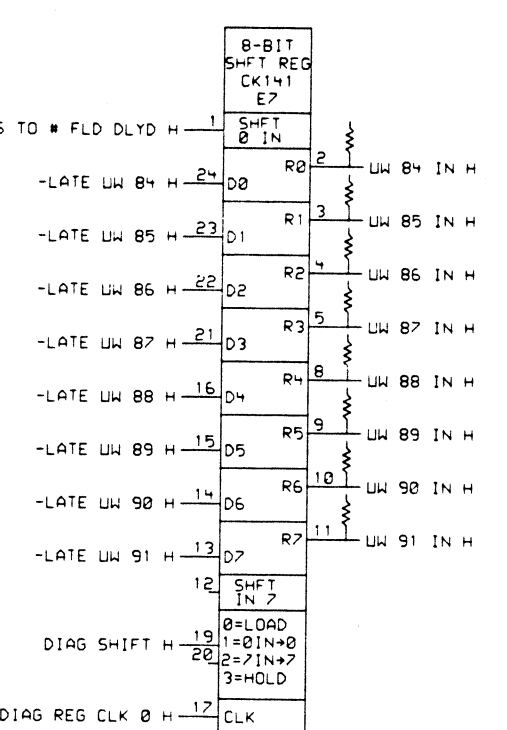
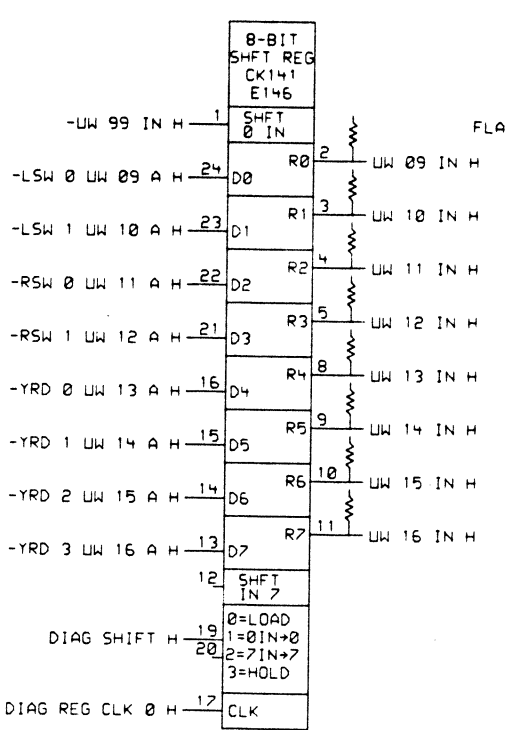
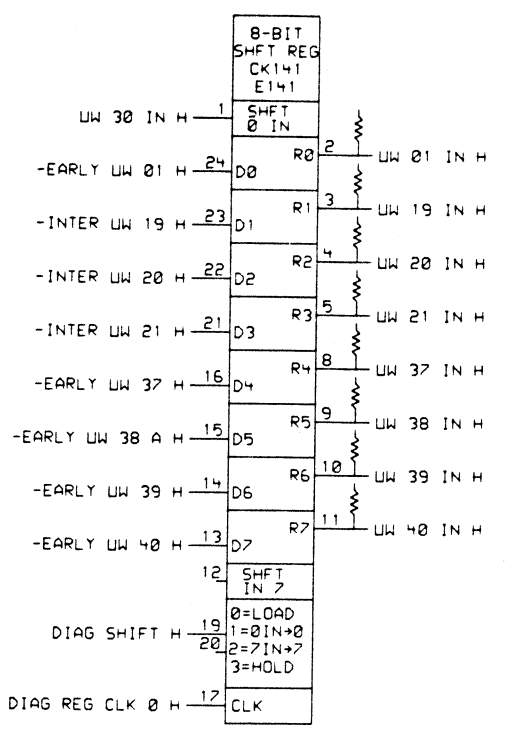
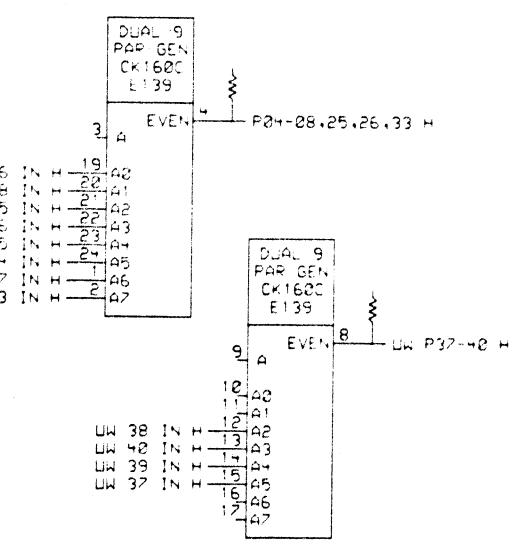
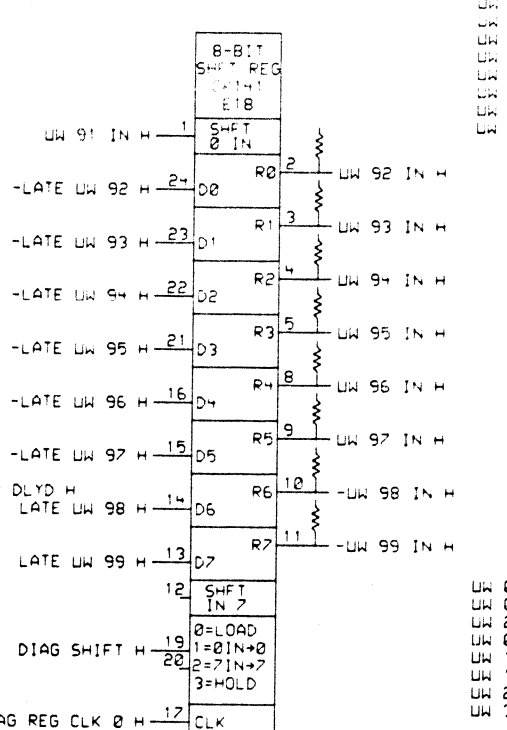
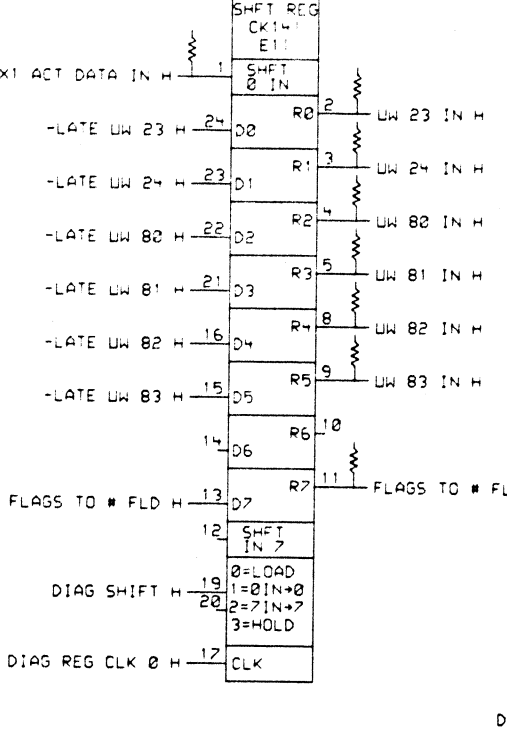
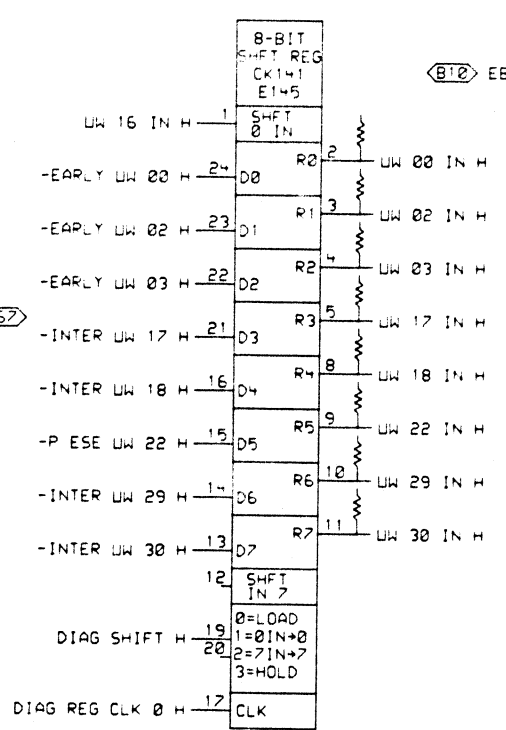
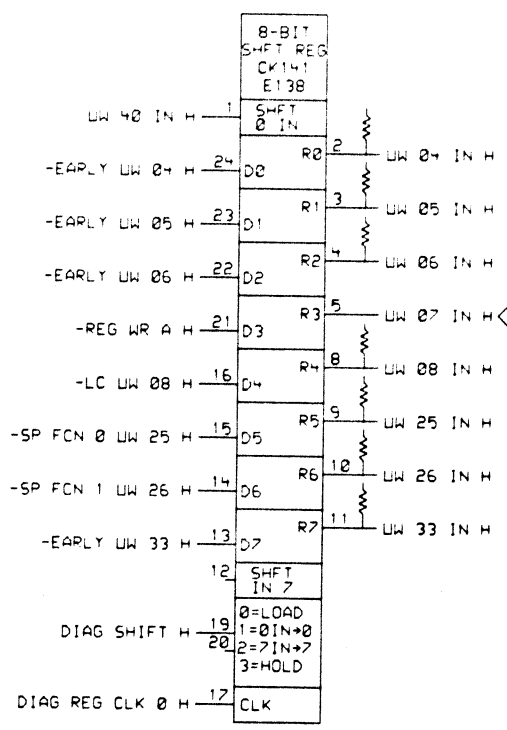
digital *DRN. J. J. J.* DATE 22-DEC-82 ENG. BOARD LOCATION: SHEET OF
 VULCAN: 2000 L0050-0-ESED.DRW 20-DEC-82 16:46 NEXT HIGHER ASSEMBLY: FIRST USED ON OPTION/MODEL:

TITLE:	UW 08, 17-24
NUMBER	29, 30, 33
SIZE CODE	D C5
NUMBER	L0050-0-ESED
REV.	

REV. NUMBER L0050-0-ESED
 SIZE CODE D C5
 B A



DRN.	DATE	ENG.	DATE	TITLE:
CHK'D.	DATE	BOARD LOCATION:		# FLD MUX 00-17
V.L.CANI (2088.L0050) ESEF.DRW	18-JUN-81	20:51	NEXT HIGHER ASSEMBLY:	TO SHR, PAR CHK
FIRST USED ON OPTION/MODEL:				
SIZE	CODE	NUMBER	REV.	
D	C5	L0050-0-ESEF		



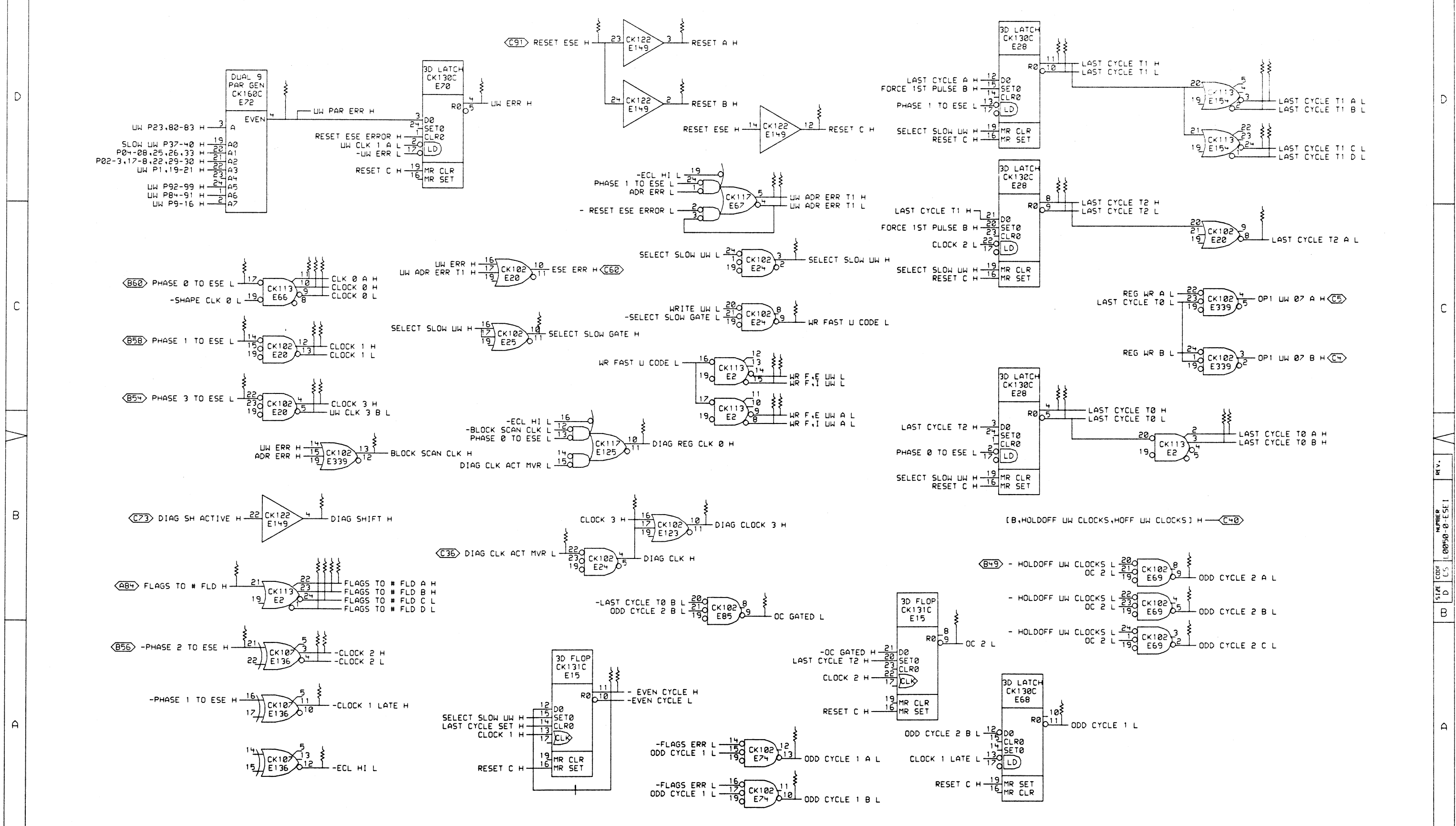
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REV.	CHG	NO.	REV

DATE	ENG.	DATE	TITLE:
11-JAN-83	J. Jansky		UW DIAG REG 00-26
DATE	BOARD LOCATION:	SHEET	OF
10-JAN-83 21:02			
FIRST USED ON OPTION/MODEL:		SIZE	CODE
		D	CS
		NUMBER	REV.
		L0050-0-ESEG	

DATE	ENG.	DATE	TITLE:
11-JAN-83	J. Jansky		UW DIAG REG 00-26
DATE	BOARD LOCATION:	SHEET	OF
10-JAN-83 21:02			
FIRST USED ON OPTION/MODEL:		SIZE	CODE
		D	CS
		NUMBER	REV.
		L0050-0-ESEG	

REV. NUMBER L0050-0-ESEG



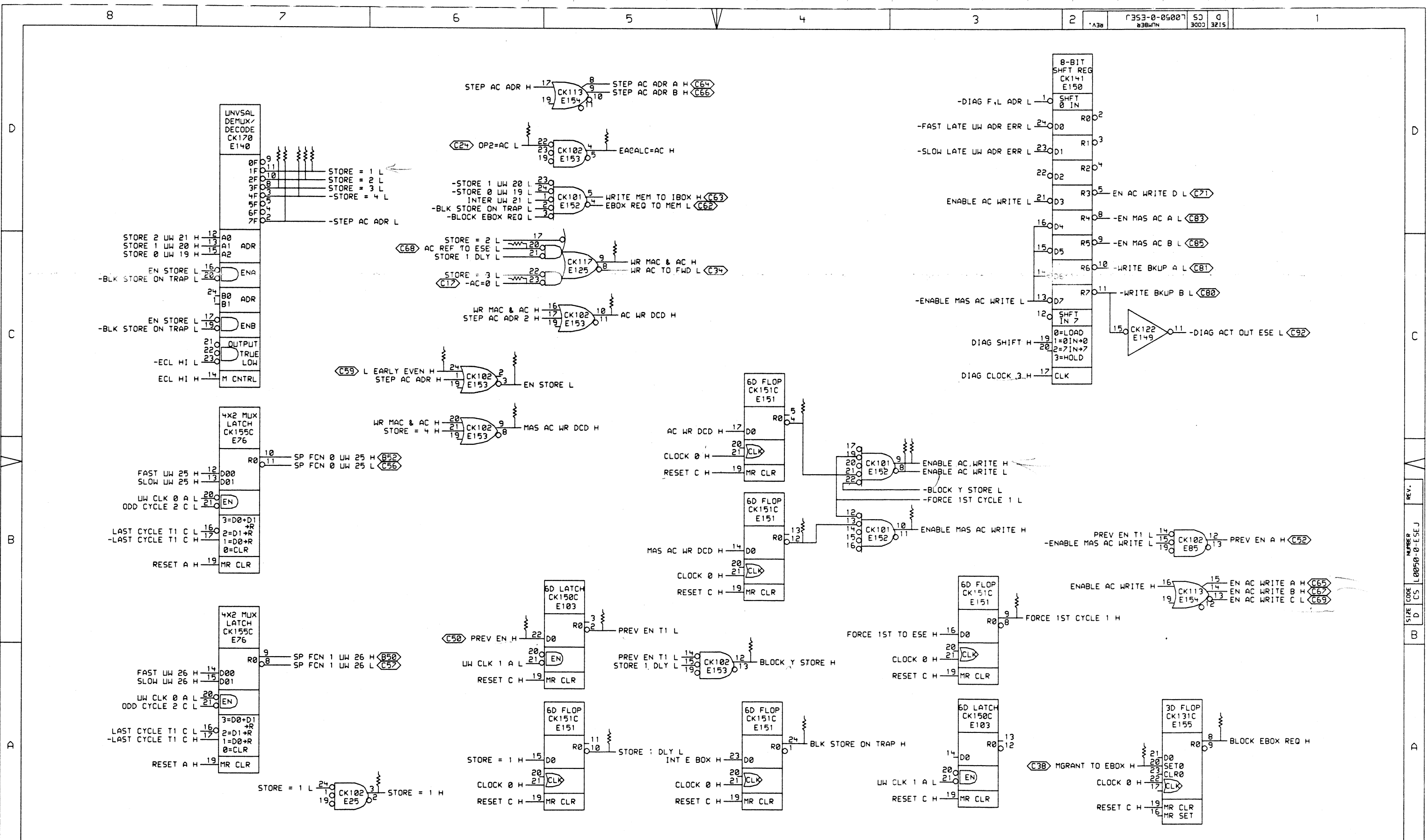
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REVISIONS	
CHK	CHANGE NO. REV

8	7	6	5	4	3	2	1
---	---	---	---	---	---	---	---

	DATE: 19-JAN-83 DATE: 18-JAN-83 10:27 FIRST USED ON OPTION/MODEL:	ENG. DATE: 19-JAN-83 BOARD LOCATION: SHEET OF	TITLE: MISC CLOCKS, ERRORS, CTLS NUMBER: L0050-0-ESE1 REV.
	NEXT HIGHER ASSEMBLY:		

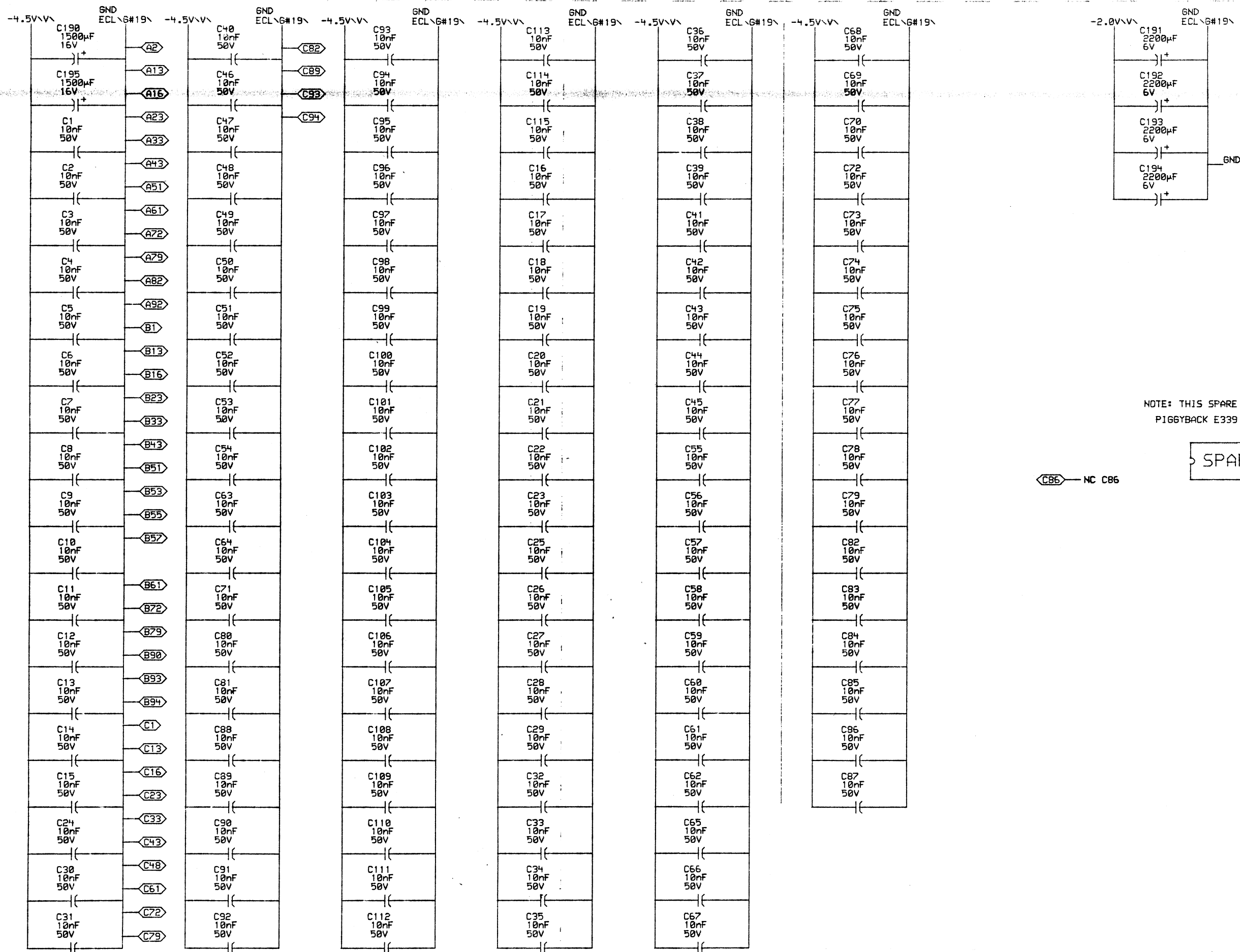
REV. 1
 SIZE D
 CODE US
 NUMBER L0050-0-ESE1



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REVISIONS	
CHK	CHANGE NO. REV

	DATE	ENG.	DATE	TITLE:
	14-JAN-83	J. Jansky	14-JAN-83	WRITE CONTROLS FAST, SLOW MUX 25-6
VULCANI (2000.L0050) ESEJ.DRW		SHEET		REV.
FIRST USED ON OPTION/MODEL:		NEXT HIGHER ASSEMBLY:		
SIZE	CODE	NUMBER	REV.	
D	CS	L0050-0-ESEJ		



NOTE: THIS SPARE SHOULD NOT BE USED.
P166YBACK E339 WILL DISPLACE IT.

SPARE E148

NC C86

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REVISIONS		
CHK	CHANGE NO.	REV

	DRN. <i>J. Looney</i>	DATE	ENG.	DATE	TITLE:
	CHK'D.	01-OCT-82			ESE POWER AND GROUND
VIL CAN: <P&B L0050> RESEL.DRW		30-SEP-82 21:33	NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL: 2080		B-DD-L0050-0	NUMBER		REV.
			D CS L0050-0-1ESEL		B