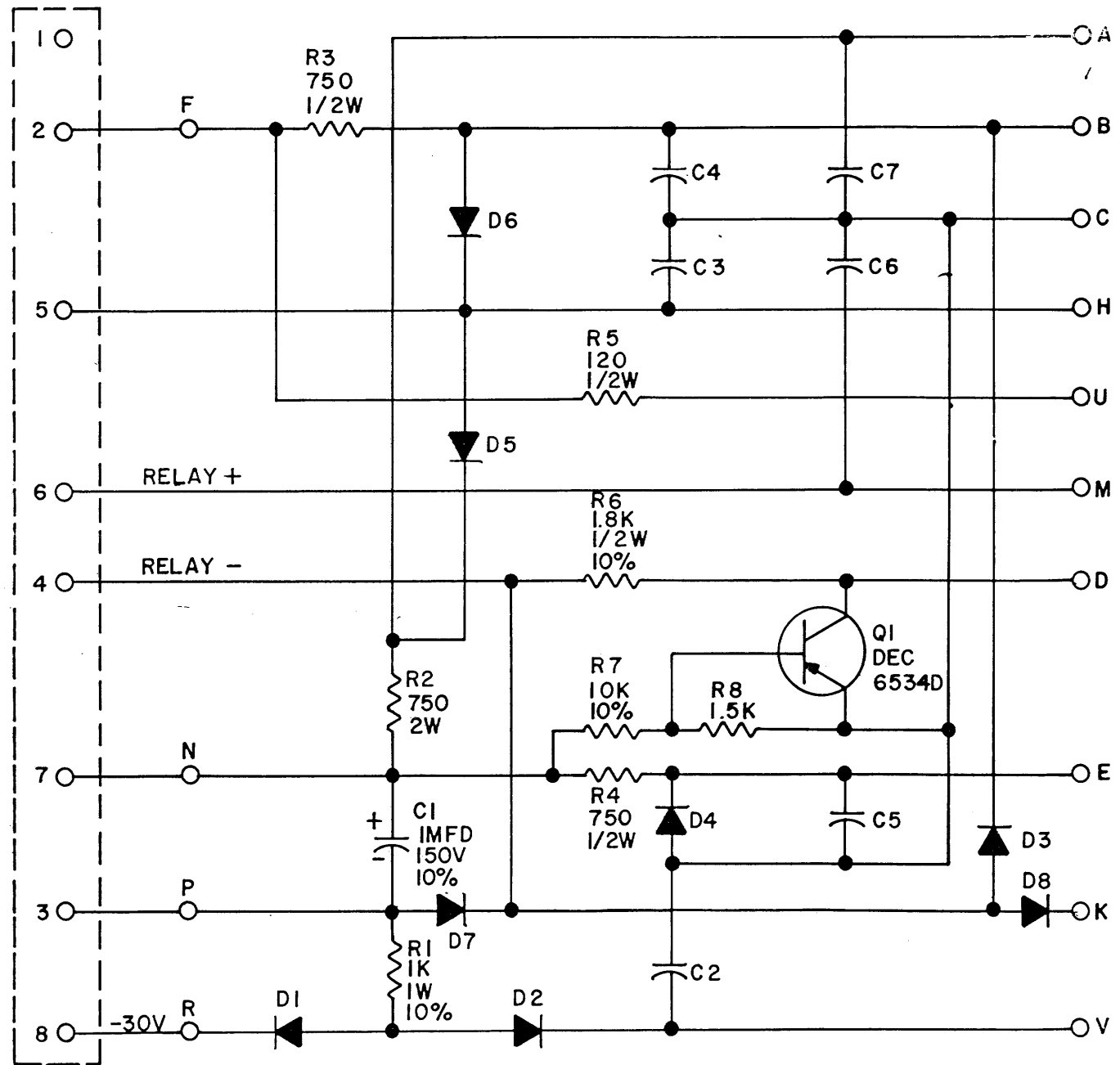


THIS SCHEMATIC IS FURNISHED ONLY FOR TEST AND MAINTENANCE PURPOSES. THE CIRCUITS ARE PROPRIETARY IN NATURE AND SHOULD BE TREATED ACCORDINGLY. COPYRIGHT 1969 BY DIGITAL EQUIPMENT CORPORATION

TYPICAL PIN ASSIGNMENTS			
PIN	8	8I, 8L	DC08
A	+10V	+5V	+5V
B	-15V	-15V	+5V
C	GND	GND.	GND
H	LOGIC INPUT	LOGIC INPUT	—
U	—	—	LOGIC INPUT
M	READER ENABLE	READER ENABLE	READER ENABLE
D	LOGIC OUTPUT	—	—
E	—	LOGIC OUTPUT	LOGIC OUTPUT
K	—	—	-15V
V	-30V OPTIONAL	-30V OPTIONAL	-30V OPTIONAL

AMP CONNECTOR



UNLESS OTHERWISE INDICATED:
 CAPACITORS ARE .01MFD 100V, 20%
 DIODES ARE D671
 RESISTORS ARE 1/4W, 5%
 CONNECTOR IS DEC# 1209456

REVISIONS CHK CHG NO. REV.	DRN. <i>R. Blum</i>	DATE 9-26-68	TRANSISTOR & DIODE CONVERSION CHART		digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE TELETYPE CONNECTOR W078			
	CHK'D <i>L. B. Hallett</i>	DATE 01/13/69	DEC	EIA		SIZE	CODE	NUMBER	REV.
	ENG. <i>A. Quinlan</i>	DATE	D671	1N3653	B	CS	W078-0-1		
	PROD.	DATE	DEC6534D	MPS6534D	PRINTED CIRCUIT REV.			A	