

**DK8-EC crystal
real time clock
engineering drawings**

This drawing and specifications, herein, are the property of Digital Equipment Corporation and shall not be reproduced or copied or used in whole or in part as the basis for the manufacture or sale of items without written permission.

DIGITAL EQUIPMENT CORPORATION
MAYNARD, MASSACHUSETTS

ENGINEERING SPECIFICATION

DATE *10/13/71*

TITLE Engineering Specifications for DK8-EC M8830 Real Time Clock (crystal)

REVISIONS

REV	DESCRIPTION	CHG NO	ORIG	DATE	APPD BY	DATE

ENGINEERING SPECIFICATION

CONTINUATION SHEET

TITLE Engineering Specs for DK8-EC (M8830) Real Time Clock (crystal)

DK8-EC Real Time Clock (crystal)

1. General Description

1.1 The DK8-EC(M8830) counts intervals of time at any one of four different selectable rates 1 Hz, 50 Hz, 500 Hz and 5000 Hz. These rates are selected by adding jumpers on the M8830. The M8830 is shipped with a machine inserted jumper selecting the 1 Hz rate.

The DK8-EC plugs into the OMNIBUS of the PDP8-E processor. The frequency is determined by a 20 MHz crystal oscillator divided down through MSI decade counters.

1.1.1 The crystal oscillator, decade counter and the remaining logic is contained on one 8 1/2 inch quad module. All three IOT's for the DK8-EC are decoded on this module and are listed below:

MNEMONIC	CODE	OPERATION
CLEI	6131	Set Interrupt
CLDI	6132	Clear Interrupt
CLSK	6133	Skip on clock flag and clear flag

1.2 Operation

Refer to DK8-EC block diagram and timing diagram. 6132 or initialize will clear the interrupt request flag. The interrupt request line will be asserted if the slave clock flag is set from the master clock flag and will remain asserted until it is cleared by 6132, initialize or 6133. The master clock flag is set every time the frequency source goes high 1, 50, 500 or 5000 times a second.

ENG A. DeLuca	APPD <i>Luca</i>	SIZE A	CODE SP	NUMBER DK8-EC-4	REV
------------------	---------------------	-----------	------------	--------------------	-----

SIZE A	CODE SP	NUMBER DK8-EC-4	REV
-----------	------------	--------------------	-----

TITLE Engineering Specs for DK8-EC M8830 Real Time Clock (crystal)

1.2

Operation (continued)

The skip line is pulled to ground, asserted, if the slave clock flag has been set and the IOT 6133 is issued. The skip line stays at ground for the duration of the IOT 6133. At the end of this IOT the master clock flag will be cleared. TP1 the start of any IOT will clear or set the slave flag depending upon the output state of the master flag. If the master flag has been cleared then the slave will be cleared. With both the master and slave cleared no skip can occur until the next clock pulse which will set the master flag and TP1 will set the slave.

It takes two IOT's to sync to the frequency source the first to clear the master clock flag and the second to skip the moment the master clock flag goes high which will be when the frequency source clock pulse goes high.

Sample Program:

```
6133
JMP.-1
6133
JMP.-1
```

2. Operating Conditions

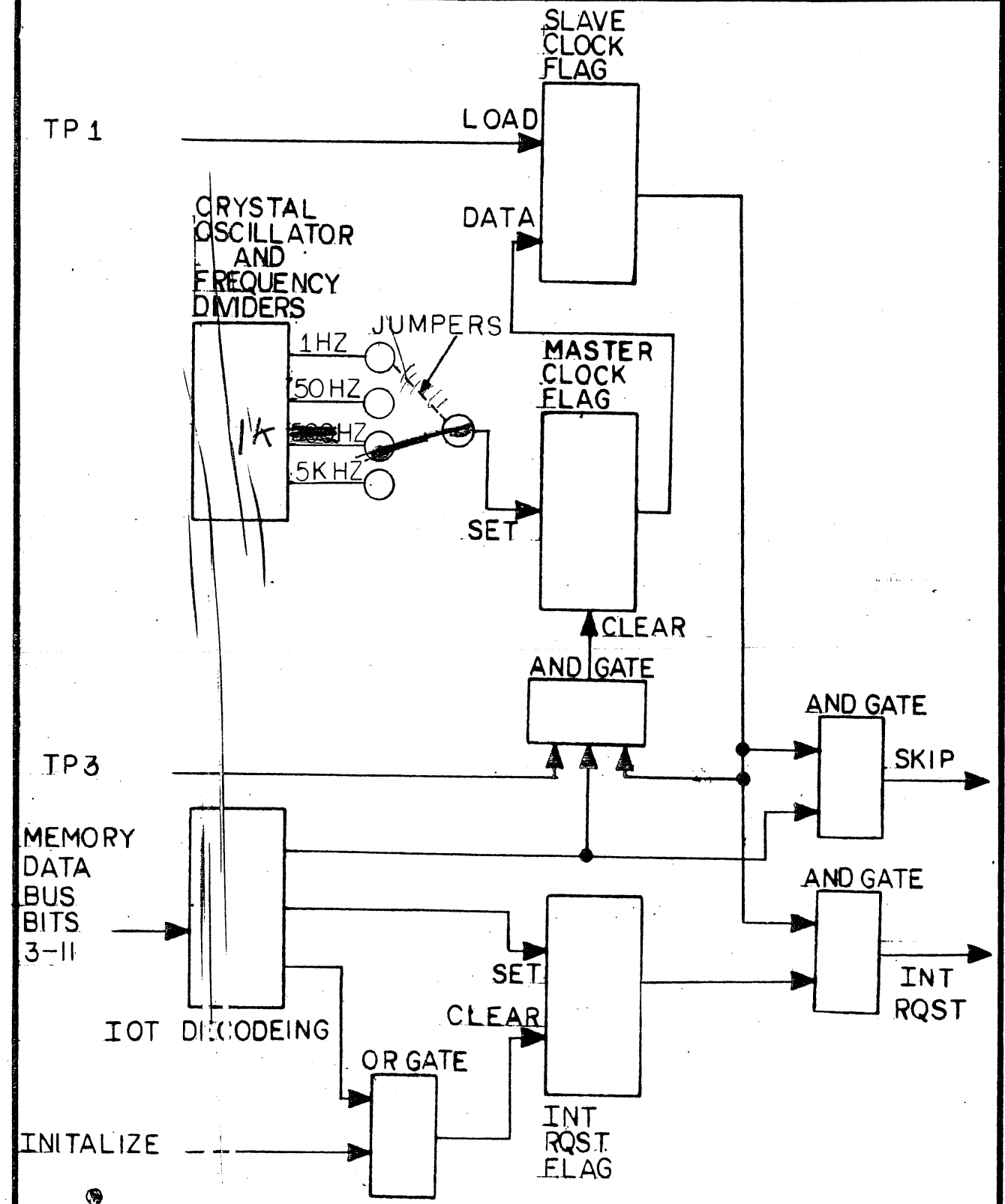
Temperature -30°F - 130°F
 Humidity -10% - 90% non condensing
 Power required +5 volts - 400 ma
 Frequency stability = .01%

3. Software:

MAINDEC-8E-D8AA-D-(D) Write Up
 MAINDEC-8E-D8AA-PB Tape

SIZE A	CODE SP	NUMBER DK8-EC-4	REV
-----------	------------	--------------------	-----

TITLE DK8-EC BLOCK DIAGRAM



SIZE A	CODE SP	NUMBER DK8-EC-4	REV
-----------	------------	--------------------	-----