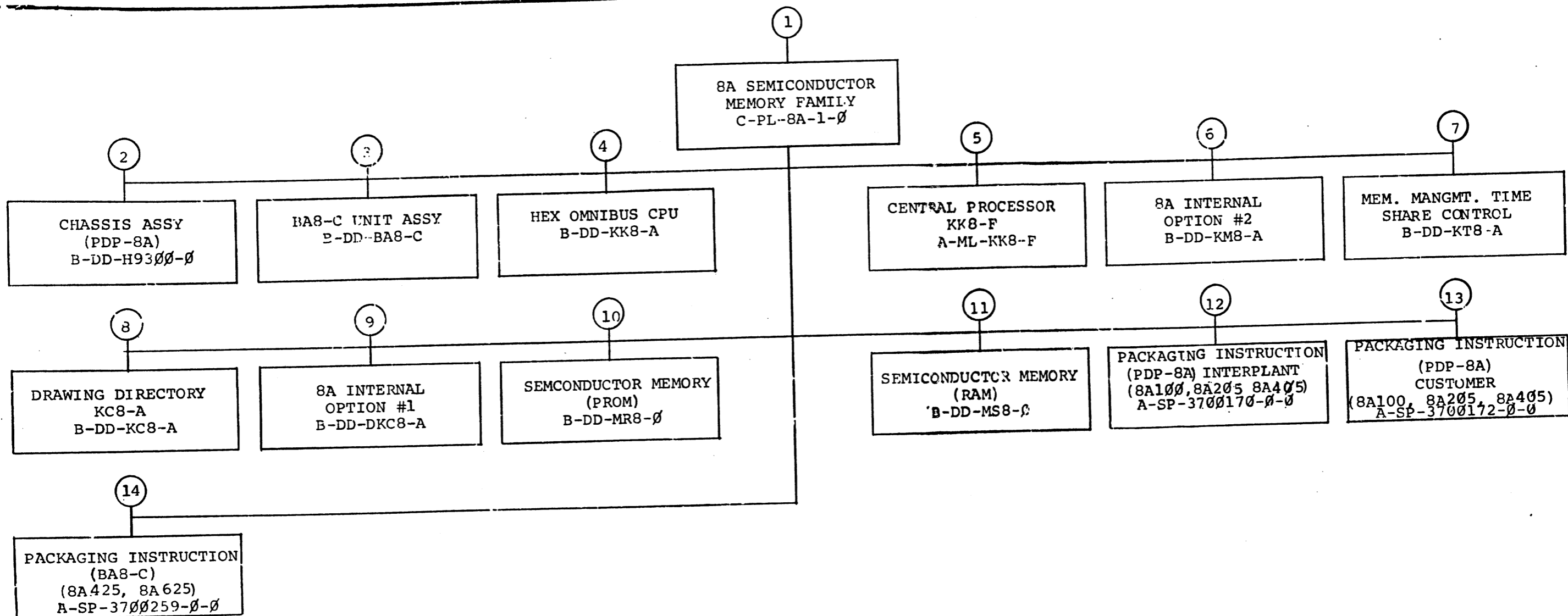


D-CS-G8019-0-1	POWER DISTRIBUTION BOARD CONTROL		
D-UA-G8019-0-0	POWER DISTRIBUTION BOARD CONTROL		
D-CS-G8016-0-1	REG. BOARD 8A SEMICONDUCTOR	D-FD-KM8-A-6	FLOW CHART OPTION #2
D-CS-G8018-0-1	REG. BOARD 8A CORE	A-SP-KM8-A-7	ROM PROG. INST.
D-CS-M8315-0-1	HEX OMNIBUS CPU	A-SP-DKC8-A-1	FIELD INST. AND ACCEPTANCE PROCEDURE
D-FD-M8315-0-16	FLOW DIAGRAM INDEX	D-CS-M8316-0-1	OPTION BOARD #1
D-FD-M8315-0-17	FLOW DIAGRAM	D-UA-M8316-0-0	UNIT ASSY
D-FD-M8315-0-18	FLOW DIAGRAM	B-IL-M8316-0-0	PARTS LIST
D-FD-M8315-0-19	FLOW DIAGRAM	E-UA-KC8-A-0	BEZEL ASSY
D-FD-M8315-0-20	FLOW DIAGRAM	D-AD-7010644-0-0	KEYBOARD ASSY
D-FD-M8315-0-21	FLOW DIAGRAM	D-CS-5411241-0-1	INDICATOR DISPLAY
D-FD-M8315-0-22	FLOW DIAGRAM	D-CS-5411316-0-1	REGISTERS AND CONTROL
D-FD-M8315-0-23	FLOW DIAGRAM	A-SP-KT8-A	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-FD-M8315-0-24	FLOW DIAGRAM	D-UA-M8416-0-0	KT8-A UNIT ASSY
D-FD-M8315-0-25	FLOW DIAGRAM	B-PL-M8416-0-0	PARTS LIST
D-FD-M8315-0-26	FLOW DIAGRAM	D-CS-M8416-0-1	MEMORY MANAGEMENT OPTION
D-FD-M8315-0-27	FLOW DIAGRAM	D-CS-M9020-0-1	KT8-A TERMINATOR CARD
E-FD-PDP8/E-0-06	PROCESSOR FLOW CHART	A-SP-MS8-C-0	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-TD-PDP8/E-0-05	TIMING (PDP8/E)	D-CS-M8417-0-1	PDP-8 MOS MEMORY
E-CS-M8300-0-1	MAJOR REGISTERS	D-UA-M8417-0-0	UNIT ASSY
E-CS-M8310-0-1	MAJOR REGISTERS CONTROL	B-PL-M8417-0-0	PARTS LIST
E-CS-M8320-0-1	BUS LOADS	A-SP-MR8-F-2	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
E-CS-M8330-0-1	TIMING GENERATOR	D-CS-M8349-0-1	1K PROM
A-SP-KM8-A-1	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE	A-SP-MS8-A-1	FIELD INSTALLATION AND ACCEPTANCE PROCEDURE
D-CS-M8317-0-1	OPTION BOARD #2	D-CS-M8311-0-1	4K X 12 MOS MEMORY
D-UA-M8317-0-0	UNIT ASSY		
B-PL-M8317-0-0	PARTS LIST		
D-TD-KM8-A-4	AUTO RESTART/BOOT SEQUENCE		
D-TD-KM8-A-5	BOOTSTRAP TIMING		

TITLE	SIZE	CODE	NUMBER	REV
8A SEMICONDUCTOR MEMORY FAMILY	B	TC	8A-1-1	
SHEET 2 OF 2				



TITLE	SHEET	SIZE	CODE	NUMBER	REV
8A SEMICONDUCTOR MEMORY FAMILY	2 OF 3	B	DD	8A-1	

FIND NO.	DRAWING NO.	DESCRIPTION	TYPE
1	MP00415	FIELD MAINTENANCE PRINT SET (MP)	-
	B-TC-8A-1-1	FIELD MAINTENANCE PRINT SET (TC)	-
	C-PL-8A-1-0	8A SEMICONDUCTOR MEMORY FAMILY (8A100, 8A200, 8A300, 8A320, 8A720)	E/M
2	B-DD-H9300	CHASSIS ASSY (PDP-8A)	-
3	B-DD-BA8-C	BA8-C UNIT ASSY	-
4	B-DD-YK8-A	HEX OMNIBUS CPU	-
5	A-ML-KK8-F	CENTRAL PROCESSOR KK8-F	-
6	B-DD-KM8-A	8A INTERNAL OPTION #2	-
7	B-DD-KT8-A	MEM MANAGEMENT AND TIME SHARE CONTROL	-
8	B-DD-KC8-A	DRAWING DIRECTORY KC8-A	-

FIND NO.	DRAWING NO.	DESCRIPTION	TYPE
9	B-DD-DK8-A	8A INTERNAL OPTION #1	-
10	B-DD-MR8-0	SEMICONDUCTOR MEMORY (PROM)	-
11	B-DD-MS8-0	SEMICONDUCTOR MEMORY (RAM)	-
12	A-SP-3700170-0-0	PACKAGING INSTRUCTION, INTERPLANT (PDP-8A)	-
13	A-SP-3700172-0-0	PACKAGING INSTRUCTION, CUSTOMER (PDP-8A)	-
14	A-SP-3700259-0-0	PACKAGING INSTRUCTION, INTERPLANT W/CONSOLE (BA8-C)	-

TYPE: E ELECTRICAL
M MECHANICAL
E/M ELECTRO/MECHANICAL

