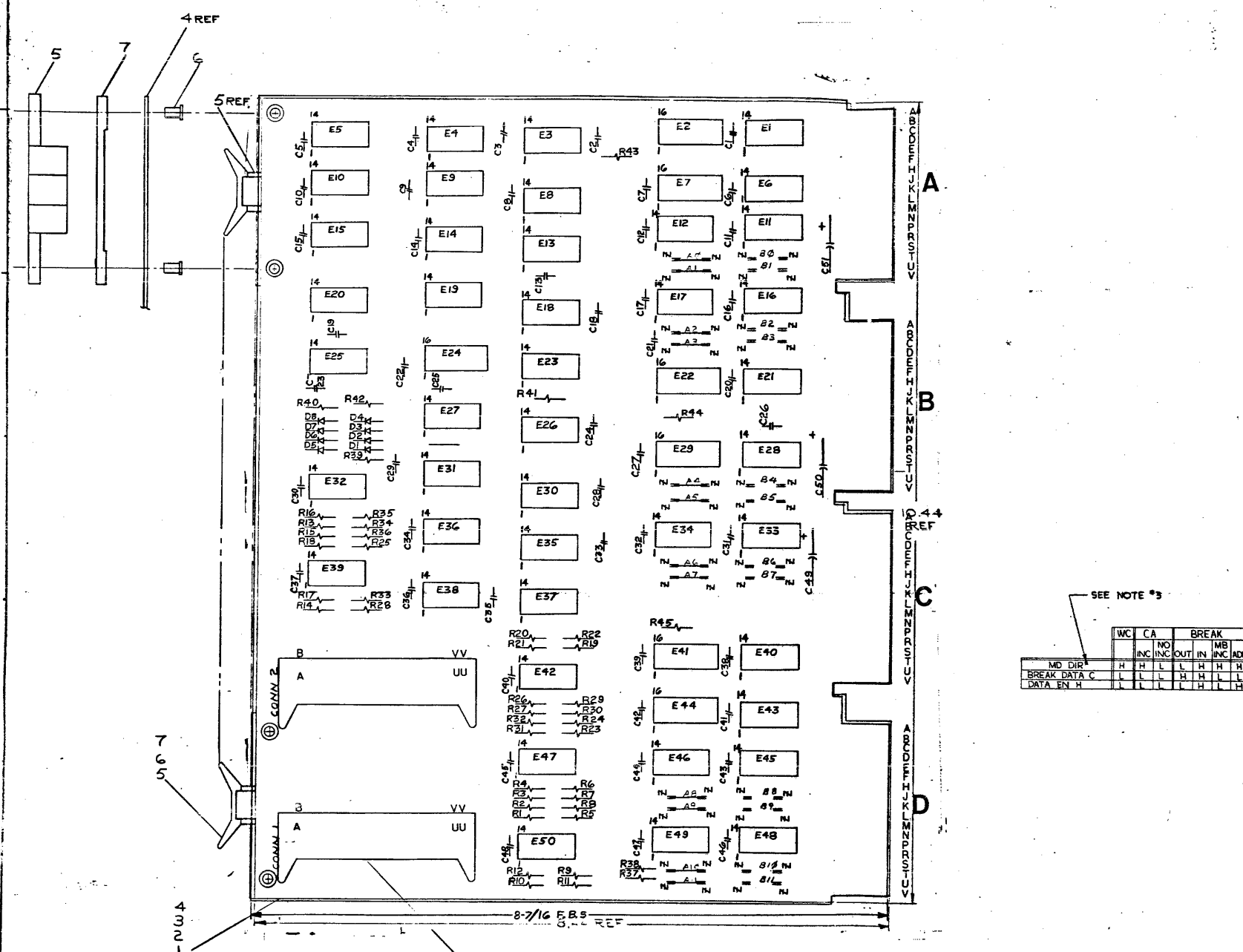


The dimensions and manufacturing tolerances shown on this drawing are in inches unless otherwise noted. All dimensions are to be held unless otherwise noted. All dimensions are to be held unless otherwise noted.



- NOTES:
- UNLESS OTHERWISE NOTED:
CAPACITORS ARE .01UF, 100V, 20%
DIODES ARE D662
RESISTORS ARE 220, 1/4W, 5%
I.C.'S ARE DEC 97401
 - ALL 'A' JUMPERS ARE INSTALLED AT MANUFACTURING. TO ESTABLISH HIGHER PRIORITIES REMOVE AN 'A' JUMPER AND INSTALL A 'B' JUMPER. B0 IS HIGHEST PRIORITY WHILE B11 IS LOWEST PRIORITY.
 - MD DR IS LOW FOR EVERY MEMORY READ. THIS LEVEL IS TRUE DURING WRITE.

SEE NOTE #3

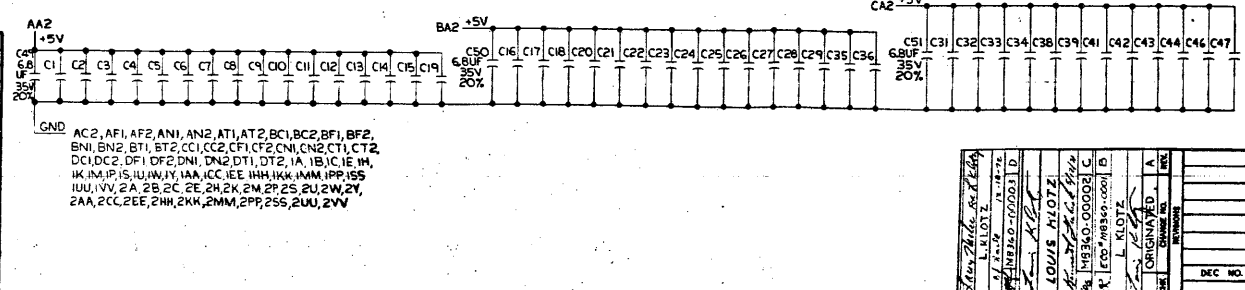
MD DIR	WC		CA		BREAK	
	INC	NO INC	INC	OUT	IN	MB INC ADM
BREAK DATA C	H	H	L	L	H	H
DATA EN H	L	L	L	L	H	H

QTY	REF DESIGNATION	DESCRIPTION	PART NO.	REV
4	E1,E2,E37,E40	I.C. DEC 8881	1909705	31
NR		WIRE #22 AWG SOLID BUS	9107560-01	30
5	E32,E39,E42,E47,E50	DIODE PAK72501	1910010	23
2	E3,E8	DIODE PAK7412	1909955	28
3	E7,E29,E44	I.C. 8266	1909934	27
2	E25,E27	I.C. 7416	1909928	26
4	E16,E48,E23,E33	I.C. DEC 97401	1909973	25
2	E12,E46	I.C. DEC 6314	1909972	24
3	E11,E19,E45	I.C. 7404	1909686	23
5	E4,E9,E31,E36,E38	I.C. 74H74	1909667	22
4	E2,E22,E24,E41	I.C. 8271	1909615	21
1	E30	I.C. 384	1909486	20
7	E6,E17,E28,E34,E35,E43,E49	I.C. DEC 6380	1909971	19
1	E13	I.C. 74H11	1909267	18
1	E14	I.C. 74H10	1909057	17
3	E10,E20,E26	I.C. 7402	1909004	16
1	E5	I.C. 7440	1905574	15
2	E15,E18	I.C. 7400	1905575	14
45	R1-R45	RES. 220, 1/4W, 5%	300271	13
8	D1-D8	DIODE D662	1100113	12
48	C1-C48	CAP. 0.01F, 100V, 20%	1000150	11
3	C49-C51	CAP. 6.8UF, 35V, 20% S. TANT	1000067	10
2		RIGHT ANGLE HEADER 40 PIN	1209941	9
48		SPLIT LUG	9006735	8
4		SPACER (CABLE CLAMP)	1202704	7
8		EYELET G54-11 STIMPSON	9006750	6
4		HANDLE FLIP CHIP - MAGENTA	9028331-06	5
1		ETCHED CIRCUIT BOARD	8009447	4
REF		MODULE HISTORY LIST	B-MH-4830-0-6	3
REF		ASSY/DRILLING HOLE LAYOUT	-AM-4830-0-5	2
REF		X-Y COORDINATE HOLE LOC.	X-CO-4830-0-4	1

DEC 2501	+5V	PIN 1	14	NONE
DEC 6314	1	8		
IC 384	1	8		
DEC 6380	1	8		
K TYPE	GND	+5V		

SEE NOTE #2

ITEM NO	AWG	FROM PT	TO PT



GND AC2, AF1, AF2, AN1, AN2, AT1, AT2, BC1, BC2, BF1, BF2, BN1, BN2, BT1, BT2, CC1, CC2, CF1, CF2, CN1, CN2, CT1, CT2, DC1, DC2, DF1, DF2, DN1, DN2, DT1, DT2, IA, IB, IC, IE, IH, IK, IM, IP, IS, IU, IW, IY, IAA, ICC, IEE, IHH, IKK, IMM, IPP, ISS, IUU, IVV, VA, VB, VC, VE, ZH, ZK, ZM, ZP, ZS, ZU, ZV, ZY, ZAA, ZCC, ZEE, ZHH, ZKK, ZMM, ZPP, ZSS, ZUU, ZVV

ETCH BOARD REV B

DATE	BY	DESCRIPTION

LOUIS ALTOZ

DATE: 10/10/74

TIME: 10:00 AM

PROJECT: DATA BREAK INTERFACE

SCALE: 1:1

SHEET: 1 OF 3

SEMICONDUCTOR CONVERSION CHART

DEC NO.	EIA NO.	DEC NO.	EIA NO.
D662	IN 64E		

DATE: 10/10/74

TIME: 10:00 AM

PROJECT: DATA BREAK INTERFACE

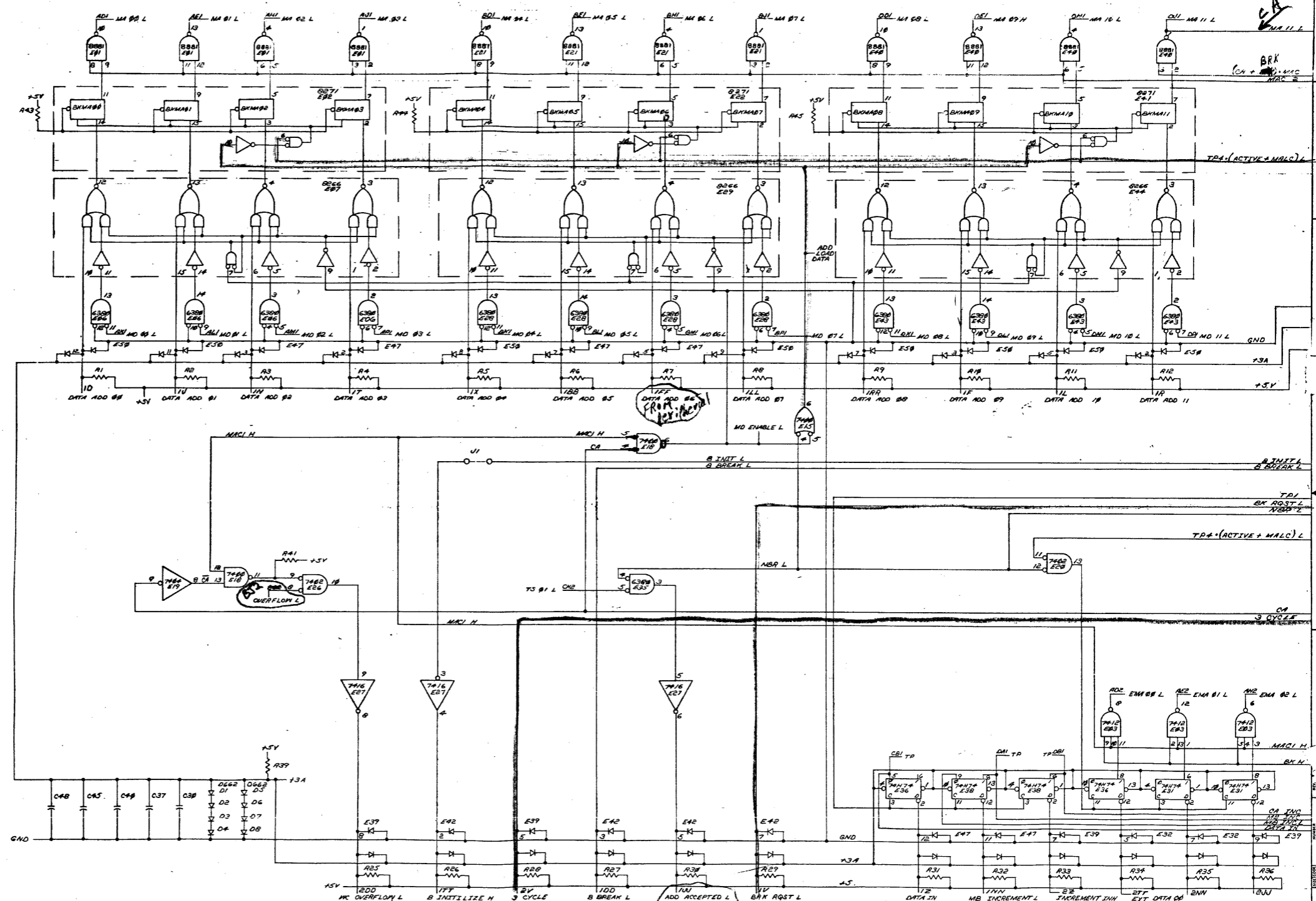
SCALE: 1:1

SHEET: 1 OF 3

SEMICONDUCTOR CONVERSION CHART

DEC NO.	EIA NO.	DEC NO.	EIA NO.
D662	IN 64E		

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FIRST USED ON DTTBOM NO.	QTY.	DESCRIPTION	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED				
DIMENSIONS IN INCHES				
TOLERANCES				
FINISH				
MATERIAL				
SCALE				
SHEET				

Vol II Maint Manual

1

