

M775 Time State Generator

The M775 module contains a 5-18 MHz variable clock and a four-stage ring counter. This module is used in the central processor of the PDP-15 to divide each of the three time states of each cycle into four phases. The clock frequency is adjusted to obtain a period equivalent to one time phase.

The following are the input, output, and power characteristics of the M775 module.

INPUTS: The table below lists all input connections and the TTL loading they present.

Name	Pin	Loading
REPEAT TS2	F1	1.25
SING TIME LOOP	E1	3.75
TS2	C1	1.25
ADD*TS2*E	A1	1.25
ADD*TS2*E	P2	1.25
CLOCK	R2	10
CLEAR	T2	10
STOP CLOCK	P1	1.25

OUTPUTS: The table below lists all output connections and their unit load-driving capabilities.

Name	Pin	Drive
TIME STATE 1	J1	9
TIME STATE 1	K2	36
TIME STATE 1	L1	36
TIME STATE 2	H2	9
TIME STATE 2	J2	36
TIME STATE 2	L2	36
TIME STATE 2A	D1	8
TIME STATE 2A	M2	36
TIME STATE 3	V2	9
TIME STATE 3	D2	36
TIME STATE 3	N2	36
HS CLOCK	E2	11
HS CLOCK	F2	36

POWER: Power dissipated in the M775 module is 5V at 325 mA (maximum).