

# M622 I/O Bus Drivers

The M622 I/O Bus Driver is an M-series single-height module containing eight I/O bus drivers. Each driver consists of an AND/OR integrated circuit gate and a discrete component open-collector driver.

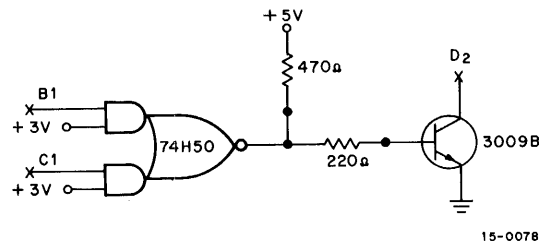
The following are the input, output, and power characteristics of the M622 module.

**INPUTS:** Inputs are standard TTL positive logic levels.  
The input load is 1.25 units.

**OUTPUTS:** Outputs are standard PDP-15 positive I/O bus signals. Output characteristics are:  
Risetime - 15 ns at the input to the cable  
Current sink - 100 mA (maximum) at  $V_{ce\text{sat}} = 0.4\text{V}$  (maximum)  
Falltime - 10 ns at the input to the cable  
Input-Output Delay - 45 ns (maximum)

**POWER:** The power dissipation of the M622 module is 1.05W (maximum) at +5V.

The M622 module was designed specifically to drive PDP-15 I/O bus signals for devices that use positive logic modules. The M622 module is pin compatible with the M632 module.



M622 Simplified Diagram