

## M218 MQ Register

The M218 module contains a 9-bit storage register with input gating and shifting logic. Two M218 modules are used in the PDP-15 central processor equipped with the EAE option to form the 18-bit MQ register. (Refer to Engineering Drawings D-BS-KE15-0-1 and D-BS-KE15-0-2.) This register extends the AC shifter (M227), facilitating high-speed arithmetic operations (shifting, normalizing, division, and multiplication) and double-precision results.

The following are the input, output, and power characteristics of the M218 module.

**INPUTS:** All inputs but the CLOCK input present 1.25 TTL unit loads. The CLOCK input presents 18 unit loads.

**OUTPUTS:** Each output is capable of driving 10 TTL unit loads.

**POWER:** Power dissipation of the M218 module is 5V at 310 mA (maximum).