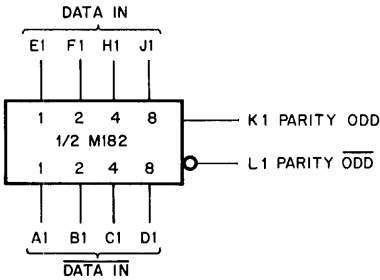


# M182 Parity Circuit

The M182 module is a parity detector and contains two high-speed parity circuits. Each circuit indicates whether the binary data presented to it contains an odd or even number of 1s. The data and its complement are required for inputs as shown by the illustration. Indication of odd parity is given by a HIGH level at pins K1 and V1. Pins L1 and V2, when HIGH, indicate even parity or no input.

The following are the input, output, and power characteristics of the M182 module.

- INPUTS:** Each input presents 5 unit loads.
- OUTPUTS:** Pins L1 and V2 can each drive up to 12.5 unit loads. Pins K1 and U2 can each drive up to 7.5 unit loads.
- POWER:** Power dissipated in the M182 module is 5V at 200 mA (maximum).



15-0112

M182 Simplified Diagram