

NEW NUMBER = 5CKTC

ABSTRACT = Keys test.

This program checks the operation of each access key for each of the four Unibus cycles (or combination of cycles) which may reference an address thru segmentation. These cycles are DATI, DATO (no DATIP), DATIP=DATO and DATIP DATOB. Each of these cases is tested with and without memory management enable set, thus eight cases are tested for each key, SR7, SR1, SR, the corresponding PDR's, and the proper execution or prevention of execution of the instruction are checked in each case.

REQUIREMENTS =

PDP-11/45 with KT11=C option;

STORAGE = Program requires 5K of memory, starting at location 0.

LOADING = Absolute Loader.

EXECUTION TIME = Each pass takes approximately 1 minute with core memory.

STARTING PROCEDURE = Load address 200.

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = 1 of UP ... HALT ON ERROR
 SW14 = 1 of UP ... SCOPE LOOP
 SW13 = 1 of UP ... INHIBIT PRINTOUT
 SW11 = 1 of UP ... INHIBIT ITERATIONS
 SW08 = 1 of UP ... LOAD MICROBREAK REGISTER WITH
 VALUE IN SW00-SW07 (AT START OF TEST ONLY);