NEW NUMBER = DCKTA

ABSTRACT =

This program and the next (DCKTB) incrementally test the basic logic functions of the KT11-C memory management option for the PDP-11/45. They fully test relocation direct and indirect addressing of the memory management registers, and correct operation of all the bits in the registers. The various aborts are tested, as is proper "locking" and "unlocking" of the error tracking logic.

REQUIREMENTS =

PDP-11/45 with KT11-C option.

STORAGE = Program requires memory locations 0 to 17474.

LOADING = Absolute Loader.

EXECUTION TIME = Each pass takes approximately 1 minute with core memory.

STARTING PROCEDURE = Load address 200.

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = 1 or up ;;; HALT ON ERROR
SW14 = 1 or up ;;; SCOPE LOOP
SW13 = 1 or up ;;; INHIBIT PRINTOUT
SW11 = 1 or up ;;; INHIBIT ITERATIONS
SW08 = 1 or up ;;; LOAD MICROBREAK REGISTER WITH VALUE IN SW 00-07.