

NEW NUMBER = DCFPR

ABSTRACT =

This program is an exerciser of LDD/STD instructions. It uses random numbers, floating 1's, and floating 0's, and checks all memory with LDF/STF into an AC.

REQUIREMENTS =

PDP-11/45, FP11

STORAGE = The routines use memory 0 = 17776

LOADING = Absolute Loader

EXECUTION TIME = 15 secs.

STARTING PROCEDURE = Program should always be started at 200.

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15	=	1	...	HALT ON ERROR
SW14	=	1	...	SCOPE LOOP
SW13	=	1	...	INHIBIT PRINTOUT
SW12	=	1	...	INHIBIT TRACE TRAPPING
SW11	=	1	...	INHIBIT ITERATIONS OF SUBTEST
SW10	=	1	...	BELL ON ERROR
		0	...	BELL ON PASS COMPLETE
SW09	=	1	...	LOOP ON ERROR
SW08	=	1	...	LOOP ON TEST IN SW(710)
		0	...	LOAD SW(710) INTO UB REGISTER