

NEW NUMBER = DCFPA DCFPB to DCFPL

## ABSTRACT =

These programs test the FP11 in all modes with fixed number patterns. The programs should be run in order for at least 2 passes with all switches down. The sequence of testing should be =

LDFPS, STFPS, SETI, SETL, SETF, SETD, CFCC  
 STST  
 LDF, LDD, STF, STD  
 ADDF, ADDD, SUBF, SUBD  
 CMDF, CMPD  
 MULF, MULD  
 DIVE, DIVD  
 CLRF, CLRD, TSTF, TSTD, ABSF, ABSD, NEGF, NEGD  
 LDCF, LCCDF, STCF, STCDF  
 LDCIF, LDCIF, LDCID, LDCID, STCFI, STCFI, STCFI, STCFI  
 LDEXP, STEXP  
 MODF, MODD

## REQUIREMENTS =

PDP-11/45, FP11

STORAGE = The routines use memory 0 = 17776.

LOADING = Absolute loader.

EXECUTION TIME = Less than 1 minute.

STARTING PROCEDURE = Always start at 200

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = 1 ... HALT ON ERROR  
 SW14 = 1 ... SCOPE LOOP  
 SW13 = 1 ... INHIBIT PRINTOUT  
 SW12 = 1 ... INHIBIT TRACE TRAPPING  
 SW11 = 1 ... INHIBIT ITERATIONS OF SUBTEST  
 SW10 = 1 ... BELL ON ERROR  
       0 ... BELL ON PASS COMPLETE  
 SW09 = 1 ... BELL ON ERROR  
 SW08 = 1 ... LOOP ON TEST IN SW(710)  
       0 ... LOAD SW(710) INTO UB REGISTER