

MAINDEC-11-08H\*

NEW NUMBER = DZUDA

## UDC11 Control Test

## ABSTRACT

UDC11 Control Test tests virtually all of the control logic up to the UDC BUS. Maintenance logic is used to generate UDC interrupts and to single step the scan register. Note: the UDC Bus cable to the system units can be removed from the control while this test is run if errors result due to interrupt modules generating interrupts. If the modules generating interrupts are in the first four addresses (0 - 006), the modules must be removed since removing the bus cable will not disconnect these modules from the UDC bus.

A power fall test is included, starting address = 000204. This test will type a message that it is waiting for a power failure and will type which one of two types of failures occur (UDC DC power or PDP-11) when and if they happen.

## REQUIREMENTS

PDP-11  
ASR33/35 Teletype  
UDC11 Control

STORAGE = 0 = 712500

LOADING = Absolute Loader

EXECUTION TIME = 1 min. = Bell will ring

STARTING PROCEDURE = 200 = Control Diagnostic  
204 = Power Fall Test  
Restart at 1000

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = HALT ON ERROR  
SW14 = ENTER SCOPE MODE  
SW13 = INHIBIT ERROR PRINTOUT  
SW11 = INHIBIT ITERATION  
SW10 = HALT AT END OF TEST CURRENTLY EXECUTING  
SW 9 = SELECT THE TEST SPECIFIED BY SW7 = SW0  
SW 7 = SW0 = NUMBER OF TEST TO BE SELECTED