

MAINDEC-11-D1C-

NEW NUMBER = DZMMC

No Dual Address Test

ABSTRACT

The no dual address test checks the unique selection of each memory address tested; The address of each failure location and its contents are printed on the teletype. Scope loop options are provided to facilitate any additional diagnostic procedures that may be used in conjunction with this test;

REQUIREMENTS

PDP-11

STORAGE = 200 = 3562

LOADING = Absolute Loader

EXECUTION TIME = 6.5 min. for 4K of memory = Bell will ring

STARTING PROCEDURE = Start at 200 for automatic test limits up to 28K
Start at 202 for selected test limits

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = HALT ON ERROR
SW14 = SCOPE LOOP
SW13 = INHIBIT PRINTOUT
SW12 = HALT UPON COMPLETION OF A PASS