

MAINDEC=11-D2G*

NEW NUMBER = DZKWB

KW11P Real Time Clock

ABSTRACT

This program tests the KW11P real time clock. It contains a series of incremental routines that test the control and status register, count set buffer, counter, and interrupt vector address using 122KHZ, 12KHZ, 60HZ, and external frequencies.

REQUIREMENTS

PDP-11
KW11P

STORAGE = This program occupies memory from 0 to 4664

LOADING = Absolute Loader

EXECUTION TIME = Basic test requires 15 sec. per pass, timing test
requires 4 min. per pass

STARTING PROCEDURE = 200 basic test
204 timing test

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = 1 OR UP ... HALT ON ERROR
SW14 = 1 OR UP ... SCOPE LOOP
SW13 = 1 OR UP ... INHIBIT PRINTOUT
SW12 = 1 OR UP ... INHIBIT SUB-TEST ITERATIONS