

MAINDEC-11-DRR

NEW NUMBER = DZDCC

ABSTRACT

This is a logic test of the DR11C. For this test to operate a special maintenance cable must be connected (BC08R). This test will check up to 32 sequential DR11C's.

REQUIREMENTS

PDP-11 standard computer
 DR11C
 BC08R for each DR11C

STORAGE = The routine uses memory from 0000 to 5200.

LOADING = Absolute Loader.

EXECUTION TIME = For each DR11C about 1 min.

STARTING PROCEDURE = Starting at SA 200 all switches should be down to zero. (If not zero, bit 0 to 8 will be starting vector)

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = 1 of up ; ; HALT ON ERROR
 SW14 = 1 of up ; ; SCOPE LOOP
 SW13 = 1 of up ; ; INHIBIT PRINTOUT
 SW12 = 1 of up ; ; INHIBIT TRACE TRAPPING
 SW11 = 1 of up ; ; INHIBIT ITERATION LOOP
 SW10 = 1 of up ; ; INHIBIT ADVANCING TO NEXT DR11C
 SW08 = 0 will be used as vector address if not zero.
 (DR11A has floating vectors)

The assigned sequence for floating vectors are:

- 1, Starting at 300 all DC11's will be assigned.
- 2, Then any KL11/DL11A called for (VT05,VT06,LC11)
- 3, Then any DP11 called for.
- 4, Then any DM11A called for.
- 5, Then any DM11 called for.
- 6, Then any DM11BB called for.
- 7, Then any DR11A called for.
- 8, Then any DR11C called for.

The DR11A device address will be assigned in the user area of 767796 to 764000. The assignment of address will start at the high address limit and proceed downward. Users and special systems should start their assignment of special devices at the low address limit and work

up,

767776	to	767772	DC11C	#0
767766	to	767762	DR11C	#1
767776	to	767722	DR11C	#7
767676	to	767602	DR11C	#15