

MAINDEC=11=DRL

NEW NUMBER = DZDRA

DR11A Device Register Test

ABSTRACT

This is a logic test of the DR11A. For this test to operate, a special maintenance module must be connected (M980). This test will check up to 32 sequential DR11A's.

REQUIREMENTS

PDP=11
 DR11A
 M980 for each DR11A

STORAGE = 0 = 5000

LOADING = Absolute Loader

EXECUTION TIME = About 2 min. for each DR11A = Bell will ring at end of pass.

STARTING PROCEDURE = Start at 200
 = The low byte of the SR should contain the first DR11A vector address.

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = HALT ON ERROR
 SW14 = SCOPE LOOP
 SW13 = INHIBIT PRINTOUT
 SW12 = INHIBIT TRACE TRAPPING
 SW11 = INHIBIT ITERATION LOOP
 SW8 to 0 will be used as vector address if not zero. (DR11A has floating vectors)

The assigned sequence for floating vectors are:

1. Starting at 300 all DC11's will be assigned.
2. Then any KL11 called for (VT05, VT06, LC11)
3. Then any DP11 called for.
4. Then any DM11A called for.
5. Then any DM11 called for.
6. Then any DM11BB called for.
7. Then any DR11A called for.

The DR11A device address will be assigned in the user area of 767776 to 764000. The assignment of address will start at the high address limit and proceed downward. Users and special systems should start

their assignment of special devices at the low address limit and work up.

767776	to	767770	DR11A #0
767766	to	767760	DR11A #1
767776	to	767700	DR11A #7
767676	to	767600	DR11A #15