

MAINDEC-11-D9F\*

NEW NUMBER = DZDMA

## DM11 Logic Tests

## ABSTRACT

Two separate diagnostic programs are provided for testing the DM11 (Asynchronous Data Multiplexer), MAINDEC-11-D9F\* (DM11 Logic Tests), and MAINDEC-11-D9G\* (DM11 Multiple Line Data Tests). The logic tests individually test each of the 16 DM11 lines and all common logic. The multiple line data tests run several lines concurrently and are used to test line interaction and data transmission/reception reliability. This document describes the logic tests, the tests are:

PRG0 = Logic Test  
 PRG1 = Transmitter Scope Loop  
 PRG2 = Transmit/Receive Scope Loop

## REQUIREMENTS

PDP-11  
 DM11  
 Jumpers connecting 16 transmitters to their respective receivers.

STORAGE = All of 4K except that area reserved for the loaders.

LOADING = Absolute Loader

EXECUTION TIME = 20 min, (110) Baud) Single Iteration.

STARTING PROCEDURE = Start and Restart at 200

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW0-5 = ROUTINE TO BE RUN  
 SW8 = RING BELL ON ERROR  
 SW9 = LOOP SELECTED ROUTINE  
 SW11 = INHIBIT ITERATION  
 SW13 = INHIBIT PRINTOUT  
 SW14 = SCOPE  
 SW15 = HALT ON ERROR