

# TC11

# ZTCAA0

## TC11 BASIC LOGIC TEST #1

### ABSTRACT :

*This program is part-1 of 3 and contains a series of basic logic tests that checks that each of the controller registers can be referenced without causing bus error traps and all bits can be set and cleared. A special routine (test 0) is available in the program as a maintenance aid in adjusting the TC11 control delays.*

### OPERATING PROCEDURES :

*.R ZTCAA0*

*SA=1000 restart address*

*The program identifies itself, types setup instructions, SR options message, and halts.*

*Perform setup (all transports must be off, set WRTM switch and WALL switch to off position) and set SW if any.*

*Press continue.*

### SWITCH SETTINGS :

*SW15= 1 halt on error*

*SW14= 1 enter scope mode*

*SW13= 1 inhibit error typeouts*

*SW12= 1*

*SW11= 1 inhibit iterations*

*SW10= 1 halt at end of test currently executing*

*SW09= 1 loop on test as per SW<07-00>*

*To get to the adjustment maintenance routine, load address 1000, set SW9= 1 and SW07 to SW00 = 0 and press start.*