

MAINDEC=11-D3A

NEW NUMBER = DZTCA

TC1 = TC11 Test 1

ABSTRACT

TC1 = TC11 Test 1 is Part 1 of a five program package used to test the TC11 DEStape Control. TC1 is a bit checker program that checks that each of the control's registers can be referenced without causing bus error traps, that read-write bits can be set and clear, that read/write bits are cleared by reset instruction, and that read only bits are set to their power clear state by reset instruction.

A special routine (Test 0) is available in the program as a maintenance aid in adjusting the TC11 control delays.

REQUIREMENTS

PDP=11
 ASR33=35 Teletype
 TC11 DEStape Control and at least one TU56 Dual Transport

STORAGE = 0 = 16000

LOADING = Absolute Loader

EXECUTION TIME = 1. One normal pass = 1 minute
 2. Single iteration pass = 13 seconds;
 = Printout will occur

STARTING PROCEDURE = Start at 200
 Restart at 1000

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = HALT ON ERROR
 SW14 = SCOPE
 SW13 = INHIBIT ERROR PRINTOUT
 SW11 = INHIBIT ITERATION
 SW10 = HALT AT END OF CURRENT ROUTINE
 SW 9 = SELECT ROUTINE