

RK06/07

ZR6NE3

RK06/07 DYNAMIC TEST PART 2

ABSTRACT :

This RK06/07 test provides a functional shakedown of the entire subsystem, including the unibus interface and access to main memory. The testing in part 2 employs worst-case situations involving head offsetting, memory addressing and data transfers, unibus cycle contention and multiple drive operations. In addition, an RK06/07 head alignment aid is provided to do ON-LINE alignment of drive heads.

OPERATING PROCEDURES :

.R ZR6NE3

*SA=204 request bus address, vector address and priority modification
normal RKCS1 address 177440, vector 210
SA=224 head alignment aid program start*

Load program, scratch pack installed and drive ready, drives not to be tested must have both ports deselected.

*Runtime first pass : 2 minutes
Runtime subsequent : 3 minutes*

SWITCH SETTINGS :

*SW15= 1 halt on error
SW14= 1 loop on test
SW13= 1 inhibit error timeout
SW12= 1 report description only, on error
SW11= 1 inhibit test iterations
SW10= 1 ring bell on error
SW09= 1 loop on error
SW08= 1 apply random stall between operations
SW07= 1
SW06= 1 report one error per transfer in tests 2-4
SW02= 1
SW01= 1 inhibit writes in test
SW00= 1 report all software compare errors in tests 2-4*

use "CONTROL C" to change software SWR at loc. 176