

RK06/07 DYNAMIC TEST PART 1

ABSTRACT :

This RK06/07 test provides a functional shakedown of the entire subsystem, including the unibus interface and access to main memory. The testing in part 1 employs worst-case situations involving mechanical positioning, disk addressing and data transfers. In addition, measurements are made pertaining to drive operational timing.

OPERATING PROCEDURES :

.R ZR6ME1

*SA=204 request bus address, vector address and priority modification
normal RKCS1 address 177440, vector 210.
SA=220 dual-access data test 22*

*Load program, scratch pack installed and drive ready, drives not to be tested
must have both ports deselected.*

*Runtime first pass : 14 minutes
Runtime subsequent : 21 minutes*

Patch location 166 from 000000 to 000001 for 50 Hz timing.

SWITCH SETTINGS :

*SW15= 1 halt on error
SW14= 1 loop on test
SW13= 1 inhibit error timeout
SW12= 1 report description only, on error
SW11= 1 inhibit test iterations
SW10= 1 ring bell on error
SW09= 1 loop on error
SW08= 1 apply random stall between operations
SW07= 1 do explicit seeks in tests 1-12
SW06= 1 report one error per transfer in tests 17,21
SW05= 1 inhibit writes in test 21
SW04= 1 inhibit write checks in test 21
SW03= 1 inhibit reads and software compares in test 21
SW02= 1 inhibit software compares in test 21
SW01= 1 read after a write check error in test 21
SW00= 1 report all software compare errors in tests 17,21*

use "CONTROL G" to change software SWR at loc. 176