

NEW NUMBER # DZLSA

LS11 CENTRONICS PRINTER TEST

ABSTRACT

This program is designed to test and exercise centronics printers. The basic logic tests executed and exercises performed are as follows:

1. Manual intervention (optional) to check hardware alarms. For further details see Test 0 under PROGRAM DESCRIPTION.
2. Status and buffer register addressability.
3. Done bit = clear and set) as well as mode and IE bits after RESET.
4. Interrupt enable bit = clear and set.
5. Interrupts with processor at levels 4 thru 7
6. Interrupt with processor at level 3.
7. Deselect interrupt
8. Format control characters
9. Bell
10. Relative timing technique for checking time to execute a certain line length (4 lengths in this program) and slewing rate, (see Section 8.4 to Interpret relative time test results).
11. Data transfer lines
12. Character generation
13. Lower case letters forced to UPPER CASE
14. Switch register input (optional) to print lines of characters regular or elongated. For further details see Test 31 under PROGRAM DESCRIPTION.
15. Print time free pulse generator (optional). For further details see Test 31 under PROGRAM DESCRIPTION.

REQUIREMENTS

PDP-11 standard computer with console teletype, A
centronics printer and LS11 Interface.

STORAGE = 4K

LOADING =

EXECUTION TIME = 11 minutes

STARTING PROCEDURE = 200

PRINTOUTS =

SWITCH REGISTER OPTIONS = Yes

SW<15>=1, . . . , HALT ON ERROR
 SW<14>=1, . . . , SCOPE LOOP
 SW<13>=1, . . . , INHIBIT ERROR PRINTOUT
 SW<12> SELECT "PRINT TIME FREE" PULSE GENERATOR
 SEE PROGRAM DESCRIPTION OF TEST 31
 SW<11> NOT USED
 SW<17>=1, . . . , ELONGATION ON SWR Input test
 see program description of Test 31
 sw< 9>=1, . . . , selection of a particular test
 sw< 8>=1, . . . , select manual intervention
 sw<7 thru 2>, . . . , contains the test no. = used
 with sw<9>