

.9EM

IDENTIFICATION

PRODUCT CODE: AC-F998R-MC  
PRODUCT NAME: CYOL890 DL11-E MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT

DXL89 IS AN IOMOD THAT EXERCISES ONE DL11-E ASYNCHRONOUS COMMUNICATIONS INTERFACE (41880). THE PROGRAM CONSISTS OF TWO MAJOR SECTIONS AS DESCRIBED BELOW:

SECTION ONE:

THE FIRST SECTION CONSISTS OF A LOGICALLY SEQUENCED SET OF STATIC REGISTER TESTS TO VERIFY THE DL11-E HARDWARE REQUIRED TO PERFORM INPUT/OUTPUT DATA TRANSFERS IN INTERRUPT MODE. ERRORS DETECTED IN THIS SECTION THAT ARE DETERMINED TO BE FATAL ARE REPORTED VIA THE STANDARD DEC/X11 ERROR PRINTOUT AND THEN THE MODULE IS DROPPED FROM THE EXERCISE. NON-FATAL ERRORS ARE SIMPLY REPORTED AND THEN THE PROGRAM CONTINUES IN NORMAL SEQUENCE.

SECTION TWO:

THE SECOND SECTION TRANSFERS 256 BYTE BLOCKS OF DATA USING THE MAINTENANCE MODE TO TURN THE DATA AROUND. THE 256 BYTES OUTPUT ARE COMPARED WITH THE 256 BYTES INPUT FOR DATA COMPARISON ERRORS. ALL DATA COMPARISON ERRORS ARE REPORTED ON THE CONSOLE DEVICE. THE 256 BYTE TRANSFER IS REPEATED FOR FOUR DIFFERENT DATA BIT PATTERNS AS DESCRIBED BELOW:

- A. NULL-BIT-NULL SEQUENCE (000,377,000.....000,377)
- B. BINARY UP-COUNT SEQUENCE (000,001,002.....376,377)
- C. BINARY DOWN COUNT SEQUENCE (377,376,375.....001,000)
- D. WORST CASE PATTERN (376,377,001,000.,000,200)

2.0 REQUIREMENTS

HARDWARE: A PDP11 COMPUTER WITH A DL11-E INTERFACE  
STORAGE:: DLB REQUIRES:  
1. DECIMAL WORDS: 1354  
2. OCTAL WORDS: 02512  
3. OCTAL BYTES: 5224

3.0 PASS DEFINITION

ONE PASS OF "DXL89" CONSISTS OF TWO ITERATIONS OF SECTION TWO OF THE MODULE CODE WHICH RESULTS IN 2048(10) BYTES TRANSFERRED.

4.0 EXECUTION TIME

AT 300 BAUD RUNNING ALONE ON A PDP11/40 A SINGLE ERROR FREE

PASS TAKES APPROXIMATELY 40. SECONDS THIS TIME WILL VARY  
DEPENDING UPON THE BAUD RATE AND CPU TYPE.

5.0 CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DVA: 175610 VCT: 300 BR1: 4 BR2: 0  
DVC: 1 SRI: 0

REQUIRED PARAMETERS:

SRI TO EXERCISE THOSE STATIC TESTS REQUIRING THE USE  
OF THE H315 MODEM TEST CONNECTOR (MODEM CONTROL LOGIC)  
BIT 15 OF SRI MUST BE SET TO A "1". IE SRI=100000.

NOTE: IF SRI BIT 15=1 AND THE MODEM TEST CONNECTOR  
IS NOT INSTALLED, FALSE ERRORS WILL BE REPORTED.

6.0 DEVICE/OPTION SETUP

IF THE MODEM CONTROL LOGIC IS TO BE TESTED, THE USER MUST  
DISCONNECT THE MODEM AND CONNECT THE H315 TEST CONNECTOR TO  
THE DLT11-C DEVICE CABLE. SRI MUST BE SETUP AS DESCRIBED IN  
(5.0) OR THE TESTS WILL BE SKIPPED.

7.0 MODULE OPERATION

7.1 TEST SEQUENCES

A. STATIC REGISTER TESTS

- DLT01: TEST THAT ALL BITS IN THE RCSR ARE CLEAR WHEN  
THE MODULE IS INITIALIZED TO RUN.
- DLT02: TEST THAT ONLY THE "RDY" BIT IS SET  
IN THE XCSR WHEN THE MODULE IS INITIALIZED TO RUN.
- DLT03: TEST THAT THE "MAINT" BIT IN THE XCSR CAN BE  
SET AND CLEARED.
- DLT04: TEST THAT THE "INTR ENAB" BIT IN THE XCSR CAN  
CAUSE AN INTERRUPT TO THE PROPER VECTOR WHEN  
SET AND ALSO THAT "INTR ENAB" CLEARS PROPERLY.
- DLT05: TEST THAT A RECEIVER INTERRUPT OCCURS TO THE  
PROPER VECTOR WHEN "RDY" GETS SET WITH THE  
"INTR ENAB" BIT IN THE RCSR SET TO A ONE.  
ALSO TEST THAT THE CORRECT DATA IS RECEIVED.

TESTS DLT06 THRU DLT13 ASSUME THAT THE H315 MODEM

TEST CONNECTOR IS INSTALLED. THE USER INDICATES  
THIS BY SETTING BIT 15 OF SRI. THE MODULE LOOKS AT  
SRI AND WILL SKIP AROUND DLT06 THRU DLT13 IF BIT15=0.

- DLT06: TEST THAT "REQ TO SEND" CAN ASSERT "RING"  
WHEN SET AND THAT BOTH "REQ TO SEND" AND "RING"  
CAN BE CLEARED PROPERLY.
- DLT07: TEST THAT "SEC XMIT" WHEN SET ASSERTS  
"SEC REC" WHICH SETS "DATA SET INT" AND THAT  
READING THE RCSR CLEARS "DATA SET INT".  
ALSO TESTS THAT CLEARING "SEC XMIT" NEGATES  
"SEC REC" WHICH ALSO CAUSES "DATA SET INT" TO  
SET.
- DLT10: TEST THAT "DTR" ASSERTS "CLR TO SEND" AND  
"CAR DET" WHICH IN TURN SET "DATA SET INT".  
ALSO TESTS THAT "CLR TO SEND" AND "CAR DET"  
CLEAR WHEN "DTR" IS CLEARED.
- DLT11: TEST THAT "DATA SET INTR ENABLE" CAN BE SET  
AND CLEARED.
- DLT12: TEST THAT "DATA SET INTR ENABLE" IN THE XCSR  
CAUSES AN INTR. WHEN ENABLED.
- DLT13: TEST THAT THE BREAK BIT IN THE XCSR CAN BE SET  
AND CLEARED.

NOTE: BASIC TESTS DLT01 THRU DLT13 ARE EXECUTED  
ONLY ONCE WHEN THE MODULE IS FIRST INITIALIZED.  
IF ANY FATAL ERRORS ARE DETECTED THE MODULE IS DROPPED  
PRIOR TO THE DATA TRANSFER TESTS. AFTER PASS 1 THE  
MODULE IS RESTARTED AT THE ENTRY POINT TO THE DATA  
TRANSFER TESTS.

B. DATA TRANSFER TESTS

AFTER THE BASIC TESTS ARE RUN, FOUR 256(10)  
BYTE DATA TRANSFERS ARE EXECUTED IN THE MAINTENANCE  
MODE. EACH 256(10) BYTE BLOCK TRANSFER IS DIFFERENT  
IN THAT FOUR DIFFERENT DATA PATTERNS ARE XMITTED AND  
RECEIVED AS DESCRIBED IN PARA. 1-0.

THE TEST SEQUENCE FOR THE DATA TRANSFER TESTS IS AS  
FOLLOWS:

- 1.) CLEAR BOTH THE INPUT AND OUTPUT BUFFERS IN CORE  
(256(10) BYTES EACH).
- 2.) LOAD THE OUTPUT BUFFER WITH THE APPROPRIATE DATA  
PATTERN.
- 3.) ENABLE BOTH THE XMIT AND RCVR INTERRUPTS AND  
INITIATE THE DATA TRANSFERS.

- 4.) AFTER 256(10) BYTES HAVE BEEN OUTPUT AND INPUT COMPARE THE OUTPUT AND INPUT BUFFERS, BYTE BY BYTE FOR DATA COMPARE ERRORS. REPORT ALL DATA ERRORS ON THE CONSOLE DEVICE.
- 5.) IF ALL FOUR DATA PATTERNS HAVE BEEN TRANSFERRED, GO TO (6) BELOW - IF NOT REPEAT (1) THRU (4) FOR THE NEXT PATTERN.
- 6.) DECREMENT A PASS COUNTER (INITIALIZED TO 2,) AND TEST FOR ZERO. IF ZERO GO TO (7) - IF NOT REPEAT (1) THRU (5) AGAIN.
- 7.) REPORT END OF PASS TO THE MONITOR AND RESTART AT (1) WITH THE FIRST DATA PATTERN.

NOTES:

- (1) ON EACH "XMIT" INTERRUPT THE "READY" FLAG IS TESTED AND IF NOT SET, THE ERROR IS REPORTED AND THE MODULE IS DROPPED. (FALSE INTERRUPTS ARE CLASSIFIED AS FATAL ERRORS).
- (2) ON EACH "RCVR" INTERRUPT THE "DONE" FLAG IS TESTED AND IF NOT SET THE MODULE IS DROPPED THE SAME AS FOR A "XMIT FALSE INTERRUPT".
- (3) IF A SOFT ERROR (PARITY-FRAMING-OVERRUN) IS DETECTED BY RCVR INT. SERVICE, THE PENDING BLOCK TRANSFER IS RESTARTED FROM THE BEGINNING OF THE BLOCK. IF AFTER THREE RETRIES THE ERROR PERSISTS, TRANSFER OF THE PENDING DATA PATTERN IS ABORTED AND THE PROGRAM GOES ON TO THE NEXT DATA PATTERN. ALL SOFT ERRORS ARE REPORTED ON THE CONSOLE DEVICE.

7.2 SUBROUTINE ABSTRACTS

SEGX: THIS SUBROUTINE SERVES AS A MINI-MONITOR THAT CONTROLS THE SEQUENCING OF THE FOUR DIFFERENT 256(10) BYTE BLOCK TRANSFERS. IT IS CALLED AFTER THE BASIC TESTS AND PERFORMS THE FOLLOWING FUNCTIONS:

- 1. CALLS A SUBROUTINE TO CLEAR THE DATA BUFFERS
- 2. CALLS THE APPROPRIATE SUBROUTINE TO SET UP THE OUTPUT BUFFER WITH THE REQUIRED DATA PATTERN.
- 3. CALLS A SUBROUTINE TO ENABLE INTERRUPTS AND INITIATE THE DATA TRANSFER
- 4. SERVICES RETRIES REQUESTED BY SOFT ERRORS.
- 5. PERFORMS "BREAK" CALLS TO THE MONITOR TO PREVENT TIMEDOUTS FROM HANGING THE MODULE
- 6. CALLS THE SUBROUTINE TO CHECK THE DATA BUFFERS WHEN THE BLOCK TRANSFER IS COMPLETE.

KICKOF: THIS SUBROUTINE IS CALLED FROM "SEGX" AND CONTAINS THE CODE TO ENABLE INTERRUPTS AND INITIATE THE BLOCK TRANSFER FOR EACH 256(10) BYTE BLOCK TRANSFER.

CHKDAT: THIS SUBROUTINE IS CALLED FROM "SEGX" AND CHECKS FOR DATA COMPARISON ERRORS AFTER EACH BLOCK TRANSFER.

STATK: THIS SUBROUTINE IS CALLED FROM THE BASIC TESTS AND SETS UP THE ERROR INFORMATION FOR ALL ERRORS RELATING TO THE RECEIVER CSR.

STATX: THIS ROUTINE IS CALLED FROM THE BASIC TESTS AND SETS UP THE ERROR INFORMATION FOR ALL ERRORS RELATING TO THE TRANSMITTER CSR.

CLDLBF: THIS ROUTINE IS CALLED FROM "SEGX" AND CLEARS BOTH THE OUTPUT AND INPUT DATA BUFFERS IN CORE.

LDOU11: THIS ROUTINE IS CALLED FROM "SEGX" AND IS USED TO LOAD THE OUTPUT BUFFER WITH THE NULL-DEL-NULP PATTERN.

LDOU17: THIS ROUTINE IS CALLED FROM "SEGX" AND IS USED TO LOAD THE OUTPUT BUFFER WITH A BINARY UP-COUNT PATTERN.

LDOU13: THIS ROUTINE IS CALLED FROM "SEGX" AND IS USED TO LOAD THE OUTPUT BUFFER WITH A BINARY DOWN-COUNT PATTERN.

LDOU14: THIS ROUTINE IS CALLED FROM "SEGX" AND IS USED TO LOAD THE OUTPUT BUFFER WITH THE MONITOR'S WORST CASE PATTERN.

8.0 OPERATOR OPTIONS

- A. USE THE MOD COMMAND TO MODIFY LOCATION "DLR 16" TO CHANGE SRI. REFER TO PARA. 5.0.
- B. MODIFYING THE CONTENTS OF MODULE LOCATION "RESTRY +2" ALLOWS THE USER TO VARY THE TOTAL NO. OF BYTES TRANSFERRED PER PASS. THIS IS DEFAULTED AT LOAD TIME TO 2 WHICH RESULTS IN 2048. BYTES TRANSFERRED.

9.0 NON-STANDARD ERROR PRINTOUTS

A. IF ANY ONE OF THE FOUR DATA PATTERNS OUTPUT CANNOT BE SUCCESSFULLY COMPLETED DUE TO SOFT ERRORS (3 RETRIES ATTEMPTED) OR A MONITOR "BREAK" TIMEDOUT ONE OF THE FOLLOWING APPROPRIATE PRINTOUTS WILL OCCUR:

MSG1: "NULL-DEL-NULP SEQUENCE ABORTED"  
 MSG2: "BINARY UP-COUNT SEQUENCE ABORTED"  
 MSG3: "BINARY DOWN-COUNT SEQUENCE ABORTED"



000040

.REPT SPSIZ ;MODULE STACK STARTS HERE.  
.WLST  
.WORD 0  
.LIST  
.ENDR

000224\*

MODSP: ;\*\*\*\*\*

407

409  
409 000224\* 016700 177556  
410 000230\* 010067 003332  
411 000234\* 005720  
412 000236\* 010067 003326  
413 000242\* 005720  
414 000244\* 010067 003322  
415 000250\* 005720  
416 000252\* 010067 003316  
417  
418  
419  
420  
421  
422  
423 000256\* 005077 003304  
424 000262\* 005077 003300  
425 000256\* 005077 003300  
426 000272\* 005777 003272  
427 000276\* 005777 003266  
428  
429  
430  
431  
432  
433 000302\* 005777 003260  
434 000306\* 001415  
435 000310\* 004767 003052  
436 000314\* 012767 000025 177564  
437 000322\* 104405 000900\* 000000  
438  
439 000330\* 104403 000000\* 003640\*  
440 000336\* 104410 000000\*  
441  
442  
443  
444  
445 000342\* 022777 000200 003222  
446 000350\* 001415  
447 000352\* 004767 003026  
448 000356\* 012767 000025 177522  
449  
450 000364\* 104405 000000\* 000000  
451  
452 000372\* 104403 000000\* 003640\*  
453 000400\* 104410 000000\*  
454  
455  
456  
457  
458 000404\* 052777 000004 003160  
459 000412\* 022777 000204 003152  
460 000420\* 001415  
461 000422\* 004767 002756  
462 000426\* 012767 000033 177452  
463

```

START:  MOV  ADDR,RO      ;GET BASE DEVICE ADDRESS
        MOV  RO,DLRCSR   ;SET UP RCVR CSR ADDRESS
        TST  (RO)+
        MOV  RO,DLRDBR   ;SET UP RCVR DBR ADDRESS
        TST  (RO)+
        MOV  RO,DLXCSR   ;SET UP XMITTR CSR ADDRESS
        TST  (RO)+
        MOV  RO,DLXDBR   ;SET UP XMITTR DBR ADDRESS

;
; *****
; SECTION ONE
; *****
;
DLINIT: CLR  @DLRCSR     ;CLEAR OUT BOTH CSR'S
        CLR  @DLXCSR     ;MAKE SURE ALL DATA COMM BITS CLEARD
        CLR  @DLRDBR     ;FLUSH RCVR DONE BIT
        TST  @DLRDBR
        TST  @DLXDBR

;THIS TEST VERIFIES THAT RCVR CSR GOT CLEARD UPON ENTRY
-----
DLT01:  TST  @DLRCSR     ;IS RCVR CSR ALL ZEROES ??
        BEQ  DLT02      ;BR IF YES
        JSR  PC,STATX    ;GO SET UP ERROR INFO
        MOV  #25,ERRTYP
;*****
        HDRFS,REGIN,NULL ;CANT'T CLEAR OUT RCVR CSR
;*****
        MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
        ENDS,REGIN

;TEST THAT READY BIT IS ONLY BIT SET IN XMITTR CSR
-----
DLT02:  CMP  #200,@DLXCSR ;READY SET ??
        BEQ  DLT03      ;BR IF YES
        JSR  PC,STATX    ;GO SET UP ERROR INFO
        MOV  #25,ERRTYP
;*****
        HDRFS,REGIN,NULL ;READY NOT SET OR OTHER BITS DIDN'T CLEAR IN XMIT CSR
;*****
        MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
        ENDS,REGIN

;TEST THAT MAINT. BIT CAN BE SET AND CLEARD IN XMIT CSR
-----
DLT03:  BIS  #4,@DLXCSR   ;SET THE MAINT. BIT
        CMP  #204,@DLXCSR ;DID IT SET ??
        BEQ  IS         ;BR IF YES
        JSR  PC,STATX    ;GO SET UP ERROR INFO
        MOV  #33,ERRTYP
;*****

```

```

464 000434 104405 000000 000000
465 000434 104405 000000 000000
466 000447 104403 000000 003640
467 000450 104410 000000 000000
468 000454 042777 000004 003110
469 000460 012770 000700 003102
470 000470 001415
471 000472 004767 002705
472 000476 012767 000025 177402
473
474 000504 104405 000000 000000
475
476 000512 104403 000000 003640
477 000520 104410 000000
478
479
480
481
482 000524 005067 003060
483 000530 016700 177254
484 000534 062700 000004
485 000540 012770 000634
486 000546 012770 177242
487 000550 005001
488 000552 052777 000100 003012
489
490
491 000564 104407 000000
492 000570 005767 003014
493 000574 001025
494 000576 053077
495 000600 001367
496 000602 004767 002576 177272
497 000606 012767 000023
498
499 000614 104405 000000 000000
500
501 000622 104403 000000 003640
502 000630 104410 000000
503 000634 042777 000100 002730
504 000642 005167 002742
505 000646 000002
506 000650 022777 000200 002714
507 000656 001415
508 000660 004767 002520
509 000664 012767 000027 177214
510
511 000672 104405 000000 000000
512
513 000700 104403 000000 003640
514 000706 104410 000000
515
516
517
518
519 000712 005067 002672

```

```

HDRFRS,REGIN,NULL ;MAINT RIT WON'T SET OR IT CLEARED READY
;*****
MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
ENDS,BEGIN
;
BIC #4,ADLXCSR ;NOW CLR THE MAINT RIT
CMP #200,ADLXCSR ;DID IT CLEAR ??
BEQ #04 ;RR IF NO
JSR PC,STATX ;GO SET UP ERROR INFO
MOV #25,ERRTYP
;*****
HDRFRS,REGIN,NULL ;MAINT RIT WON'T SET OR IT CLEARED READY
;*****
MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
ENDS,BEGIN
;
;TEST THAT RIT 06 IN XCSR CAN CAUSE AN INTERRUPT
;-----
DLT04: CLR INTPLG ;INIT THE SOFTWARE INTR. FLAG
MOV VECTOR,RO ;GET BASE VECTOR ADDRESS
ADD #4,RO ;GENERATE ADDR OF XMIT VECTOR
MOV #25,(RO)+ ;GO TO 25 ON XMIT INTERRUPT
MOV #01,(RO) ;PRIORITY LEVEL = RRI
CLR R1 ;INIT BREAK TIMER
BIS #100,ADLXCSR ;SET INTR. ENAB
;
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
TST INTPLG ;DID XMIT INTR OCCUR YET ??
BNE JS ;RR IF IT DID
DEC R1 ;COUNT BREAK TIMER
BNE JS ;RR IF NO TIMEOUT
JSR PC,STATX ;GO SET UP ERROR INFO
MOV #23,ERRTYP
;*****
HDRFRS,REGIN,NULL ;XMITTR FAILED TO GENERATE INTERRUPT
;*****
MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
ENDS,BEGIN
;
BIC #100,ADLXCSR ;DISABLE XMITTR INTR ENABLE
COM INTPLG ;SET THE INTR. FLAG
RTI ;RETURN CONTROL TO OTHER GUY
CMP #200,ADLXCSR ;DID I.E. GET CLEARED IN INTR. SERVICE
BEQ #04 ;RR IF NO
JSR PC,STATX ;GO SET UP ERROR INFO
MOV #27,ERRTYP
;*****
HDRFRS,REGIN,NULL ;"NONE" OR RCVR INTR. ENAB FAILED TO CLEAR
;*****
MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
ENDS,BEGIN
;
;TEST THAT A RCVR INTR CAN OCCUR WHEN "NONE" GETS SET
;-----
DLT05: CLR INTPLG ;INIT SOFTWARE INTR. FLAG

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```

520 000716 016700 177066
521 000722 012770 001620
522 000726 116710 177060
523 000732 052777 000100 002626
524 000740 032777 000100 002620
525 000746 005767
526 000750 001767 002412
527 000754 012767 000033 177124
528
529 000762 104405 000000 000000
530
531 000770 104403 000000 003640
532 000776 104410 000000
533 001002 042777 000100 002556
534 001010 032777 000100 002550
535 001016 001415
536 001020 004767 002342
537 001024 012767 000023 177054
538
539 001032 104405 000000 000000
540
541 001040 104403 000000 003640
542 001046 104410 000000
543 001052 052777 000100 002506
544 001060 052777 000004 002504
545 001066 005001
546 001070 112777 000252 002476
547 001076
548 001076 104407 000000
549 001102 104407 000000
550 001106 005767 002476
551 001112 001036
552 001114 005301
553 001116 001367
554 001120 004767 002342
555 001124 005077 002436
556 001130 005077 002436
557 001134 012767 000023 176744
558
559 001142 104405 000000 000000
560
561 001150 104403 000000 003640
562 001156 104410 000000
563 001162 117677 002402 176720
564 001170 042777 000100 002370
565 001176 005077 002370
566 001202 005167 002402
567 001206 000002
568 001210 005777 002352
569 001214 001421
570 001216 004767 002144
571 001220 005077 002340
572 001226 005077 002340
573 001232 012767 000025 176646
574
575 001240 104405 000000 000000

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MOV VECTOR,RO ;GET THE BASE VECTOR ADDRESS
MOV #45,(RO)+ ;GO TO 45 ON RCVR INTERRUPT
MOV #01,RO ;SET PRIORITY
BIS #100,ADLXCSR ;SET I.E. IN RCVR CSR
BIT #100,ADLXCSR ;DID IT SET
BNE JS ;RR IF IT DID
JSR PC,STATX ;GO SET UP ERROR INFO
MOV #33,ERRTYP
;*****
HDRFRS,REGIN,NULL ;CAN'T SET RIT 06 IN RCSR I.E.
;*****
MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
ENDS,BEGIN
;
BIC #100,ADLXCSR ;NOW CLEAR THE I.E. BIT
BIT #100,ADLXCSR ;DID I.E. BIT GET CLEARED ??
BEQ #04 ;RR IF NO
JSR PC,STATX ;GO SET UP ERROR INFO
MOV #23,ERRTYP
;*****
HDRFRS,REGIN,NULL ;CAN'T CLEAR RCVR INTR. ENAB. BIT
;*****
MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
ENDS,BEGIN
;
BIS #100,ADLXCSR ;NOW TURN IT ON FOR REAL
BIS #4,ADLXCSR ;TURN ON MAINT. MODE
CLR R1 ;INIT BREAK TIMER
MOV #252,ADLXDBR ;LOAD THE XMITTR OUTPUT DATA BUFFER
;
BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
TST INTPLG ;DID RCVR INTR. YET ??
BNE JS ;RR IF IT DID
DEC R1 ;COUNT BREAK TIMER
BNE JS ;RR IF NO TIMEOUT
JSR PC,STATX ;GO SET UP ERROR INFO
CLR ADLXCSR ;CLEAR BOTH CSRS
MOV #23,ERRTYP
;*****
HDRFRS,REGIN,NULL ;RCVR FAILED TO INTR. ON TIME
;*****
MSGNS,BEGIN,DRPMS ;ASCII MESSAGE CALL WITH COMMON HEADER
ENDS,BEGIN
;
MOV#R,ADLDRR,AWAS ;GET THE RECEIVED DATA
BIC #100,ADLXCSR ;TURN OFF I.E.
CLR ADLXCSR ;TURN OFF MAINTENANCE MODE
COM INTPLG ;SET SOFTWARE INTR. FLAG
RTI ;RETURN TO OTHER GUY
TST ADLXCSR ;DID INTR SERVICE CLEAR THE RCVR CSR ??
BEQ #5 ;RR IF IT DID
JSR PC,STATX ;GO SET UP ERROR INFO
CLR ADLXCSR ;CLEAR BOTH CSRS
MOV #25,ERRTYP
;*****
HDRFRS,REGIN,NULL ;RCVR INTR SERVICE FAILED TO CLEAR I.E. AND DONE

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575
577 001246 104403 000000 003640
578 001247 104403 000000 000000
579 001260 127677 000252 176622 6S:
580 001266 001421
581 001270 016767 002272 176602
582 001276 017677 002284 176576
583 001304 012767 000252 176574
584 001312 016767 002252 176564
585 001320 016767 002250 176554
586
587 001325 104404 000000
588
589
590
591
592
593
594 001332 005767 176460
595 001336 100402
596 001340 000167 000770
597
598
599
600
601 001344 052777 000004 002214 DLT06: B1S #4,DLRCSR ;SET REQ TO SEND
602 001352 032777 000004 002206 BIT #4,DLRCSR ;DID IT SET ??
603 001360 001010 JSR PC,STATR ;BR IF YES
604 001362 004767 002000 ;GO SET UP ERROR INFO
605 001366 012767 000025 176512 MOV #25,ERRTYP
606
607 001374 104405 000000 000000
608
609 001402 032777 040000 002156 1S:
610 001410 001010 B1C #40000,DLRCSR ;DID "RING" GET ASSERTED ??
611 001412 004767 0001750 JSR PC,STATR ;BR IF YES
612 001416 012767 000025 176462 MOV #25,ERRTYP
613
614 001424 104405 000000 000000
615
616 001432 042777 000004 002126 2S:
617 001440 005777 002122 BIT #4,DLRCSR ;TURN OFF "REQ TO SEND"
618 001444 001410 TST DLRCSR ;ARE ALL BITS NOW CLEAR ??
619 001446 001410 BRQ DLT07 ;BR IF BOTH "RING" AND "REQ TO SEND" CLEARED
620 001452 012767 000025 176426 JSR PC,STATR ;GO SET UP ERROR INFO
621
622 001460 104405 000000 000000
623
624
625
626
627
628 001466 052777 000010 002072 DLT07: B1S #10,DLRCSR ;SET SEC XMIT
629 001474 005777 002066 TST DLRCSR ;DID DATA SET INT GET SET ??
630 001500 100410 B1C #1 JSR PC,STATR ;BR IF YES
631 001502 004767 001660 JSR PC,STATR ;GO SET UP ERROR INFO

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632 001506 012767 000025 176372 MOV #25,ERRTYP
633
634 001514 104405 000000 000000
635
636 001522 022777 002010 002036 1S:
637 001530 001410 CMP #2010,DLRCSR ;SEC XMIT AND REC SET - DATA SET INT CLEAR
638 001532 004767 BRQ #2 ;BR IF YES
639 001536 012767 000025 176342 JSR PC,STATR ;GO SET UP ERROR INFO
640
641 001544 104405 000000 000000
642
643 001552 042777 000010 002006 2S:
644 001560 005777 002002 TST DLRCSR ;DID SEC XMIT GOING OFF SET DATA SET INT ?
645 001564 100410 B1C #10,DLRCSR ;TURN OFF SEC XMIT
646 001566 004767 B1S #1 JSR PC,STATR ;BR IF YES
647 001572 012767 000020 176306 MOV #20,ERRTYP
648
649 001600 104405 000000 000000
650
651 001606 005777 001754 3S:
652 001612 001410 BRQ DLT10 ;ALL BITS NOW CLEAR
653 001614 004767 001546 JSR PC,STATR ;GO SET UP ERROR INFO
654 001620 012767 000020 176260 MOV #20,ERRTYP
655
656 001626 104405 000000 000000
657
658
659
660
661
662 001634 005077 001726 DLT10: CLR DLRCSR ;CLR THE RCVR CSR
663 001640 052777 000002 001720 B1S #2,DLRCSR ;SET DATA RCVR READY
664 001646 005777 001714 TST DLRCSR ;DID DATA SET INT SET ??
665 001652 100407 B1C #1 JSR PC,STATR ;BR IF YES
666 001654 004767 001506 JSR PC,STATR ;GO SET UP ERROR INFO
667 001660 005067 176222 CLR DLRCSR
668
669 001664 104405 000000 000000
670
671 001672 022777 030002 001666 1S:
672 001700 001407 CMP #30002,DLRCSR ;DTR CLR TO SEND, CAR DET, SET AND DATA SET INT CLEAR ?
673 001702 004767 BRQ #3 ;BR IF YES
674 001706 005067 176174 JSR PC,STATR ;GO SET UP ERROR INFO
675
676 001712 104405 000000 000000
677
678 001720 042777 000002 001640 2S:
679 001726 005777 001634 TST DLRCSR ;DATA SET INT SHOULD HAVE SPT
680 001732 100407 B1C #1 JSR PC,STATR ;BR IF IT DID
681 001734 004767 001426 JSR PC,STATR ;GO SET UP ERROR INFO
682 001740 005067 176142 CLR DLRCSR
683
684 001744 104405 000000 000000
685
686 001752 005777 001610 3S:
687 001756 001407 TST DLRCSR ;ALL BITS NOW CLEAR ??
688 BRQ DLT11 ;BR IF YES

```

```

688 001760* 004767 001402 JSR PC,STATR ;GO SET UP ERROR INFO
689 001764* 005067 176116 CLR ERRTP
690 *****
691 001770* 104405 000000* 000000 HRDRS,REGIN,NULL ;CLEARING DTR FAILED TO CLEAR OTHER BITS
692 *****
693 ;TEST THAT DATA SET INTR ENAB CAN SET AND CLEAR
694 -----
695
697 001776* 052777 000040 001562 DLT11: BIS #40,BDLRCSR ;SET DATA SET I.E.
698 002004* 032777 000040 001554 BIT #40,BDLRCSR ;DID IT SET ??
699 002012* 001010 BNE IS ;BR IF YES
700 002014* 004767 JSR PC,STATR ;GO SET UP ERROR INFO
701 002020* 012767 000027 176060 MOV #25,ERRTP
702 *****
703 002026* 104405 000000* 000000 HRDRS,REGIN,NULL ;CAN'T SET DATA SET INTR.
704 *****
705 002034* 042777 000040 001524 1S: BIC #40,BDLRCSR ;CLEAR DATA SET I.E.
706 002042* 005777 001520 TST BDLRCSR ;DID IT CLEAR ??
707 002046* 001411 BFC DLT12 ;BR IF YES
708 002050* 004767 JSR PC,STATR ;GO SET UP ERROR INFO
709 002054* 012767 000027 176024 MOV #25,ERRTP
710 *****
711 002062* 104405 000000* 000000 HRDRS,REGIN,NULL ;CAN'T CLEAR DATA SET I.E.
712 *****
713 002070* 000463 BR DLT13 ;SKIP NEXT TEST
714
715 ;TEST THAT DATA SET INT CAN CAUSE A RCVR INTERRUPT WHEN ENABLED
716 -----
717
718 002072* 005067 001512 DLT12: CLR INTFLG ;INIT SOFTWARE INTR. FLAG
719 002076* 016700 175706 MOV VECTOR,RO ;GET RAS VECTOR ADDR.
720 002102* 012720 002174* MOV #25,(R0)+ ;GO TO 25 ON DATA SET INTERRUPT
721 CLR R1 ;INIT BREAK TIMER
722 002110* 052777 000040 001450 BIS #40,BDLRCSR ;ENABLE DATA SET INTR.
723 002116* 052777 000010 001442 BIC #10,BDLRCSR ;SET SEC XMIT
724 *****
725 002124* 104407 000000* BREAKS,REGIN ;TEMPORARY RETURN TO MONITOR....
726 002130* 104407 000000* BREAKS,REGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
727 002134* 005767 001450 TST INTFLG ;DID INTR OCCUR YET ??
728 002140* 001023 BNE JS ;BR IF YES
729 002142* 005301 DFC R1 ;COMPT THE BREAK TIMER
730 002144* 001367 BNE IS ;BR IF NO TIMEOUT
731 002146* 004767 001214 JSR PC,STATR ;GO SET UP ERROR INFO
732 002152* 005077 001410 CLR BDLRCSR ;TURN OFF THE I.E. BIT
733 002156* 012767 000023 175722 MOV #25,ERRTP
734 *****
735 002164* 104405 000000* 000000 HRDRS,REGIN,NULL ;DATA SET INTR. FAILED TO OCCUR
736 *****
737 BR DLT13 ;GO TO NEXT TEST
738 002174* 042777 000040 001364 2S: BIC #40,BDLRCSR ;TURN OFF DATA SET I.E.
739 002202* 005167 001402 COM INTFLG ;SET SOFTWARE INTR. FLAG
740 002206* 000002 RTI ;RETURN CONTROL TO OTHER GOV
741 002210* 032777 000040 001350 3S: BIT #40,BDLRCSR ;DID INTR SERVICE TURN OFF I.E. ?
742 002216* 001410 BFC DLT13 ;BR IF YES
743 002220* 004767 001142 JSR PC,STATR ;GO SET UP ERROR INFO

```

```

744 002224* 012767 000023 175654 MOV #25,ERRTP
745 *****
746 002232* 104405 000000* 000000 HRDRS,REGIN,NULL ;INTR SERVICE FAILED TO CLR DATA SET I.F.
747 *****
748 ;TEST THAT "BREAK" BIT CAN SET AND CLEAR
749 -----
750
751 002240* 052777 000001 001324 DLT13: BIS #1,BDLRCSR ;SET BREAK BIT
752 002246* 032777 000001 001316 BIT #1,BDLRCSR ;DID IT SET ??
753 002254* 001010 BNE IS ;BR IF YES
754 002256* 004767 001122 JSR PC,STATR ;GO SET UP ERROR INFO
755 002262* 012767 000025 175616 MOV #25,ERRTP
756 *****
757 002270* 104405 000000* 000000 HRDRS,REGIN,NULL ;CAN'T SET BREAK BIT
758 *****
759 002276* 042777 000001 001266 1S: BIC #1,BDLRCSR ;CLEAR THE BREAK BIT
760 002304* 032777 000001 001260 BIT #1,BDLRCSR ;DID IT CLEAR ?
761 002312* 001410 BFC RSTRT ;BR IF YES
762 002314* 004767 001064 JSR PC,STATR ;GO SET UP ERROR INFO
763 002320* 012767 000025 175560 MOV #25,ERRTP
764 *****
765 002326* 104405 000000* 000000 HRDRS,REGIN,NULL ;BREAK BIT WON'T CLEAR
766 *****
767

```

76R  
769  
770  
771  
772 002334\*  
773 002334\* 005067 001242  
774 002340\* 005077 001226  
775 002344\* 005077 001216  
776 002350\* 016100 175434  
777 002354\* 012720 002522\*  
778 002360\* 116710 175426  
779 002364\* 005720  
780 002386\* 012720 002452\*  
781 002372\* 116710 175414  
782 002376\* 012703 003612\*  
783 002402\* 012704 003672\*  
784 002406\* 012467 001172  
785 002412\* 005777 001152  
786 002416\* 005777 001146  
787 002422\* 012367 001204  
788 002426\* 012467 001202  
789 002432\* 004767 000436  
790 002436\* 022703 003622\*  
791 002442\* 001361  
792  
793  
794  
795  
796  
797  
798 002444\* 104411 000000\*  
799  
800 002450\* 000752  
801  
802  
803

```

;
; *****
; * SECTION TWO
; *****
RESTRT: CLR XEND ;CLEAR END FLAGS
        CLR @DLXCSR ;CLEAR THE DL11 CONTROL REGS
        CLR @DLRCSR ;JUST IN CASE
        MOV VCTR,RO ;GET START VECTOR ADDRESS
        MOV #RINT,(RO)+ ;SET UP THE RCVR AND XMIT VCTORS
        MOVR #R1,(RO)
        MOV #RINT,(RO)+
        MOVR #R1,(RO)
DOACIN: MOV #DLTAB,R3 ;POINT TO TABLE OF LOAD SUBR. POINTERS
        MOV #WTAB,R4 ;POINT TO TABLE OF MESSAGE POINTERS
        CLR R7V ;CLEAR RETRY FLAGS
        IST @DLRDRR ;FLUSH RCVR INPUT BUFFER REG
        MOV #R3)+,LDOUT ;SET UP CORRECT LOAD BUF ADDRESS POINTER
        MOV (R4)+,LMESS ;SET UP MESSAGE POINTER
        JSR PC,SEG ;GO TO A SEGMENT
        CVP #TAB,R3 ;DONE ALL FOUR SEGMENTS ??
        JNE IS ;BR IF NOT

ENDITS,REGIN ;SIGNAL END OF ITERATION.
BR DOACIN ;MONITOR SHALL TEST END UP PASS

```

904  
905  
906  
907 002452\* 105777 001114  
908 002456\* 100403  
909  
910 002460\* 000004 000000\* 002516\*  
911  
912 002466\* 022767 004522\* 001104  
913 002474\* 001405  
914 002476\* 117777 001076 001070  
915 002504\* 005267 001070  
916 002510\*  
917  
918 002510\* 000004 000000\* 002610\*  
919  
920  
921 002516\* 105767 001061  
922 002522\* 001025  
923 002524\* 016767 001042 175346  
924 002532\* 017767 001034 175342  
925 002540\* 042777 00104 001024  
926 002546\* 105167 001030  
927  
928 002552\* 012767 000011 175326  
929  
930 002560\* 104405 000000\* 000000  
931  
932  
933 002566\* 005077 000774  
934 002572\* 104410 000000\*  
935  
936 002576\* 042777 000100 000766  
937 002604\* 104400 000000\*  
938  
939 002610\* 105767 000767  
940 002614\* 001370  
941 002616\* 104400 000000\*  
942  
943  
944  
945  
946 002622\* 105777 000740  
947 002626\* 100403  
948  
949 002630\* 000004 000000\* 002702\*  
950  
951 002636\* 005777 000726  
952 002642\* 100003  
953  
954 002644\* 000004 000000\* 002774\*  
955  
956 002652\* 022767 005122\* 000716  
957 002660\* 001405  
958 002662\* 117777 000702 000706  
959 002670\* 005267 000702

```

;THIS ROUTINE SERVICES ALL XMITTR INTERRUPTS. FOR ALL 256. BYTE XFERS
;-----
XINT: ISTR @DLXCSR ;XMIT READY SPT ??
      BR IF YES
;-----
      PIRQS,BEGIN,45 ; QUEUE UP TO CONTINUE AT 45 AND RTI
;-----
      CVP #DLRDFI,DPTR ;OUTPUT 256. BYTES YET ??
      BR IF YES
      MOV @DPT, @DLXDRR ;OUTPUT A CHARACTER
      INC DPT ;POINT TO NEXT CHAR. IN BUFFER
      3S:
;-----
      PIRQS,BEGIN,65 ; QUEUE UP TO CONTINUE AT 65 AND RTI
;-----
      4S: ISTR XEND+1 ;ANY FATAL RCVR. ERRORS PENDING ??
          BNE 5S ;BR IF YES - STOP XMITTING
          MOV @DLXCSR,CSRA ;SAVE THE CSR ADDRESS
          MOV @DLXCSR,ACSR ;SAVE THE CONTENTS OF THE CSR
          BIC #104,@DLXCSR ;DISABLE XMITTR INTERRUPTS
          COMR XEND ;SET XMIT END FLAG
          MOV #11,ERRTYP
          *****
          HDRS,REGIN,NULL ;XMITTR FALSE INTERRUPT - FATAL ERROR
          *****
          CLR @DLRCSR ;TURN OFF RCVR INTR.
          ENDS,REGIN
;-----
      5S: BIC #100,@DLXCSR ;DISABLE XMITTR. INTERRUPTS
          EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
;-----
      6S: ISTR XEND+1 ;ANY FATAL RCVR. ERRORS PENDING ??
          BNE 5S ;BR IF YES
          EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
;-----
;THIS ROUTINE SERVICES RECEIVER INTERRUPTS FOR ALL 256. BYTE XFERS
;-----
RINT: ISTR @DLRCSR ;RCVR DONE SET ??
      BMI IS ;BR IF YES
;-----
      PIRQS,BEGIN,35 ; QUEUE UP TO CONTINUE AT 35 AND RTI
;-----
      IST @DLRDRR ;OVERRUN/PARITY/FRAMING ERRORS ??
      BPL 2S ;BR IF NONE
;-----
      PIRQS,BEGIN,55 ; QUEUE UP TO CONTINUE AT 55 AND RTI
;-----
      2S: CVP #RUPEND,IPTR ;INPUT BUFFER FULL ??
          BR IF YES
          MOVR @DLRDRR,@IPTR ;READ THE DL INPUT BUFFER REG.
          INC IPTR ;POINT TO NEXT CHAR. POSITION

```

```

863 002674* 7S:
861 002674* 000004 000000* 003062* ;
862 ;TRQS,BEGIN,6S ; QUEUE UP TO CONTINUE AT 6S AND RET
863 ;
864
865 002702* 105767 000674 3S: TSTR XEND ;ANY FATAL XMITTR ERROR PENDING
866 002705* 001025 BNE 4S ;BR IF YES
867 002710* 016767 000652 175162 MOV DLRCSS,CSRA ;SAVE THE RCVR. CSR ADDRESS
868 002716* 017767 000544 175156 MOV DLRCSS,ACSR ;SAVE CONTENTS OF CSR
869 002724* 042777 000100 000634 BIC #100,DLRCSS ;TURN OFF THE RCVR.
870 002732* 105167 000645 COMR XEND+1 ;SET FATAL RCVR ERROR FLAG
871
872 002736* 012767 000011 175142 MOV #11,ERRTYP
873 ;*****
874 002744* 104405 000000* 000000 HDRPRS,BEGIN,NULL ;RECEIVER FALSE INTERRUPT - FATAL ERROR
875 ;*****
876
877 002752* 005077 000614 CLR #DLXCSR ;DISABLE XMITTR TOO
878 002756* 104410 000000* ENDS,BEGIN ;
879
880 002762* 042777 000100 000576 4S: BIC #100,DLRCSS ;DISABLE RCVR INTERRUPTS
881 002770* 104400 000000* EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
882
883 002774* 105767 000602 5S: TSTR XEND ;ANY FATAL XMITTR ERRORS PENDING ??
884 003000* 001370 BNE 4S ;BR IF YES
885 003002* 016767 000560 175070 MOV DLRCSS,CSRA ;SAVE CSR ADDRESS
886 003010* 017767 000552 175064 MOV DLRCSS,ACSR ;SAVE CONTENTS OF CSR
887 003016* 017767 000546 175060 MOV DLRCSS,ACSR ;SAVE THE ERROR FLAGS
888 003024* 042777 000100 000534 BIC #100,DLRCSS ;DISABLE RCVR INTR.
889
890 003032* 012767 000017 175046 MOV #17,ERRTYP
891 ;*****
892 003040* 104405 000000* 000000 HDRPRS,BEGIN,NULL ;OVERFLOW - PARITY - FRAMING ERROR
893 ;*****
894
895 003046* 005077 000520 CLR #DLXCSR ;DISABLE XMITTR TOO
896 003052* 105767 000510 INCR RTRV ;SET RETRY FLAG
897 003056* 104400 000000* EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
898
899 003062* 105767 000514 6S: TSTR XEND ;ANY FATAL XMITTR ERRORS PENDING ??
900 003066* 001335 BNE 4S ;BR IF YES
901 003070* 104400 000000* EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.

```

```

902 ;THIS ROUTINE CONTROLS THE EXECUTION OF EACH OF THE FOUR DATA PATTERNS
903
904 003074* 000240 SFCY: NOP ;DO NOTHING FOR NOW
905 003076* 004767 000320 1S: JSR PC,CLDRP ;GO CLEAR BUFFERS
906 003102* 004777 000524 JSR PC,LDOUT ;GO SET UP PATTERN
907 003106* 004767 000106 JSR PC,KICKOFF ;GO KICK OFF XMITTR AND RCVR
908 003112* 005002 CLR R2 ;INITIALIZE BREAK TIMER
909
910 003114* 104407 000000* 2S: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR....
911 003120* 104407 000000* BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
912 003124* 005767 000452 TST XEND ;ANY FATAL ERRORS PENDING ??
913 003130* 001407 BFC 4S ;BR IF NOT
914 003132* 104400 000000* EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
915 003136* 105767 000444 3S: TSTR RTRV ;RETRY FLAG SET ??
916 003142* 001411 BFC 4S ;BR IF NOT
917 003144* 105067 000436 CLR RTRV ;CLEAR THE FLAG
918 003150* 105767 000433 INCR RTRV+1 ;COUNT ONE RETRY
919 003154* 122767 000003 000425 CMPR #3,RTRV+1 ;TRIED THREE TIMES ??
920 003162* 001345 BNE 1S ;BR IF NOT - TRY IT AGAIN
921 003164* 000406 BR 5S ;REPORT IT AND GO TO NEXT SEGMENT
922 003166* 022767 005122* 000402 4S: CMP #BUFEND,IPTR ;RECEIVED 256 CHARS. ??
923 003174* 004406 BFC 4S ;BR IF YES
924 003176* 005302 DFC R2 ;DECREMENT BREAK COUNTER
925 003200* 001345 BNE 2S ;BR IF NO TIMEOUT
926
927 003202* 104403 000000* 003634 5S: MSGNS,BEGIN,AMESS ;ASCII MESSAGE CALL WITH COMMON HEADER
928 003210* 000207 6S: RTS PC ;GO TO NEXT SEGMENT
929 003212* 004767 000034 7S: JSR PC,CHKDAT ;GO COMPARE IN/OUT DATA
930 003216* 000207 RTS PC ;GO TO NEXT SEGMENT
931
932 ;THIS ROUTINE KICKS OFF ALL 256. BYTE TRANSFERS
933
934
935 003220* 012767 004122* 000352 KICKOFF: MOV #DLBUFO,OPTR ;POINT TO BEGINNING OF OUTPUT BUFFER
936 003226* 012767 004527* 000342 MOV #DLBUFI,IPTR ;POINT TO BEGINNING OF INPUT BUFFER
937 003234* 052777 000104 000330 BIC #104,DLXCSR ;TURN ON XMITTR
938 003242* 052777 000100 000316 BIC #100,DLRCSS ;TURN ON RCVR
939 003250* 000207 RTS PC ;RETURN TO CALLING SEGMENT
940
941 ;THIS ROUTINE CHECKS FOR AND REPORTS DATA COMPARE ERRORS
942
943
944 003252* 042777 000100 000312 CHKDAT: BIC #100,DLXCSR ;DISABLE XMITTR INTR.
945 003260* 042777 000100 000300 BIC #100,DLRCSS ;DISABLE RCVR INTR.
946 003266* 012700 004122* MOV #DLBUFO,R0 ;R0 POINTS TO OUTPUT BUFFER
947 003272* 012701 004522* MOV #DLBUFI,R1 ;R1 POINTS TO INPUT BUFFER
948 003276* 122021 1S: CMPR (R0)+(R1),R0 ;COMPARE INPUT WITH OUTPUT
949 003280* 001004 BNE 2S ;BR IF NOT EQUAL
950 003302* 022701 3S: CMP #BUFEND,R1 ;END OF THE BUFFERS ??
951 003306* 001373 BNE 1S ;BR IF NOT
952 003310* 000207 RTS PC ;RETURN TO CALLER
953
954 003312* 016767 000250 174560 2S: MOV DLRCSS,CSRA ;SAVE THE CSR ADDRESS
955 003320* 114067 174562 MOVR -(R0),ASB ;SAVE THE SHOULD BE DATA
956 003324* 042767 177400 BIC #177400,ASB ;ZERO HI BYTE
957 003332* 010067 174544 MOV #R0,SBADR ;SAVE THE SHOULD BE ADDRESS

```

```

958 003336* 114167 174546 174540
959 003342* 042767 177400
960 003350* 010167 174530
961
962
963 003354* 104404 000000*
964
965
966 003360* 105720
967 003362* 105721
968 003364* 000746
969
970
971
972
973 003366* 016767 000174 174504
974 003374* 017767 000166 174500
975 003402* 000207
976
977 003404* 016767 000162 174466
978 003412* 017767 000154 174462
979 003420* 000207
980
981
982
983
984 003422* 012700 004122*
985 003424* 005020
986 003430* 022700 005122*
987 003434* 001374
988 003436* 000207
989
990
991
992
993 003440* 012700 004122*
994 003444* 105020
995 003446* 112720 000377
996 003452* 022700 004522*
997 003456* 011372
998 003460* 000207
999
1000
1001
1002
1003
1004 003462* 012700 004122*
1005 003466* 005001
1006 003470* 110120
1007 003472* 022700 004522*
1008 003476* 001407
1009 003500* 105201
1010 003502* 000772
1011 003504* 000207
1012
1013

```

```

MOVW -(R1),AWAS ;SAVE THE WAS DATA
R1C #177400,AWAS ;CLEAR OUT HI BYTE
MOV R1,WASADR ;SAVE THE WAS ADDRESS
;*****
JATPRS,REGIN ;DATA ERROR!!!
;*****
TSTR (R0)+ ;POINT TO NEXT BYTE IN THE BUFFERS
TSTR (R1)+ ;GO CHECK NEXT BYTE
RTR 3S
;THESE ROUTINES SET UP THE ERROR INFORMATION FOR THE BASIC TESTS
-----
STATR: MOV DLRCR,CSR ;SAVE THE CSR ADDRESS
MOV DLRCR,ACR ;SAVE THE CONTENTS OF THE CSR
RTS PC ;RETURN TO BASIC TESTS
STATX: MOV DLXCSR,CSR ;SAVE THE CSR ADDRESS
MOV DLXCSR,ACR ;SAVE THE CONTENTS OF THE CSR
RTS PC ;RETURN TO THE BASIC TESTS
;THIS ROUTINE IS USED TO CLEAR THE INPUT/OUTPUT BUFFERS
-----
CLOLBP: MOV #DLRUPD,R0 ;POINT R0 TO BEGINNING OF BUFFERS
1S: CLR (R0)+ ;CLEAR ONE WORD - UPDATE POINTER
CMP #RUPEND,R0 ;DONE 256 BYTES ??
BNE 1S ;RR IF NOT
RTS PC ;RETURN TO CALLING SEGMENT
;THIS ROUTINE LOADS THE OUTPUT BUFFER WITH A NULL-DEL-NUL PATTERN
-----
LDOHT1: MOV #DLRUPD,R0 ;SET UP POINTER
1S: CLRR (R0)+ ;MOV A NULL CHAR
MOVW #377,(R0)+ ;MOV A DELETE CHAR.
CMP #DLRUP1,R0 ;BUFFER FULL ???
BNE 1S ;RR IF NOT
RTS PC ;RETURN TO CALLING SEGMENT
;THIS ROUTINE IS USED TO LOAD AN ASCENDING BINARY COUNT PATTERN
-----
LDOHT2: MOV #DLRUPD,R0 ;SET UP POINTER
1S: CLR R1 ;USE R1 TO GENERATE THE PATTERN
MOVW R1,(R0)+ ;LOAD ONE CHAR.
CMP #DLRUP1,R0 ;BUFFER FULL ???
BNE 1S ;RR IF YES
INCR R1 ;GENERATE NEXT CHAR.
RR 1S ;GO MOVE IT
RTS PC ;RETURN TO CALLING SEGMENT
2S:
;THIS ROUTINE IS USED TO LOAD THE DESCENDING BINARY COUNT PATTERN

```

```

1014
1015
1016 003506* 012700 004122*
1017 003512* 012701 000377
1018 003516* 110120
1019 003520* 022700 004522*
1020 003524* 001407
1021 003526* 105101
1022 003530* 000772
1023 003532* 000207
1024
1025
1026
1027
1028 003534* 012700 004122*
1029 003540* 012701 005124*
1030 003544* 012120
1031 003546* 022700 004522*
1032 003552* 001404
1033 003554* 022701 005224*
1034 003560* 001767
1035 003562* 000774
1036 003564* 000207

```

```

-----
LDOHT3: MOV #DLRUPD,R0 ;SET UP POINTER
1S: MOV #377,R1 ;START R1 AT 377
MOVW R1,(R0)+ ;LOAD ONE CHAR.
CMP #DLRUP1,R0 ;AT END OF THE BUFFER ??
BNE 1S ;RR IF YES
DECR R1 ;GENERATE NEXT CHAR.
RR 1S ;GO MOVE IT
RTS PC ;RETURN TO CALLING SEGMENT
2S:
;THIS ROUTINE LOADS THE WORST CASE PATTERN
-----
LDOHT4: MOV #DLRUPD,R0 ;SET UP POINTERS
1S: MOV #WCASE,R1 ;POINT TO MONITOR'S WORST CASE PATTERN
2S: MOV (R1)+(R0)+ ;LOAD ONE WORD
CMP #DLRUP1,R0 ;BUFFER FULL ???
BNE 1S ;RR IF YES
INCR R1 ;END OF WORST CASE PATTERN ??
RR 1S ;GO RESET R1
RTS PC ;RETURN TO CALLING SEGMENT
3S:

```

```

1037
1038
1039
1040 003566* 000000
1041 003570* 000000
1042 003572* 000000
1043 003574* 000000
1044
1045 003576* 000000
1046 003600* 000000
1047
1048 003602* 000000
1049 003604* 000000
1050 003606* 000000
1051 003610* 000000
1052
1053 003612* 003440*
1054 003614* 003462*
1055 003616* 003506*
1056 003620* 003534*
1057 003622* 003644*
1058 003624* 003705*
1059 003626* 003750*
1060 003630* 004015*
1061
1062 003632* 000000
1063 003634* 003644*
1064 003636* 177777*
1065 003640* 004053*
1066 003642* 177777*
1067 003644* 047045 046125 026514
1068 003652* 042504 026514 052516
1069 003650* 046114 051440 050505
1070 003666* 042525 041516 020105
1071 003674* 041101 051117 042524
1072 003702* 022504 000 000
1073 003705* 0045 044502 040516
1074 003712* 054522 052440 020120
1075 003720* 047503 047125 020124
1076 003726* 042523 052521 047105
1077 003734* 042503 040440 047502
1078 003742* 052122 042105 000045
1079 003750* 041045 047111 051101
1080 003756* 020131 047504 047127
1081 003796* 041440 052517 052118
1082 003792* 051440 050505 042524
1083 004000* 041516 020105 041101
1084 004006* 051117 042524 022504
1085 004014* 000 000
1086 004015* 045 047527 051522
1087 004022* 020124 040503 042523
1088 004030* 051440 050505 042525
1089 004036* 051516 020105 041101
1090 004044* 051117 042524 022504
1091 004052* 000 000
1092 004053* 045 040506 040524

```

```

;VARIABLES, FLAGS, MESSAGES, AND BUFFERS
;-----
DLRCSR: OPEN ;CONTAINS ADDRESS OF RCVR CSR
DLRDBR: OPEN ;CONTAINS ADDRESS OF RCVR DBR
DLKCSR: OPEN ;CONTAINS ADDRESS OF XMITR CSR
DLKDBR: OPEN ;CONTAINS ADDRESS OF XMITR DBR
IPTR: OPEN ;CONTAINS POINTER TO INPUT BUFFER
OPTR: OPEN ;CONTAINS POINTER TO OUTPUT BUFFER
XFND: OPEN ;FATAL ERROR FND FLAG
EPCFR: OPEN ;END OF PASS COUNTER
RTRY: OPEN ;RETRY FLAG AND COUNTER
INTPLC: OPEN ;SOFTWARE INTR. FLAG USED BY BASIC TESTS
LDTAB: LDOU1 ;POINTER TO 1ST LOAD BUFFER SUBR.
LDOU2 ;POINTER TO 2ND LOAD BUFFER ROUTINE
LDOU3 ;POINTER TO 3RD LOAD BUFFER ROUTINE
LDOU4 ;POINTER TO 4TH LOAD BUFFER ROUTINE
MTAB: MSG1 ;POINTER TO MESSAGE 1
MSG2 ;POINTER TO MESSAGE 2
MSG3 ;POINTER TO MESSAGE 3
MSG4 ;POINTER TO MESSAGE 4
LDOUT: OPEY ;CONTAINS POINTER TO LOAD BUFFER SUBR.
AMESS: MSG1 ;MESSAGE POINTERS
DRPMS: MSG5 ;TERMINATOR
;MESSAGE POINTER
MSG1: .ASCIZ /%NULL-DEL-NULL SEQUENCE ABORTED%/
MSG2: .ASCIZ /%BINARY UP COUNT SEQUENCE ABORTED%/
MSG3: .ASCIZ /%BINARY DOWN COUNT SEQUENCE ABORTED%/
MSG4: .ASCIZ /%WORST CASE SEQUENCE ABORTED%/
MSG5: .ASCIZ /%FATAL ERROR IN STATIC REGISTER TESTS%/

```

```

1093 004060* 020114 051105 047522
1094 004066* 020122 047111 051440
1095 004074* 040524 044524 020103
1096 004102* 042522 044507 052118
1097 004110* 051105 052440 051505
1098 004116* 051524 000045
1099
1100 .RVFN
1101 ;512 WORDS RESERVED FOR TWO 256-BYTE BUFFERS
1102 ;-----
1103
1104 004122* 000400
1105 004555* 000400
1106 005122* 000000
1107
1108 005124* 177776 000001 177775
1109 005132* 000002 177773 000004
1110 005140* 177767 000010
1111 005144* 177757 000020 177737
1112 005152* 000000 177677 000100
1113 005160* 177577 000200
1114 005164* 177377 000400 176777
1115 005172* 001000 175777 002000
1116 005200* 173777 004000
1117 005204* 167777 010000 157777
1118 005212* 020000 137777 040000
1119 005220* 077777 100000
1120 005224*
1121
1122
1123 000001

```

```

DLRBUF: .RLKB 256. ;RSVD FOR OUTPUT BUFFER
DLRIBF: .RLKB 256. ;RSVD FOR INPUT BUFFER
BUPEN0: 0 ;MARK END OF BUFFER AREA
WCASE: .WORD 177776,1,177775,2,177773,4,177767,10
.WORD 177757,20,177737,40,177677,100,177577,200
.WORD 177377,400,176777,1000,175777,2000,173777,4000
.WORD 167777,10000,157777,20000,137777,40000,77777,100000
WCASE:
.FND

```



UPEV = 000000	354	360	361	352	363	380	381	382	383	384	385	386	387
	389	391	393	394	386	397	398	407#	1040	1041	1042	1043	1045
	1046	1048	1049	1050	1051	1062							
	817	814	815*	835*	1046#								
OPTD = 003600R	407#												
OTDAS = 104420	407#												
PASCNT = 000034R	368#												
PTDPS = 000004	407#	810	818	849	854	862							
PDPSP = 005726	407#												
PDPSP2 = 022626	407#												
PRTV = 000000	358#												
PRTV0 = 000000	407#												
PRTV1 = 000000	407#												
PRTV2 = 000100	407#												
PRTV3 = 000140	407#												
PRTV4 = 000200	357#	407#											
PRTV5 = 000240	407#												
PRTV6 = 000300	407#												
PRTV7 = 000340	407#												
PS = 177776	407#												
PSW = 177776	407#												
PUSH = 005746	407#												
PUSH2 = 024646	407#												
RANDS = 104417	407#												
RANWUM = 00054W	376#												
RSTRT = 002334R	395#	596	762	772#									
RFS1 = 000056W	378#												
RFS2 = 000050R	77#												
RIBT = 022622R	357#	846#											
RSTRT = 000112R	395#												
RTRV = 003606R	784*	896*	915*	917*	918*	919	1050#						
SRADR = 000102W	388#												
SGT = 000074W	379#												
SOPCNT = 000042R	371#												
SOPFRS = 104406	407#												
SOPPAS = 000046R	373#												
SPOINT = 000032R	367#												
SPSTZ = 000046	1#	400											
SR1 = 000016R	360#	594											
SR2 = 000020R	361#												
SR3 = 000024R	362#												
SR4 = 000024R	363#												
START = 000224W	366#	409#											
STAT = 000026W	365#												
STATR = 00366R	434	536	536	554	570	604	611	619	631	638	646	653	666
	447	461	471	496	508	755	763	973#					
	447	461	471	496	508	755	763	977#					
STATX = 003404R	447#												
SVR0 = 000062R	380#												
SVR1 = 000064R	381#												
SVR2 = 000066R	382#												
SVR3 = 000070R	383#												
SVR4 = 000072R	384#												
SVR5 = 000074W	385#												
SVR6 = 000076R	386#												
SYSCNT = 000052W	375#												
TSPDF = 000027	407#												
VCTDR = 000010W	356#	483	520	719	776								

WASADR = 000104R	390#	584*											
WCASE = 005124R	1029	1109#	960*										
WCASEP = 005724R	1033	1171#											
WDR = 000115W	397#												
WDTN = 000114R	396#												
WEND = 003602R	773#	821	876*	879	865	870*	883	899	912	1048#			
XFLAG = 000005W	354#												
XINT = 002452R	780	807#											
.	1104#	1105#											

. AQS. 000000 000  
 005224 001

FRUPS DETECTED: 0  
 DEFAULT GLOBALS GENERATED: 0

XDLRBO, XDLRBO/SOL/CRF:SYN=DDXCON, XDLRBO  
 RUN-TIME: 2 4.3 SECONDS  
 RUN-TIME RATIO: 31/6=4.9  
 CORE USED: 7K (13 PAGES)

DIAGNOSTIC ENGINEERING

**digital**

DECO  DEPO  SUBMISSION

FOR RELEASE ENG. USE  
 NEW  CHANGE  DELETE

**PRODUCT IDENTIFICATION**

LIBRARY	PRODUCT NUMBER	REV	PATCH	ECO TALLY	PRODUCT DATE	STATUS	DISTRIBUTION	1ST COPY - RIGHT YEAR	LAST COPY - RIGHT YEAR
ZZ	CXDLB	B	1	51	22 Jan 79	OBSOLETE	X G	1976	1979

TITLE CXDLBB1 DL11-E MODULE

AUTHOR D. RUTENHOF MAINTAINER D. RUTENHOF SPT GRP MAINTAINER D. RUTENHOF SUBMITTING ENGINEER D. RUTENHOF

**PRODUCT COMPONENTS**

CK	DESCRIPTION	PRODUCT NO.	REV	CK	DESCRIPTION	PRODUCT NO.	REV
	DOCUMENT				INDEX		
	LISTING				SOURCE MEDIA		
	OBJECT MEDIA				TEST MEDIA		
X	DEPO	AF-E998B-M1					

**PRODUCTS OBSOLETE (other than previous version)**

LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV	LIBRARY	PRODUCT NUMBER	REV
MD			MD			MD		

**PRODUCT CHARACTERISTICS**

PROCESSORS PRODUCT OPERATES WITH (Enter all applicable 2-digit codes representing the Processor the product operates with. See separate instructions.)  
 03 04 05 10 20 21 34 35 40 45 50 55 60 70

OPERATIONAL CODES (Enter all applicable 2-digit codes that describe the product. See separate instructions.)  
 02 03 04 06 50

ACT/APT/XXDP	EXT	ACT SEQ NUMBER	ACT/XXDP COMPATIBLE?	APT COMPATIBLE?	1ST PASS RUN TIME	SUBSEQUENT PASS RUN TIME
INFORMATION FIELD			<input checked="" type="checkbox"/> Y <input type="checkbox"/> N	<input checked="" type="checkbox"/> Y <input type="checkbox"/> N	40 SECONDS	10 SECONDS

**DECO/DEPO INFORMATION**

ITEMS REPORTS CLOSED: \_\_\_\_\_

DATE AFFECTED DEC/X11 MULTIMEDIA AFFECTED?  YES  NO

KIT NUMBERS	ZJ129-RZ	ZJ129-FR	ZJ130-RB
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PROBLEM:  
 Module is intended for 2/40 FRONT-END interface only, but documentation does not say so.

SOLUTION:  
 State in module header that this module is not intended for use on standalone PDP11 SYSTEMS.

**DEPO PATCH AREA**

CHANGE LOC	FROM	TO	CHANGE LOC	FROM	TO

SUBMITTING ENGINEER <i>[Signature]</i> DATE: 23-Jan-79	MANUFACTURING ENGINEER <i>[Signature]</i> DATE: 7-Feb-79	SUPPORT ENGINEER DATE:	CHARGE DECO/DEPO TO DISCRETE PROJECT NUMBER Q99-05460
MAINTAINER <i>[Signature]</i> DATE: 23-Jan-79	FIELD SERVICE DATE:	WAIVERING MANAGER DATE:	COORDINATION NO. MC# 2838