

NEW NUMBER = DZDHI

DH11 Break and Half-Duplex Test

ABSTRACT

Checks the break control logic of the DH11 and verifies that the units receive only one break character on a given line no matter how long break is asserted. The test also verifies that no characters are received on a line if the half duplex function for that line is selected.

REQUIREMENTS

- PDP-11 family standard computer with 4KW of memory
- ASR-33 teletype or equivalent
- DH11 asynchronous multiplexer
- DM11 maintenance card installed

STORAGE = program loads into 4KW of memory

LOADING = Absolute Loader

EXECUTION TIME =

STARTING PROCEDURE = start at 200
 restart at 2000

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

- SW15 = HALT ON ERROR
- SW14 = LOOP ON CURRENT TEST
- SW13 = SUPPRESS ERROR TIMEOUT
- SW11 = INHIBIT ITERATIONS
- SW10 = ESCAPE TO NEXT TEST ON ERROR
- SW09 = FREEZE VARIABLE PARAMETER IN CURRENT TEST
- SW01 = START PROGRAM AT SELECTED TEST
- SW00 = CHANGE PARAMETERS AT PROGRAM RESTART