

NEW NUMBER = DZDHC

DH11 Transmitter and Receiver Logic Test

ABSTRACT

The DH11 transmitter and receiver logic test checks the basic transmitter and receiver functions. Functions tested include interrupts, operation of transmitter NPR logic, and operation of receiver slice logic.

REQUIREMENTS

PDP-11 family standard computer with 4KW of memory
ASR-33 teletype or equivalent
DH11 asynchronous multiplexer
DM11 maintenance card installed

STORAGE = program loads into 4KW of memory

LOADING = Absolute Loader

EXECUTION TIME = 1 minute per device

STARTING PROCEDURE = start at 200
 restart at 2000

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS = Yes

SW15 = HALT ON ERROR
SW14 = LOOP ON CURRENT TEST
SW13 = SUPPRESS ERROR TYPEOUT
SW11 = INHIBIT ITERATIONS
SW10 = ESCAPE TO NEXT TEST ON ERROR
SW09 = FREEZE VARIABLE PARAMETER IN CURRENT TEST
SW01 = START PROGRAM AT SELECTED TEST
SW00 = CHANGE PARAMETERS AT PROGRAM RESTART