

NEW NUMBER = DZDHA

DH11 Static Logic Test

ABSTRACT

The DH11 static logic test is designed to provide a means for testing the correct function of all read/write bits in the following DH11 registers:

- DH11 system control register
- DH11 line parameter register
- DH11 break control register
- DH11 sll status register

In addition, tests are provided to check the function of those bits that are read only in maintenance mode. Also provided are tests of register addressability and of the function of master clear.

The diagnostic has been written so that the testing of each function is contained in an individual test loop.

REQUIREMENTS

- PDP-11 family standard computer with 4KW of memory
- ASR-33 teletype or equivalent
- DH11 asynchronous multiplexer
- DH11 maintenance card installed

STORAGE = Programs loads into 4KW of memory

LOADING = Absolute Loader

EXECUTION TIME = 1 minute per device

STARTING PROCEDURE = start at 200
restart at 2000

PRINTOUTS = Yes

SWITCH REGISTER OPTIONS =

- SW15 = HALT ON ERROR
- SW14 = LOOP ON CURRENT TEST
- SW13 = SUPPRESS ERROR TIMEOUT
- SW11 = INHIBIT ITERATIONS
- SW10 = ESCAPE TO NEXT TEST ON ERROR
- SW09 = FREEZE VARIABLE PARAMETER IN CURRENT TEST
- SW01 = START PROGRAM AT SELECTED TEST
- SW00 = CHANGE PARAMETERS AT PROGRAM RESTART