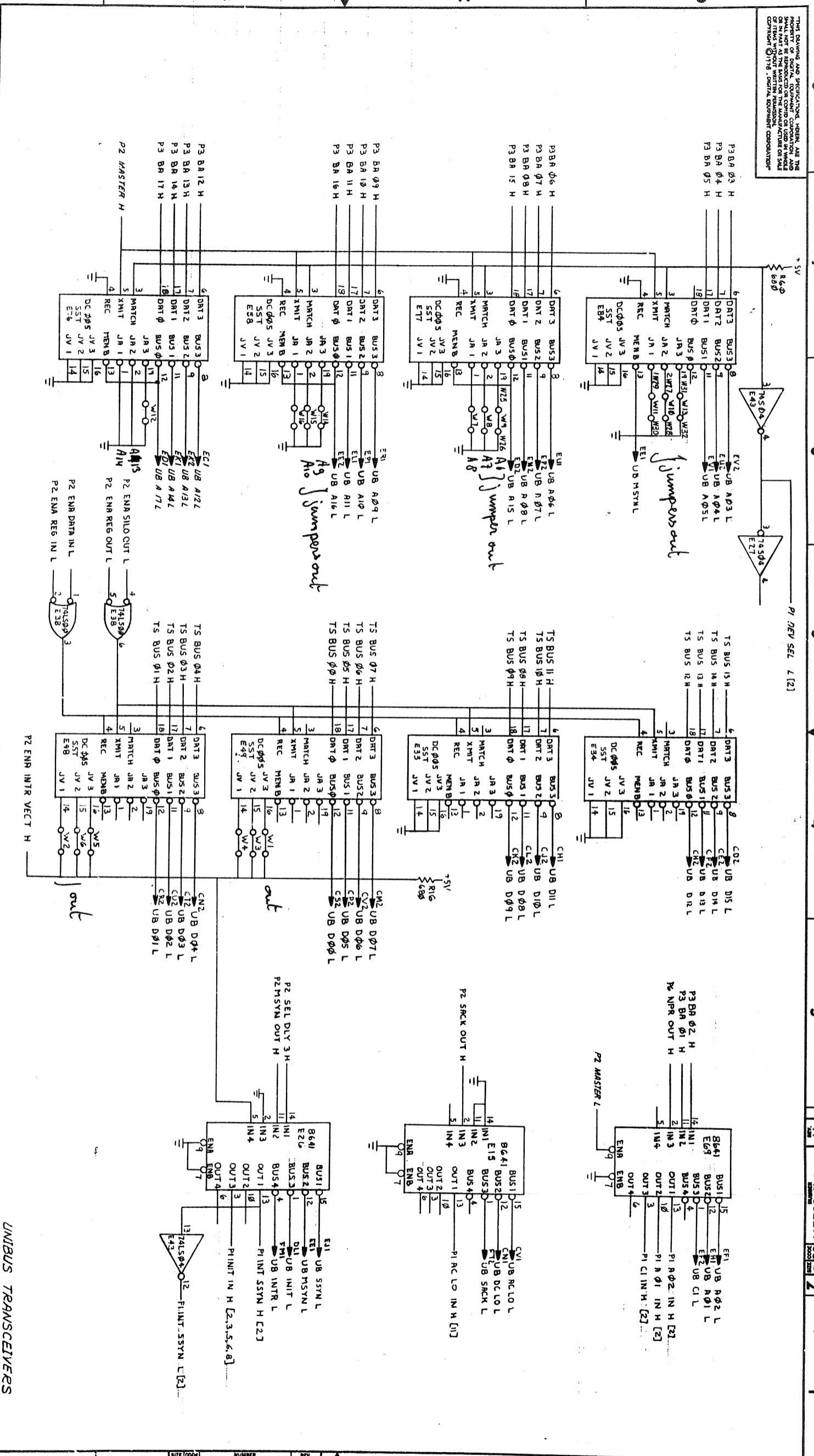


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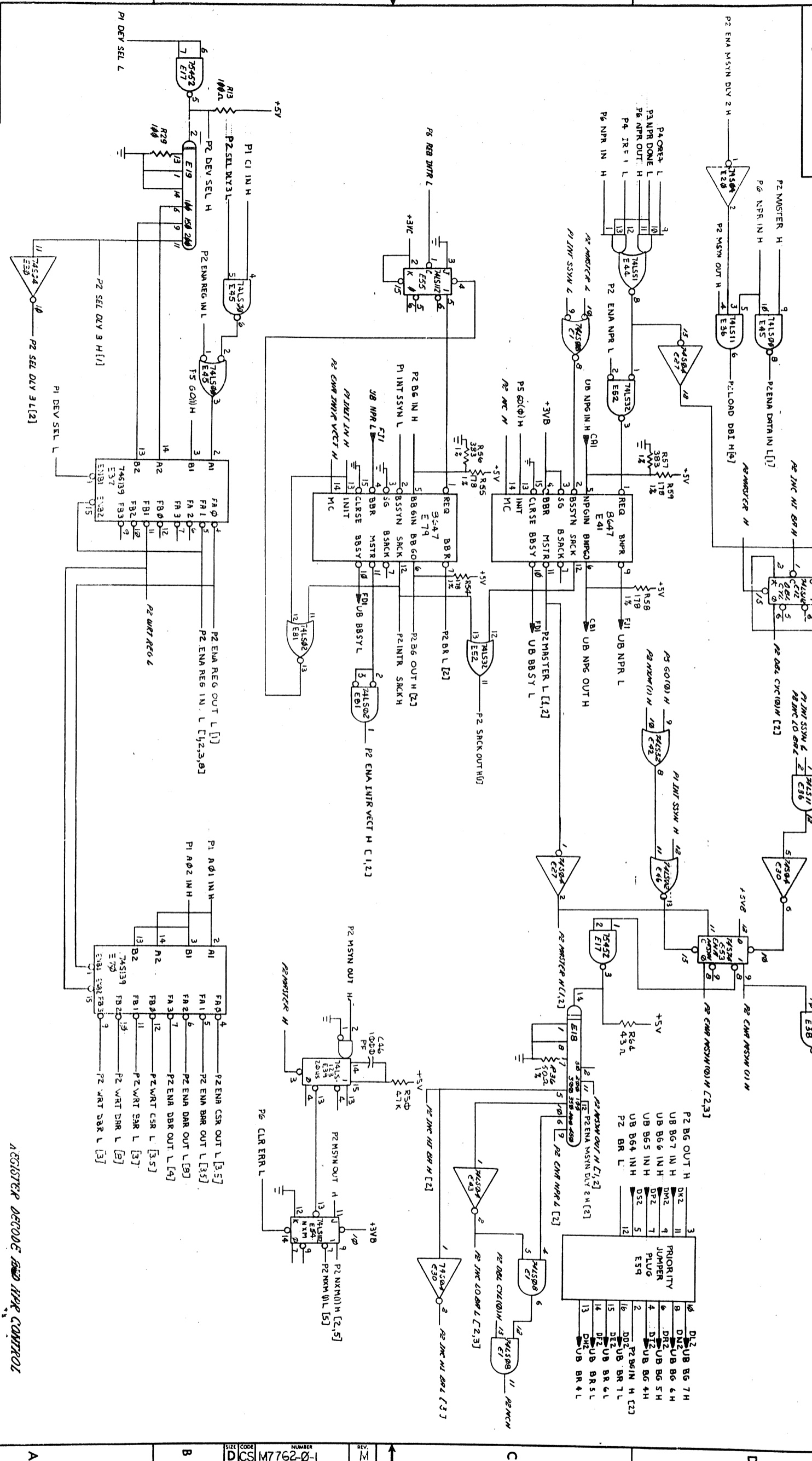
REV.	CHANGE NO.	REVISIONS
E	M7762-00001	C. LIU
F	M7762-00002	C. LIU
G	M7762-00003	C. LIU
H	M7762-00004	C. LIU
I	M7762-00005	J. VAGIAS
J	M7762-00006	J. VAGIAS
K	M7762-00007	G. DONOVAN
L	M7762-00008	G. DONOVAN
M	M7762-00009	M. BROWN

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REV.	NUMBER	DESCRIPTION
M	M7762-0-1	UNIBUS TRANSCIEVERS

REV.	NUMBER	DESCRIPTION
M	M7762-0-1	UNIBUS TRANSCIEVERS



REV	NUMBER	SIZE	TITLE	SHEET	OF	DATE
A1	0-1	DCS M7762-0-1	REGISTER DECODE AND IRR CONTROL (P2)	2	11	

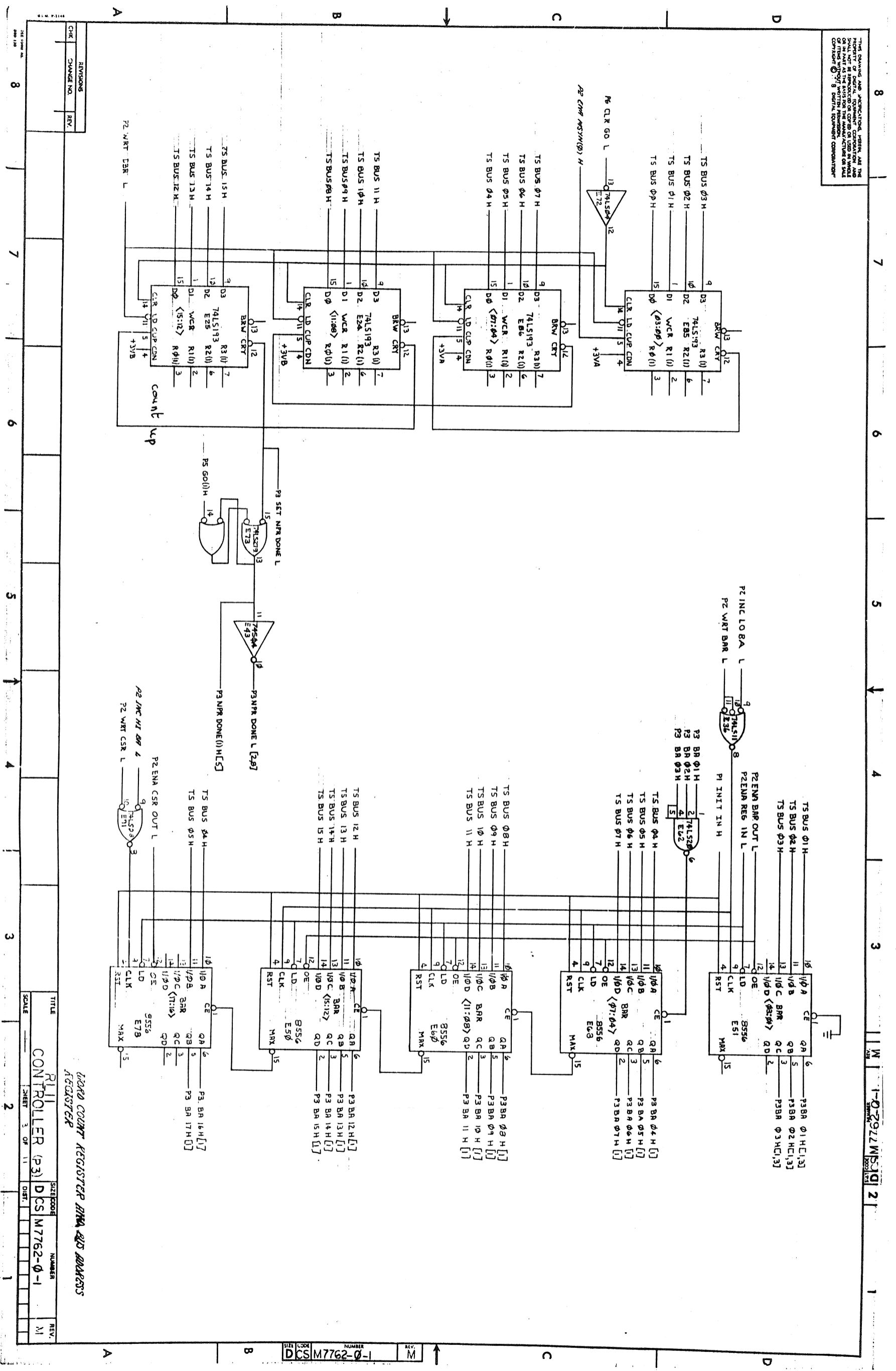
REGISTER DECODE AND IRR CONTROL

REV	CHANGE NO.	CHK

REV	CHANGE NO.	CHK

REV	NUMBER	SIZE	TITLE	SHEET	OF	DATE
A1	0-1	DCS M7762-0-1	REGISTER DECODE AND IRR CONTROL (P2)	2	11	

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REV.	CHG.	NO.	REV.

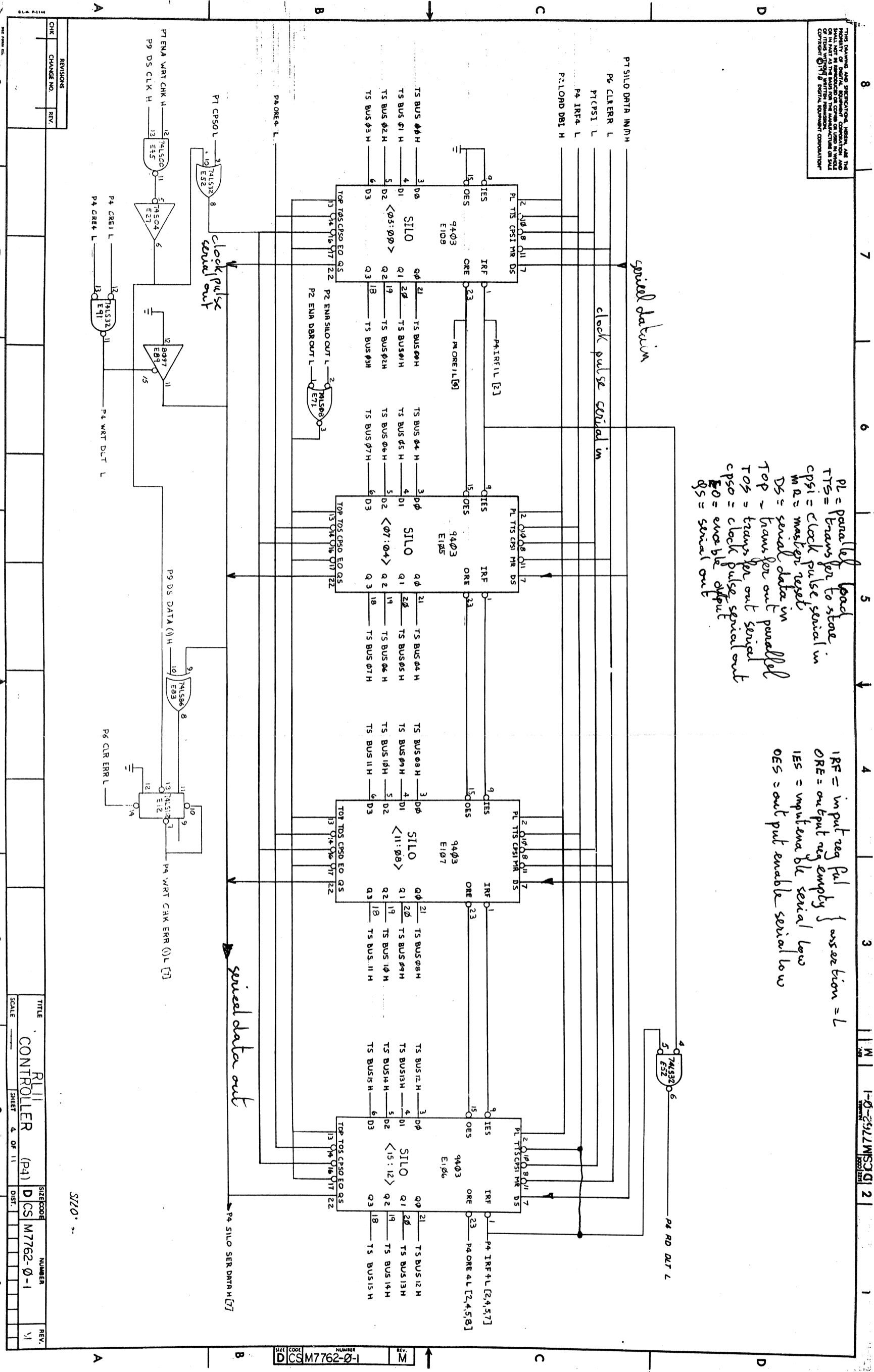
TITLE	SCALE	SHEET	OF	NO.	REV.
16-BIT COUNTER REGISTER WITH ADDRESS REGISTER		3	OF	11	M1

REV.	NO.	DATE
M	1	

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PL = parallel load
 TTS = trans for to store
 CPSI = clock pulse serial in
 MRE = master reset
 DS = serial data in
 TOP = trans for out parallel
 TOS = trans for out serial
 CPD = clock pulse serial out
 ES = enable output
 QS = serial out

IRF = input reg full } assertion = L
 ORE = output reg empty }
 IES = input enable serial low
 OES = output enable serial low



CHK	CHANGE NO.	REV.

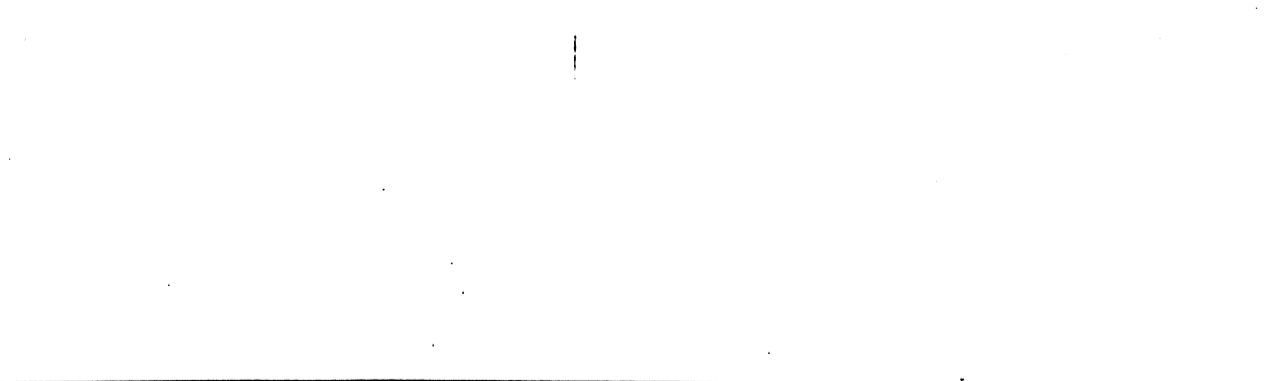
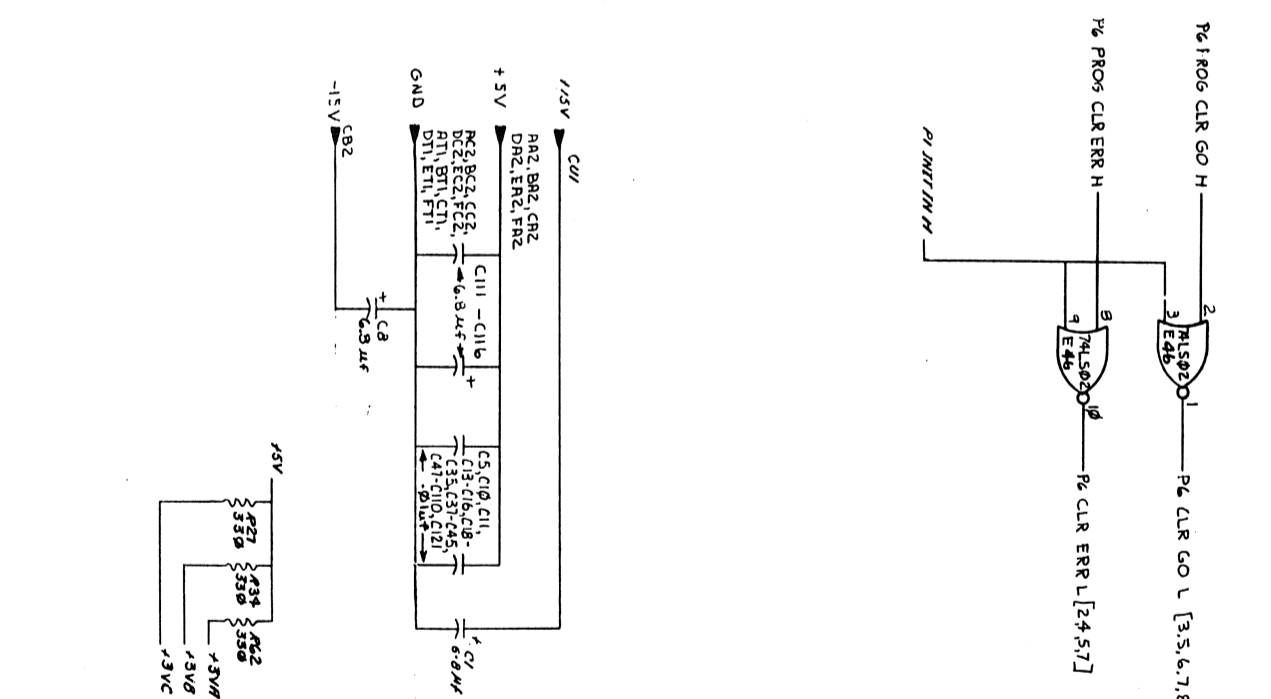
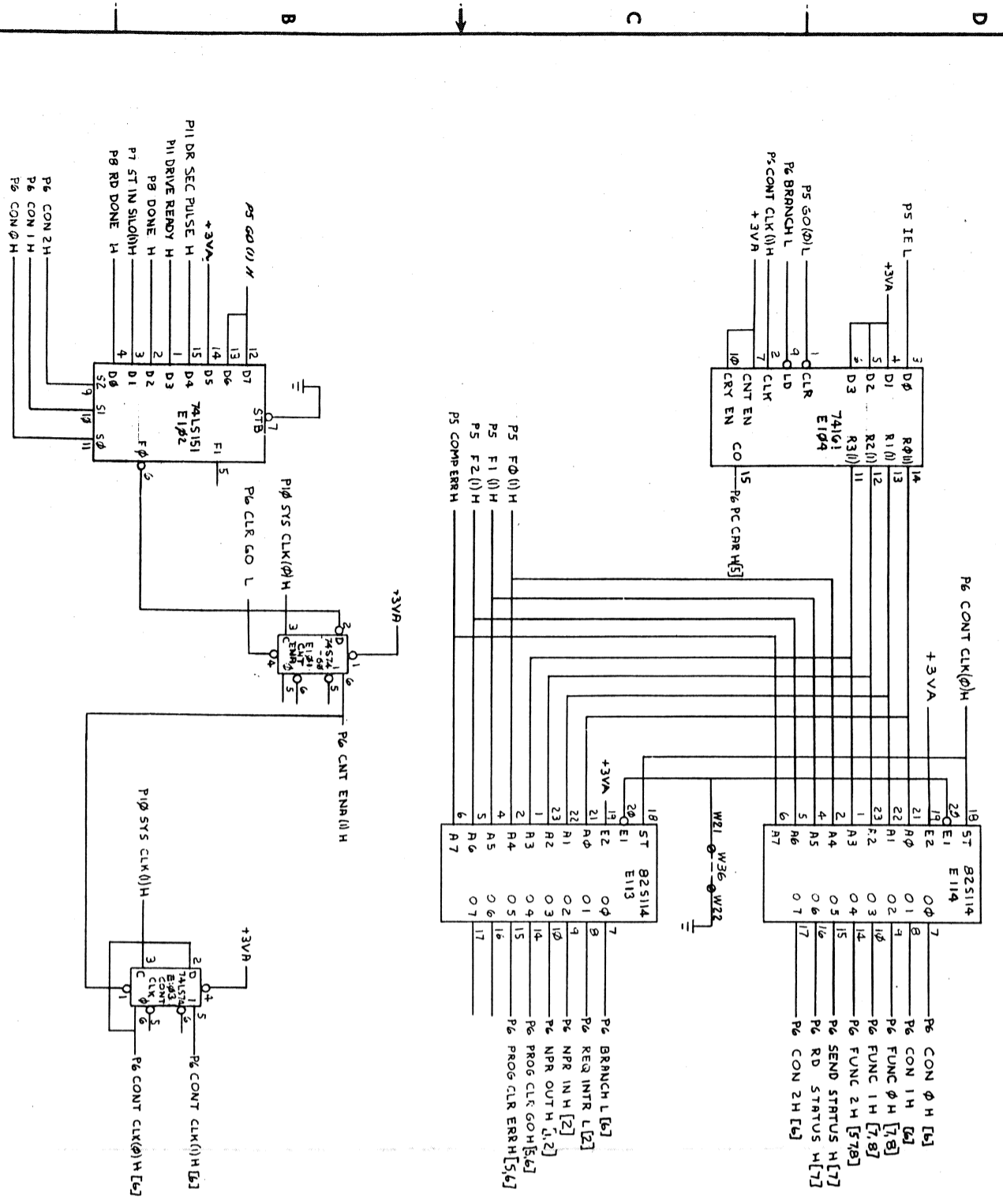
REV.	DESCRIPTION	DATE	BY

TITLE	CONTROLLER (P4)	SIZE/COORD.	DIST.	NUMBER	REV.
SCALE					
SHEET	4 OF 11				

SZ00

DCS M7762-0-1

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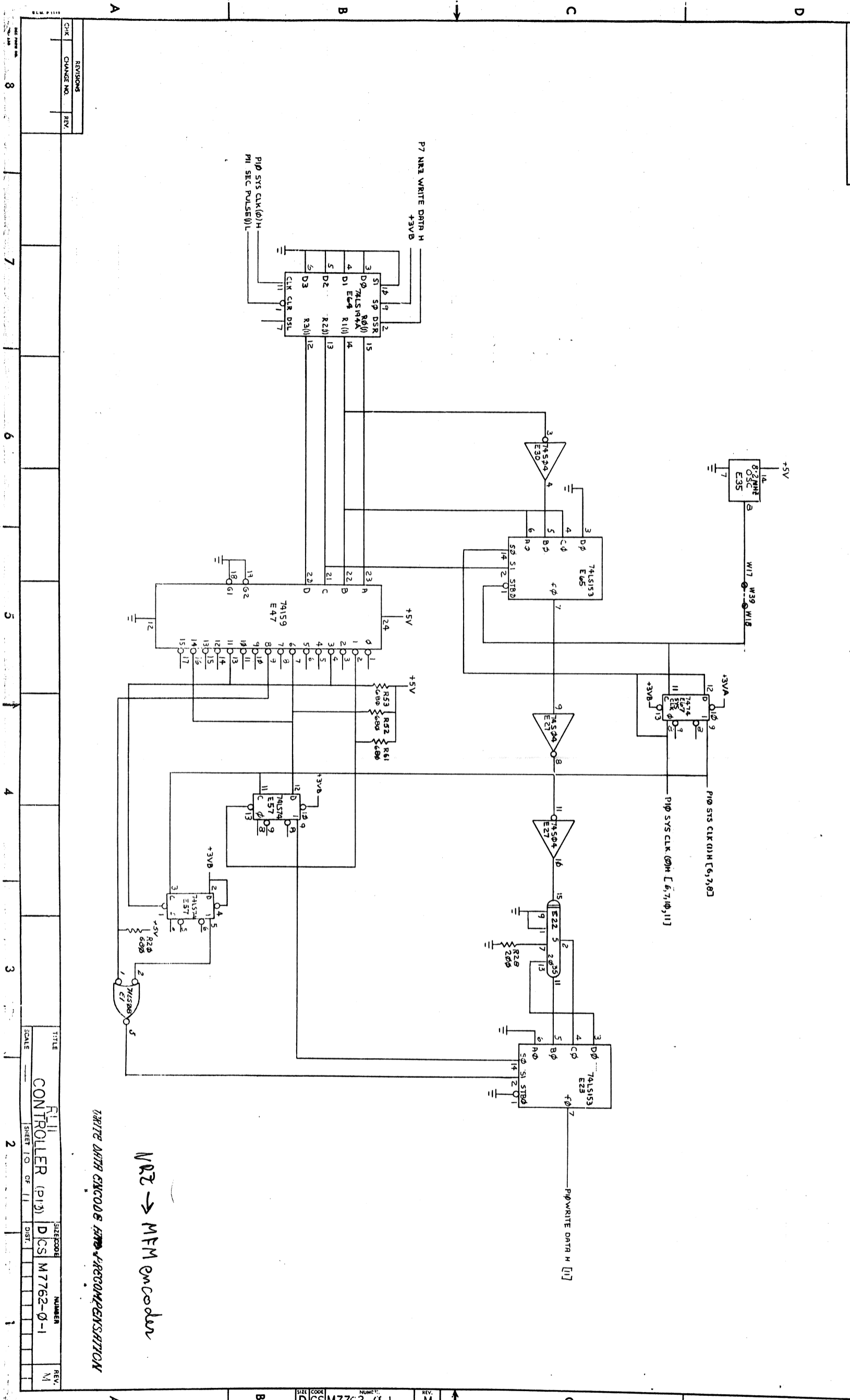
MICRO SEQUENCER FUNCTION CONTROL

REV.	NUMBER	SIZE	CODE	DATE	BY	CHK	CHANGE NO.	REV.
M1	1	DCS	M7762-0-1					

REV.	NUMBER	SCALE	TITLE
1	1	1:1	CONTROLLER (P6)

REV.	NUMBER	DATE	BY	CHK
1	1			

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NRZ → MEM Encoder

WRITE DATA ENCODER AND ADDRESS DECODER

REV.	CHANGE NO.	REV.

REV.	CHANGE NO.	REV.

TITLE	SCALE	SIZE/CODE	NUMBER	REV.
F111 CONTROLLER (P13)		DCS	M7762-0-1	M

REV.	NUMBER

DCS M7762-0-1