

bits), Is it a valid address, Is the offset of the word count register equal Indexed by the same amount? How long does that device wait before it times out?

- 15' Are there more than 20 unit loads on the system?
- 16' Is there more than 50 feet of unibus cable?
- 17' Are the terminators at the end of the bus?
- 18' If a power fail occurs, Is any power supply over loaded? What Is the line voltage?
- 19' Do all the diagnostics run with no error? Use the least complex test that fails to fix problems,
- 20' When an exerciser Is the only test that fails, do you know what the exerciser does and does not test? Exerciser must give up some form of testing in order to achieve high data rates;
- 21' If you physically make the system smaller, does it still fail? How small can you make it and still have it fail? Sometimes the device that fails is not bad, but only the exhibited symptom of a separate problem,
- 22' Are there any bus buffers/switches between the failing device and the processor? If so do same errors occur when device is run on processor side of buffer?
- 23' Does failure occur when only running to particular BR level? i.e., level 4 device only fails when running level 5 devices,
- 24' Do failures only occur when system is heavily loaded?
- 25' Do "DATA REQUEST LATE" errors occur with NPR Devices? What Is the worst case BG delay? Is that time plus processor worst case latency greater than device latency time?

OLD MAINDEC *****	MAINDEC NUMBER *****	DESCRIPTION *****
	D0AA	CPU T1 BRANCH
	D0BA	CPU T2 CON BRANCH
	D0CA	CPU T3 UNARY
	D0DA	CPU T4 BINARY
	D0EA	CPU T5 ROTATE/SHIFT
	D0FA	CPU T6 CMP EQUALITY
	D0GA	CPU T7 CMP NON-EQUALITY
	D0HA	CPU T8 MOVE
	D0IA	CPU T9 BIT SET CLEAR TEST
	D0JA	CPU T10 ADD
	D0KA	CPU T11 SUBTRACT
	D0LA	CPU T12 JUMP
	D0MA	CPU T13=JSR,RTS,RTI
	D0NC	CPU T14 FOR 11/05+11/20 TRAPS
	D0KDM	CPU T14/40 TRAPS TEST
D008	DZQKA	CPU T15 INST, EXER,
D00E	DZQKB	CPU T17 SYSTEM EXERCISER
D0RA	DZKAQ	CPU POWER FAIL 11/05,20,40
	DCKRA	CPU SXT
	DCKBB	CPU SOB
	DCKBC	CPU XOR
	DCKBD	CPU MARK
	DCKBE	CPU RTT
	DCKBF	CPU STACK LIMIT
	DCKBG	CPU SPL
	DCKBH	CPU 11/45 REGISTERS
	DCKBI	CPU ASH
	DCKBJ	CPU ASHC
	DCKBK	CPU MUL
	DCKBL	CPU DIV
	DCKBM	CPU TRAPS 11/45
	DCKBN	CPU PIRQ
	DCKBO	CPU STATES 11/45
	DCKBP	CPU 11/45 POWER FAIL
	DCKBQ	11/45 CONSOLE TEST (WITH MEM,MGMT)
		11/45 CONSOLE TEST (WITH MEM,MGMT)
	DCFPA	FP11 DKFPS,SFPFS,SETF/D,SETI/L CFCC
	DCFPB	FP11 STS,ILLEGAL INSTRUCTIONS
	DCFPC	FP11 LDF,LDD,STF,STD
	DCFPD	FP11 ADDF,ADDD,SUBF,SURD
	DCFPE	FP11 CMPF,CPFD
	DCFPF	FP11 MULF,MULD
	DCFPG	FP11 DIVF,DIVD
	DCFPH	FP11 CLR,TST,ABS,NEG
	DCFPI	FP11 LDCDF,LDCFP,STCFD,STCDF